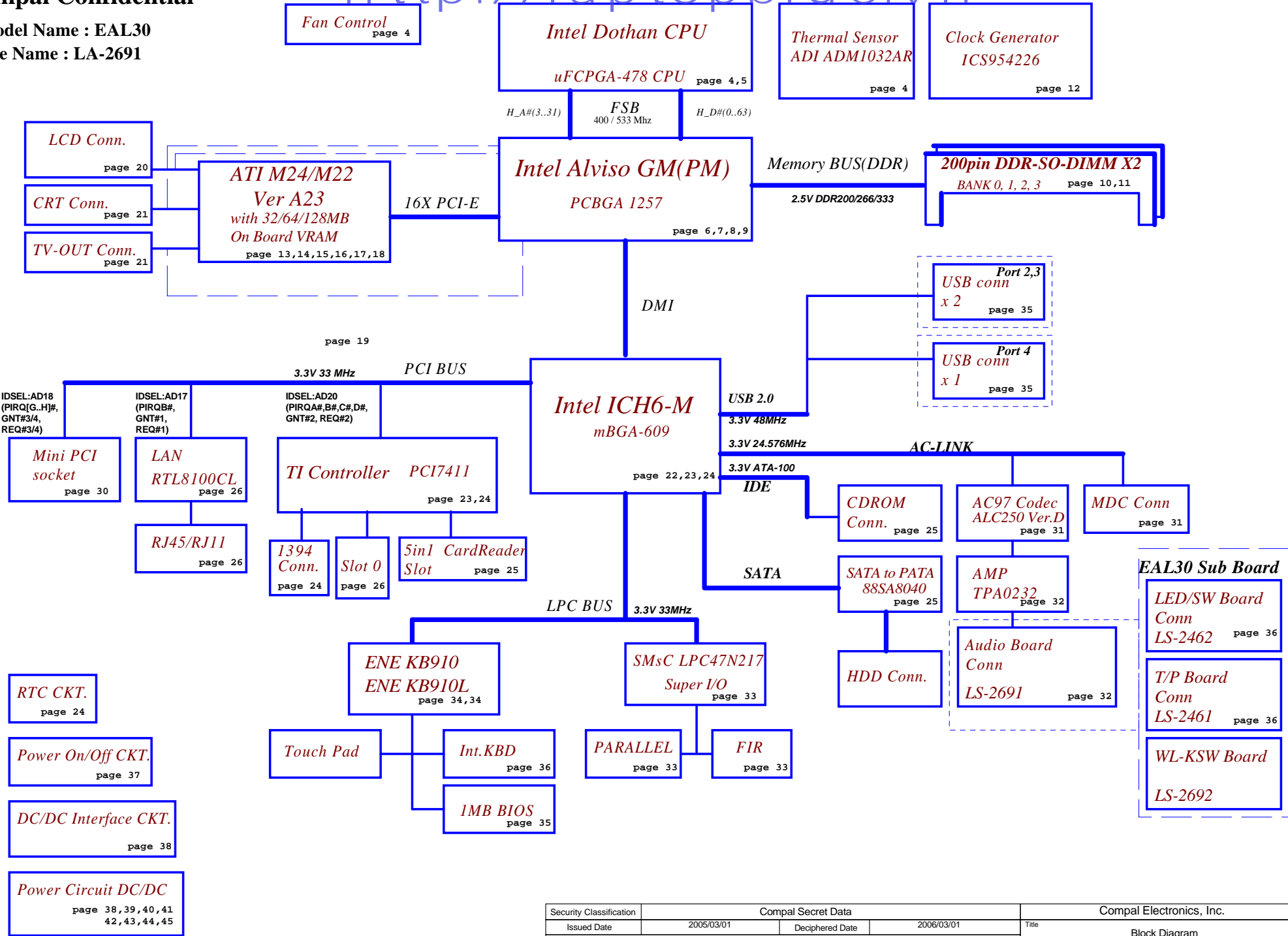


Compal Confidential

Model Name : EAL30
File Name : LA-2691



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Fortworth Alviso EAL30 LA-2691 Schematic

**uFC-PGA Dothan / 915 PM/GM/910GML
ATI M24/M24C/M22 Ver A23 with 64M/128M VRAM / ICH6-M**

2005-03-01

REV:1.0

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Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.05VS	1.05V rail for Processor I/O	ON	OFF	OFF
+1.25VS	1.25V switched power rail for DDR Vtt	ON	OFF	OFF
+VGA_CORE	1.2V/1.0V switched power rail for VGA core power	ON	OFF	OFF
+1.2VS	1.2V for VGA PCIE power	ON	OFF	OFF
+1.5VALW	1.5V always on power rail	ON	ON	ON*
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail for VGA frame buffer	ON	OFF	OFF
+2.5V	2.5V power rail for system DDR	ON	ON	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V switched power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
RTCVCC	RTC power	ON	ON	ON*

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

ICH6-M I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 X
DDR SO-DIMM 1	A2	1 0 1 0 0 0 1 X
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 X

KB910 I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
SM1 24C16	A0H	1 0 1 0 0 0 0 X b
SM1 SMART BATTERY	16H	0 0 0 1 0 1 1 X b
SM2 ADM0132 CPU THERMAL MONITOR	98H	1 0 0 1 1 0 0 X b
SM2 ALC250 codec	00H	0 0 0 0 0 0 0 X b

External PCI Devices

DEVICE	PCI Device ID	IDSEL #	REQ/GNT #	PIRQ
1394	D0	AD20	2	A,B,C,D
LAN	D1	AD17	3	F
CARD BUS	D4	AD20	2	A,B,C,D
SIN1	D4	AD20	2	A,B,C,D
Mini-PCI	D2	AD18	1	G,H

Symbol note:



:means digital ground.



:means analog ground.



:means reserved.

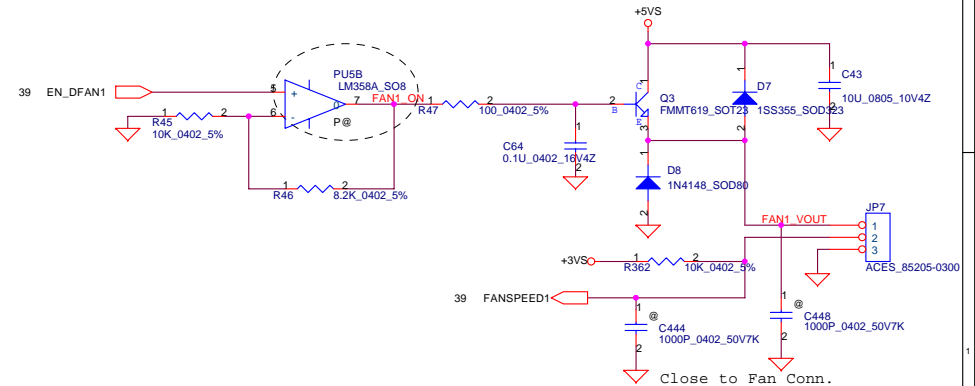
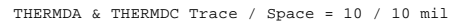
Fortworth Alviso Comparison Table

Item	* Descrite	UMA	Page
VGA	ATi M22P/24P	UMA	15 ~ 18
VRAM	128MB/64MB	N/A	19 ~ 20

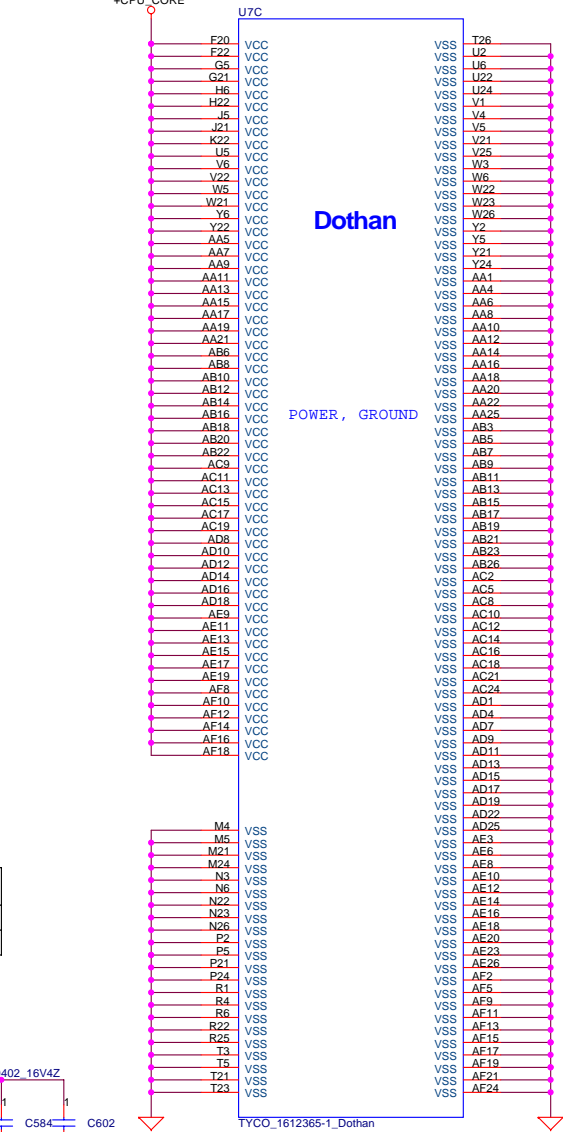
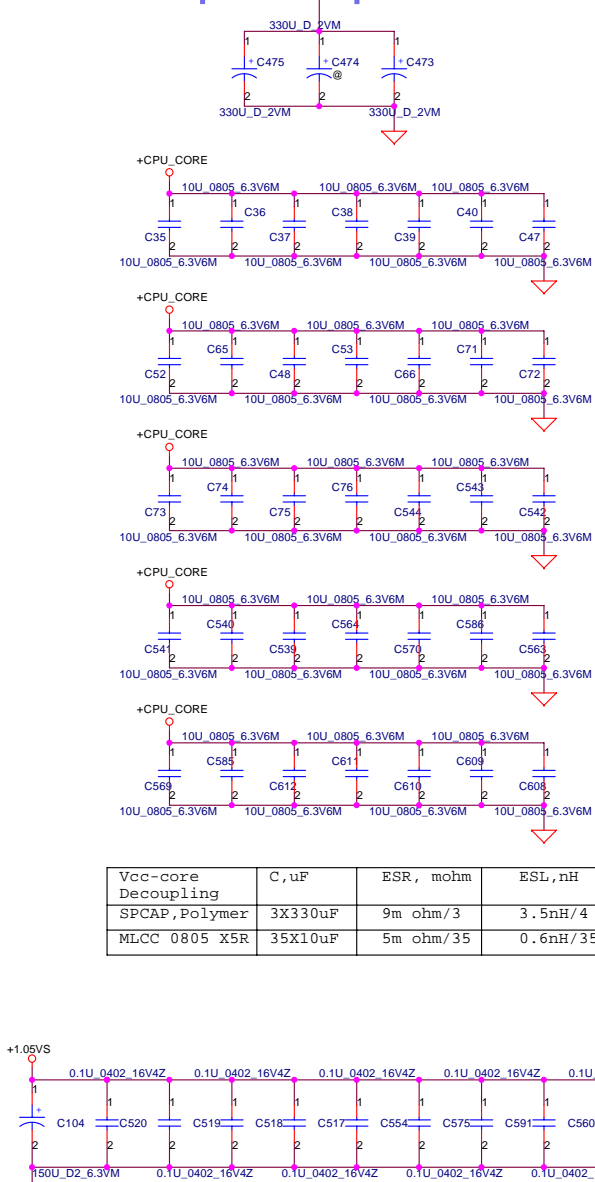
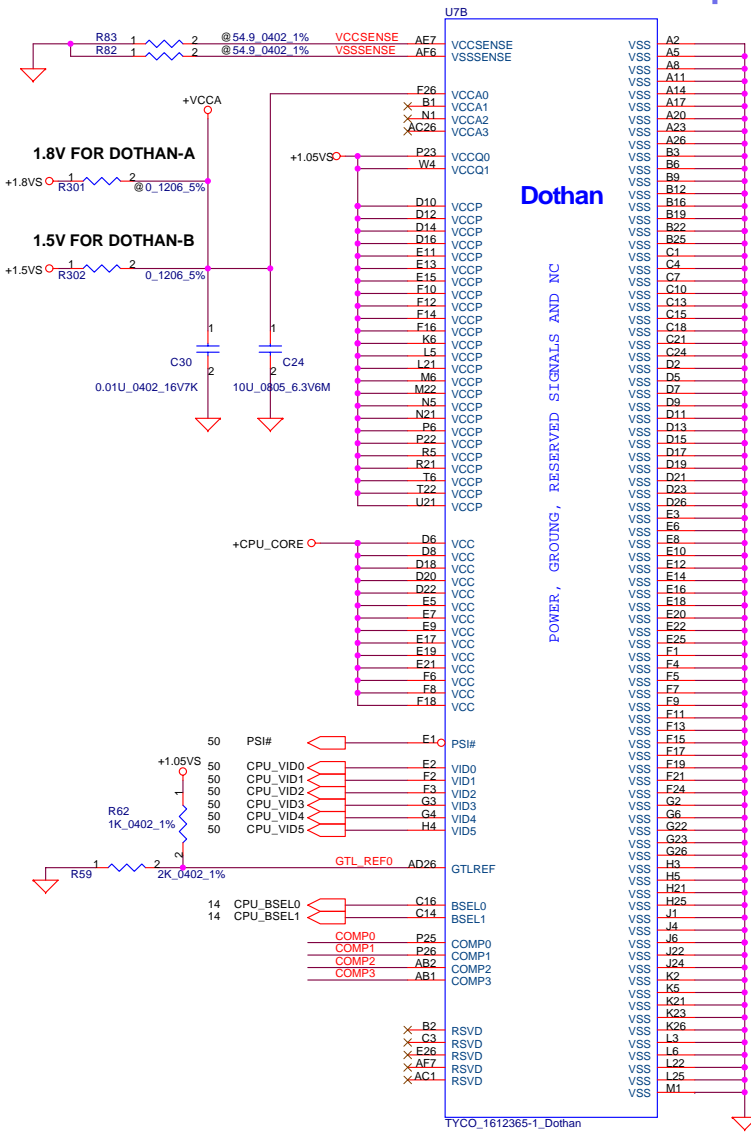
Board ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra	10K +/- 5%			
BID/FID	Rb/Rc	V _{AD_BID min}	V _{AD_BID typ}	V _{AD_BID max}
0	0	0 V	0 V	0.1 V
1	8.2K +/- 5%	1.412 V	1.486 V	1.560 V
2	18K +/- 5%	2.015 V	2.121 V	2.227 V
3	33K +/- 5%	2.406 V	2.533 V	2.659 V
4	56K +/- 5%	2.660 V	2.800 V	2.940 V
5	NC	3.135 V	3.300 V	3.465 V

Board ID	PCB Revision
* 0	0.1
1	
2	
3	
4	
5	
6	
7	



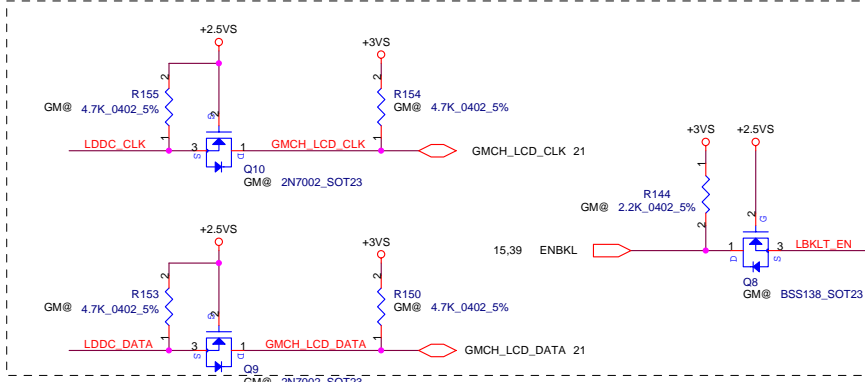
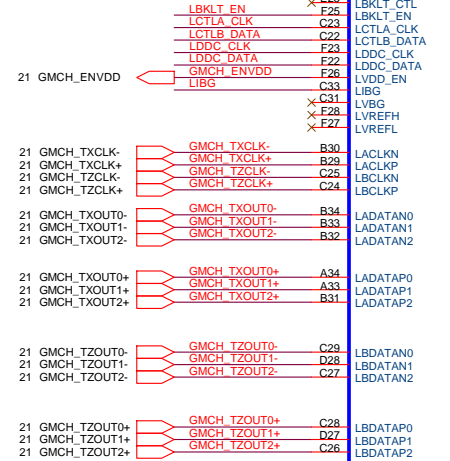
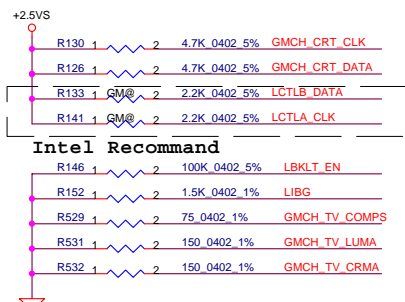
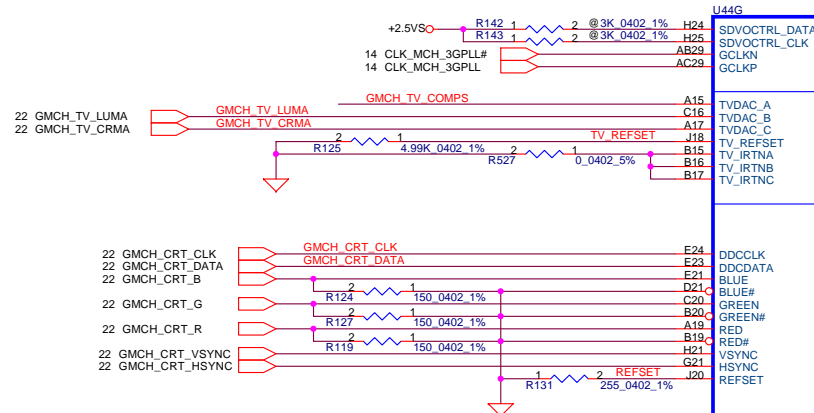
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				Size	Document Number	Rev 1.0
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Vcc-core	C, uF	ESR, mohm	ESL, nH
Decoupling			
SPCAP, Polymer	3X330uF	9m ohm/3	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35

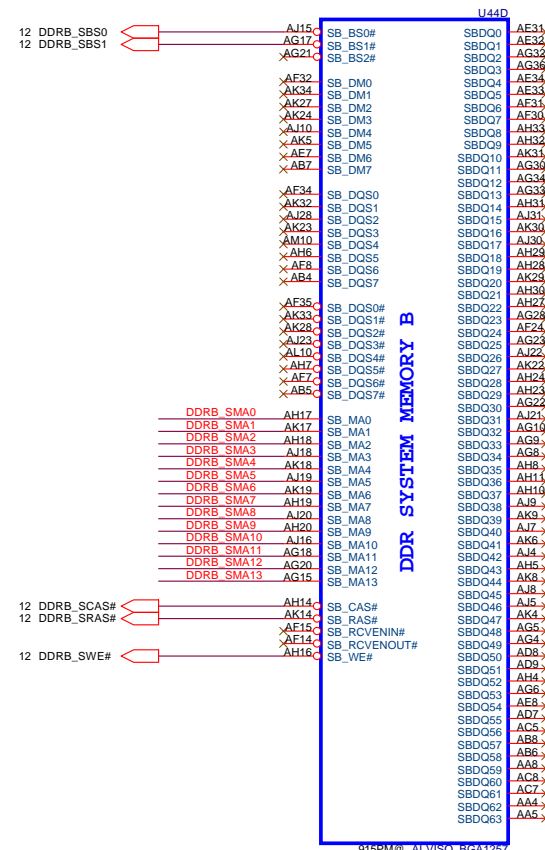
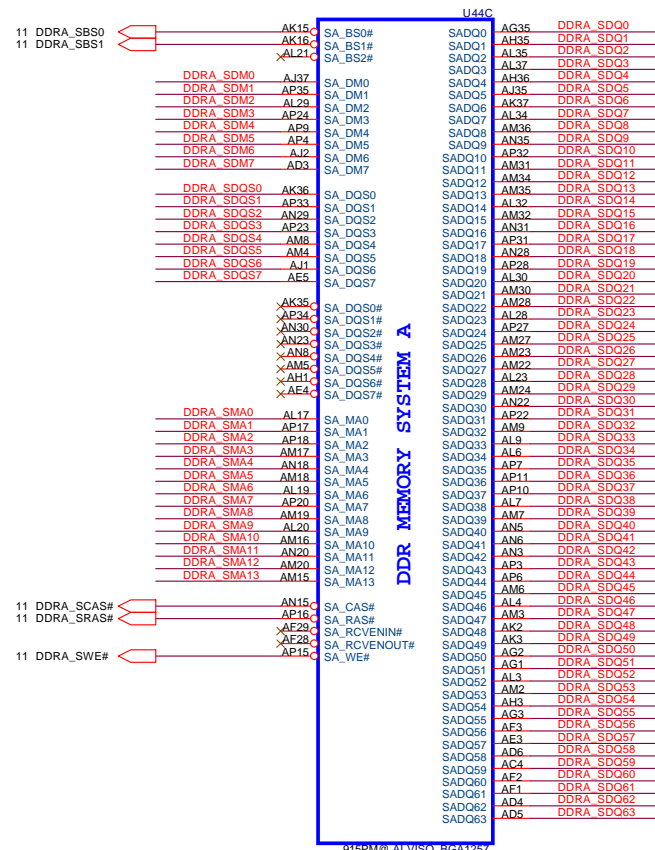
TRACE CLOSELY CPU < 0.5"
 COMP0, COMP2 layout : Width 18mils and Space 25mils
 COMP1, COMP3 layout : Space 25mils

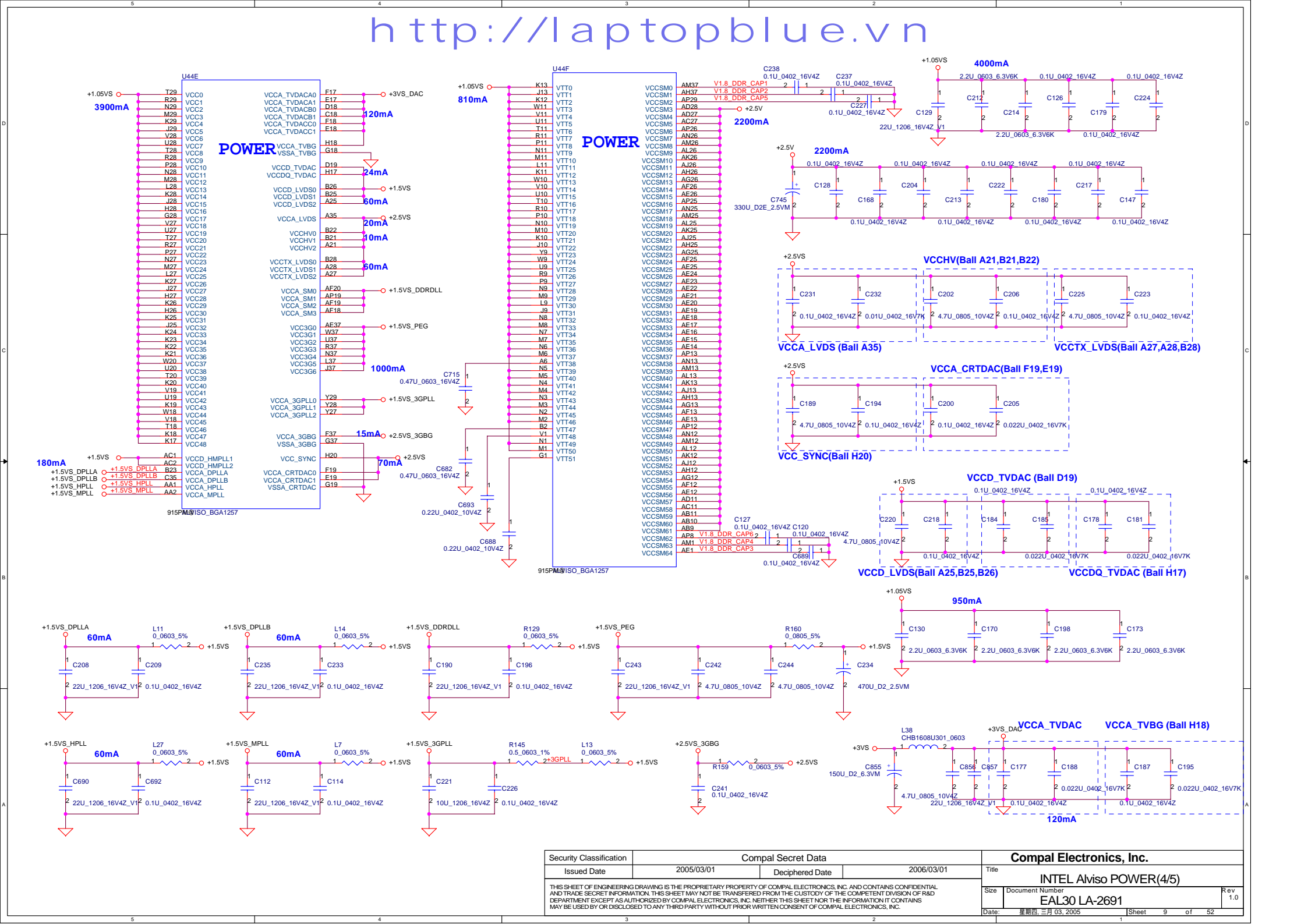
15 PCIE_MTX_C_GRX_N[0..15] < PCIE_MTX_C_GRX_N[0..15]
 15 PCIE_MTX_C_GRX_P[0..15] < PCIE_MTX_C_GRX_P[0..15]
 15 PCIE_GTX_C_MRX_N[0..15] < PCIE_GTX_C_MRX_N[0..15]
 15 PCIE_GTX_C_MRX_P[0..15] < PCIE_GTX_C_MRX_P[0..15]



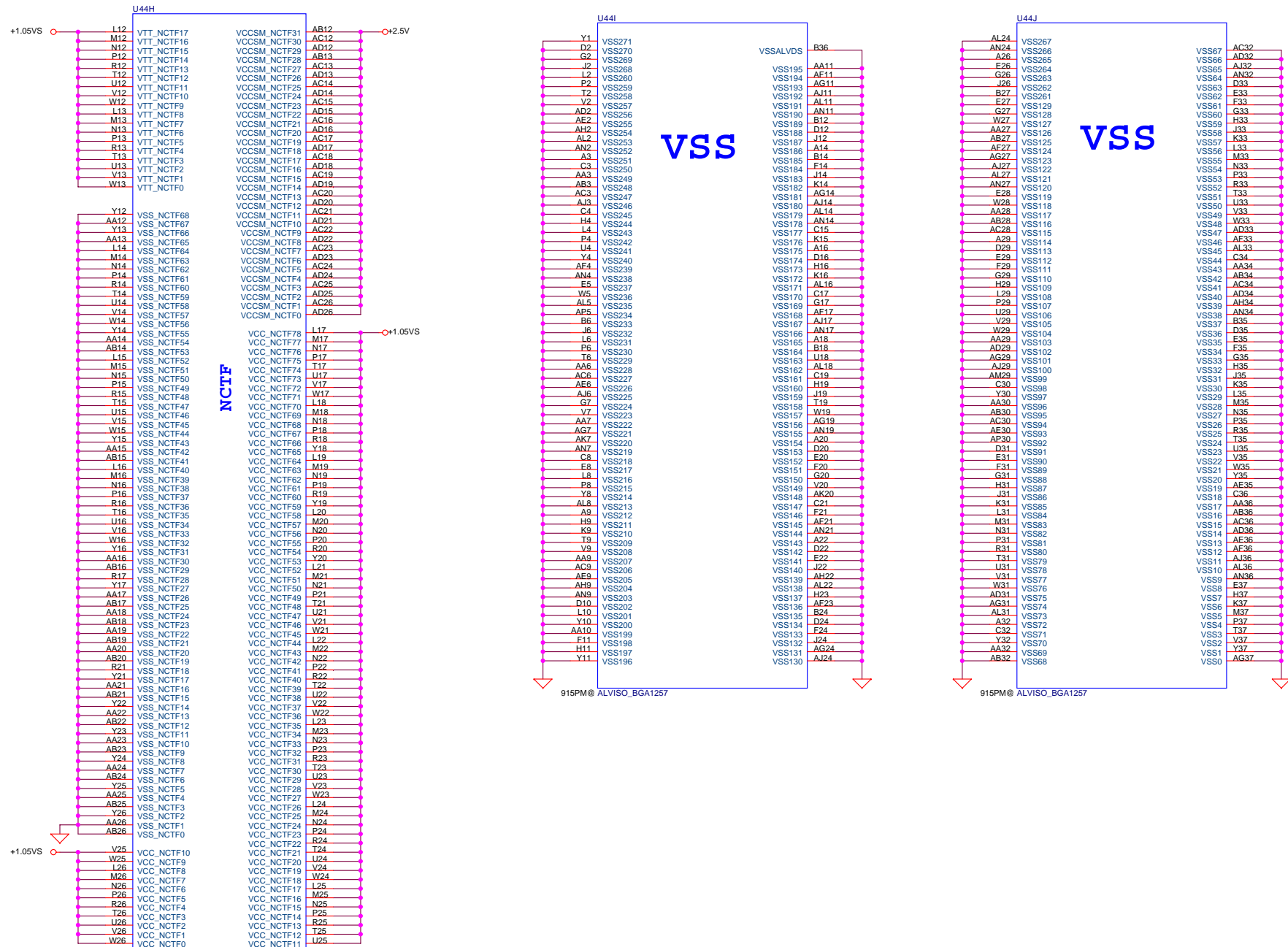
11 DDRA_SDO[0..63]  DDRA_SDO[0..63]
11 DDRA_SDM[0..7]  DDRA_SDM[0..7]
11 DDRA_SDQS[0..7]  DDRA_SDQS[0..7]
11 DDRA_SMA[0..13]  DDRA_SMA[0..13]

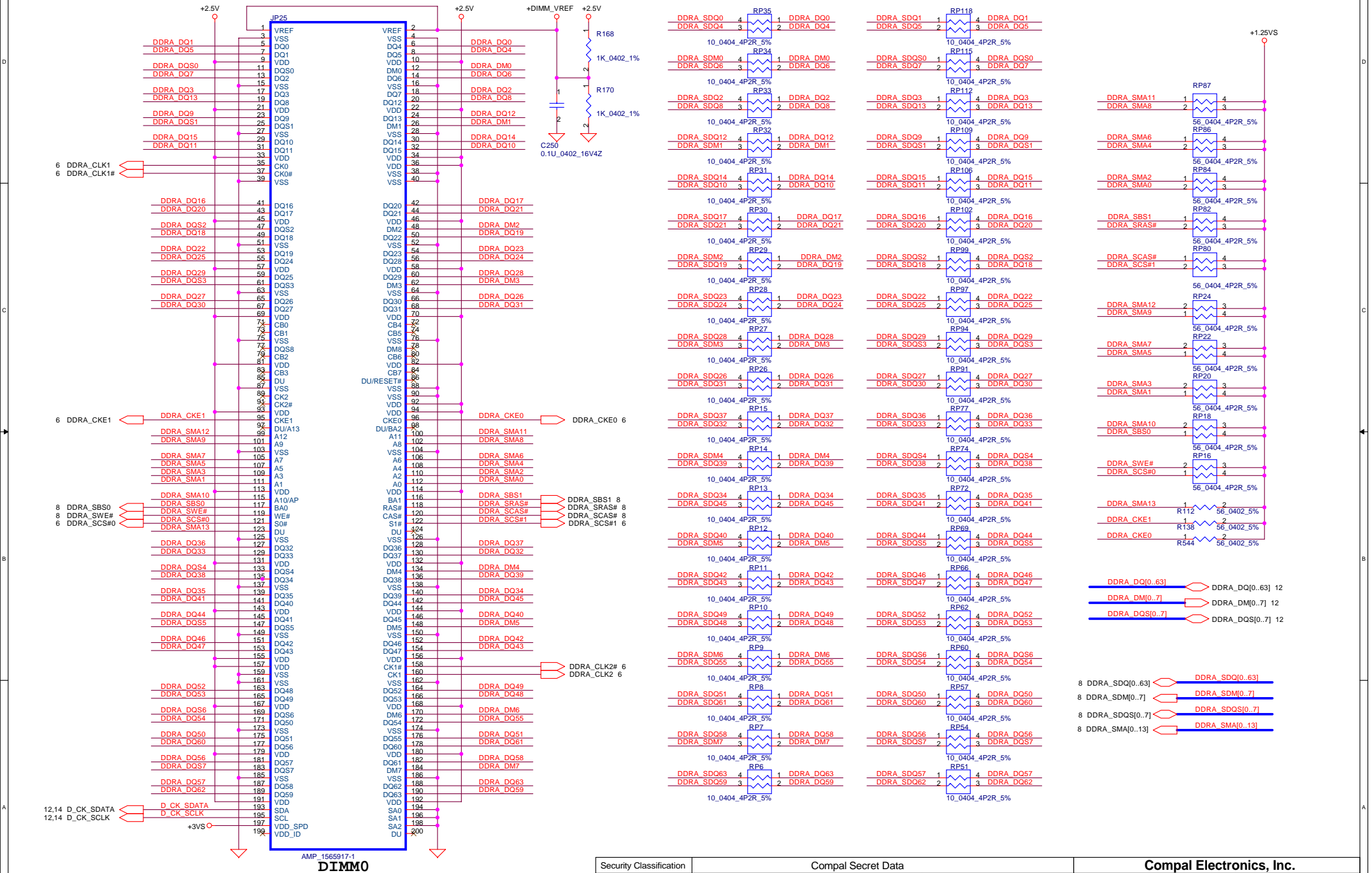
12 DDRB_SMA[0..13]  DDRB_SMA[0..13]



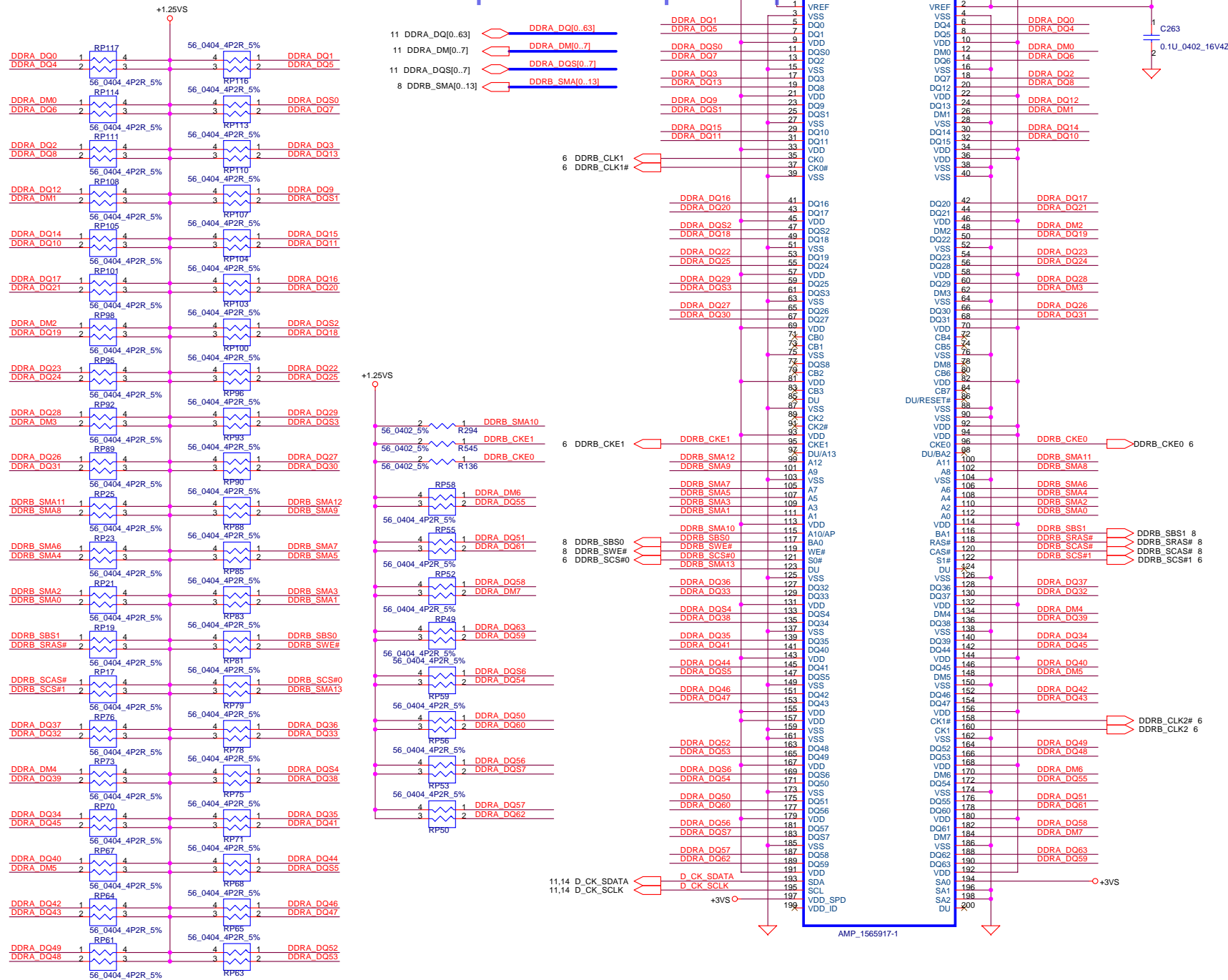
[illegible]

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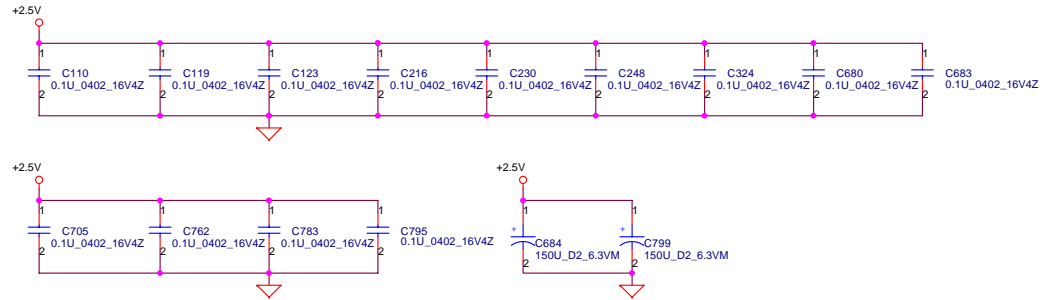
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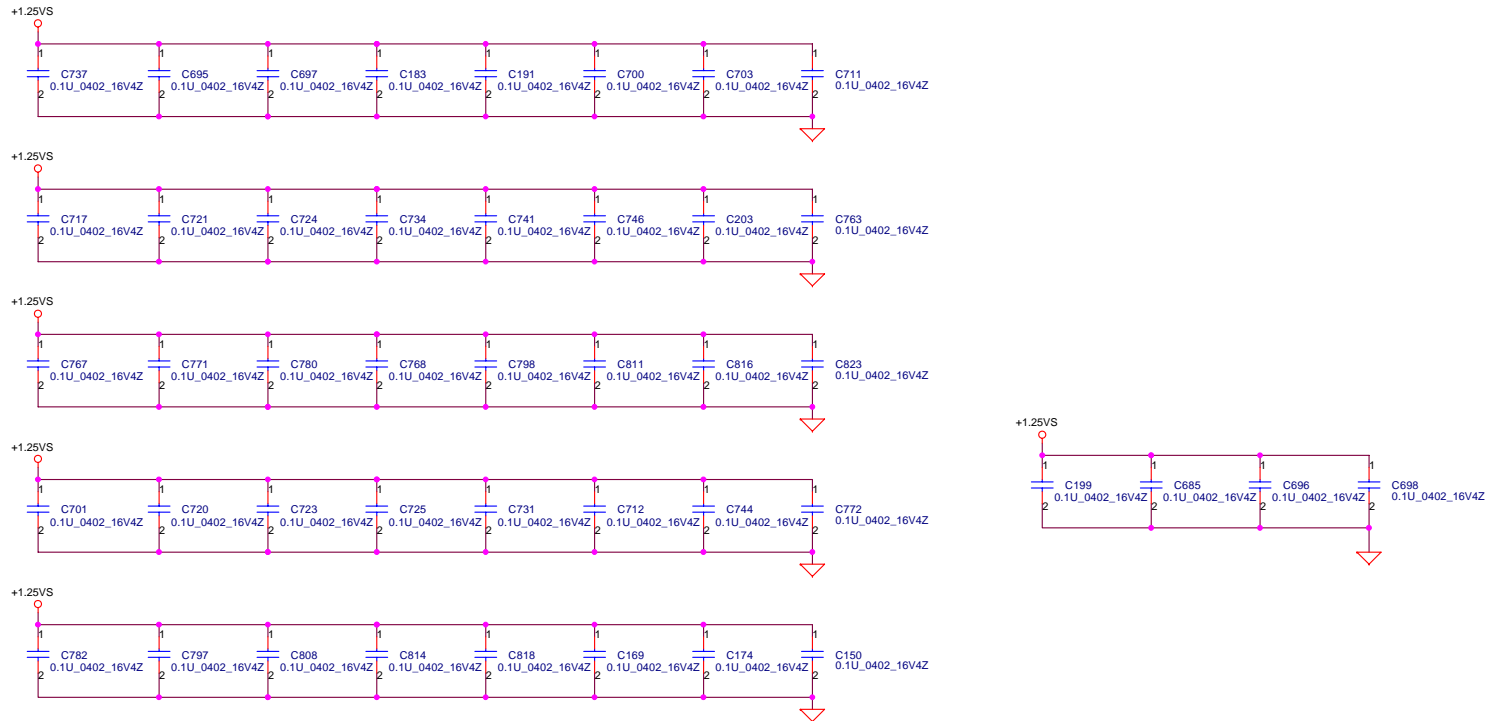
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Layout note :

Distribute as close as possible to DDR-SODIMM.

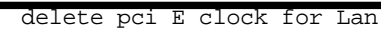
**Layout note :**

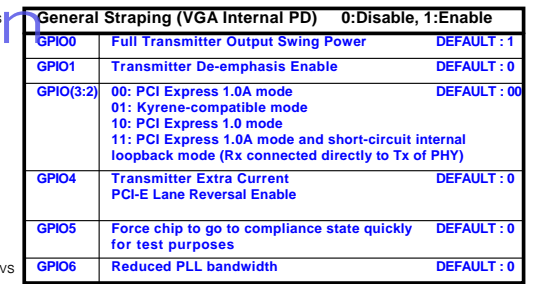
Place one cap close to every 2 pull up resistors termination to +1.25V



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Clock Generator



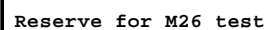


Vedio Memory Config. (VGA Internal PD) 1.8V only						
MEM_ID0	MEM_ID1	MEM_ID2		Size	Vendor	Chips
0	0	1		64MB	8Mx32	Samsung x2
0	1	1	Default	128MB	8Mx32	Samsung x4
1	0	1		64MB	8Mx32	Hynix x2
1	1	1		128MB	8Mx32	Hynix x4
0	0	0		32MB	4Mx32	Samsung x2
0	1	0		64MB	4Mx32	Samsung x4
1	0	0		32MB	4Mx32	Hynix x2
1	1	0		64MB	4Mx32	Hynix x4

Place +VREFG divider Res
and decoupling Cap close
to Ball AG4

Spread spectrum

Keep away from other signal at last 25mils



Voltage divider
Reduce Voltage from 3.3V to 1.2V

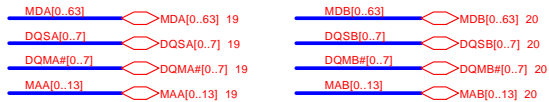
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M22P/24P PCIE,LVDS,GPIO,CLK

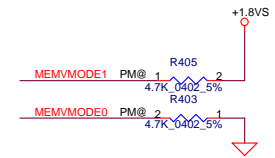
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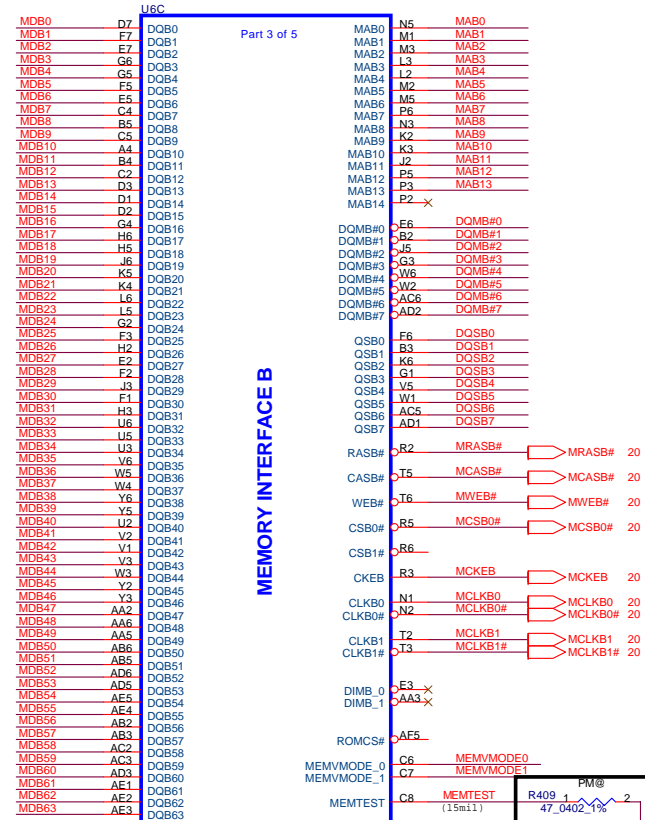
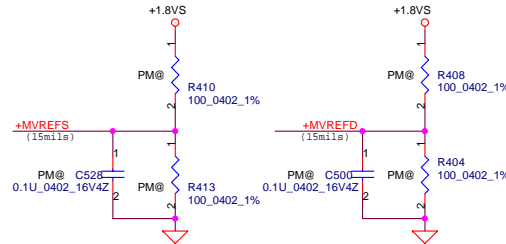


Memory speed MAX200MHz

	MEMVMODE0	MEMVMODE1	Default
M22/24(1.8V VRAM)	Pull-low	Pull-high	
M26(1.8V VRAM)	NC	NC	



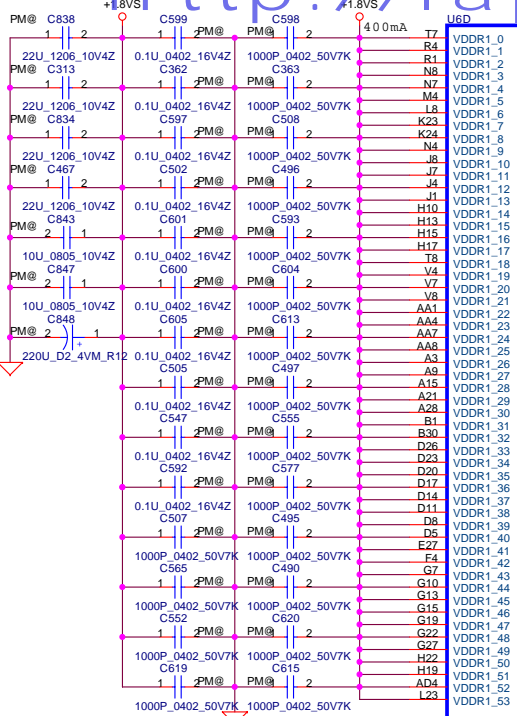
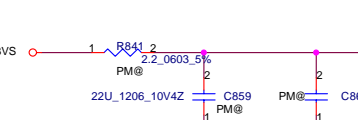
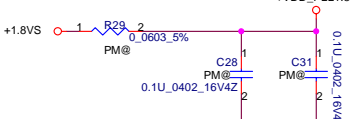
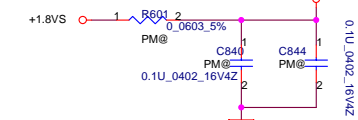
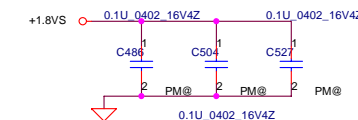
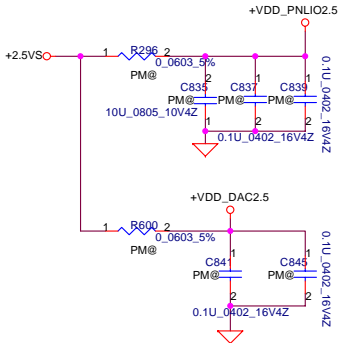
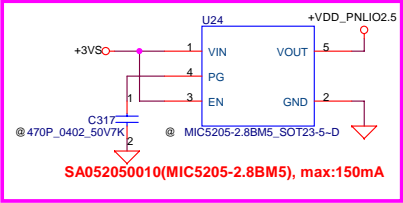
M24P_BGA708
 M24@



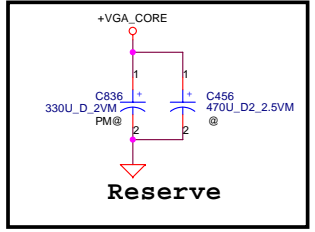
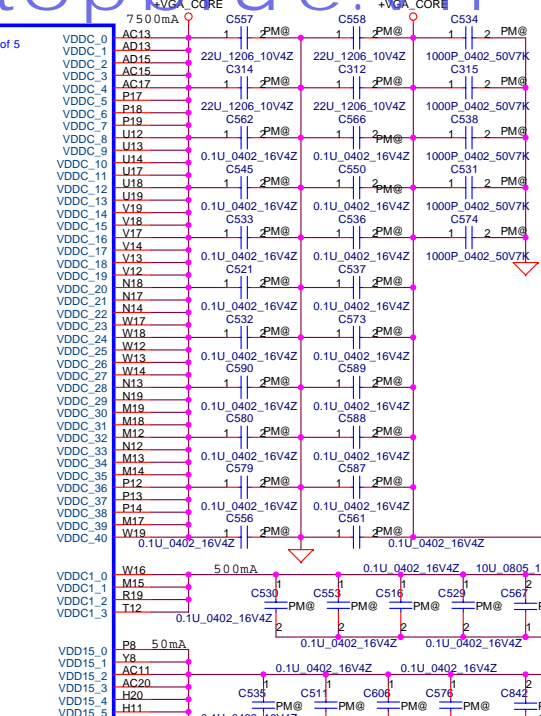
M24P_BGA708
 M24@

change to 240 ohm when use M26

ATi suggestion:
Using filter is acceptable (Default)
Using liner regulator is optimal. (Reserve)

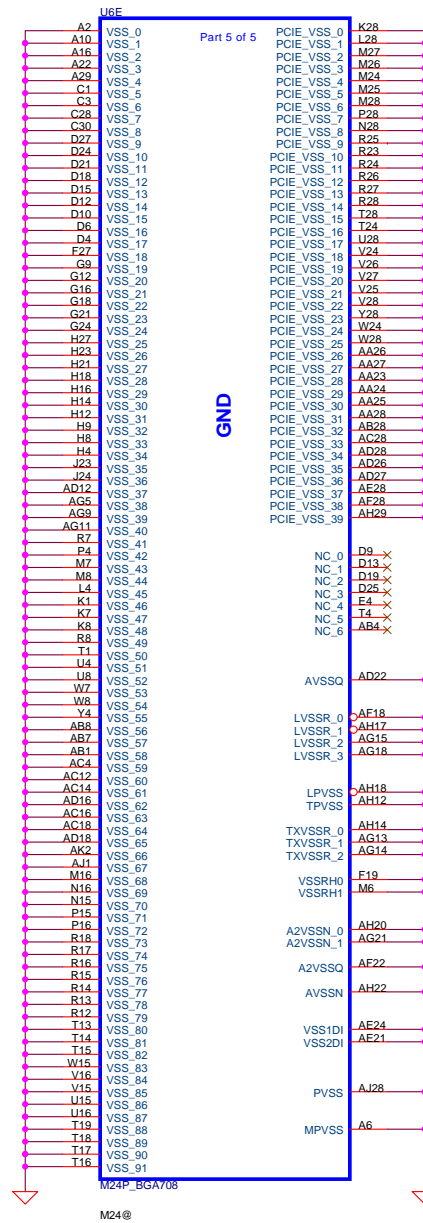


POWER

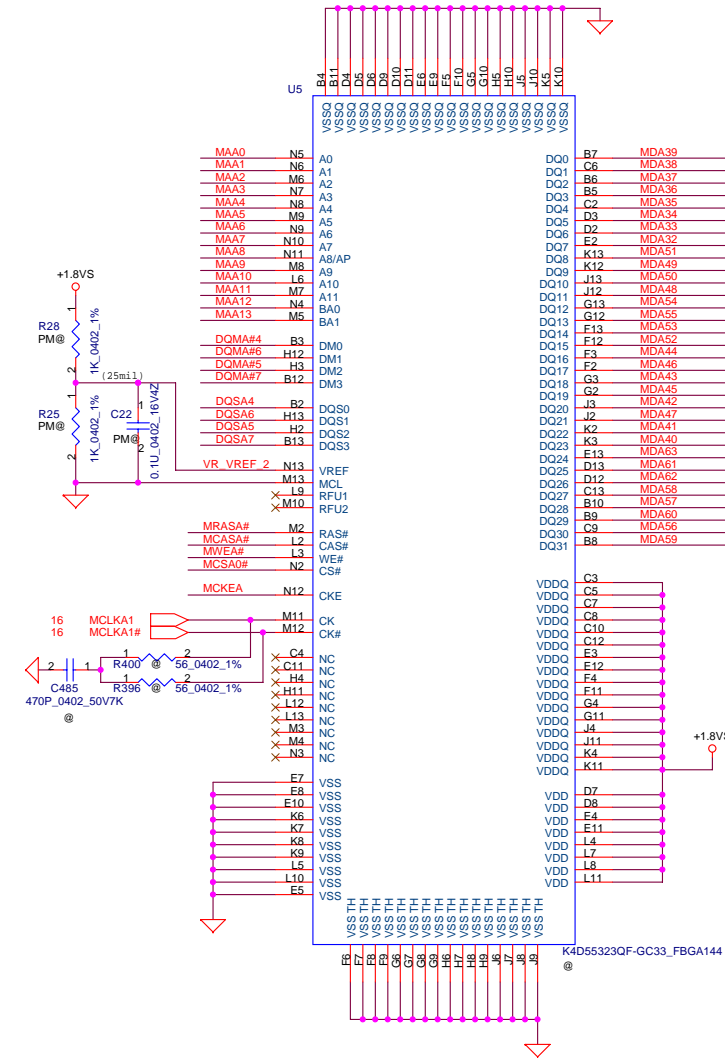


Reserve

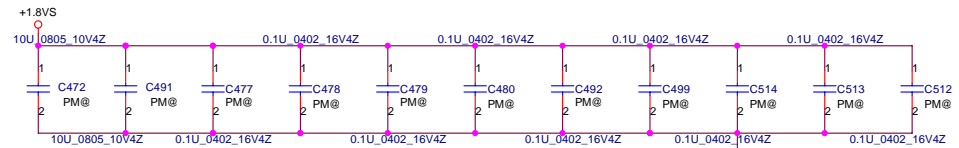
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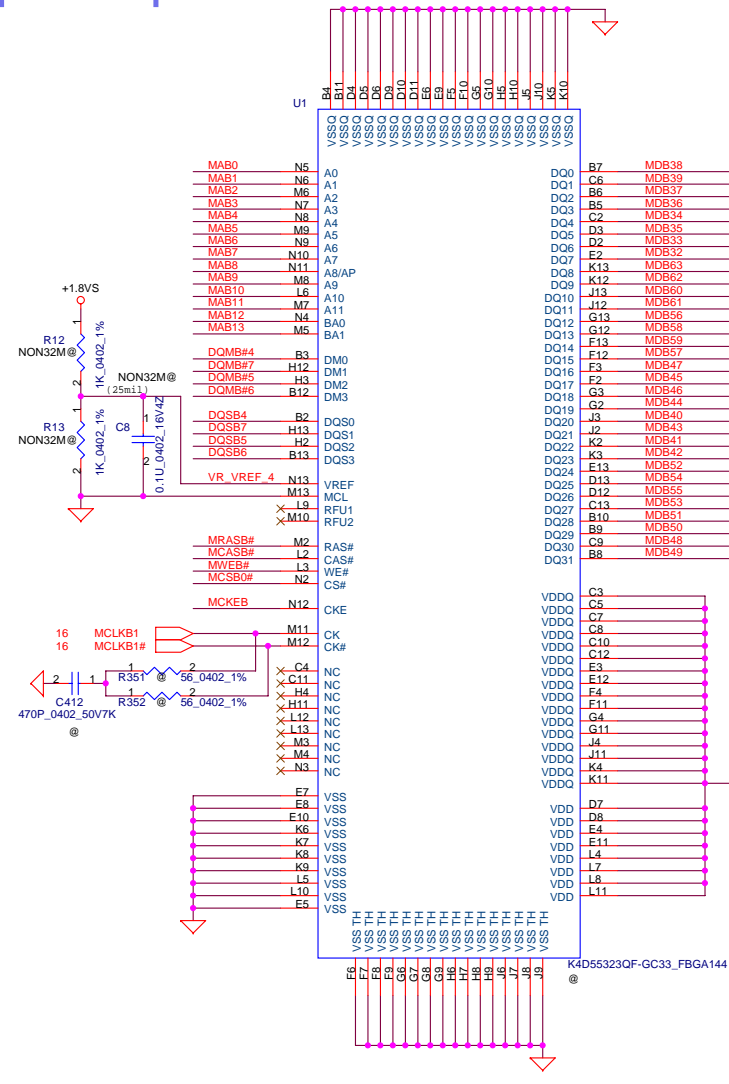


As close as possible to relative pfm



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16	DQSB[0..7]		DQSB[0..7]
16	MDB[0..63]		MDB[0..63]
16	DQMB#[0..7]		DQMB#[0..7]
16	MAB[0..13]		MAB[0..13]



As close as possible to relative pin

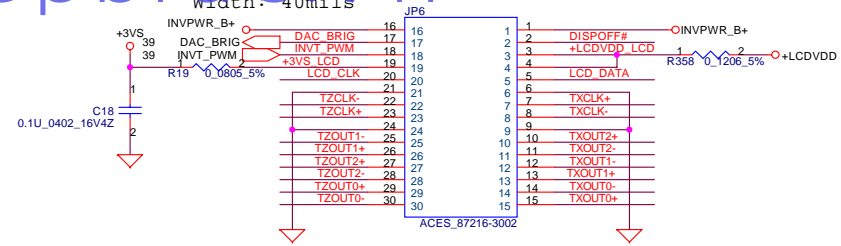
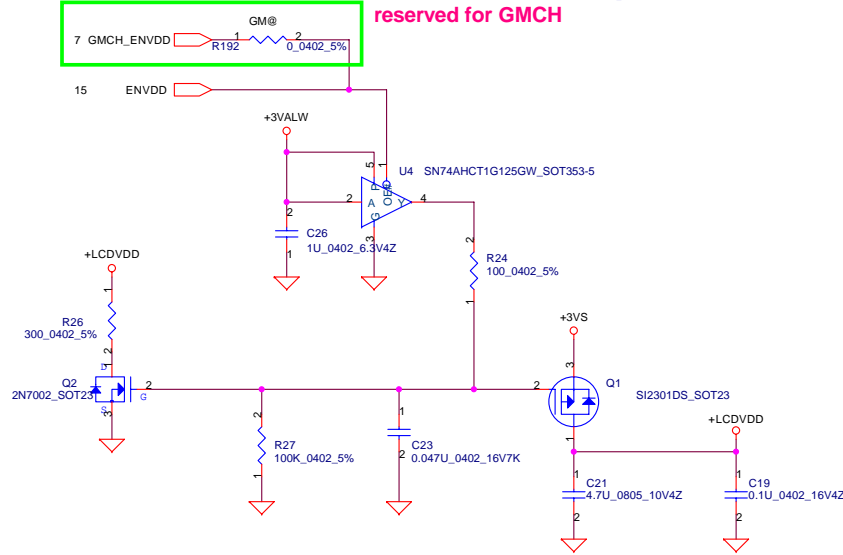
Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date	2005/03/01	Deciphered Date	2006/03/01	Title	VGA DDR CHANNEL B		
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				EAL30 LA-2691			
				Customer			
				Date:	星期四, 三月 03, 2005		Sheet 20 of 52

LCD POWER CIRCUIT

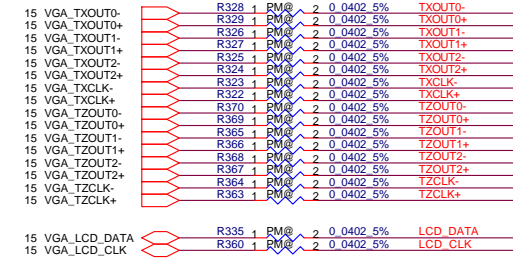
http://laptopblue.vn

LCD CONN.

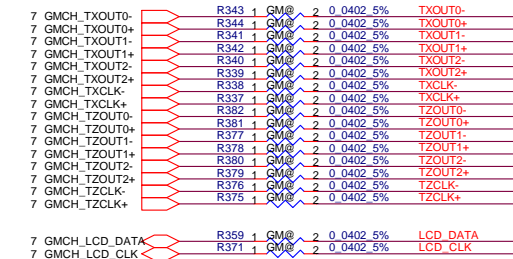
Width: 40mils



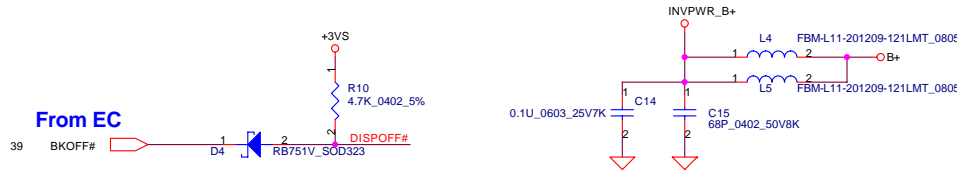
For ATI M24



For GMCH

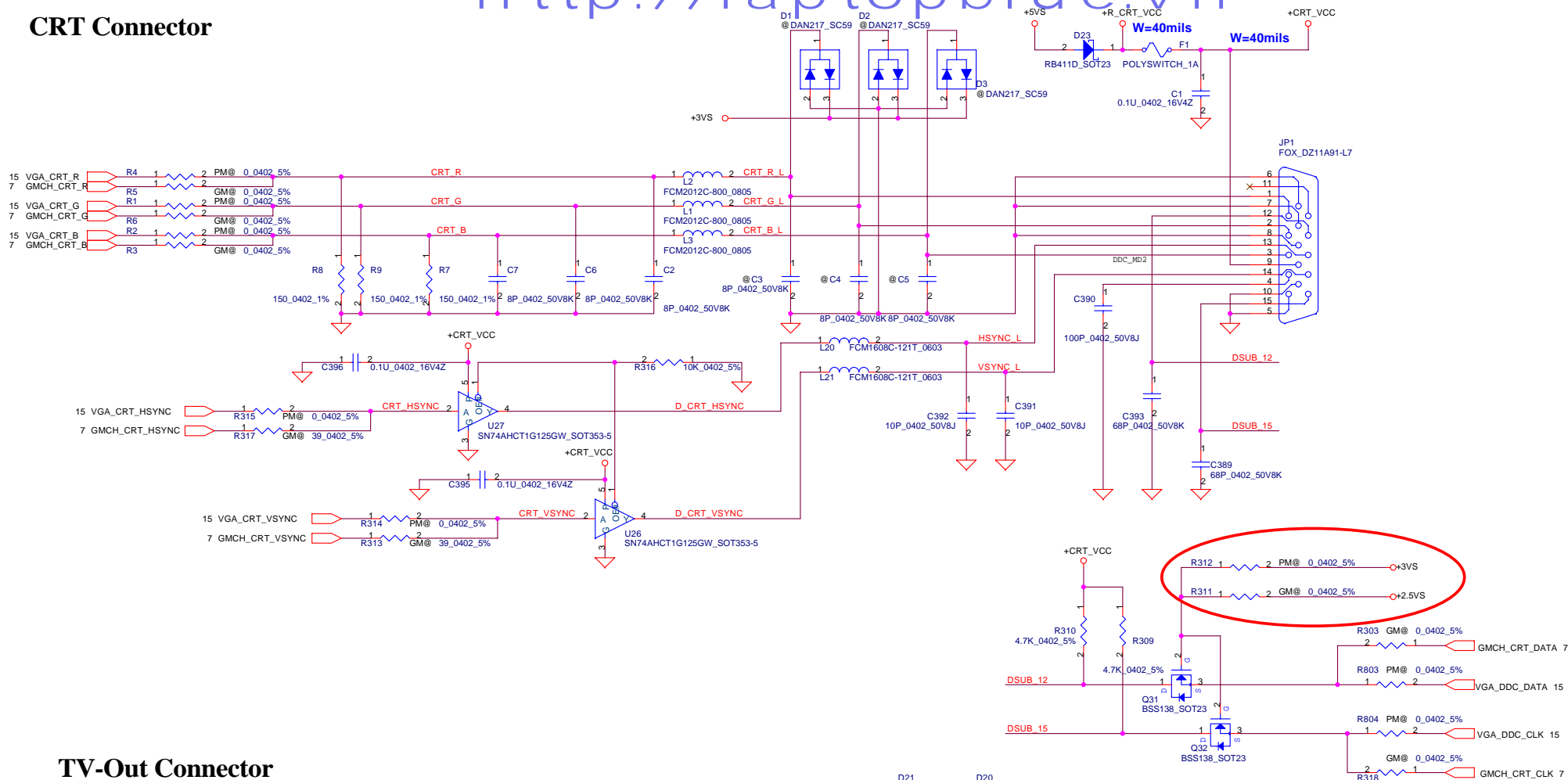


From EC

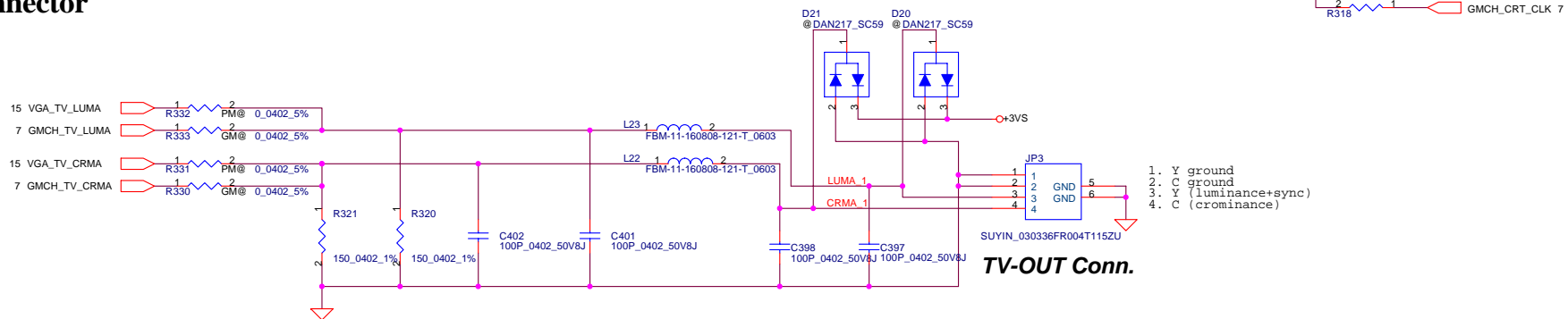


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				Size Custom
				Document Number
				EAL30 LA-2691
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				Date: 星期三, 三月 03, 2005
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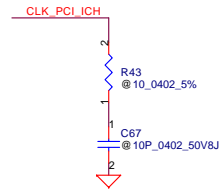
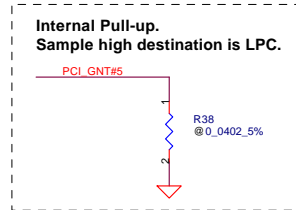
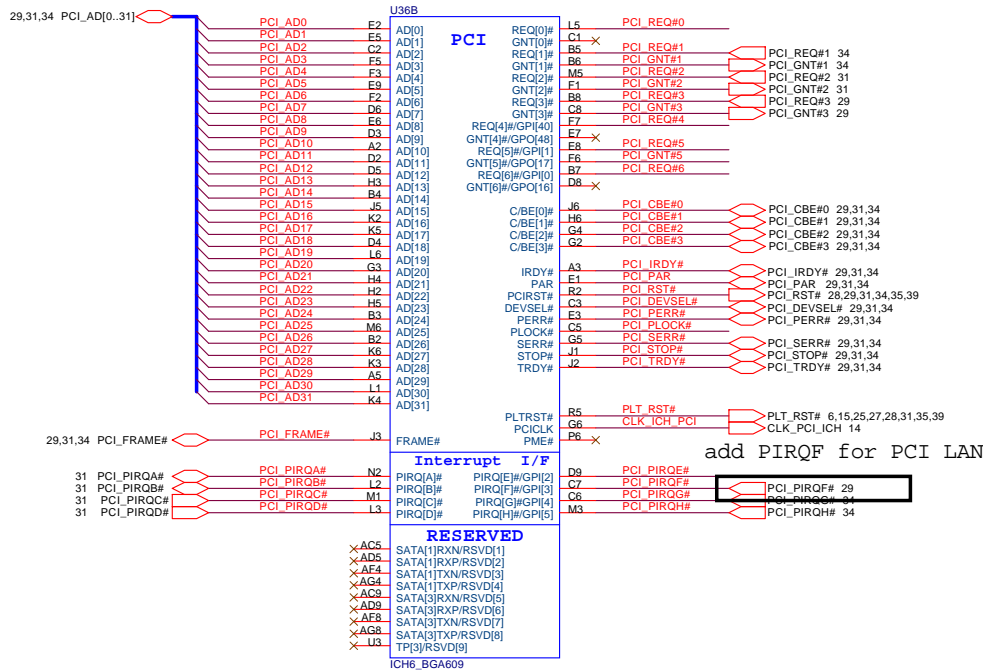
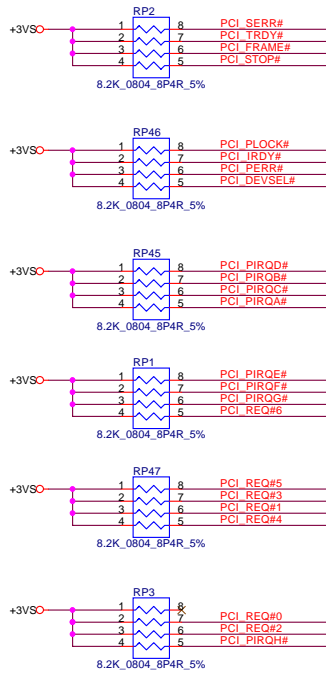
CRT Connector



TV-Out Connector



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ICH6-M
(R3:SA828010890)
(R1:SA8280108B0)

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				Date	Rev
				星期三, 三月 03, 2005	1.0
				Sheet	23 of 52

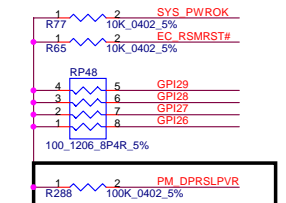
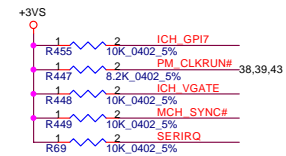
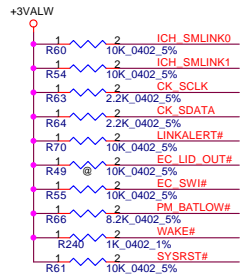
A

B

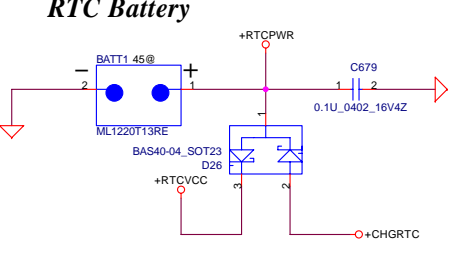
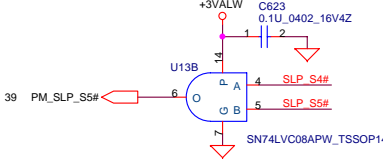
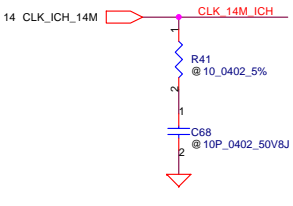
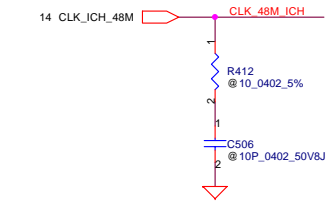
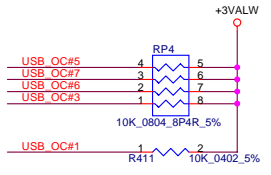
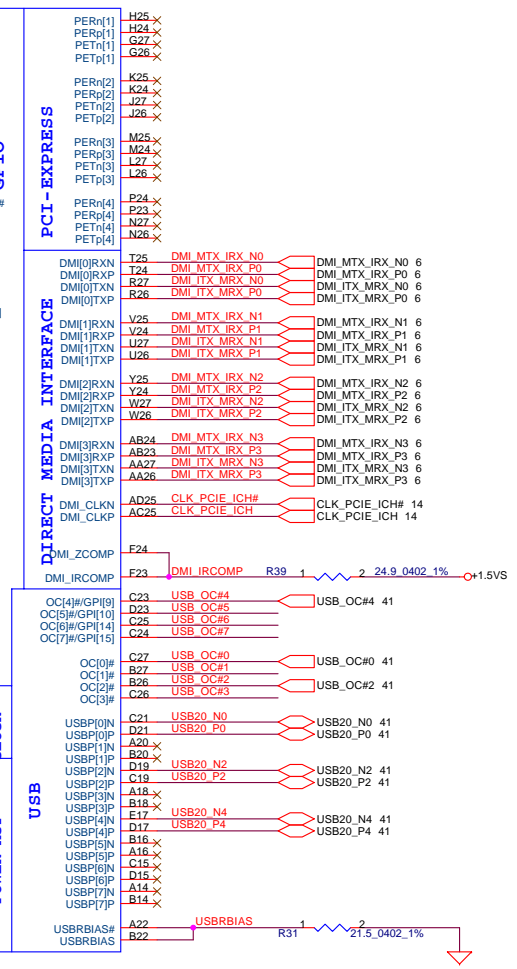
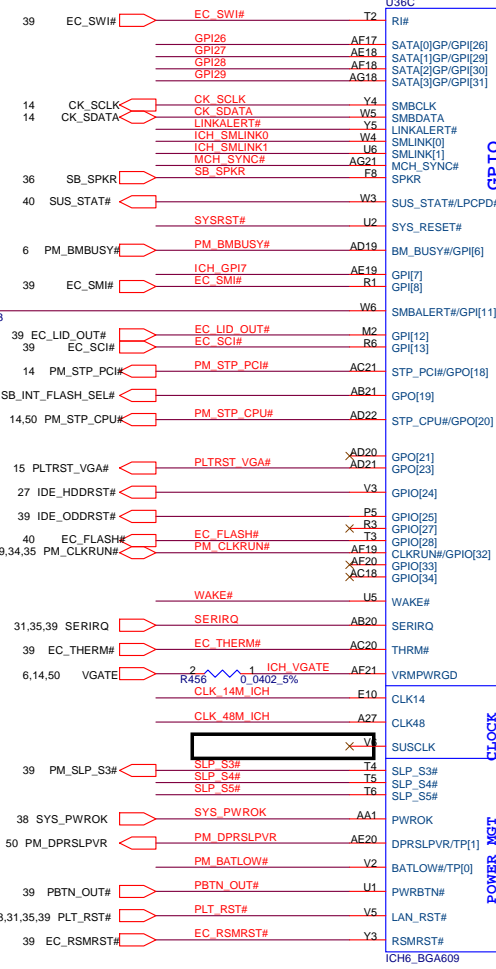
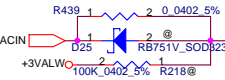
0



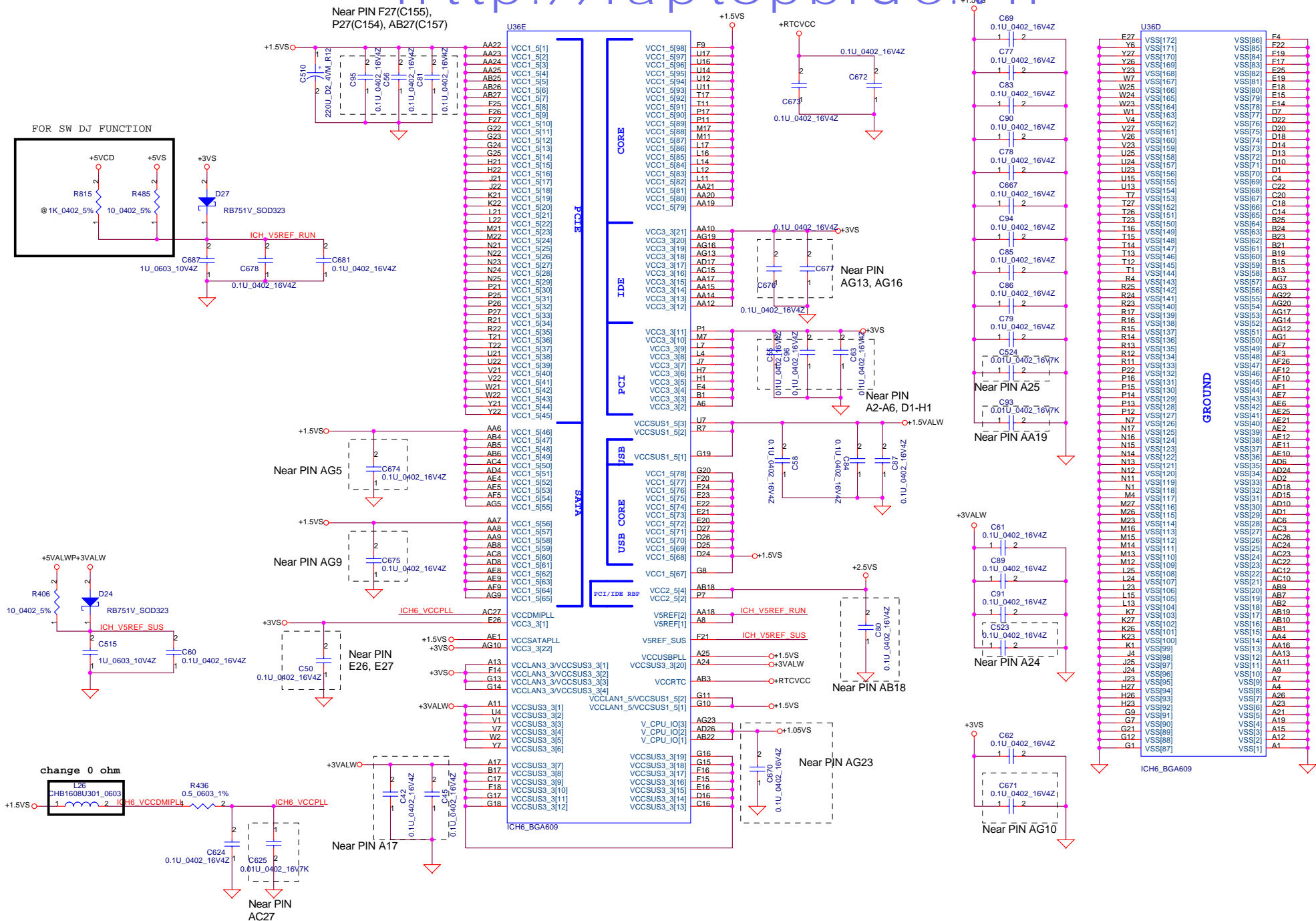
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2005/03/01	Deciphered Date	2006/03/01	Title	ICH6(2/4)_CPU,AC97,IDE,LPC	
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				Date:	星期四, 三月 03, 2005	Sheet 24 of 52

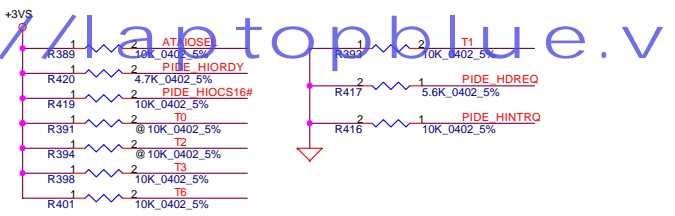
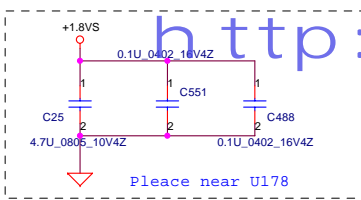
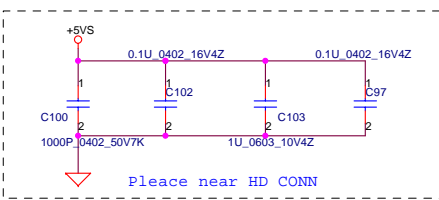


Intel new update



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				Rev	
				1.0	
				Date	
				2005.03.04	
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				52	

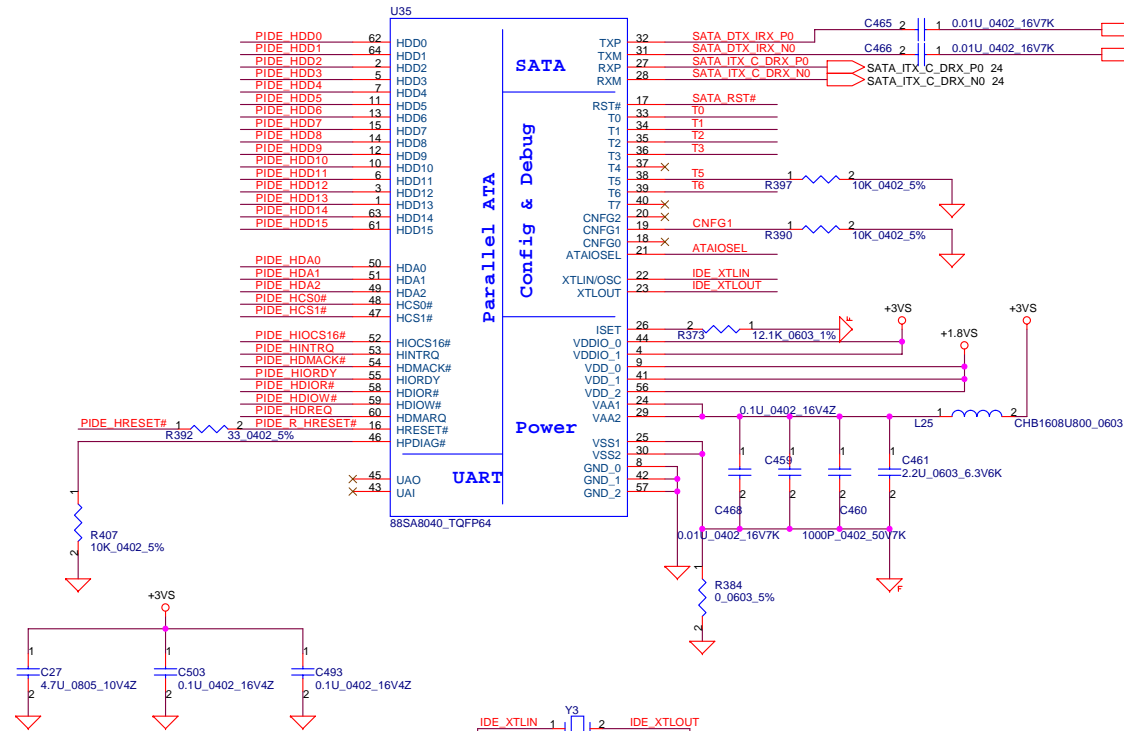




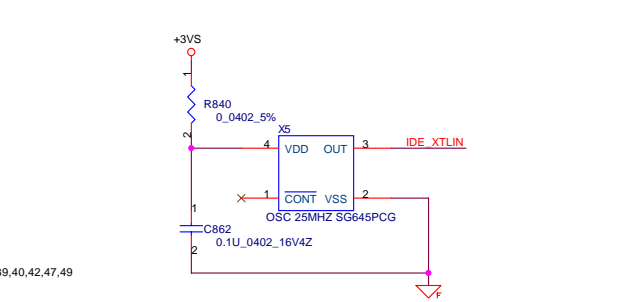
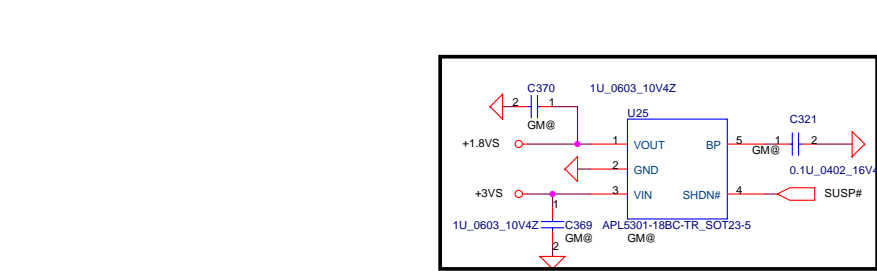
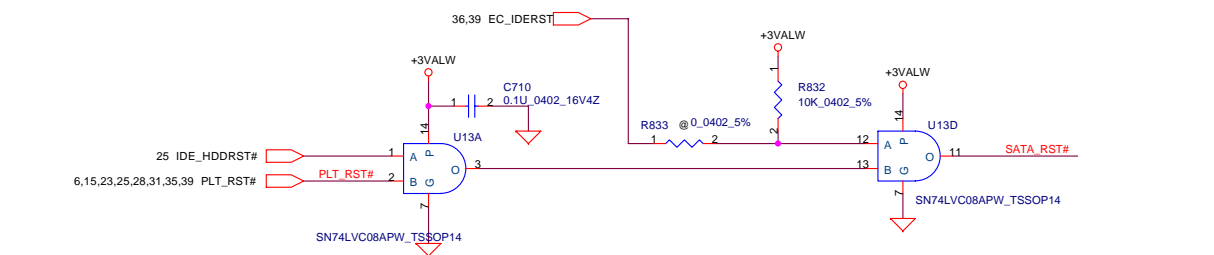
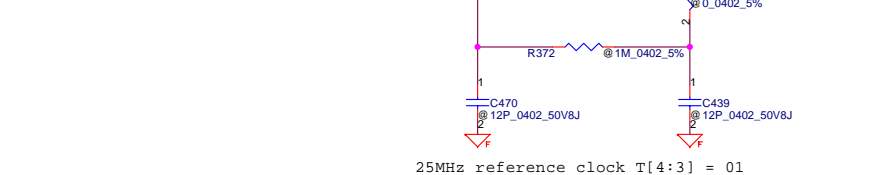
SATA Module

Sets maximum transfer rate and UDMA mode

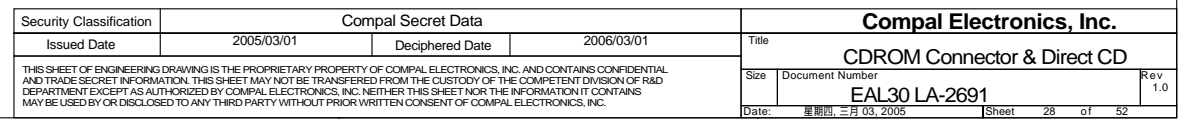
CNFG2 INT PD	CNFG1	CNFG0 INT PD	NOTE
0	0	0	Device Mode 100MB/s
0	0	1	Device Mode 133MB/s
0	1	0	Device Mode 150MB/s
1	0	0	Host Mode 100MB/s
1	0	1	Host Mode 133MB/s
1	1	0	Host Mode 150MB/s
0	1	1	Reserved
1	1	1	Reserved



Place near connector side.

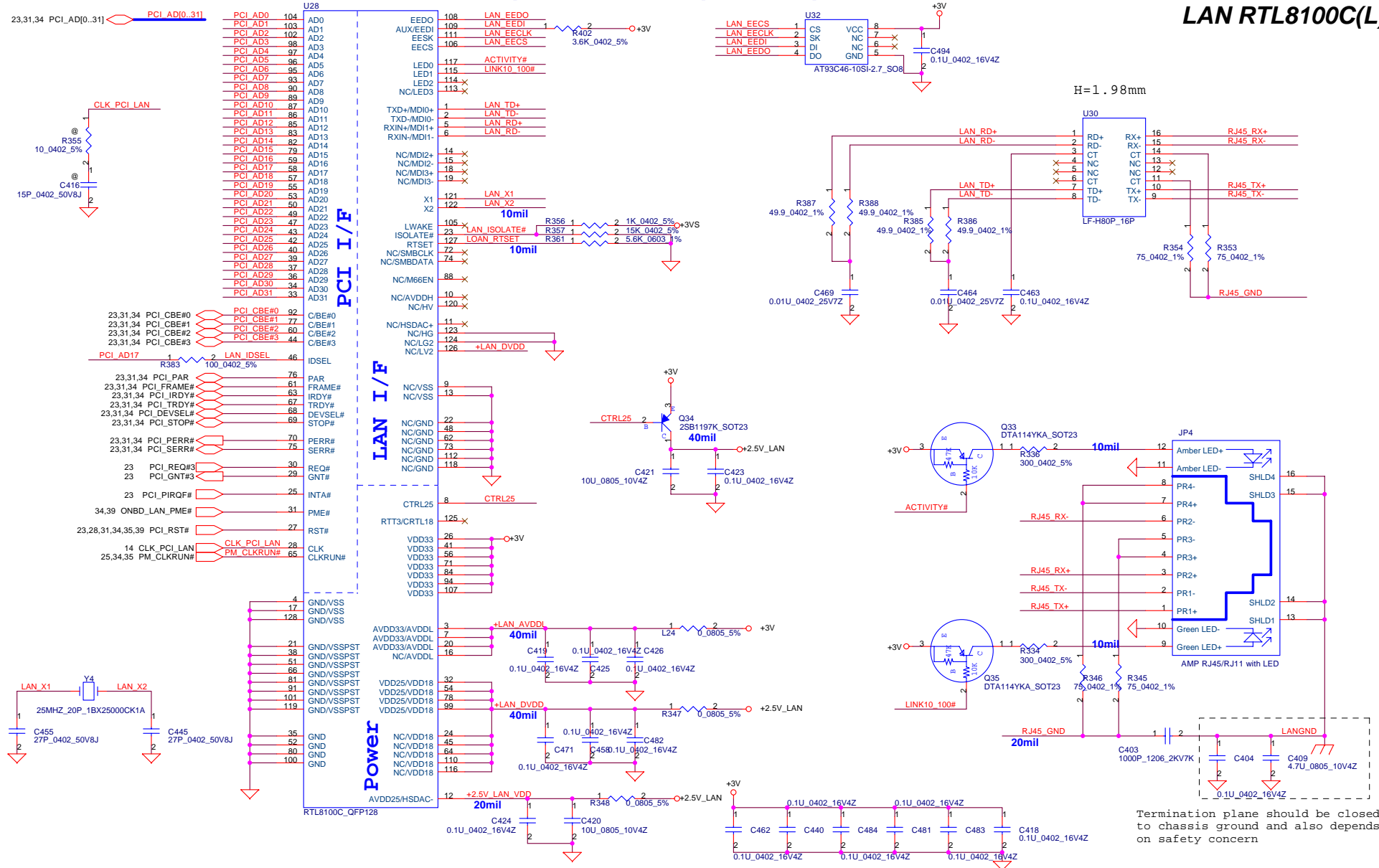


Reserve for SWD

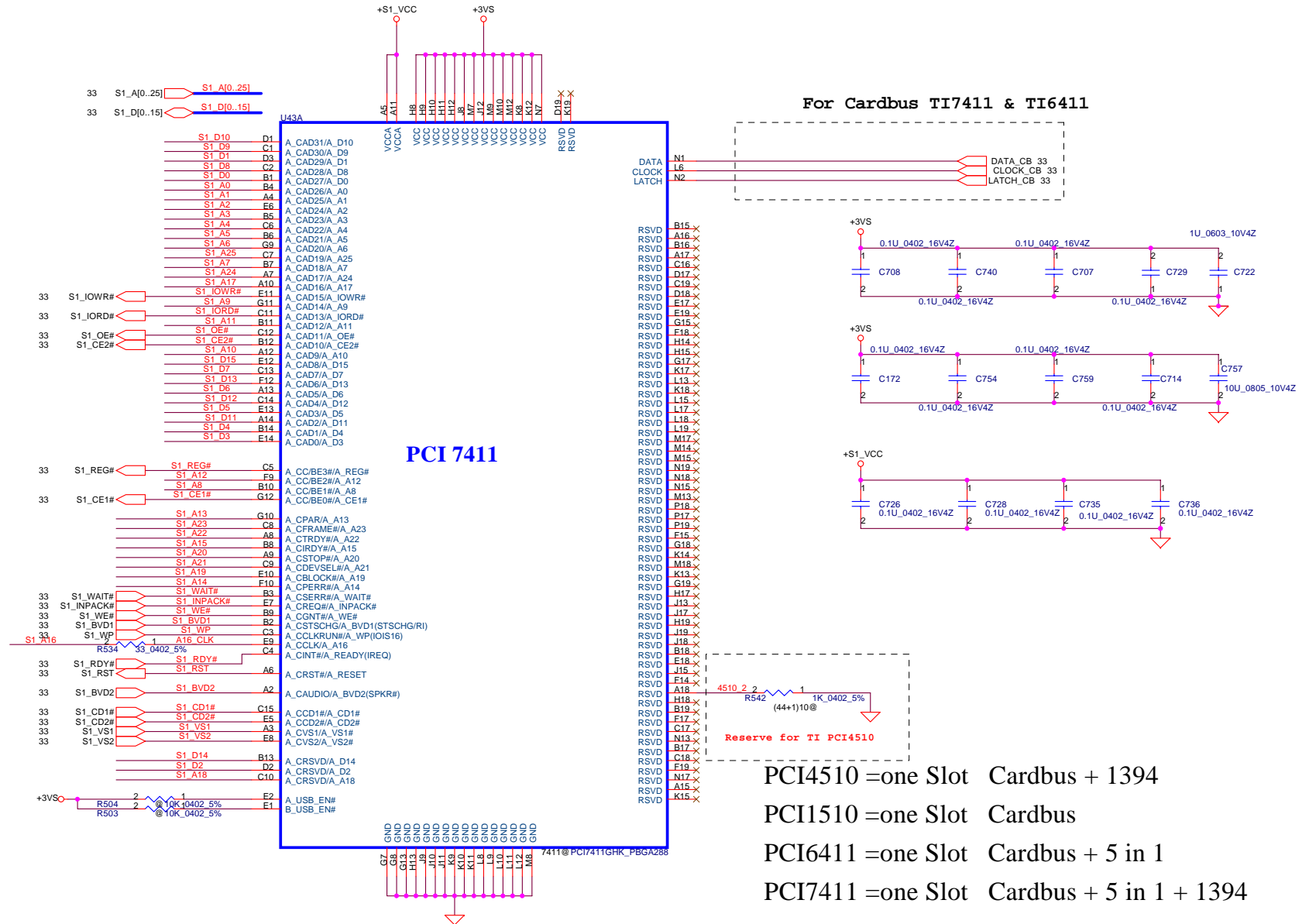


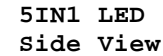
<http://laptopblue.vn>

LAN RTL8100C(L)



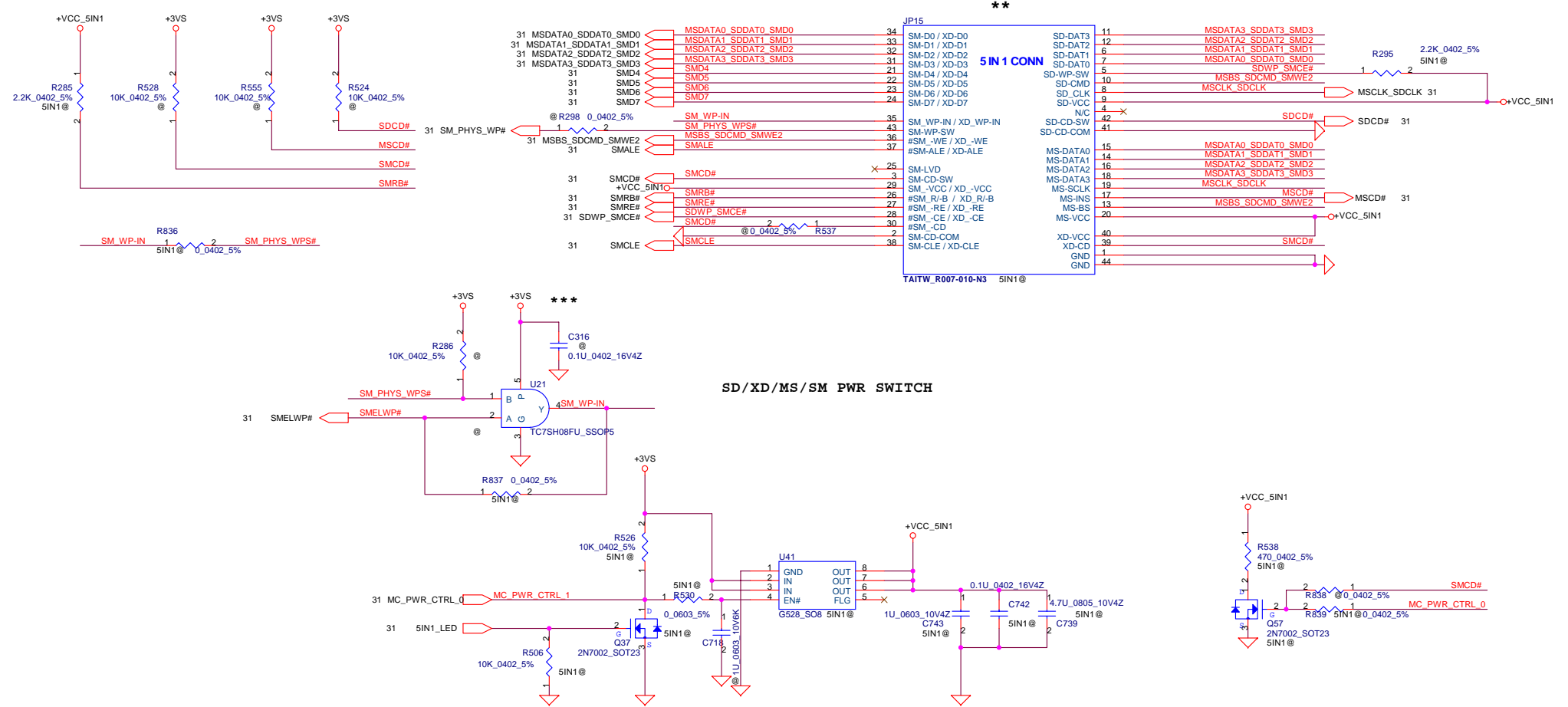
Security Classification		Compal Secret Data		Compal Electronics, Inc.			
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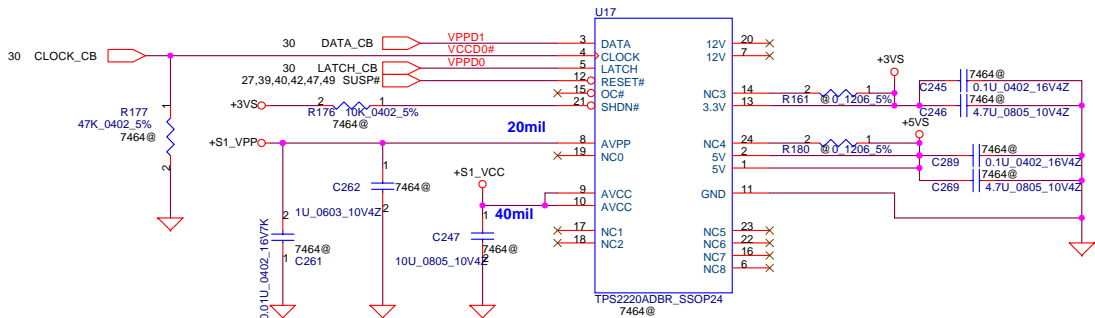
PCI1510 & PCI6411 UNMOUNT THOSE PARTS

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				Size	Document Number	Rev
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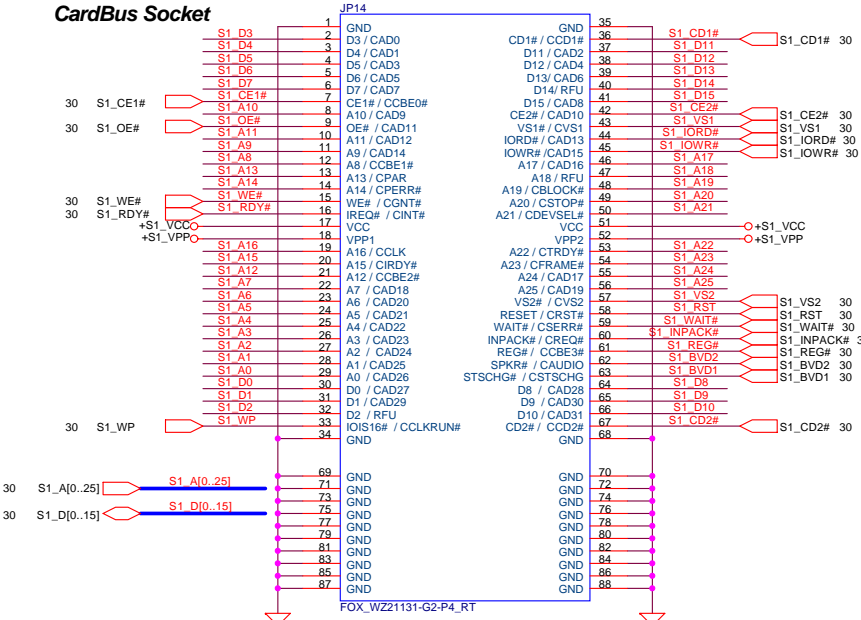


Power Switch for PCMCIA

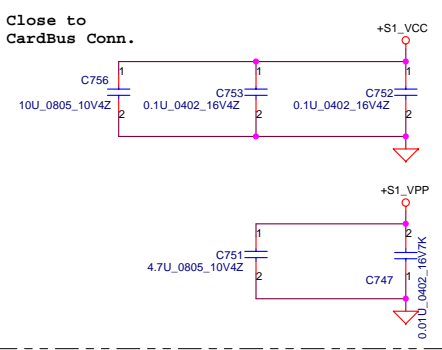
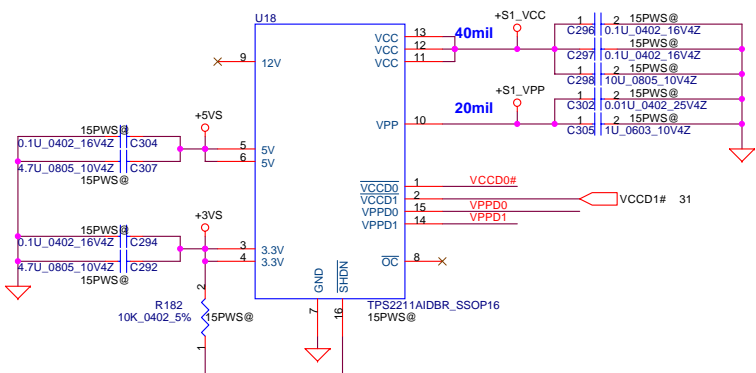
Power Switch for PCMCIA (PCI7411 & PCI6411 only)



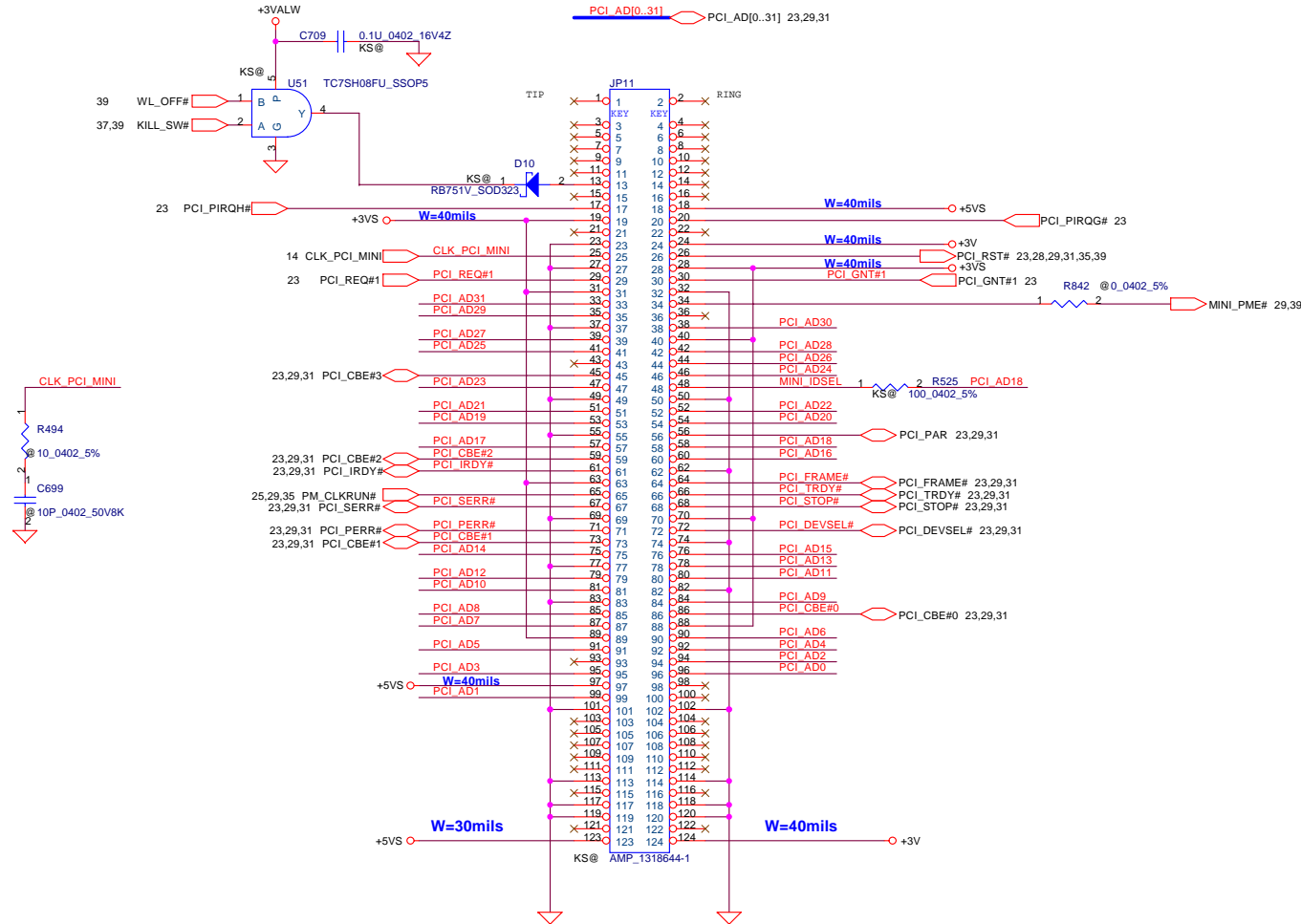
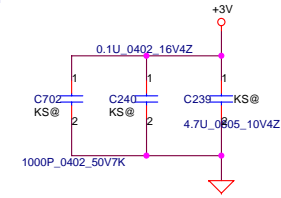
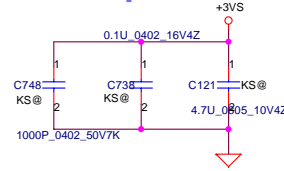
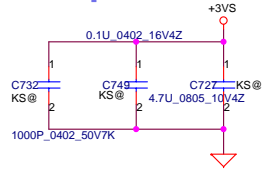
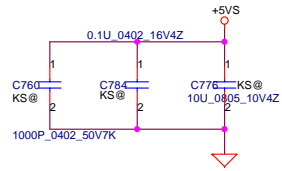
CardBus Socket



Power Switch for PCMCIA (PCI1510 & PCI4510 only)



http://laptopblue.vn



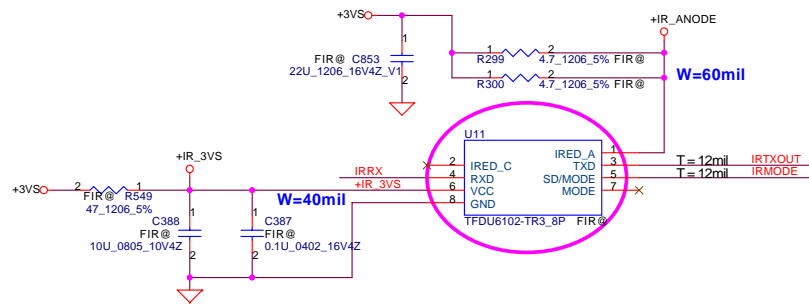
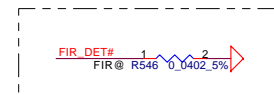
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SUPER I/O SMsC LPC47N217

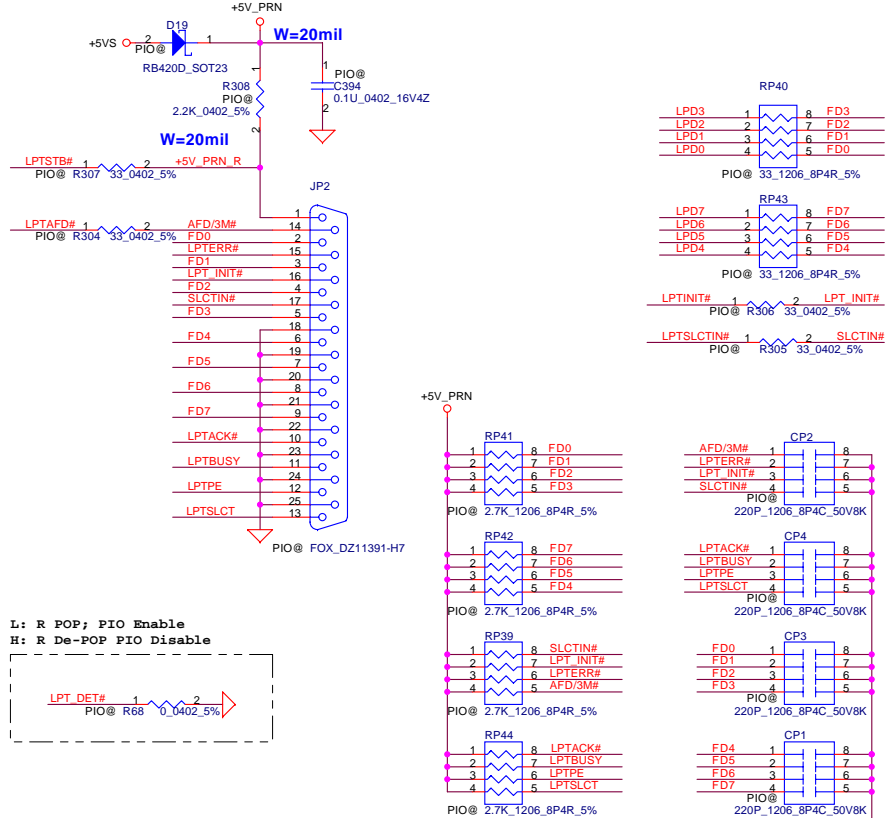
http://laptopbluevsn

FIR Module

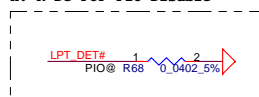
L: R POP; FIR Enable
H: R De-POP FIR Disable



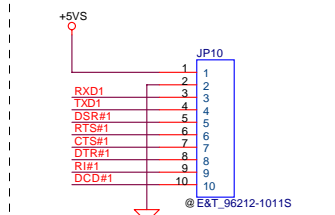
Parallel Port



L: R POP; PIO Enable
H: R De-POP PIO Disable

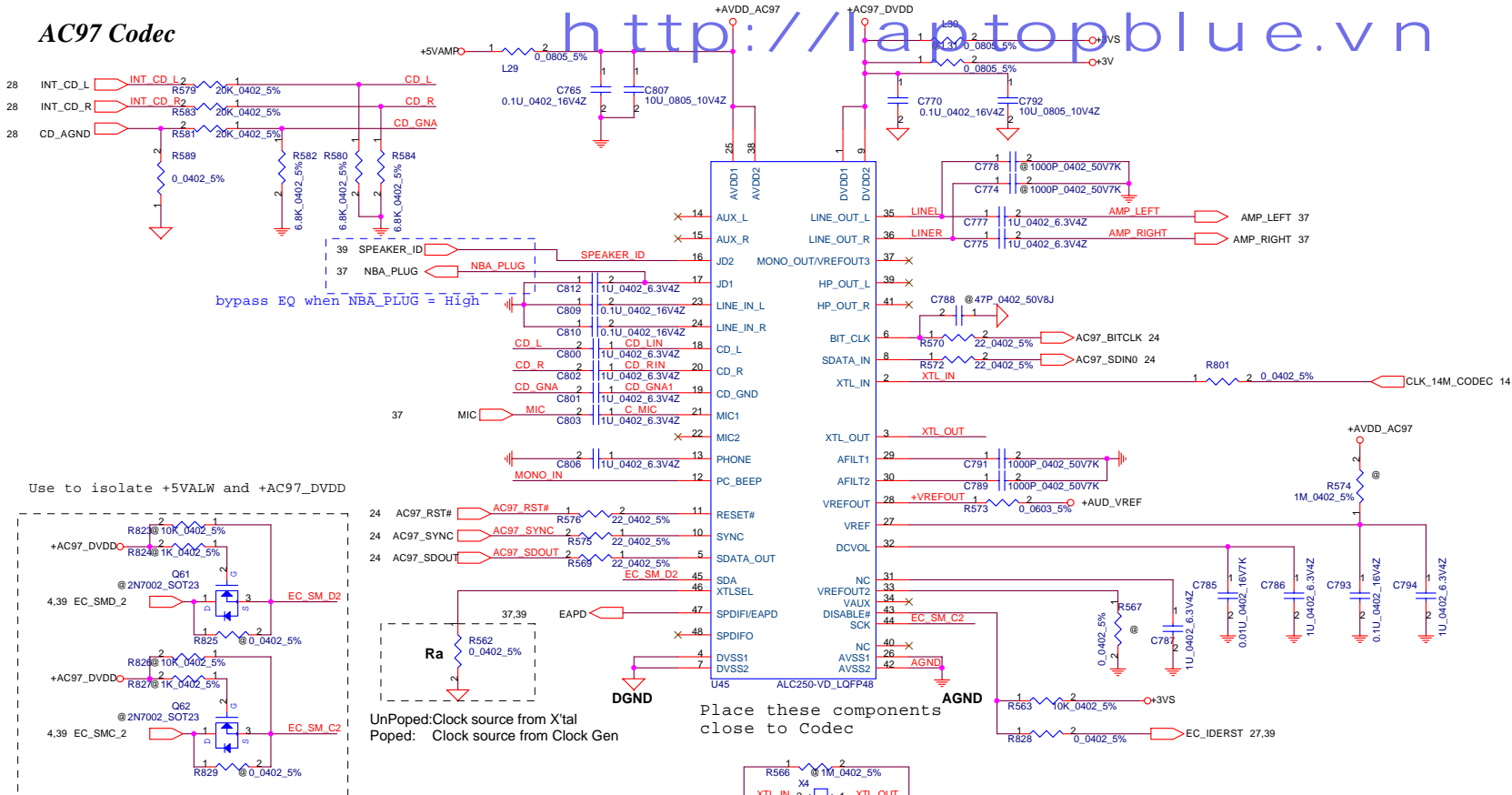


Place on the TOP side(Under MDC conn.)

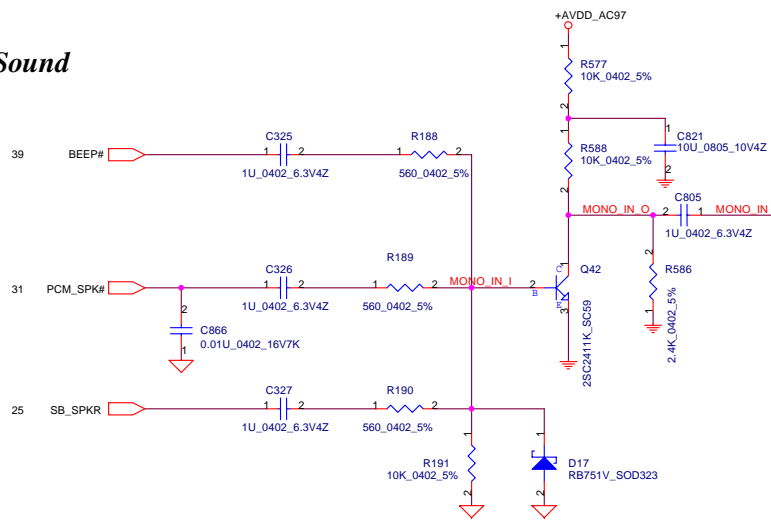


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				SMsC LAP47N217 SIO,PIO,FIR					
				Size		Document Number		Rev	
				Custom		EAL30 LA-2691		1.0	
				Date:		星期五, 三月 04, 2005		Sheet 35 of 52	

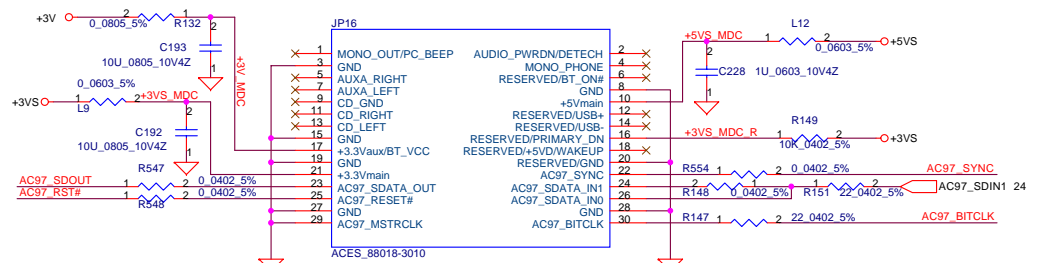
AC97 Codec



System Sound



MDC Connector

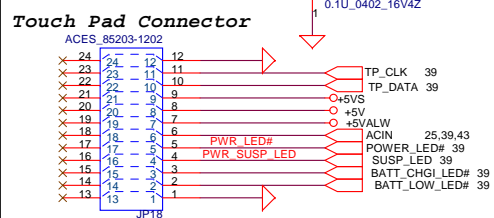
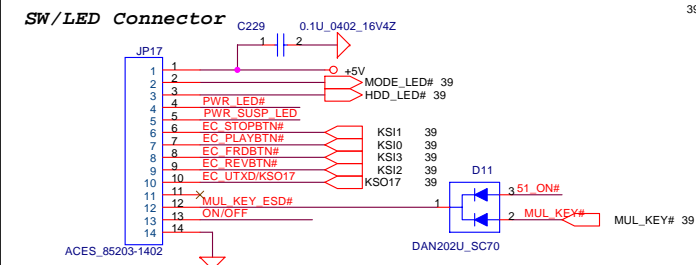
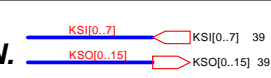


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ON/OFF BUTTON for ON/OFF switch

KEYBOARD CONN.



Battery mode Hibernation

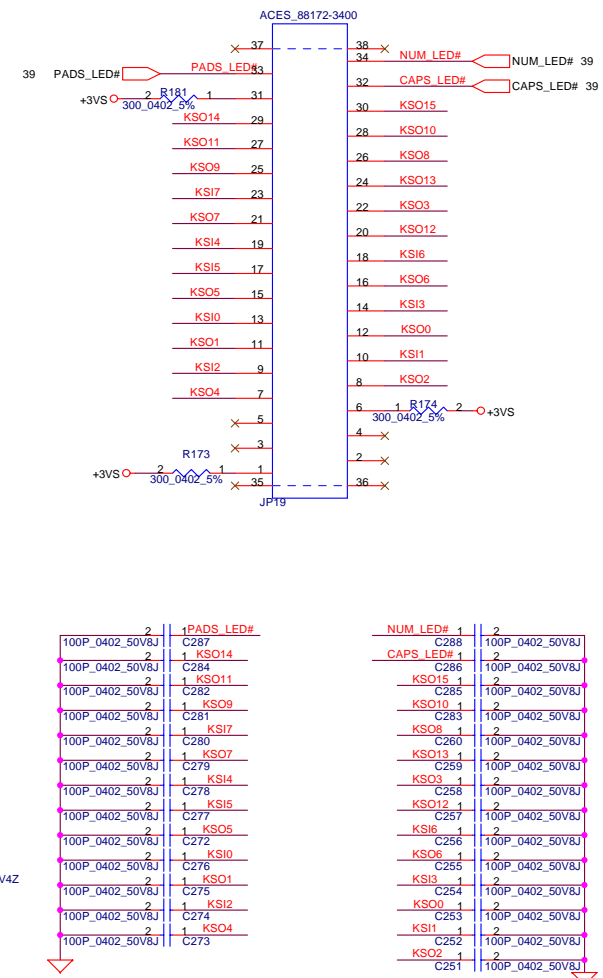
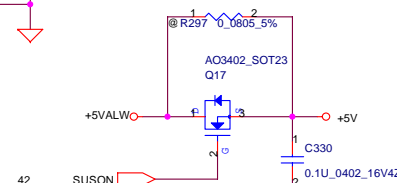
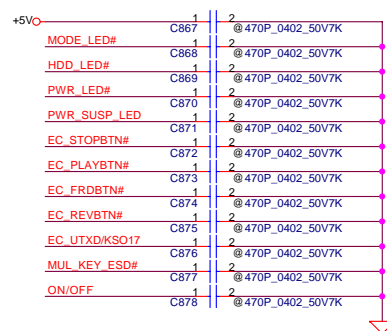
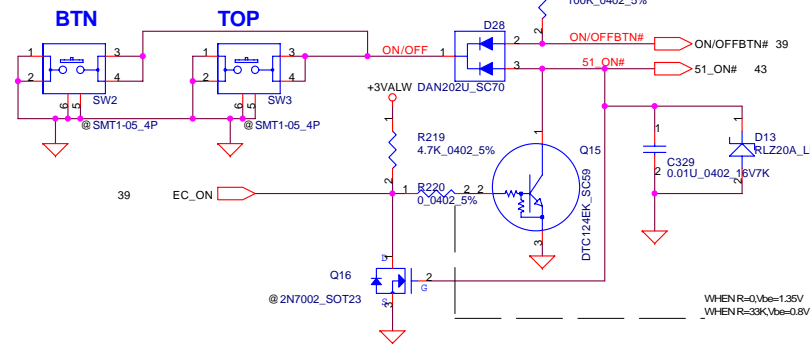
42

The schematic diagram illustrates the battery mode hibernation circuit. It features several key components and connections:

- RTC Section:** Includes resistors R195, R196, R197, R200, R201, R202, and R221. Capacitors C372, C385, C396, and C333 are used for timing and filtering. The RTCVREF pin is connected to various points in the circuit.
- Comparators and Logic:** U19 (NC7S214M5X) and U47 (74LCX74MTC) are configured as comparators and flip-flops. They receive inputs from S4_LID_SW# and S4_DATA, and output D_SET_S4.
- Transistors and MOSFETs:** Q43, Q45, Q18, and Q44 are N-channel MOSFETs (2N7002). Q44 is a PNP transistor (PSOT24C).
- Diodes:** D18 (1N4148), D15 (RB751V), and D34 (PSOT24C) are included for signal conditioning and protection.
- Control Signals:** SYSON, S4_LATCH, and S4_DATA are external signals. ON/OFF is a control input for the hibernation state.

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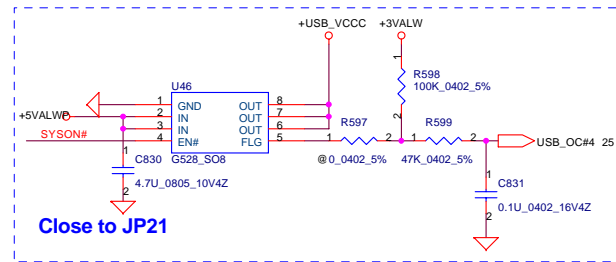
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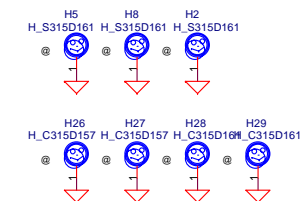
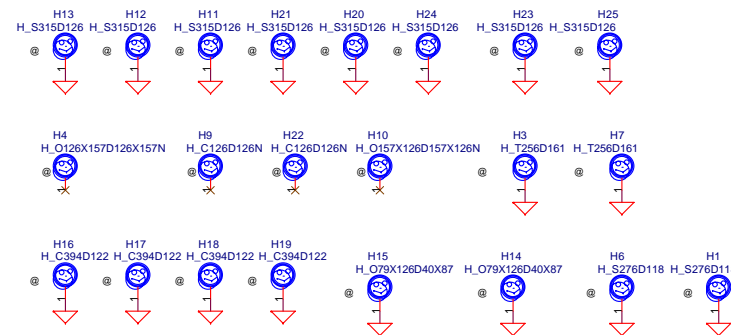
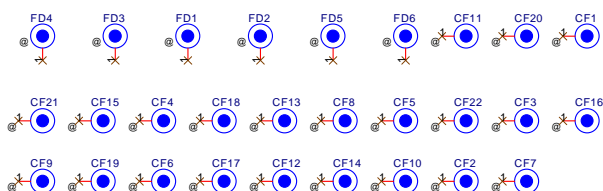
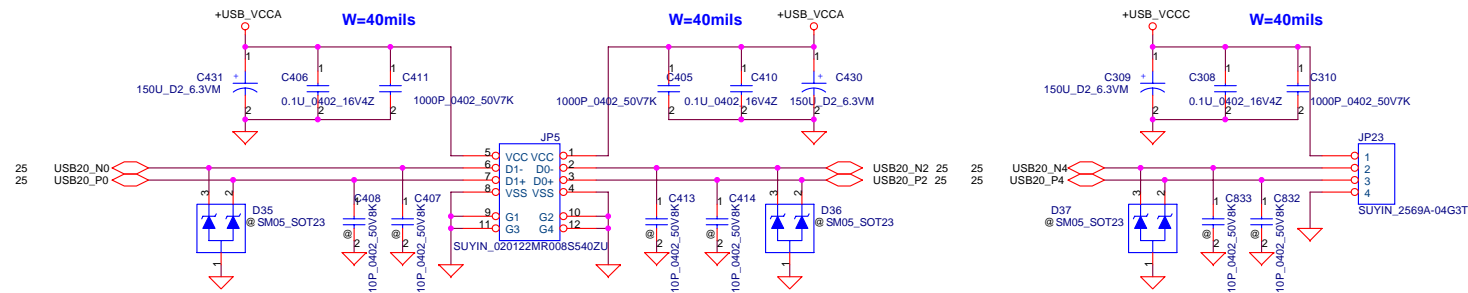
Power OK Circuit

The schematic diagram illustrates the Power OK Circuit. It features two SN74LVC14APWLE_TSSOP14 Schmitt trigger inverters (U48A and U48B). The first inverter (U48A) has its non-inverting input (pin 1) connected to a +3VS input through a 330K_0402_5% resistor (R199). Its inverting input (pin 2) is connected to ground through a 1u_0603_10V6K capacitor (C364). The output (pin 3) of U48A is connected to the non-inverting input (pin 1) of the second inverter (U48B). The inverting input (pin 2) of U48B is connected to ground through a 100K_0402_5% capacitor (R222). The output (pin 3) of U48B is connected to the SYS_PWROK pin of the microcontroller. A +3VALW input is connected to the non-inverting input (pin 1) of U48A. A +3VALW input is also connected to the non-inverting input (pin 1) of U48B. A 0.1u_0402_16V4Z capacitor (C332) is connected between the +3VALW input and the non-inverting input (pin 1) of U48B. A note 'Pull Low at SB' is shown next to the output of U48B.

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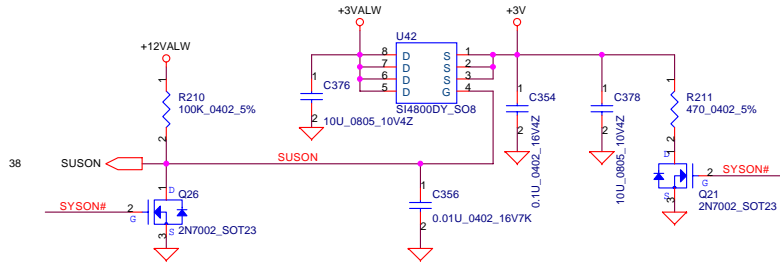


USB CONN. 2

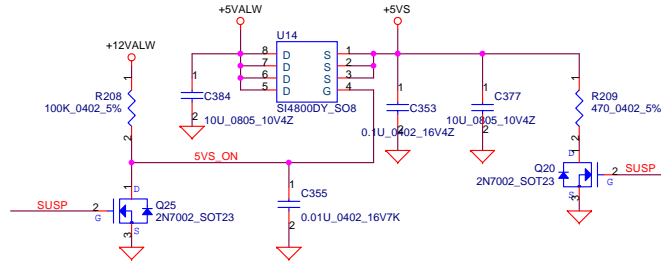


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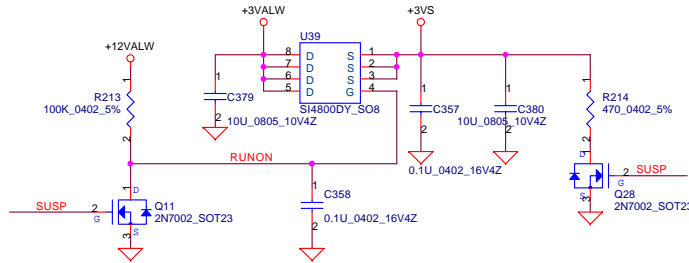
+3VALW to +3V Transfer



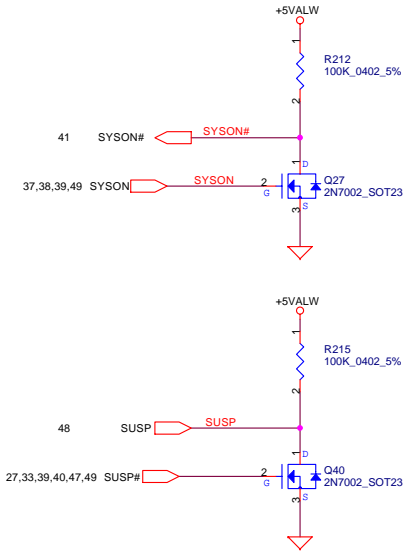
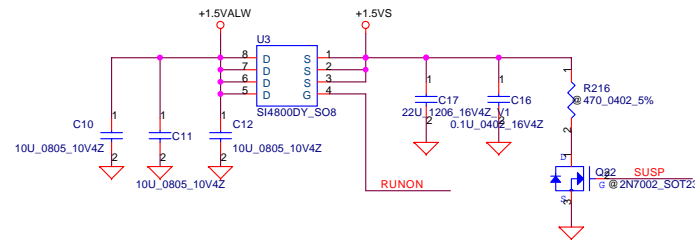
+5VALW to +5VS Transfer



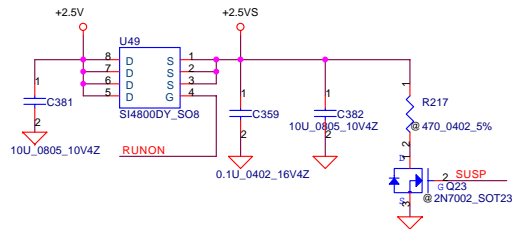
+3VALW to +3VS Transfer



+1.5VALW to +1.5VS Transfer

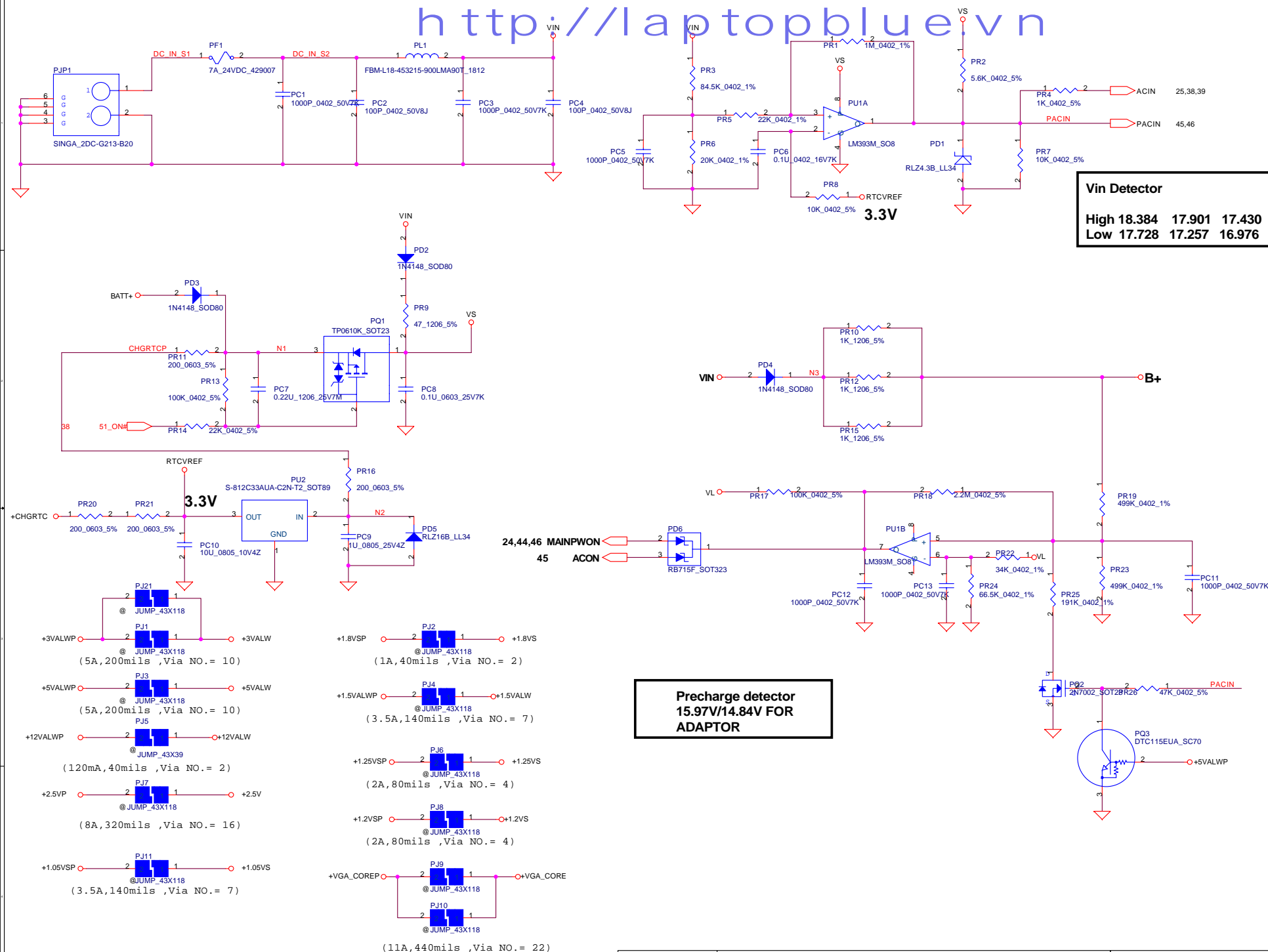


+2.5V to +2.5VS Transfer



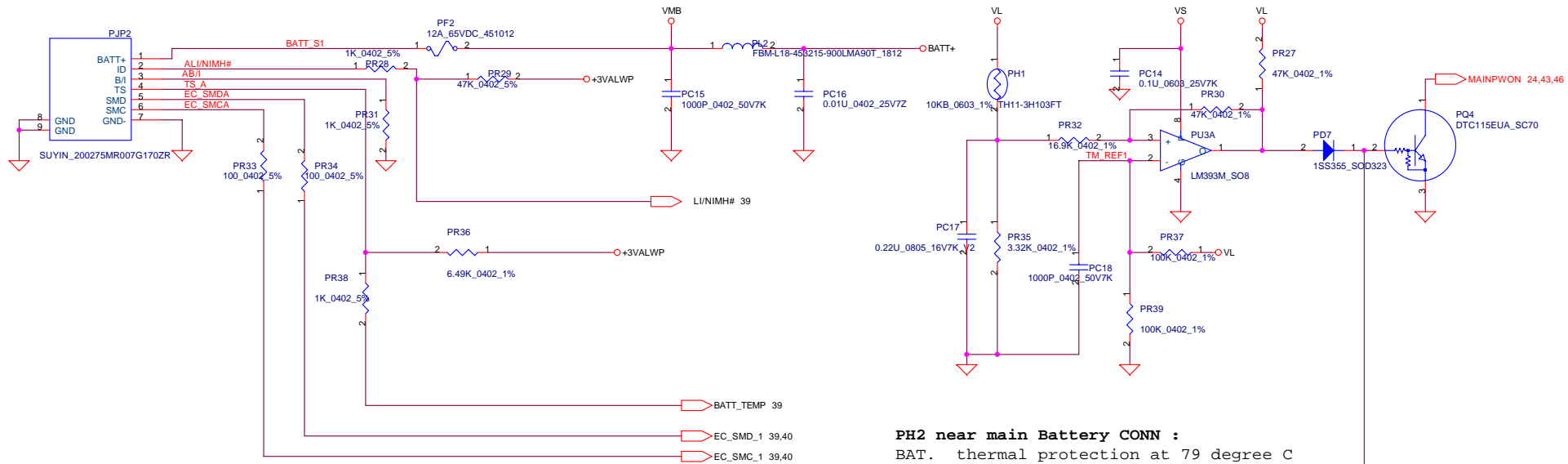
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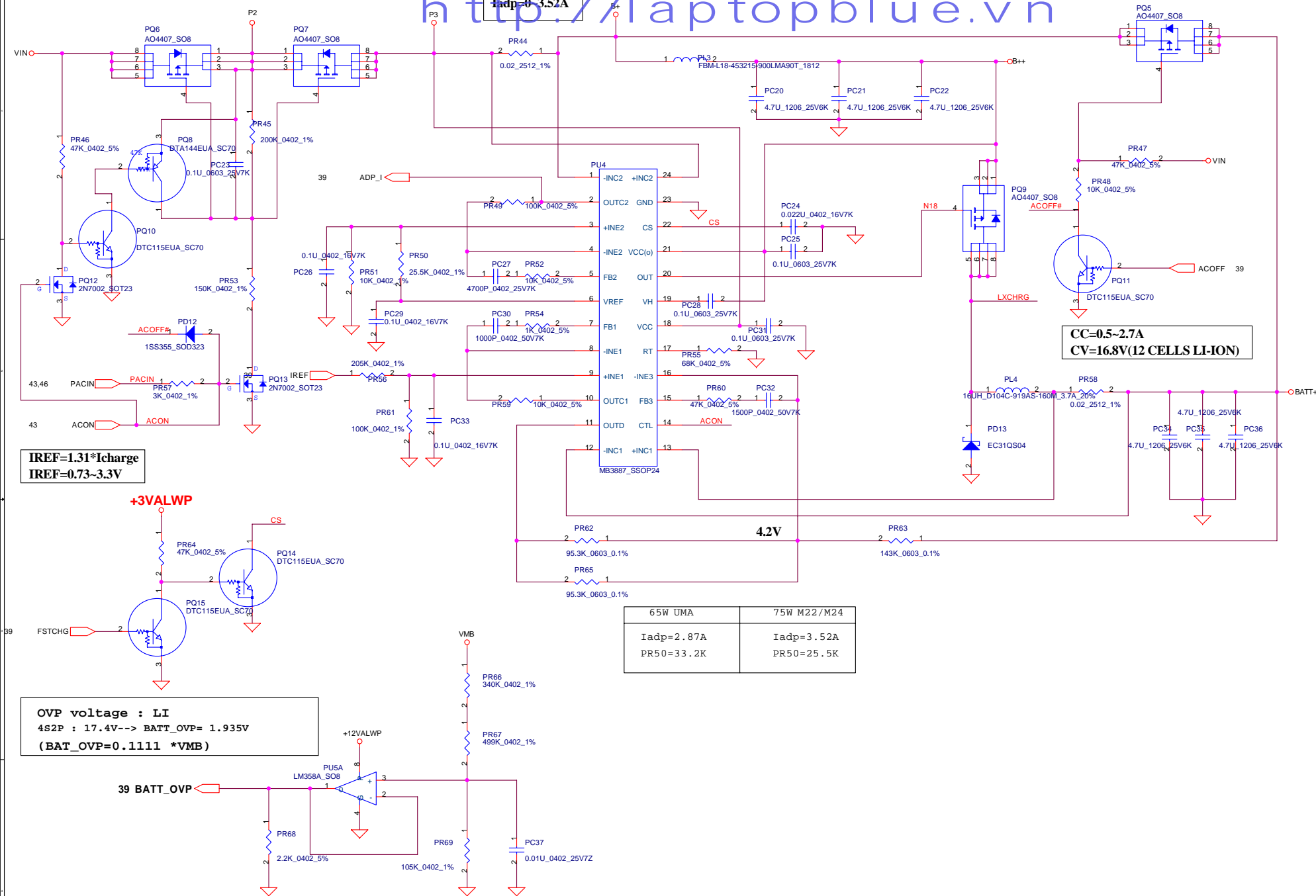


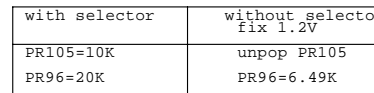
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PH1 under CPU botten side :
CPU thermal protection at 84 degree C
Recovery at 45 degree C

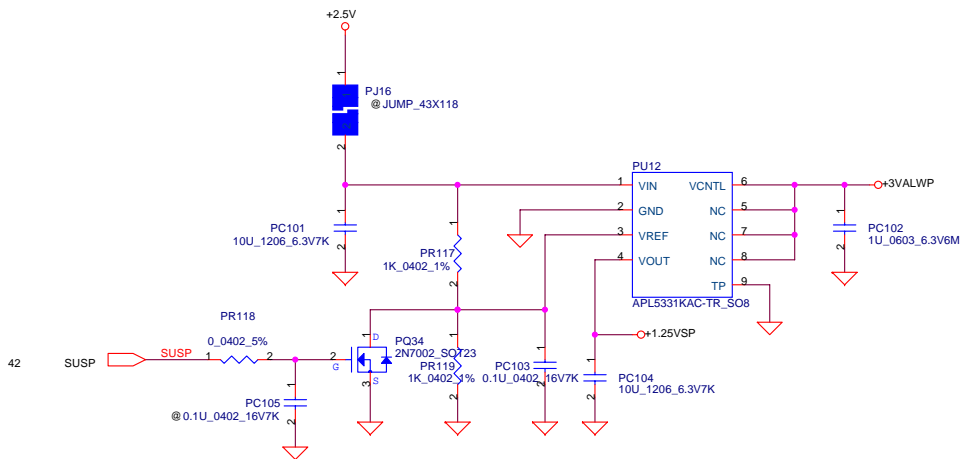
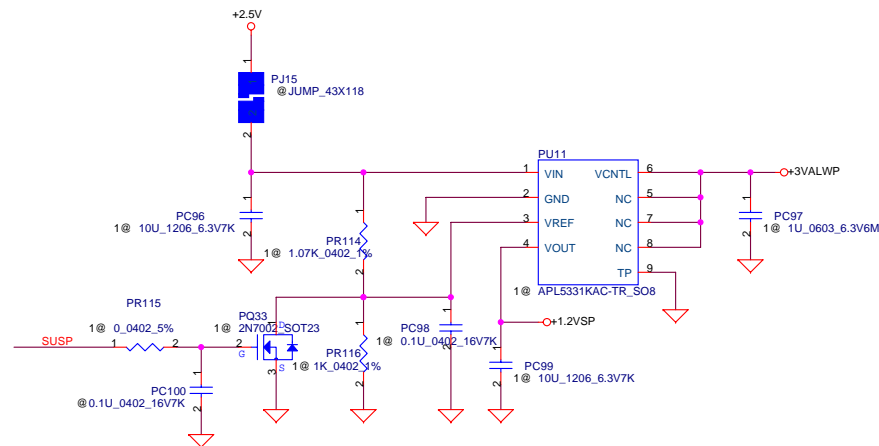
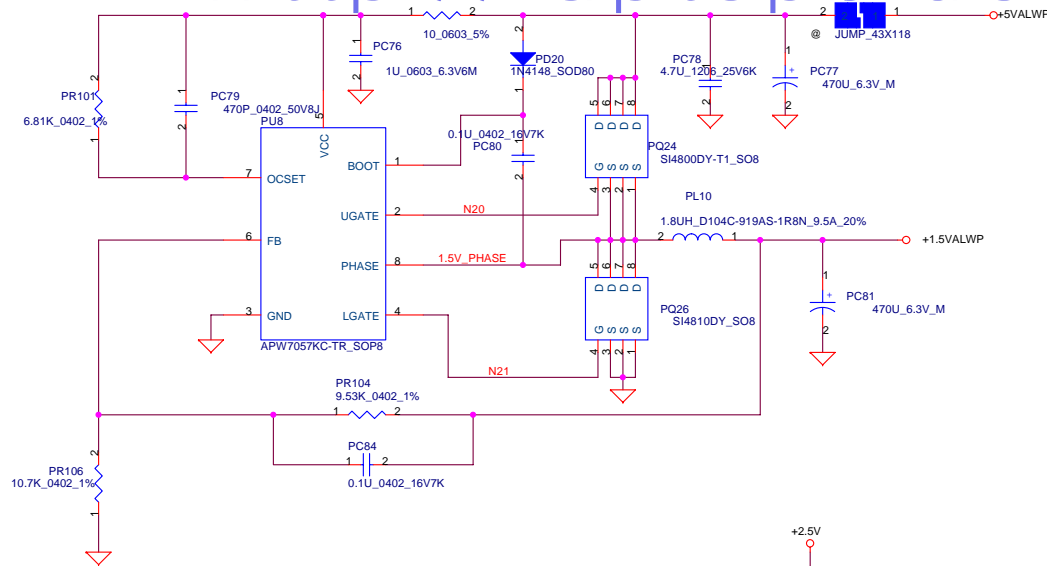


I_{adp}=0.352A





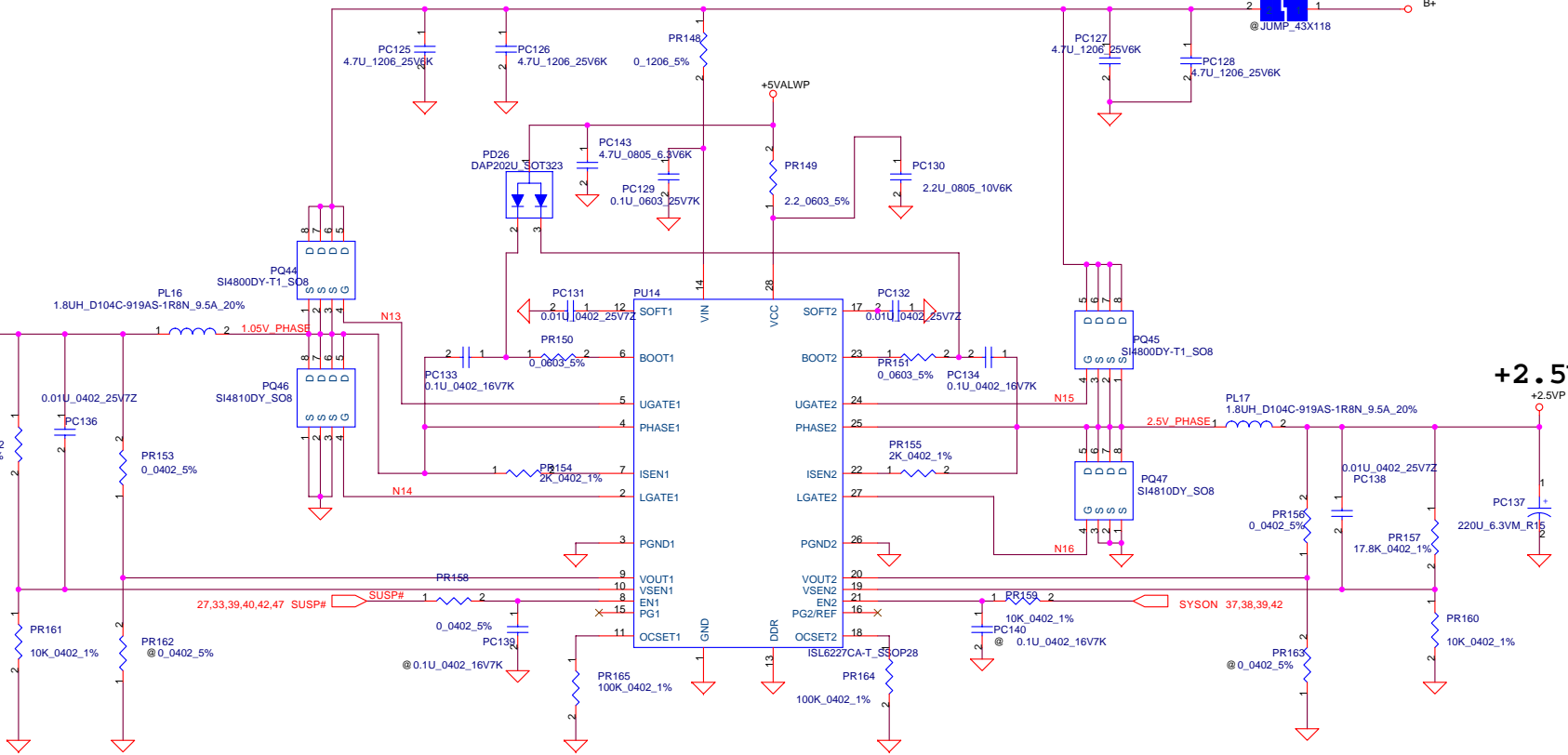
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+1.05V

+2.5V





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12/06	P.38	Add ESD diode D34	for ESD protection
12/06	P.37	Delete C267	For sound volume change slow issue
12/06	P.32	ADD C854	To solve PCI 4510 1394 issue
12/06	P.21	Change L4,L5 to FBM-L11-201209-121LMT	To solve EMI issue
12/26	P.22	Change C398,C397 from 270P to 100P C401,C402 from 330P to 100P	For customer request
12/26	P.22	Change R320,R321,R7,R8,R9 from 150_0402_5% to 150_0402_1%	For customer request
12/26	P.07	Change R124,R127,R119,R531,R532 from 150_0402_5% to 150_0402_1%	For customer request
12/26	P.15	Change R273,R274,R267,R270,R271 from 150_0402_5% to 150_0402_1%	For customer request
12/26	P.32	Change 5in1 SMWP design ,Add R836,R837	follow EAT10 design

REV 0.3			
Date	Page	Location	Description
2/06	P.09	Change R160 size to 0805 0 ohm	Add rating current
2/06	P.24	Add C859,C864 change R841 to 2.2 ohm	For TV-out wave issue
2/06	P.32	Add R838,R839	Change 5in1 card reader discharge control from card detect to card power enable .
2/06	P.27	Reserve R840,C862,X5	Reserve for chip issue

REV 1.0	Date	Page	Location	Description
2/14	P.34	ADD R842	Reserve for CyberTan issue	
2/26	P.36	Add C866	Add for solve modem card noise	
2/26	P.24	Add C865	Add for solve 3Dmark low performace issue	
2/26	P.24	Add R842	Reserve for New Wireless Lan Card	

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Page	Location	Description	Reason
45	PR50	Change from 33.2K_0402_1% to 25.5K_0402_1%	For ATI M22/M24 sku, update to 75W
47	PR96	Change from 20K_0402_1% to 6.49K_0402_1% Unpop PR105,PQ28,PR143,PQ43,PR102,PC83,PC144	Fix POWER_SEL at 1.2V
47	PR98	Change from 90.9K_0402_1% to 71.5K_0402_1%	Enlarge VGA_CORE OCP point
43	PD3	Change from RB751 to 1N4148	For common design
43	PR9	Change from 33_1206_5% to 47_1206_5%	For common design
48		Delete PC96,PR114,PU11,PC97,PR115,PQ33,PR116,PC98,PC99	For UMA SKU, delete 1.2VS
48	PR104 PR106	Change PR104 from 9.09K_0402_1% to 9.53K_0402_1% Change PR106 from 10.5K_0402_1% to 10.7K_0402_1%	For HW requirement to raise 1.5V voltage

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