


IO BASE ADDRESS	
EC	0x002E
TPM	0x004E
IO1000	0x162E
DEBUG	0x164E

MB ID Definition		
MBID2	MBID1	MBID0
0 : 100 LAN	Reserved	Reserved
1 : GIGA LAN		

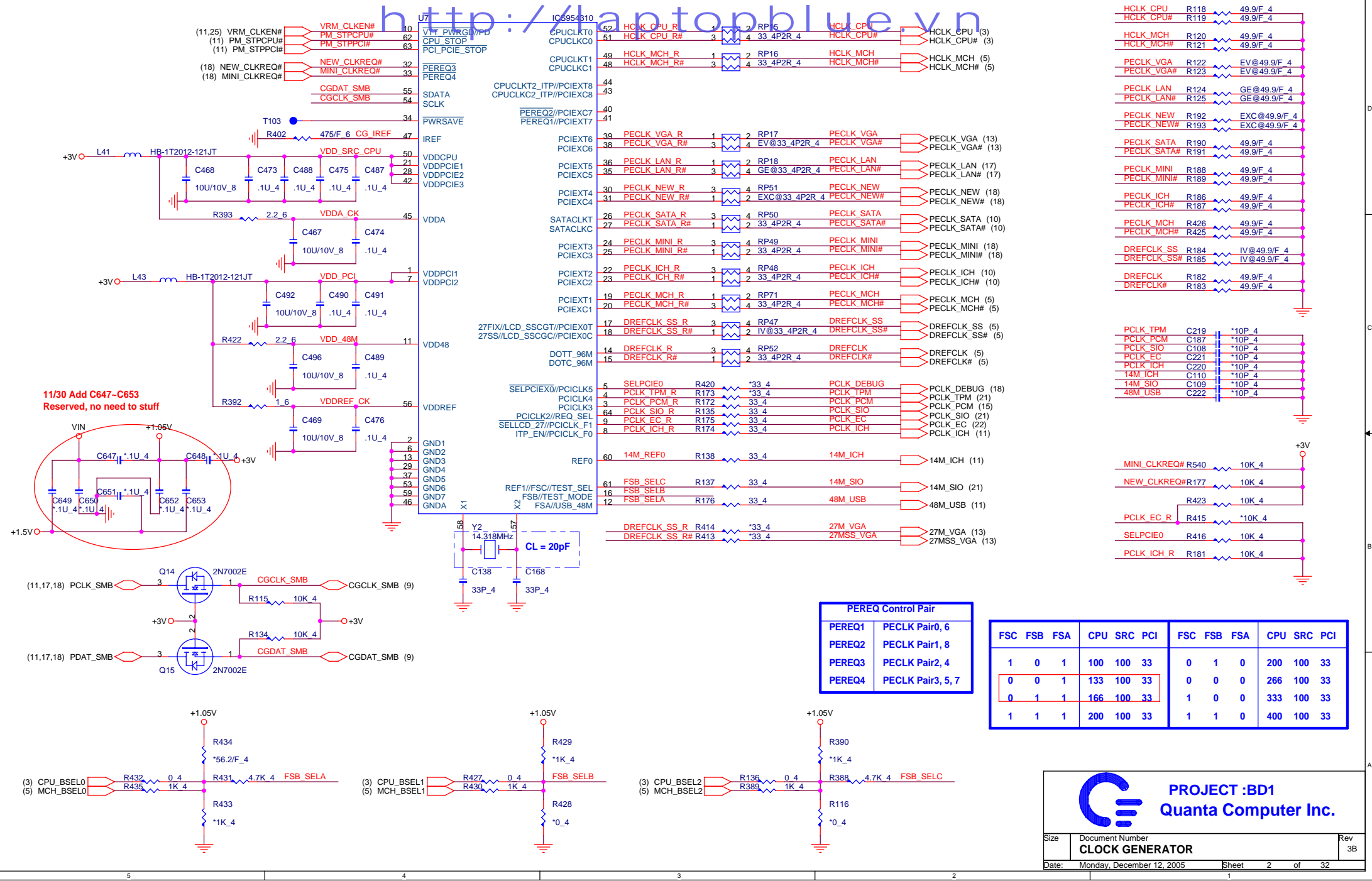
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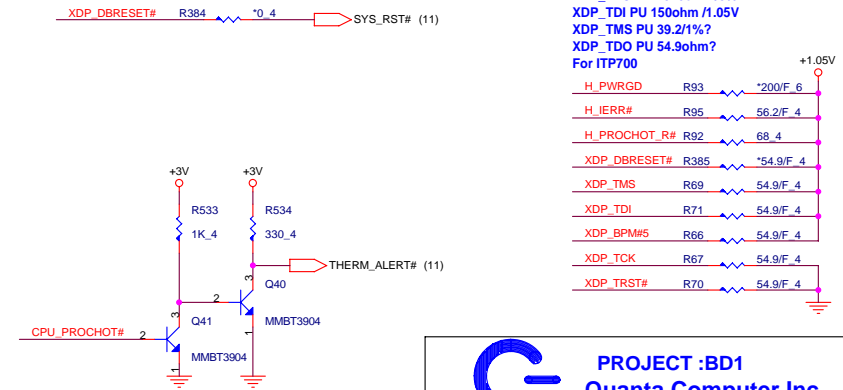
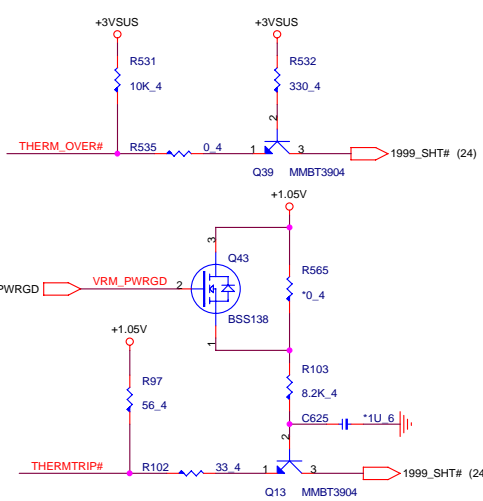
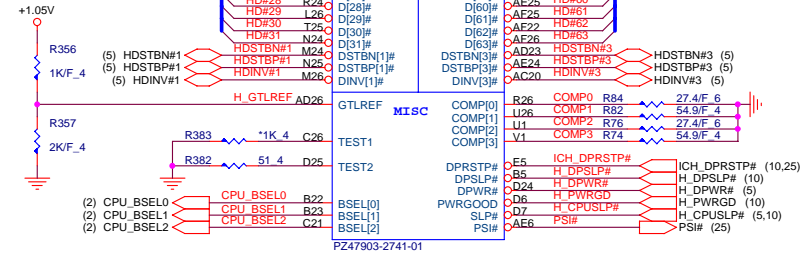
EV@ : Components For External VGA
IV@ : Components For Internal VGA
GE@ : Components For Gigabit Ethernet
FE@ : Components For Fast Ethernet
IR@ : Components For CIR
EXC@ : Components For ExpressCard
FP@ : Components For FingerPrint
DVI@ : Components For DVI Function
IVDI@ : Components For Internal VGA DVI Function

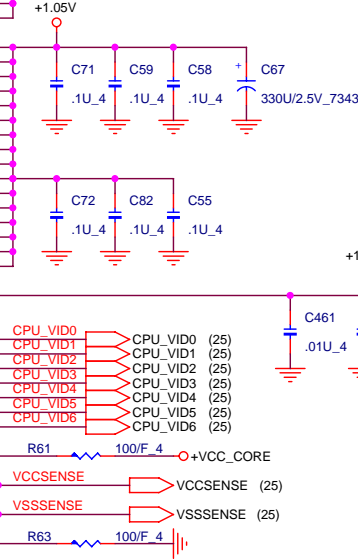
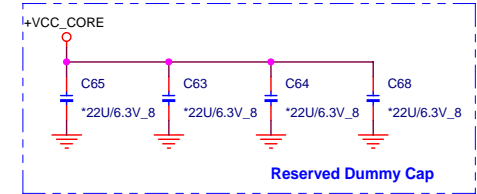
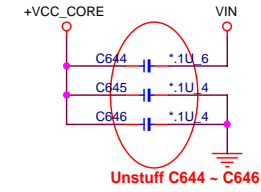
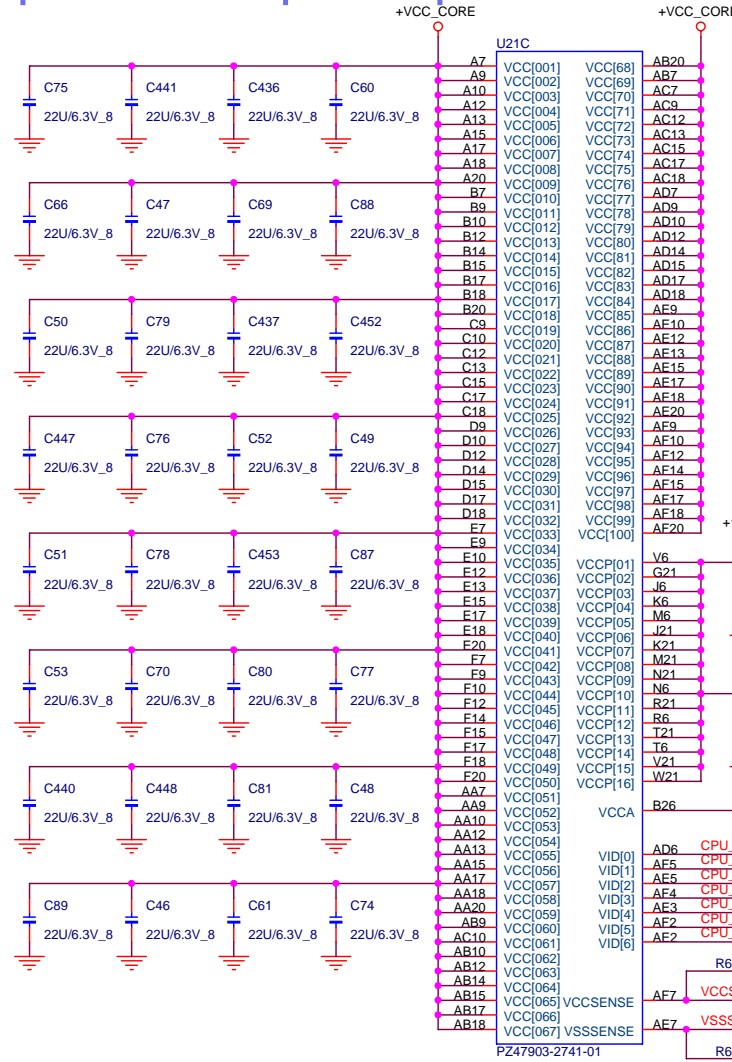
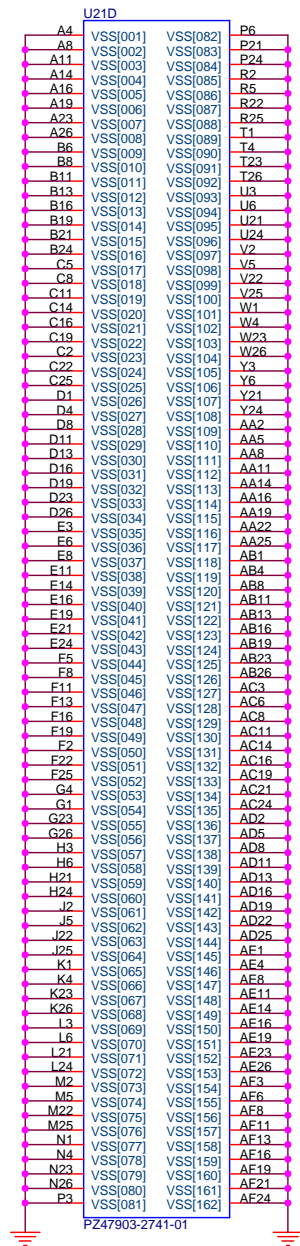
Value show with Prefix " * " means unstuff parts

	PROJECT :BD1
	Quanta Computer Inc

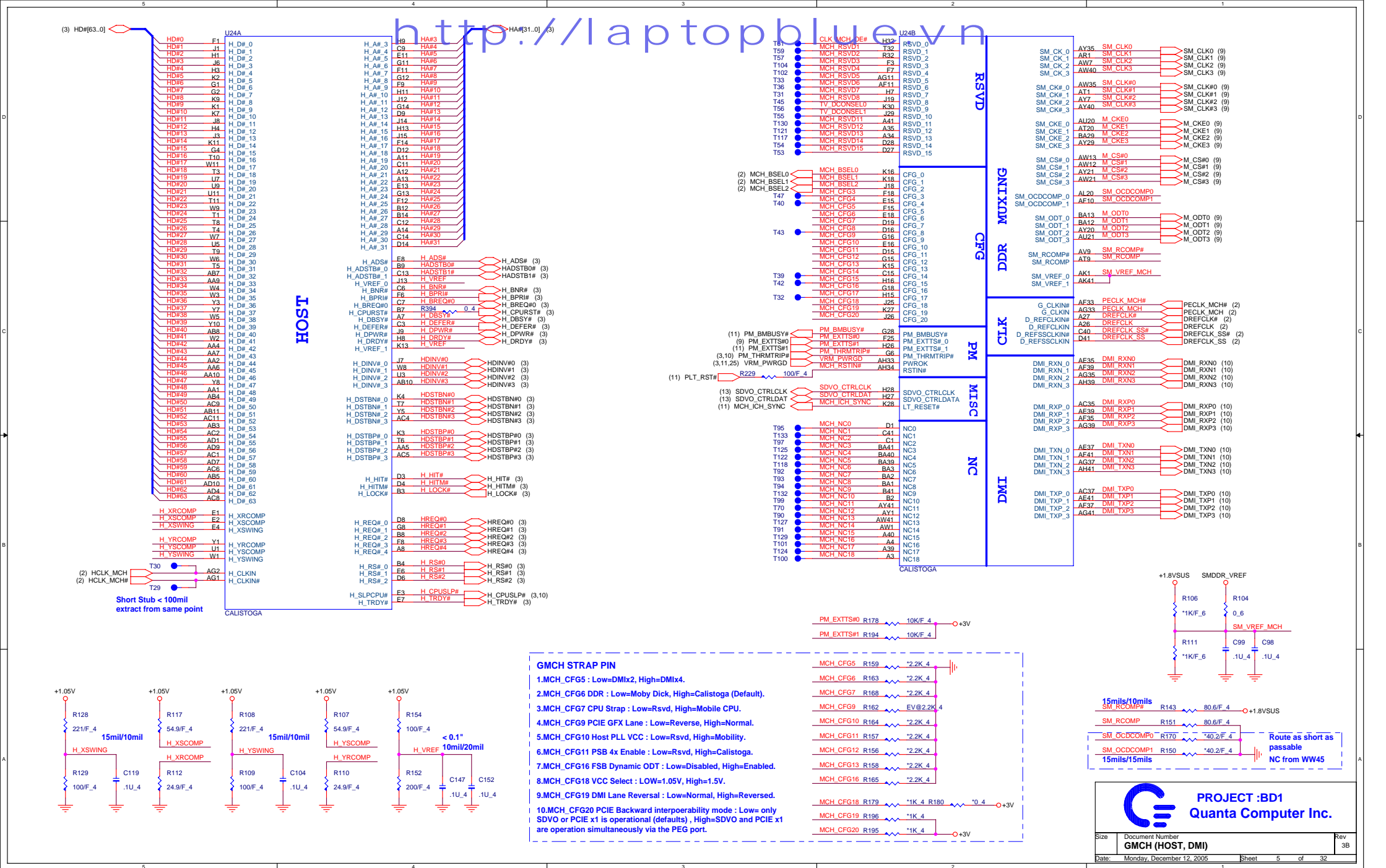
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	MOCKING BOARD DIAGRAM
Date:	Monday, September 24, 2007
Drawn:	

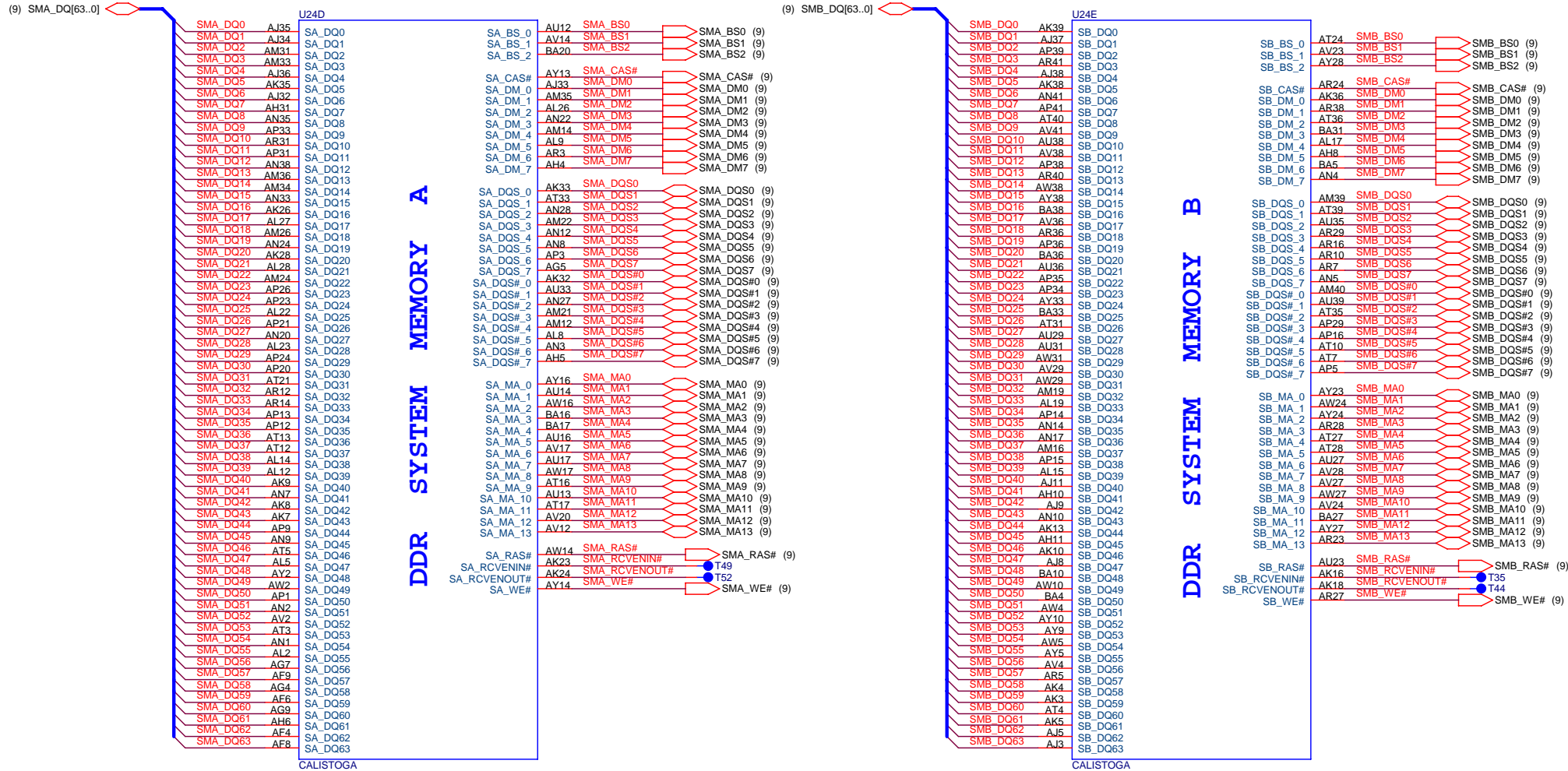






PROJECT :BD1
Quanta Computer Inc.

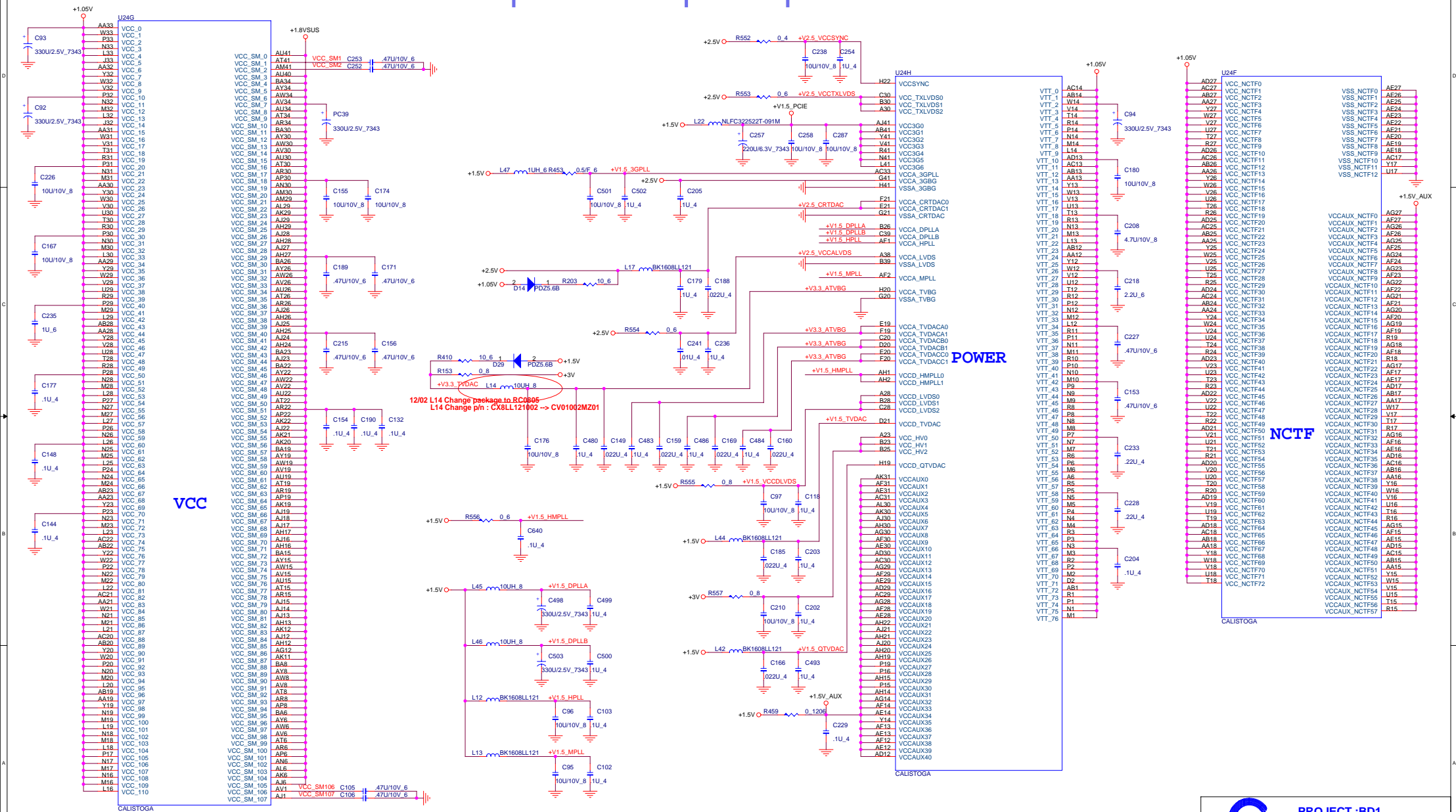


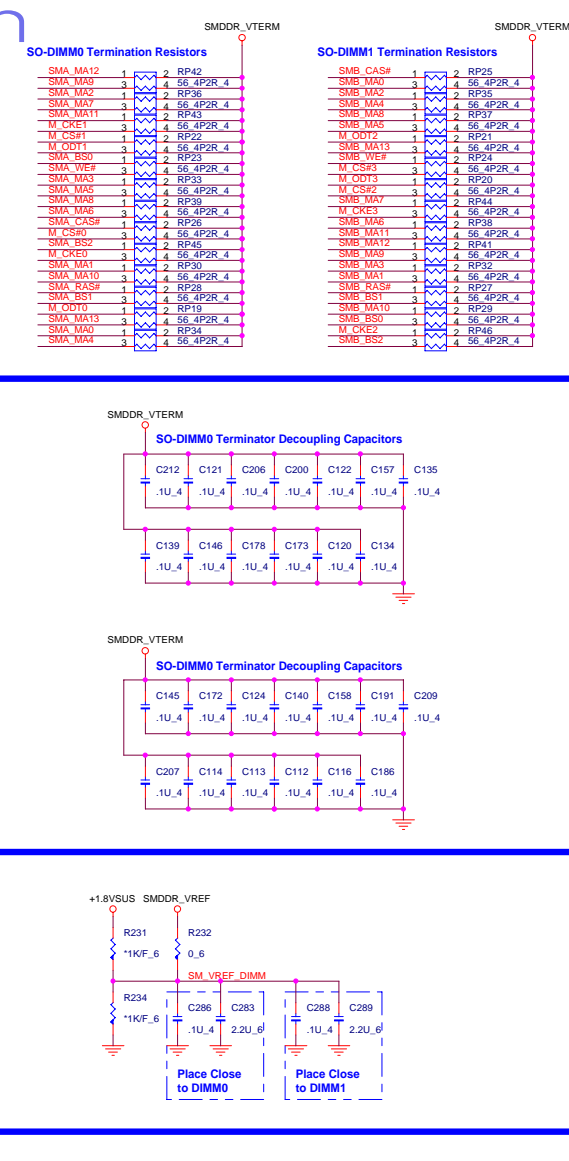
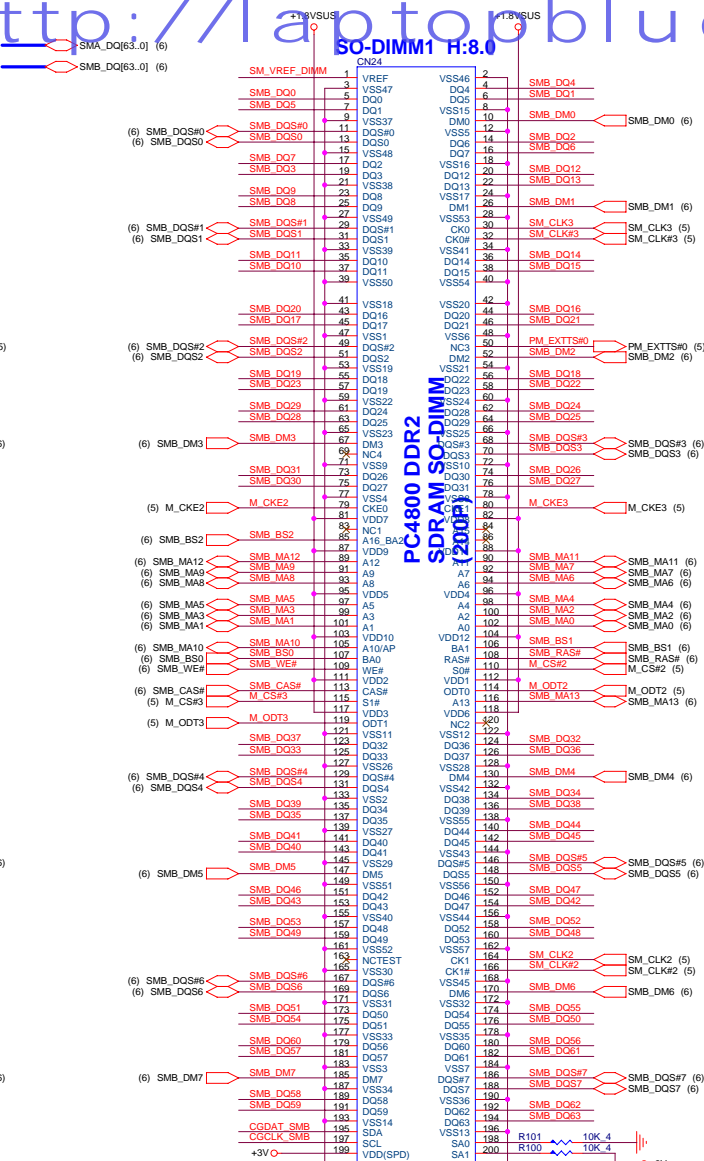
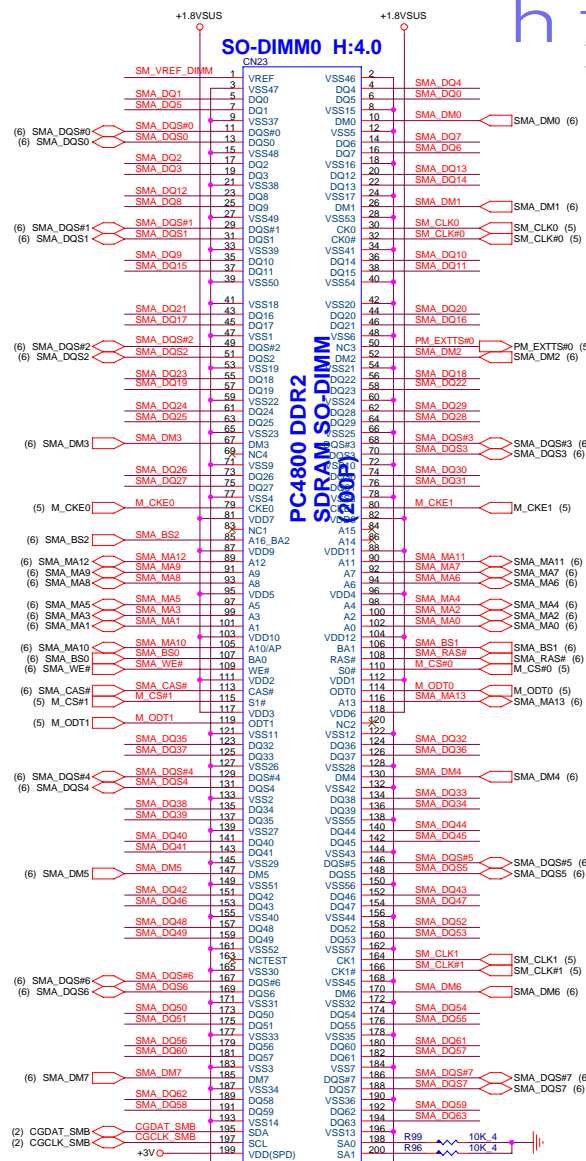


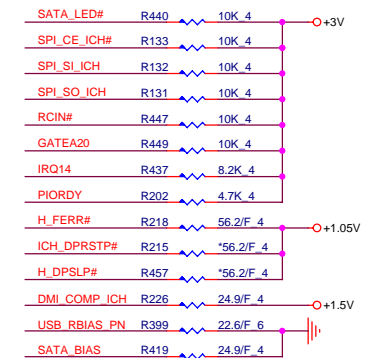
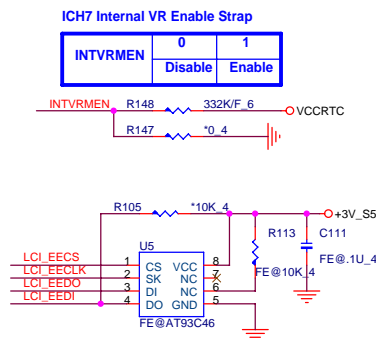
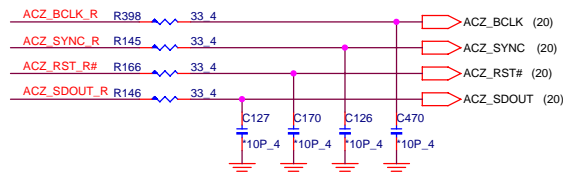
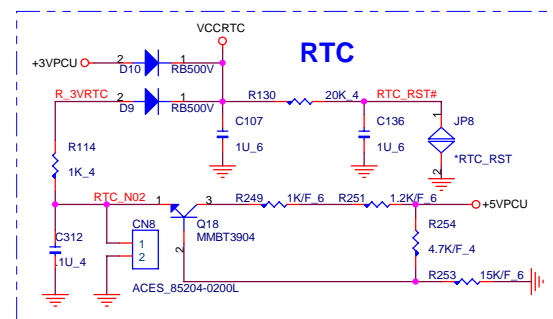
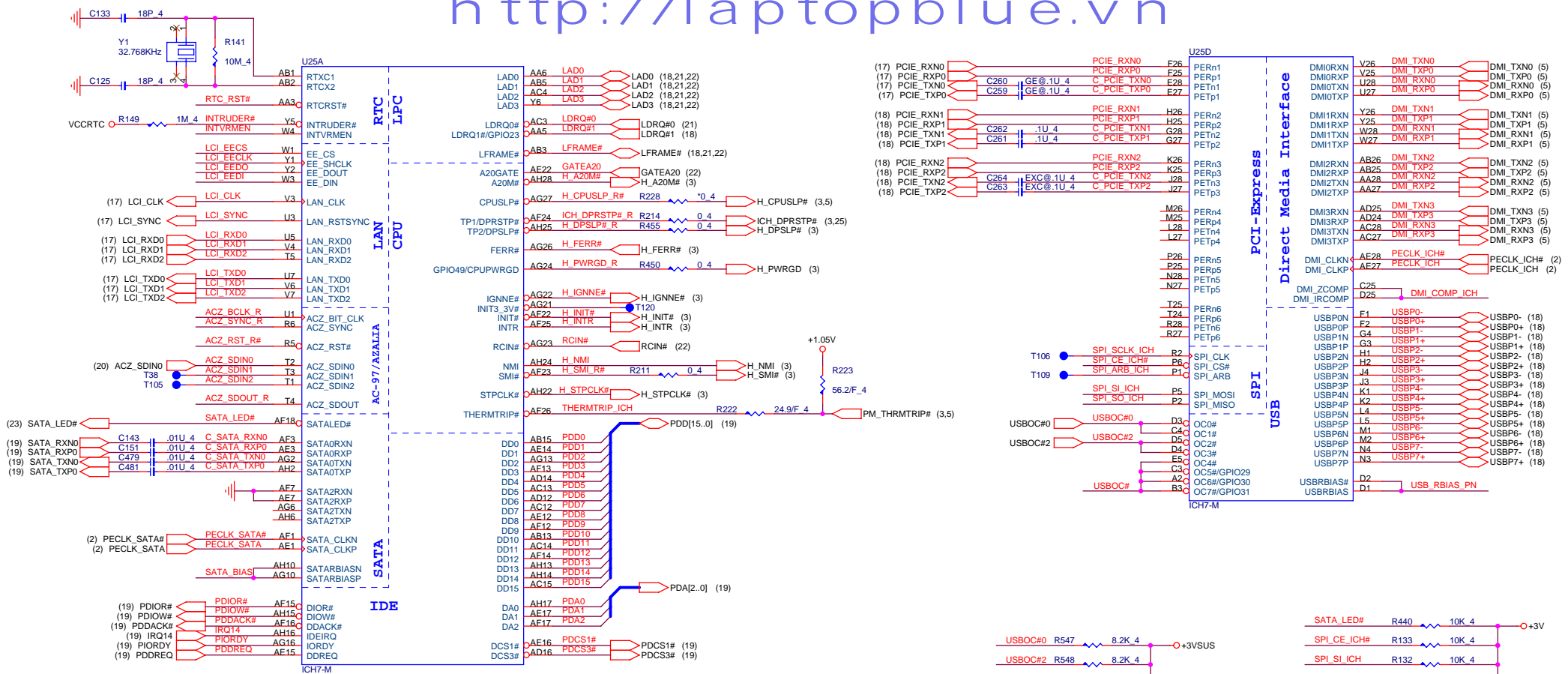
PROJECT :BD1
Quanta Computer Inc.

Size	Document Number	Rev
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Date:	Monday, December 12, 2005	Sheet 6 of 32

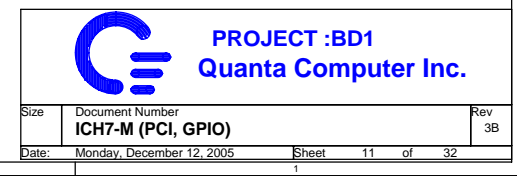
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P41	VSS_4	VSS_101	AC34
M41	VSS_5	VSS_102	W23
J41	VSS_6	VSS_103	AW33
F41	VSS_7	VSS_104	AV33
AV40	VSS_8	VSS_105	AR33
AP40	VSS_9	VSS_106	AE33
AA40	VSS_10	VSS_107	AC33
W40	VSS_11	VSS_108	W22
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U40	VSS_13	VSS_110	T33
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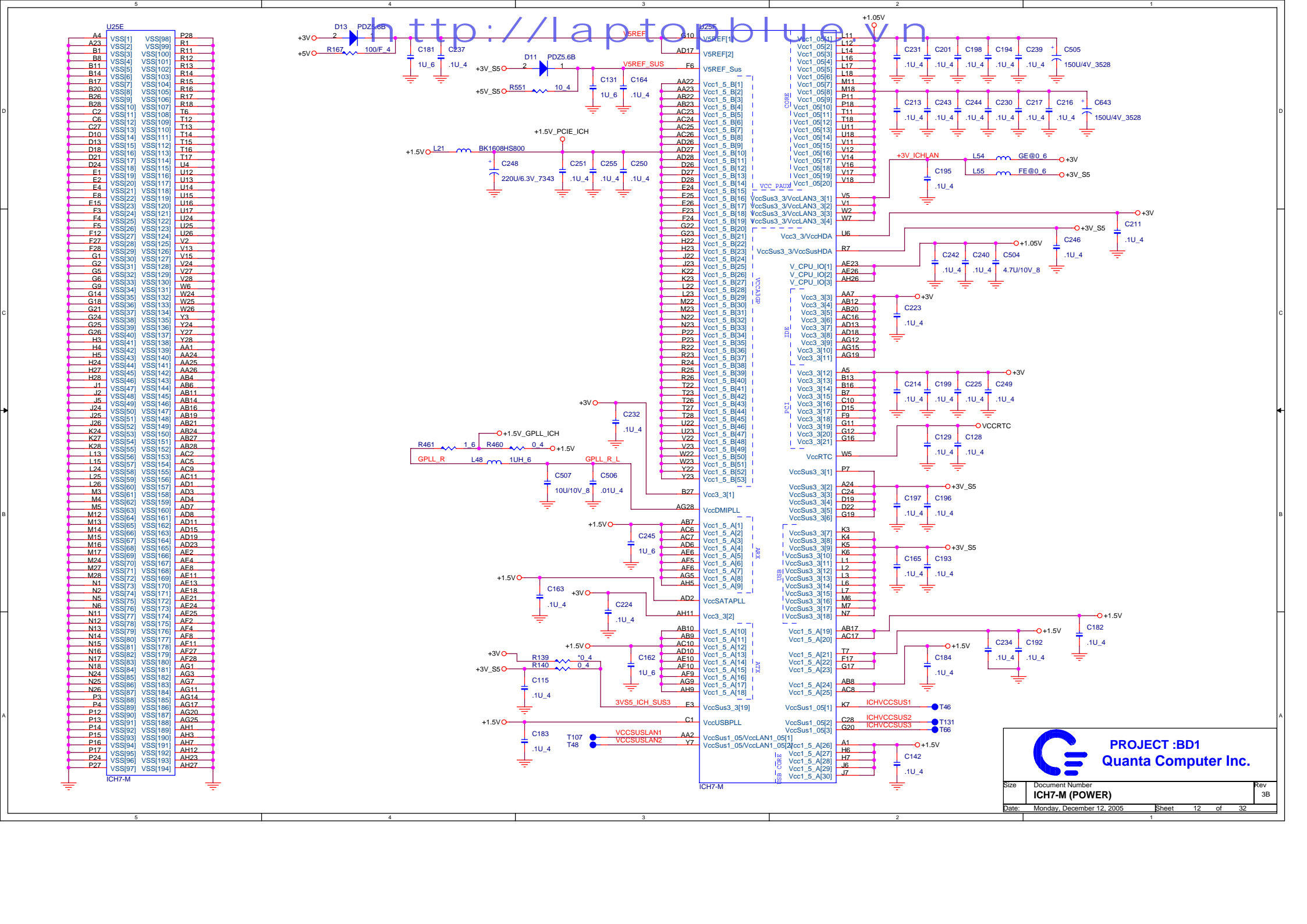






PROJECT :BD1
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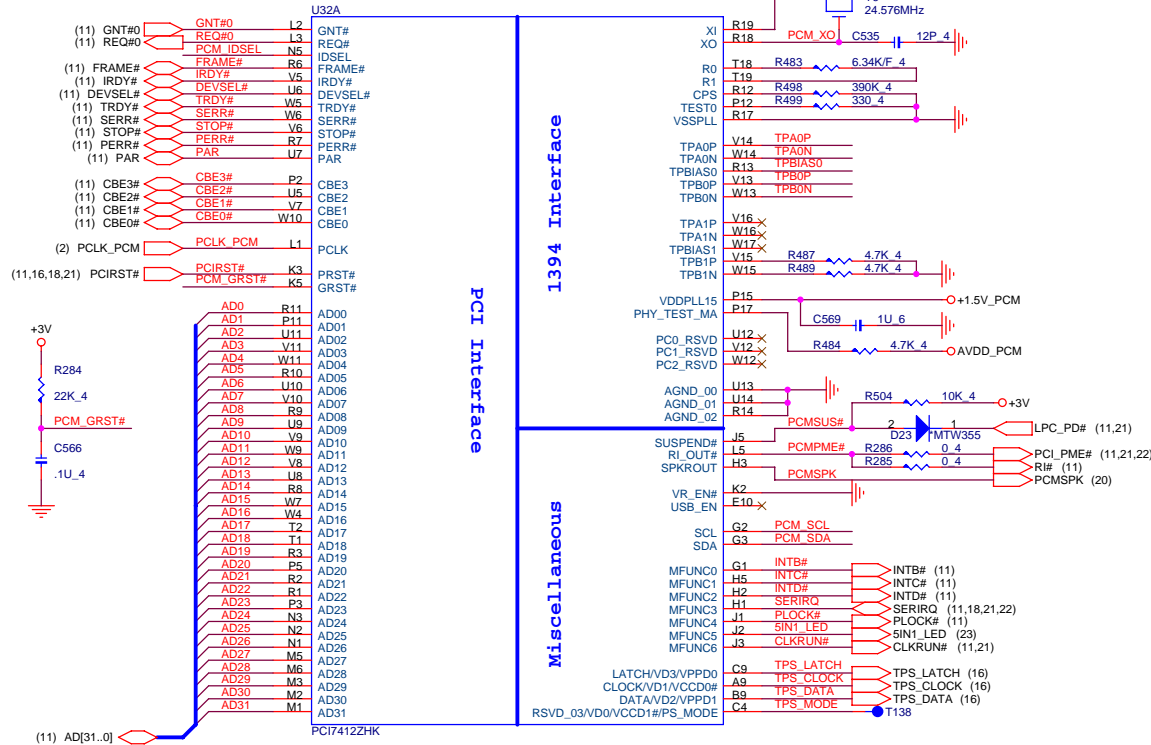


12/12 Unstuff DVI function for UMA Sku



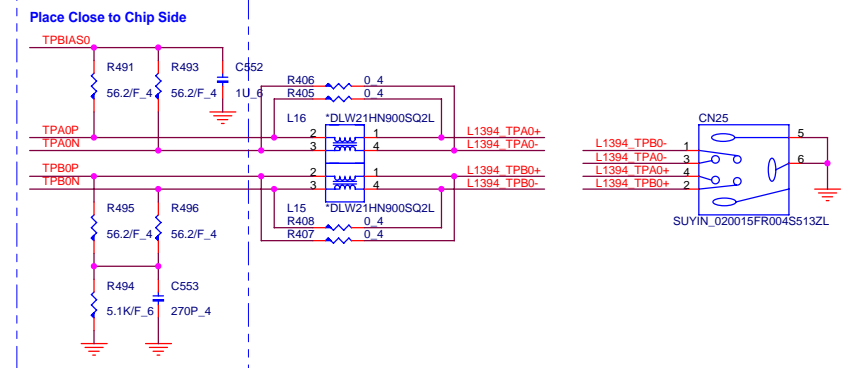
Size	Document Number VGA (LVDS/DVI/CRT/TV)	Rev 3B
Date:	Monday, December 12, 2005	Sheet 14 of 32

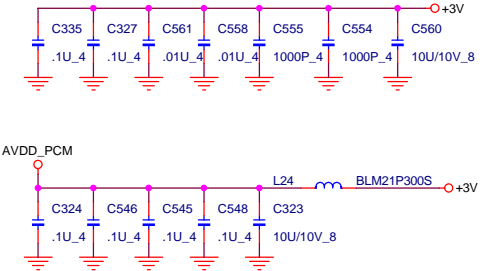
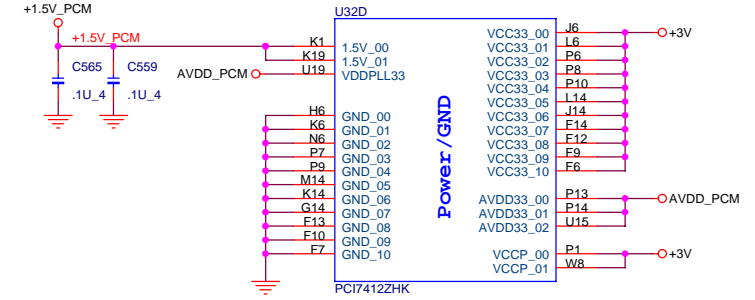
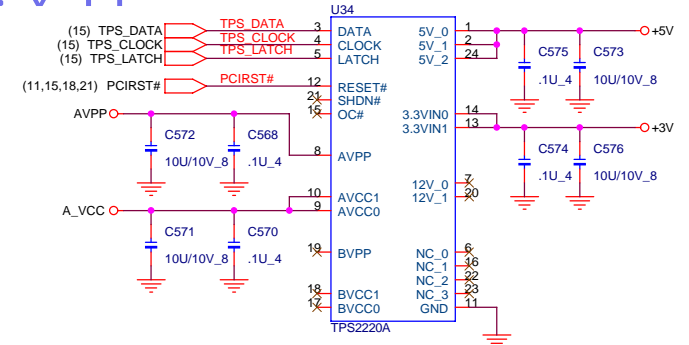
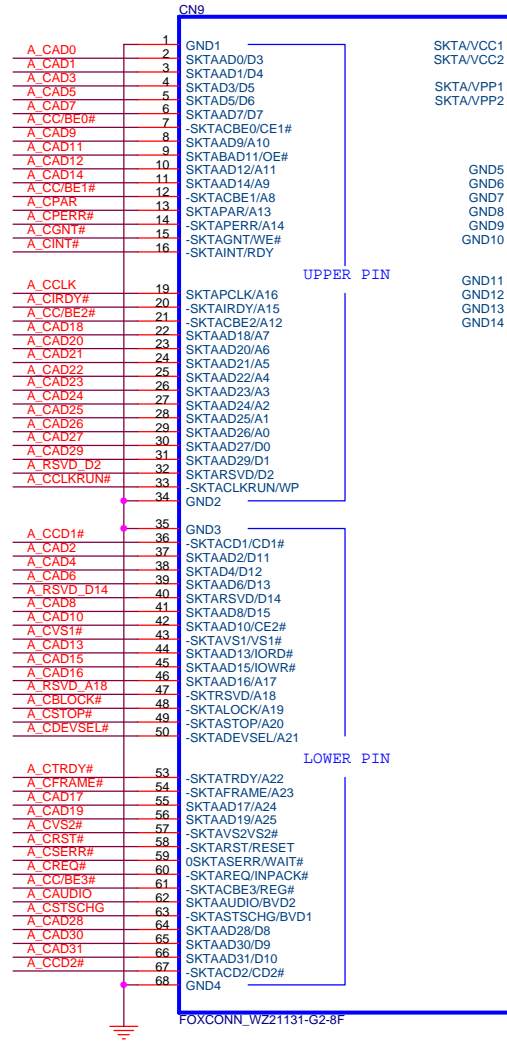
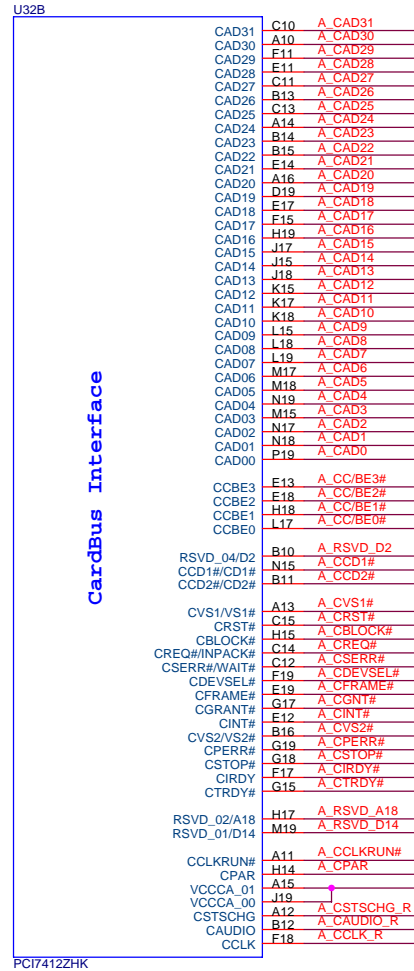
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PCLK_PCM R505 *33 4 C567 *10P 4



U32 change p/n : AJ084120T08 --> AJ074120T13

1394



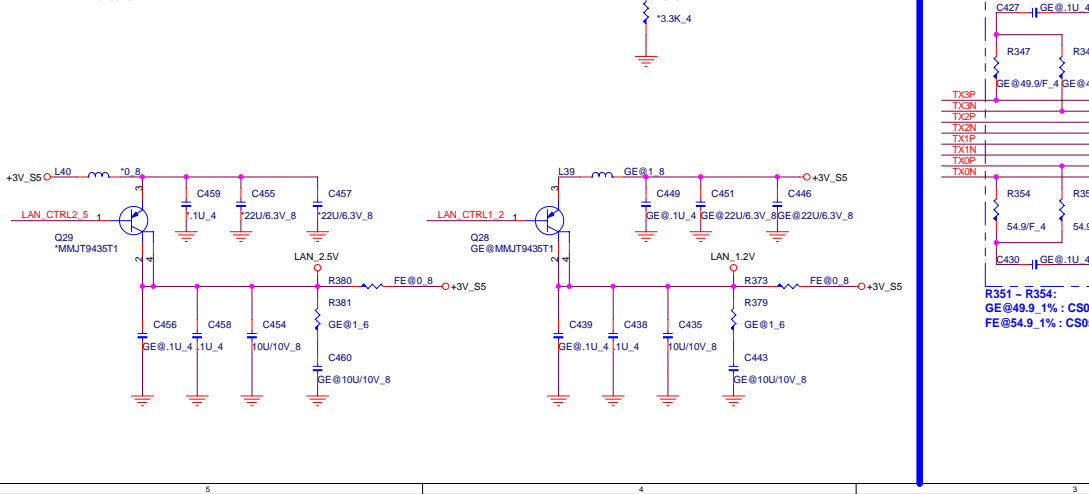
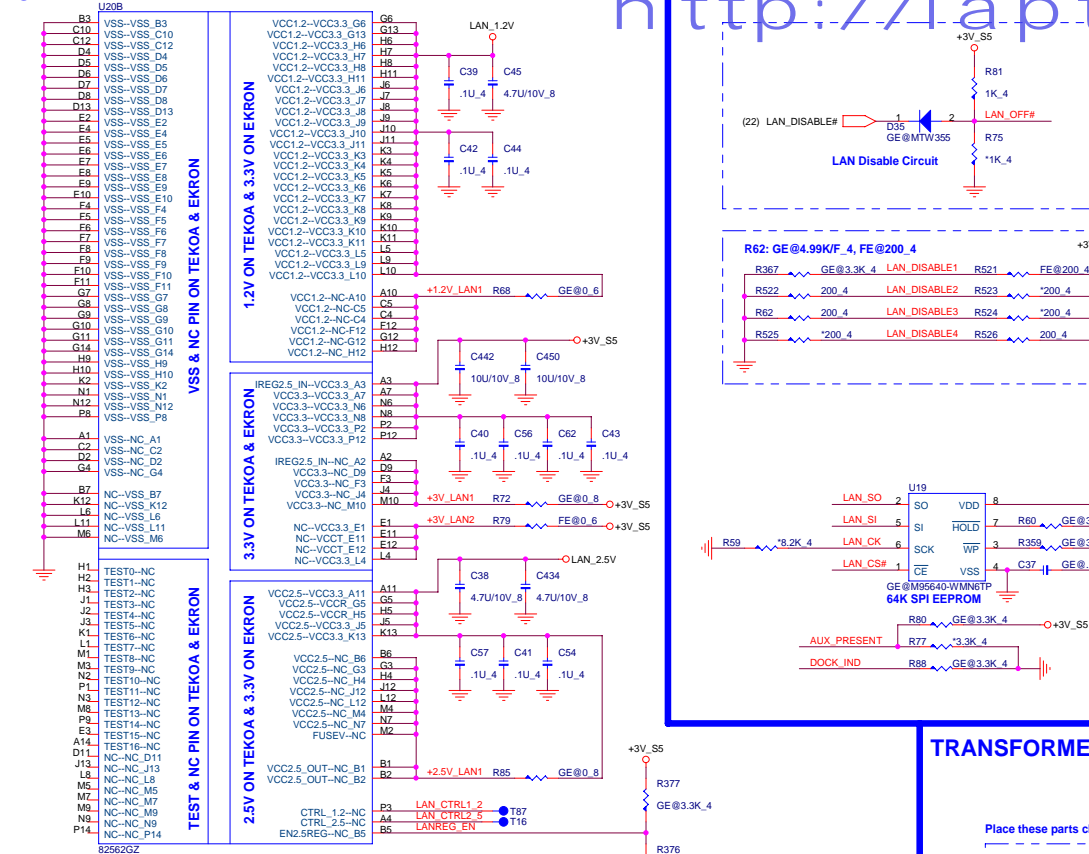


PROJECT :BD1
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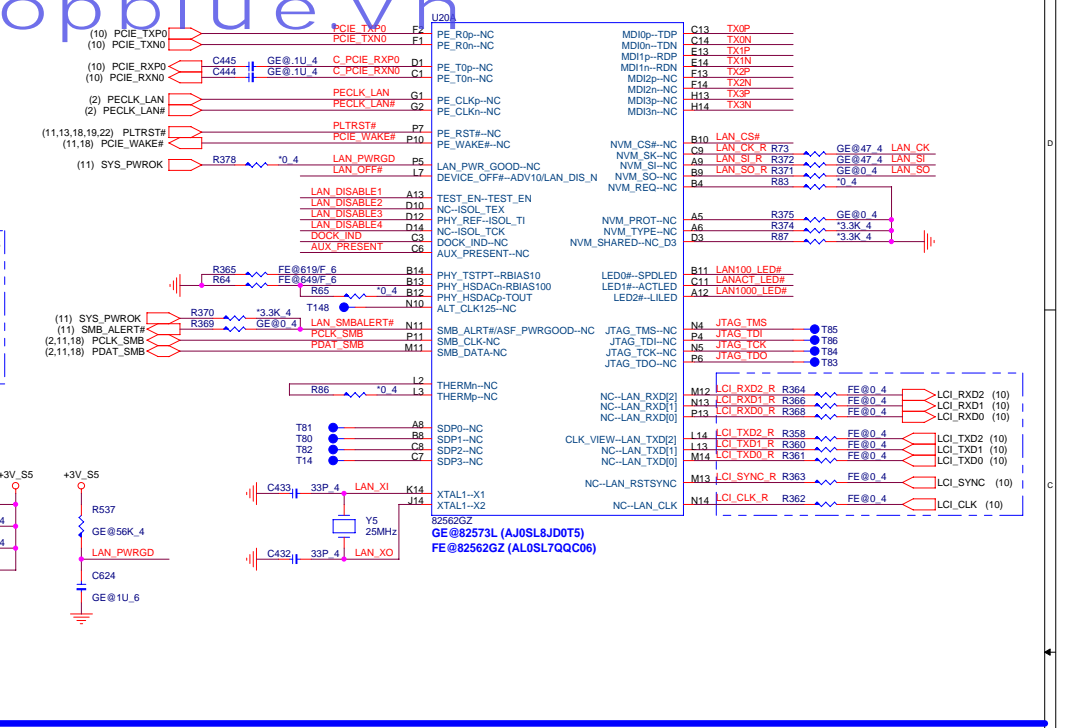
Size	Document Number	Rev
	PCI7412 (PCMCIA)	3B

Date: Monday, December 12, 2005 Sheet 16 of 32

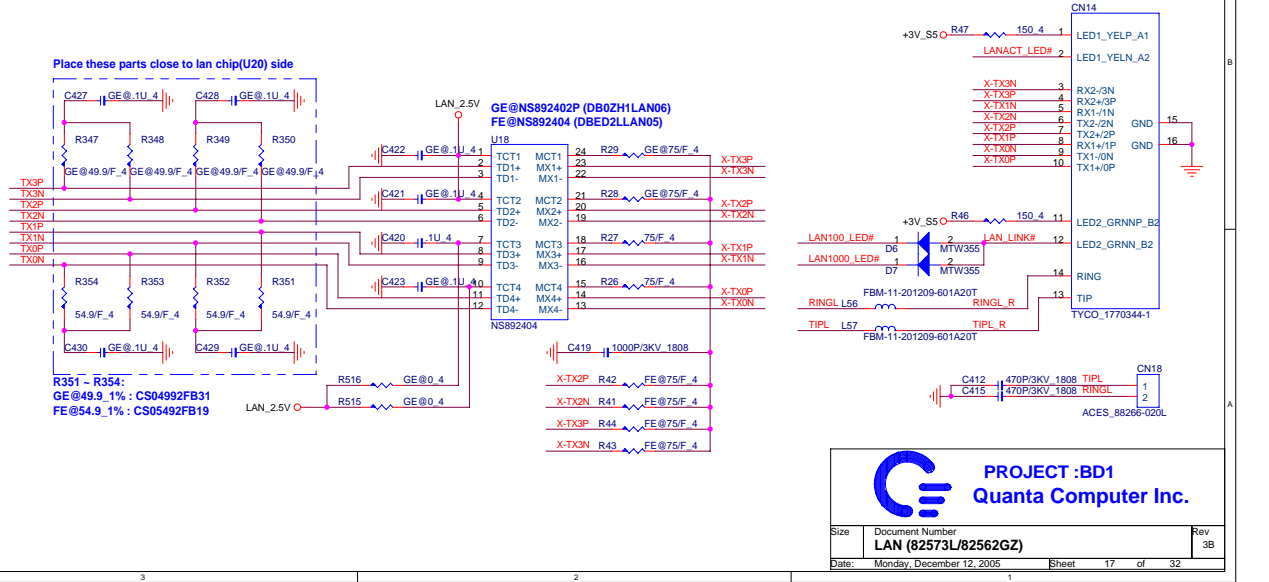
POWER



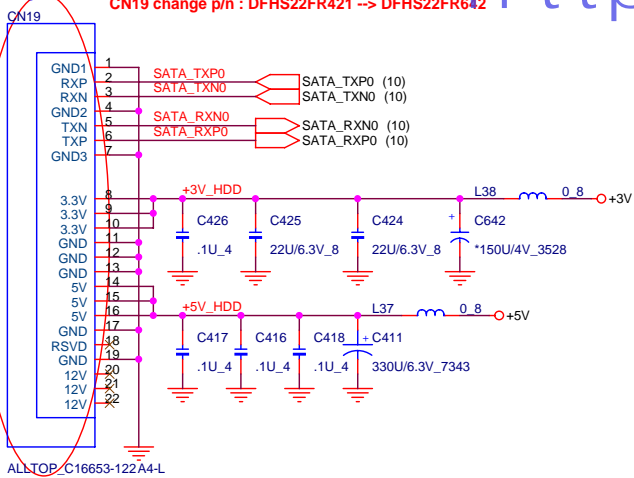
http://laptopblue.vn



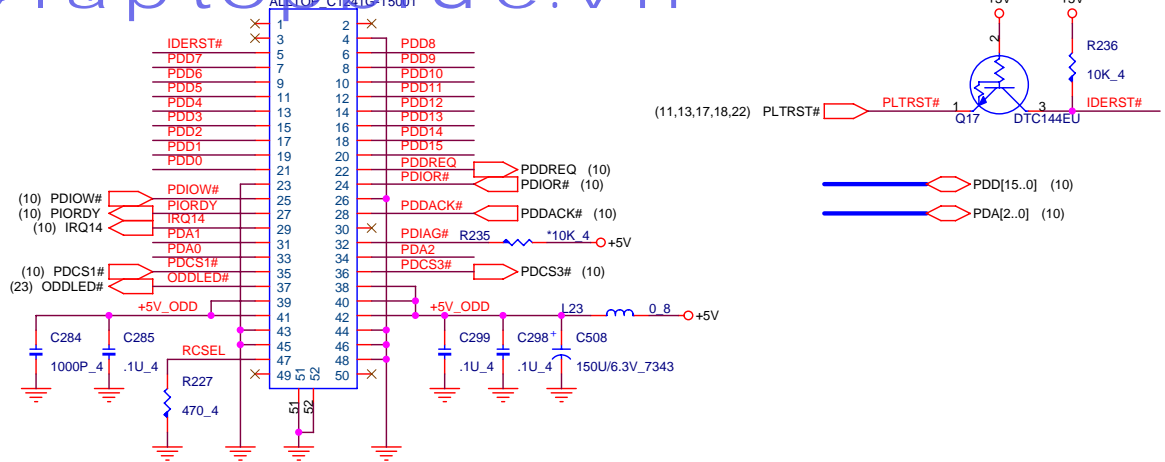
TRANSFORMER & RJ45



HDD (SATA)

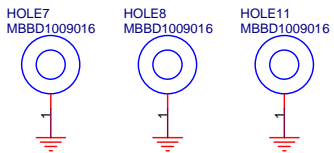


ODD (PATA)

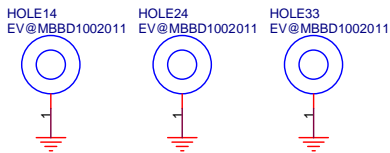


SCREW HOLE

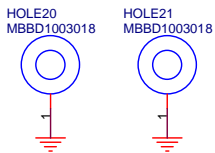
CPU



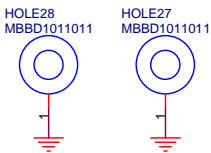
VGA BOARD



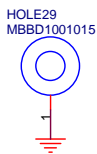
MINI CARD



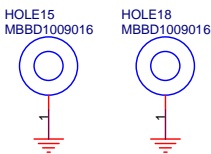
MODEM



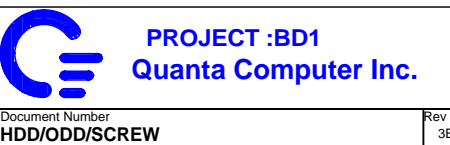
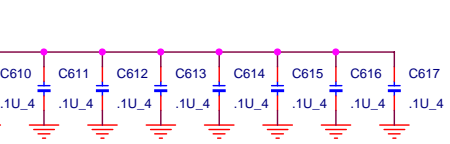
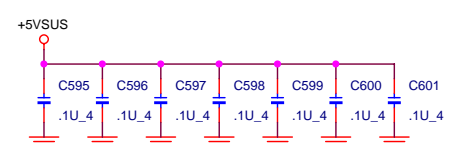
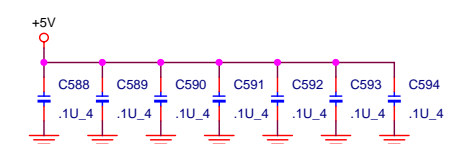
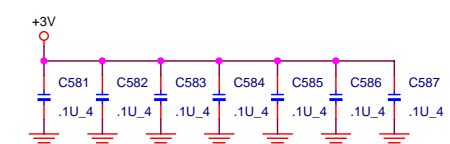
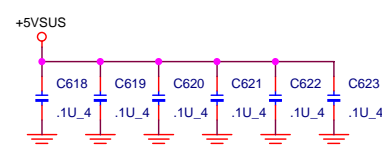
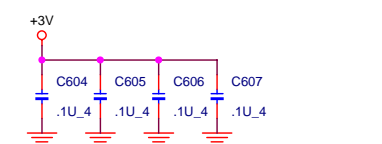
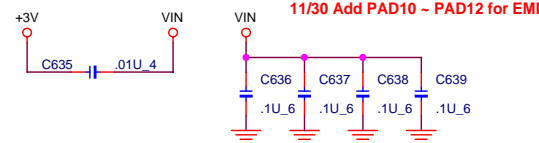
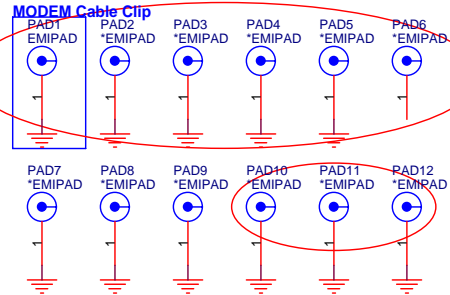
AUDIO BOARD



NB

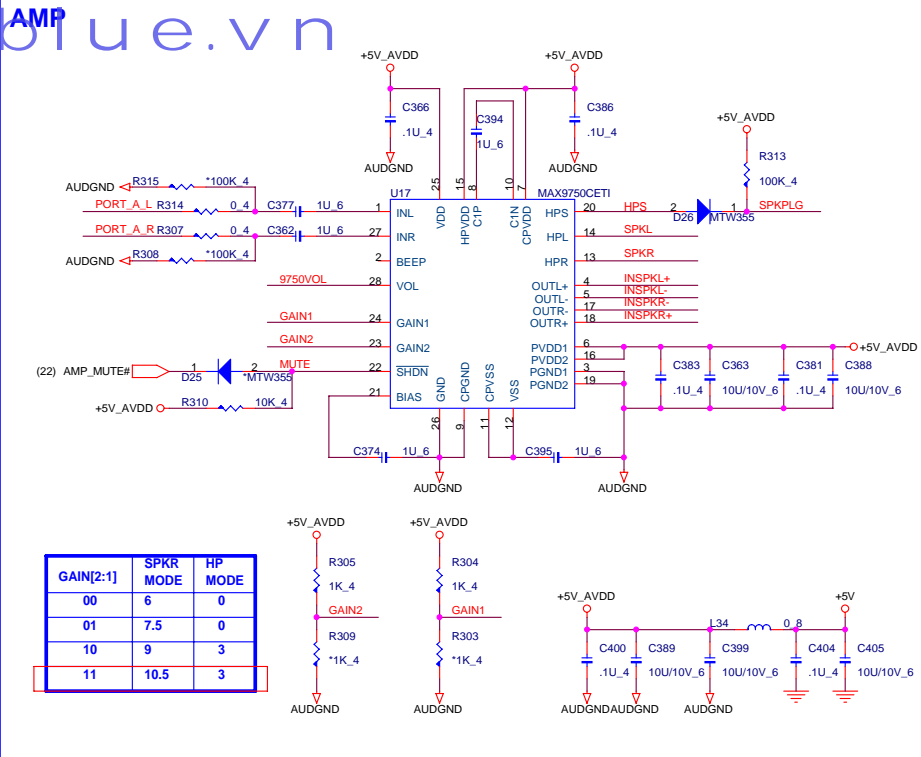
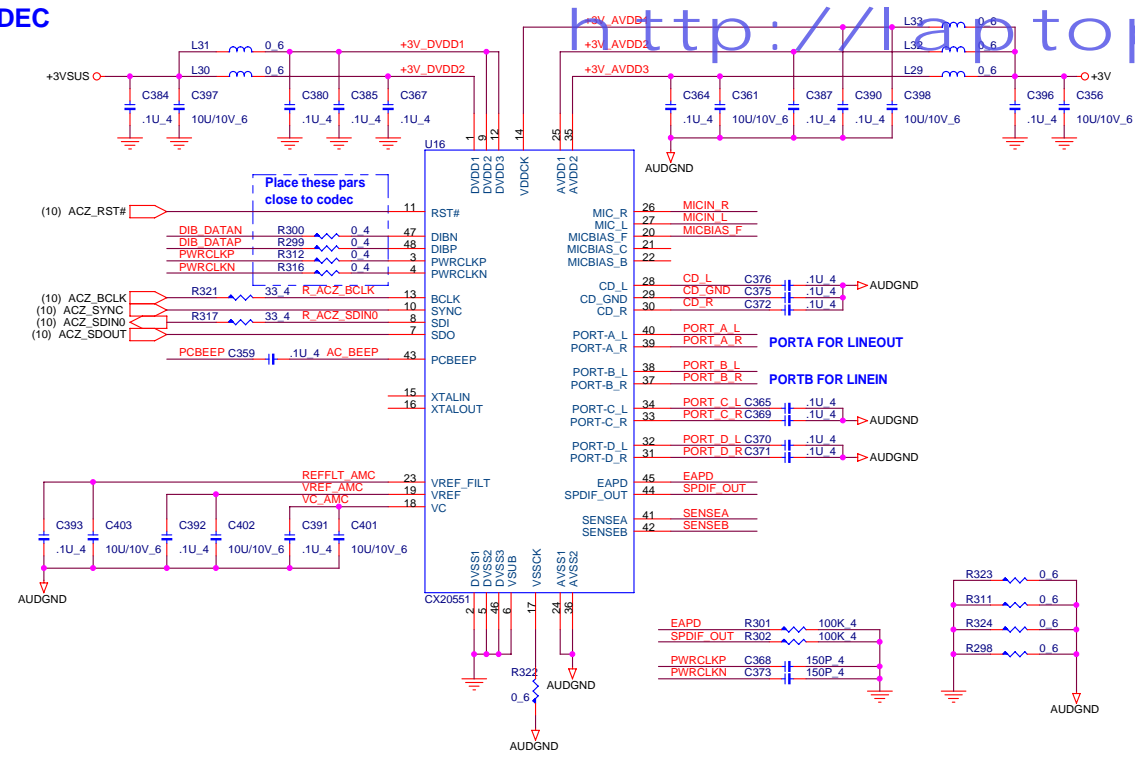


EMI

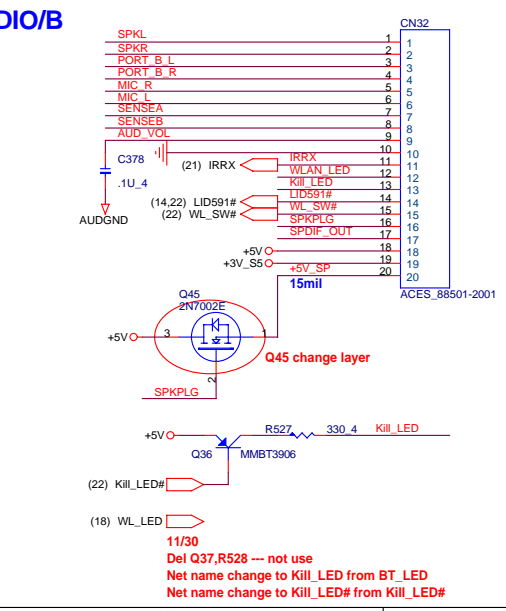


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	HDD/ODD/SCREW	3B
Date:	Monday, December 12, 2005	Sheet 19 of 32

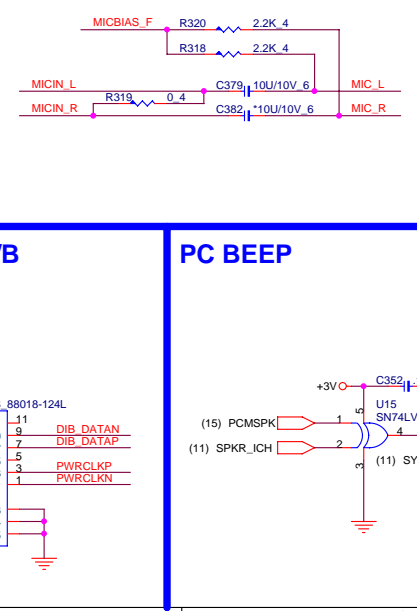
CODEC



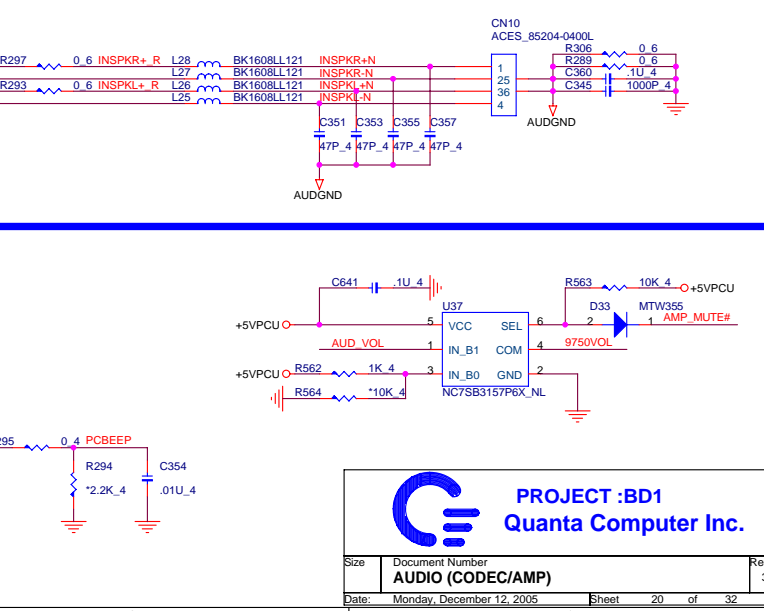
AUDIO/B



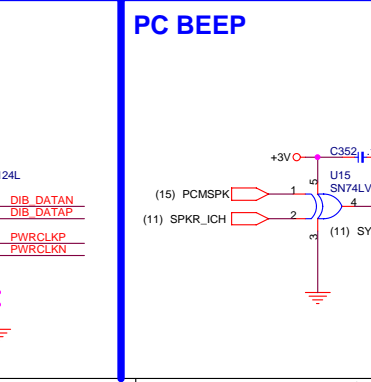
MIC IN



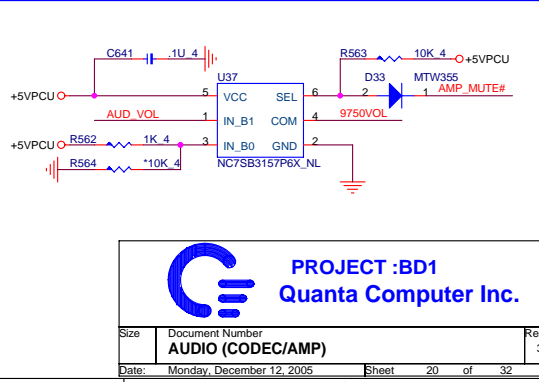
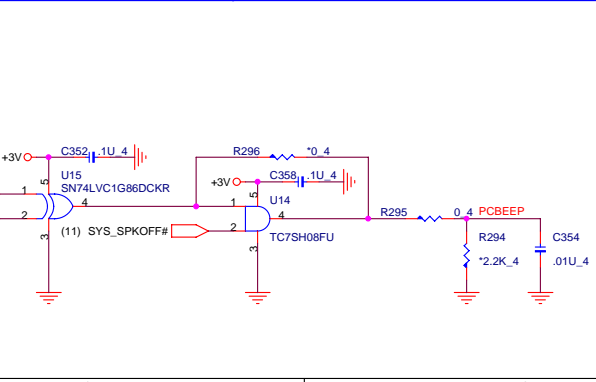
SPEAKER



MODEM/B



PC BEEP

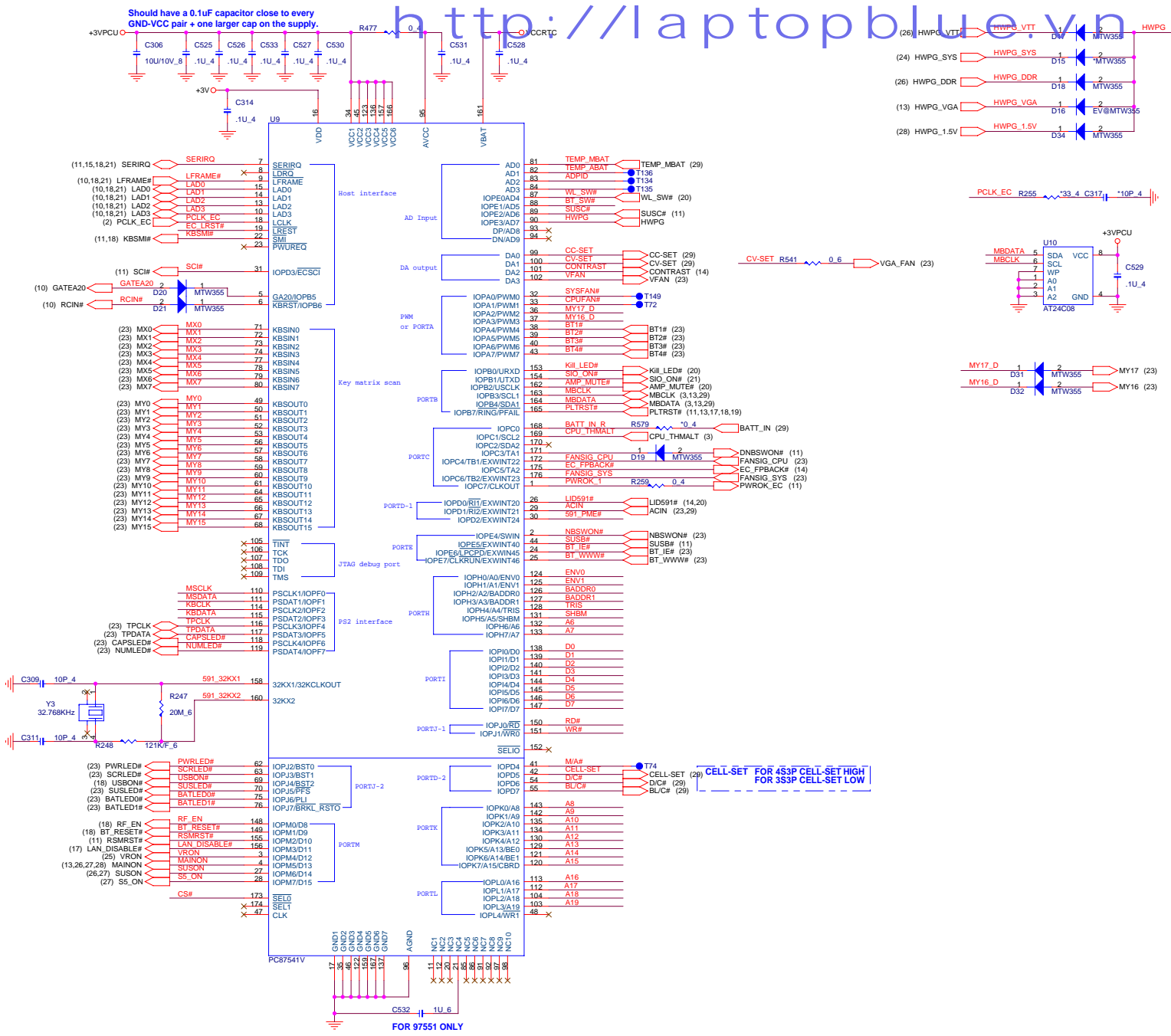


PROJECT :BD1

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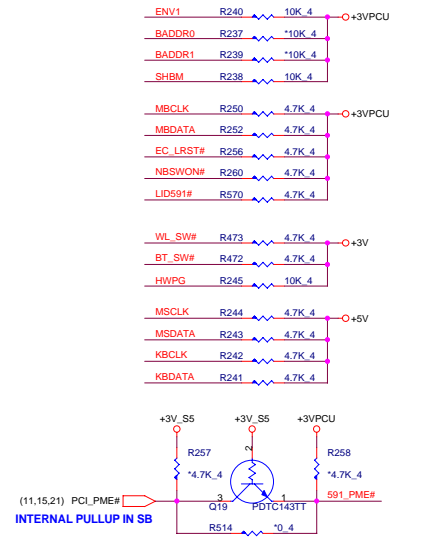
Size	Document Number	Rev
	AUDIO (CODEC/AMP)	3B
Date:	Monday, December 12, 2005	Sheet 20 of 32

Should have a 0.1uF capacitor close to every GND-VCC pair + one larger cap on the supply.

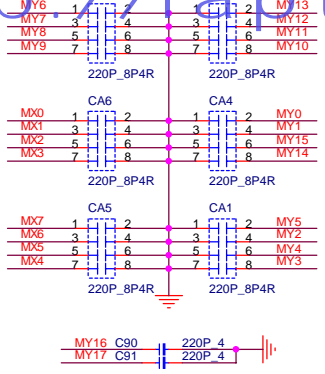
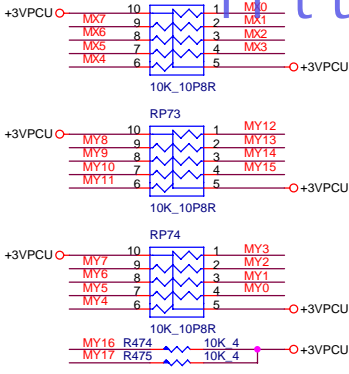
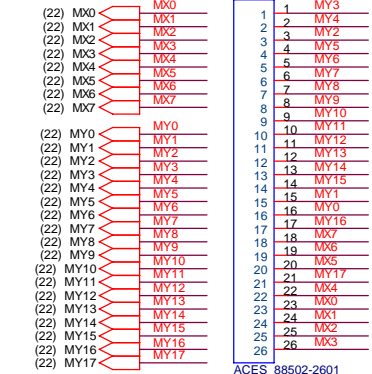


I/O Address		
BADDR1-0	Index	Data
0 0	2E	2F
0 1	4E	4F
1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1
1 1	Reserved	

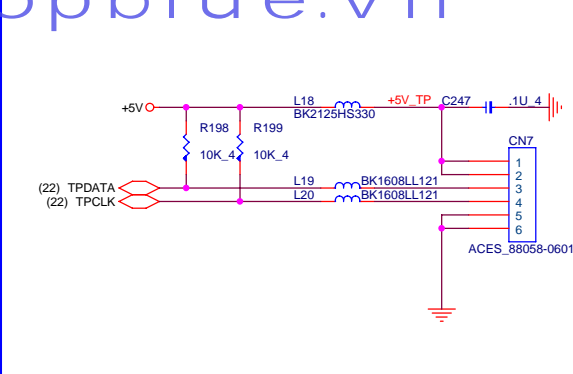
SHBM=1: Enable shared memory with host BIOS



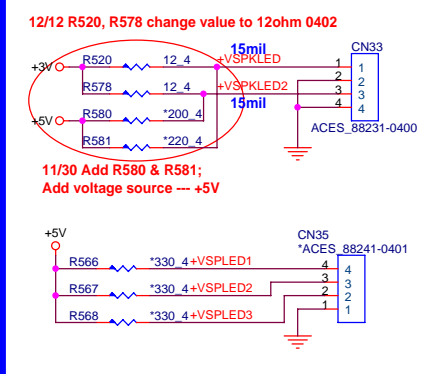
INT K/B



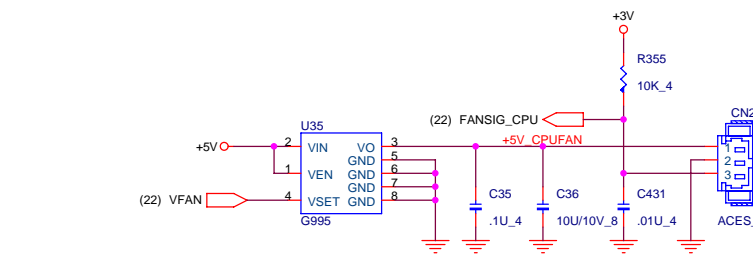
TP



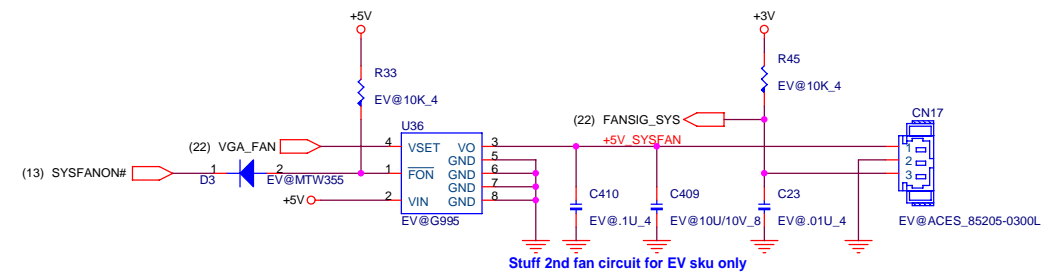
SPEAKER LED



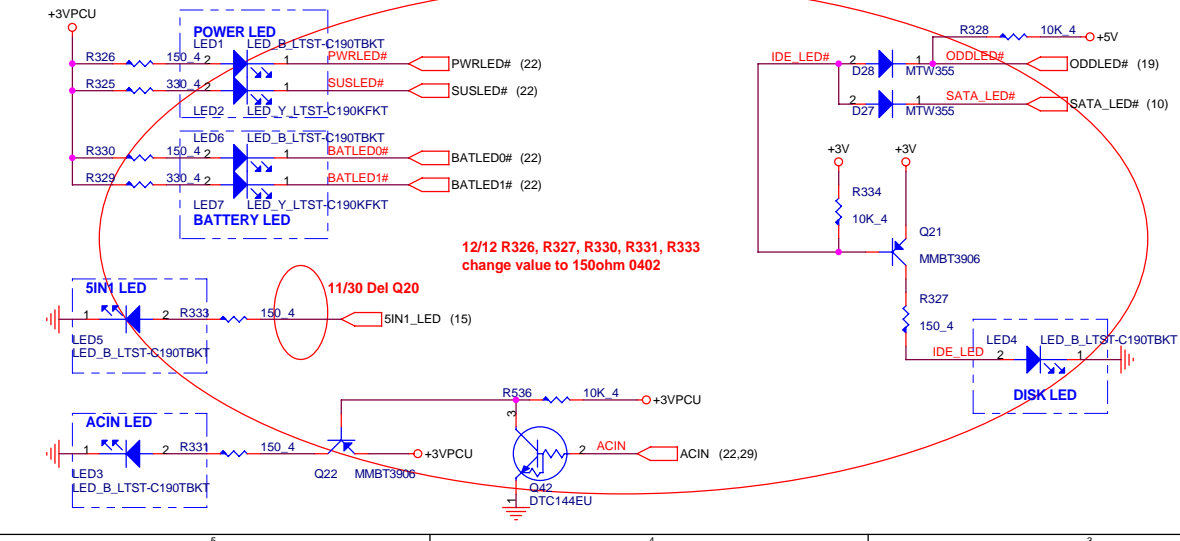
CPU FAN



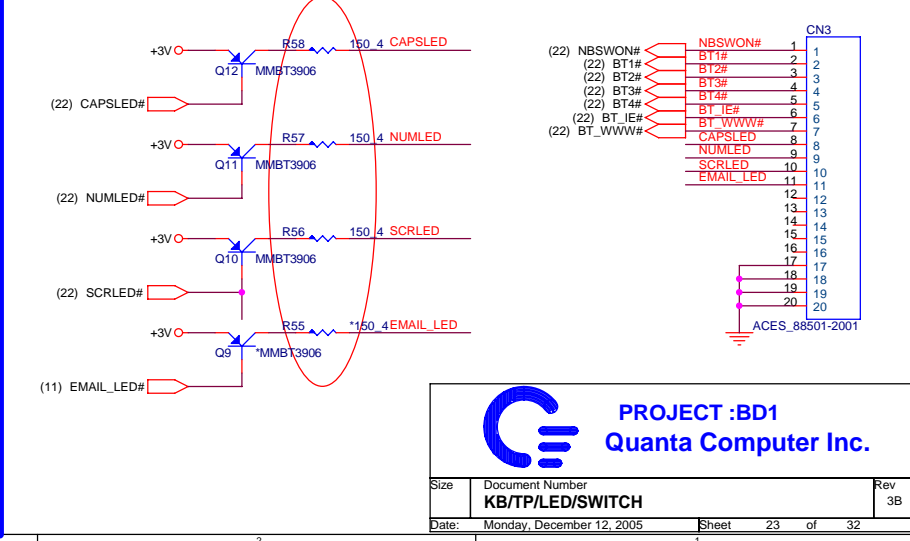
2ND FAN




LED



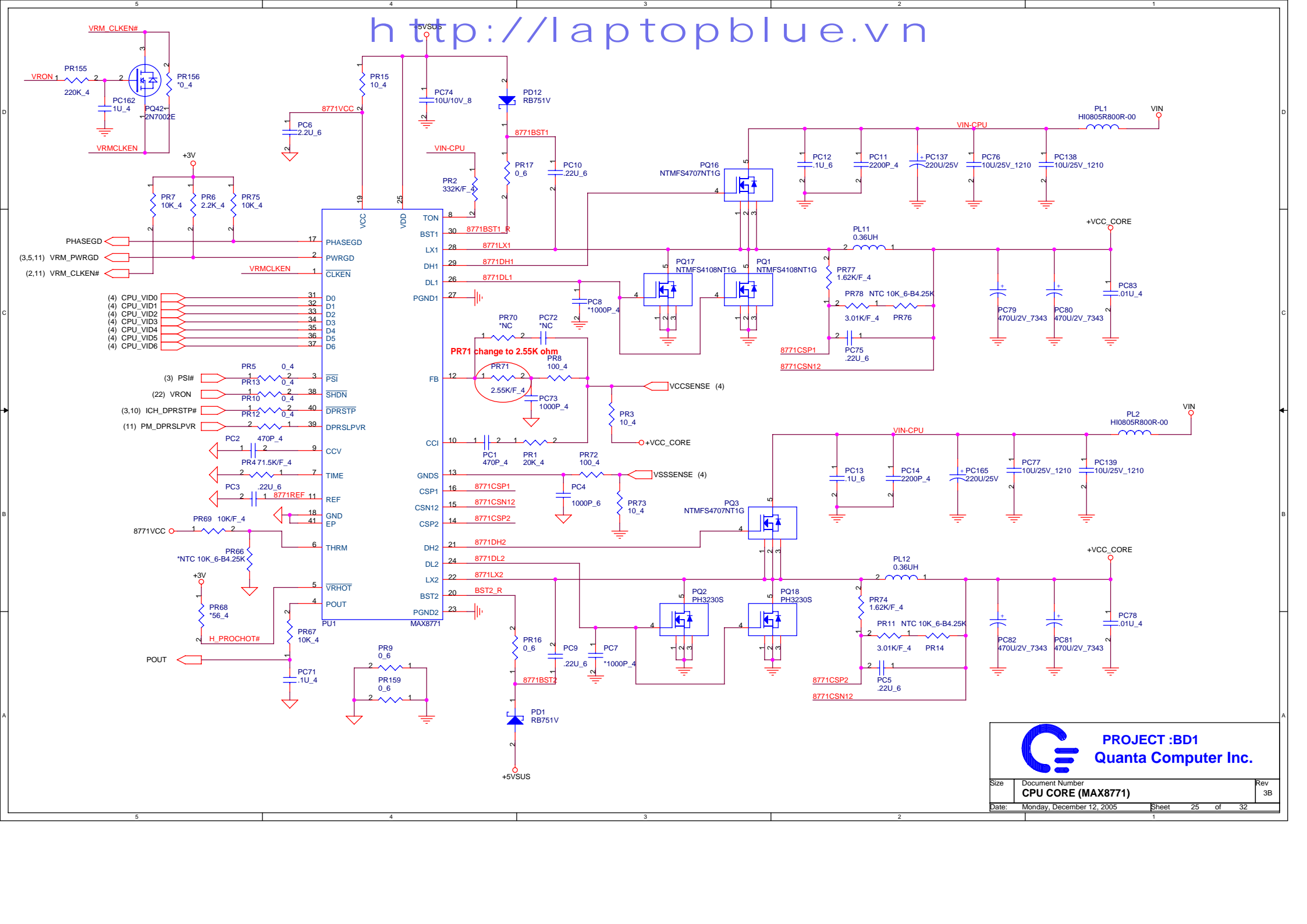
BUTTON BOARD

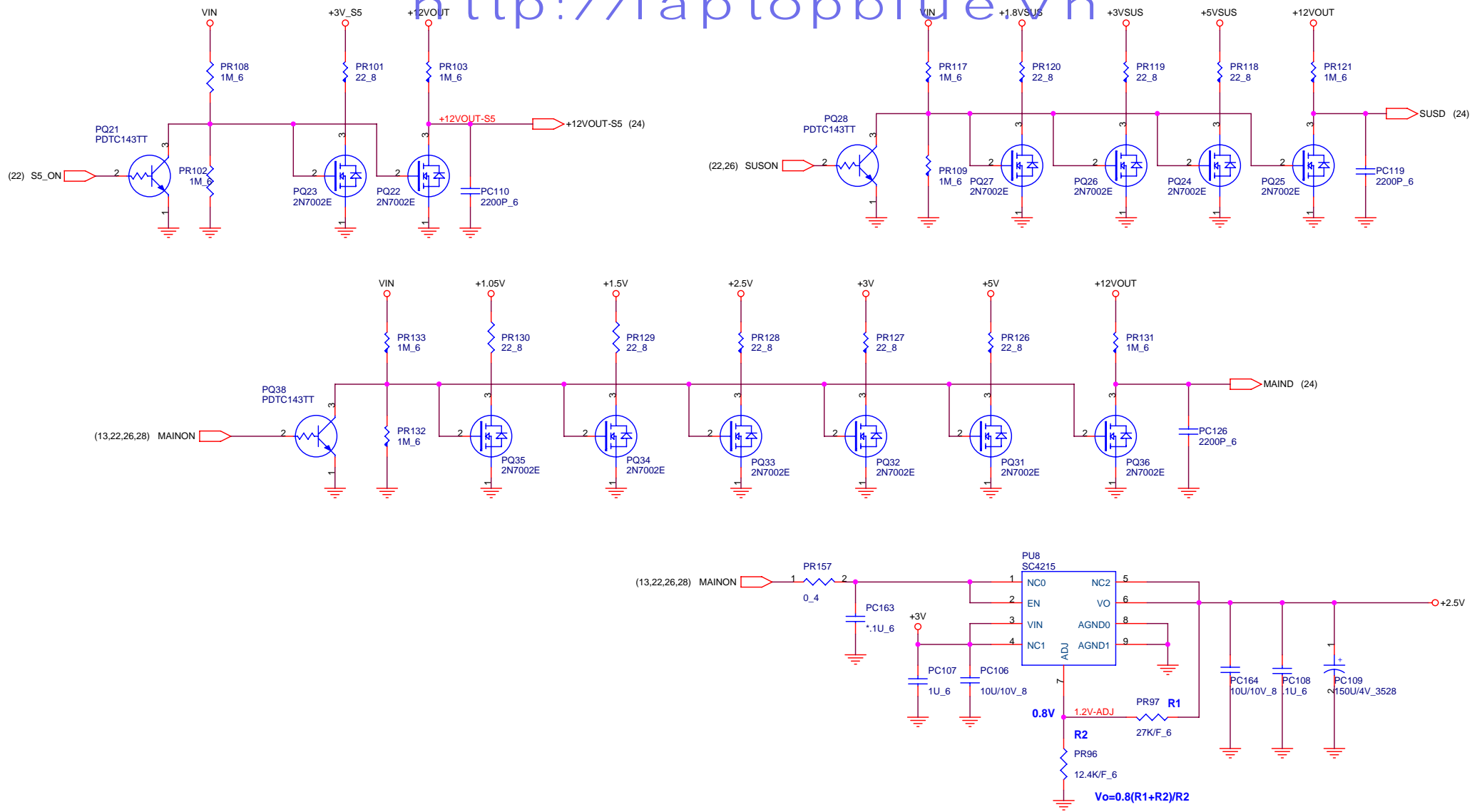




PROJECT :BD1
Quanta Computer Inc.

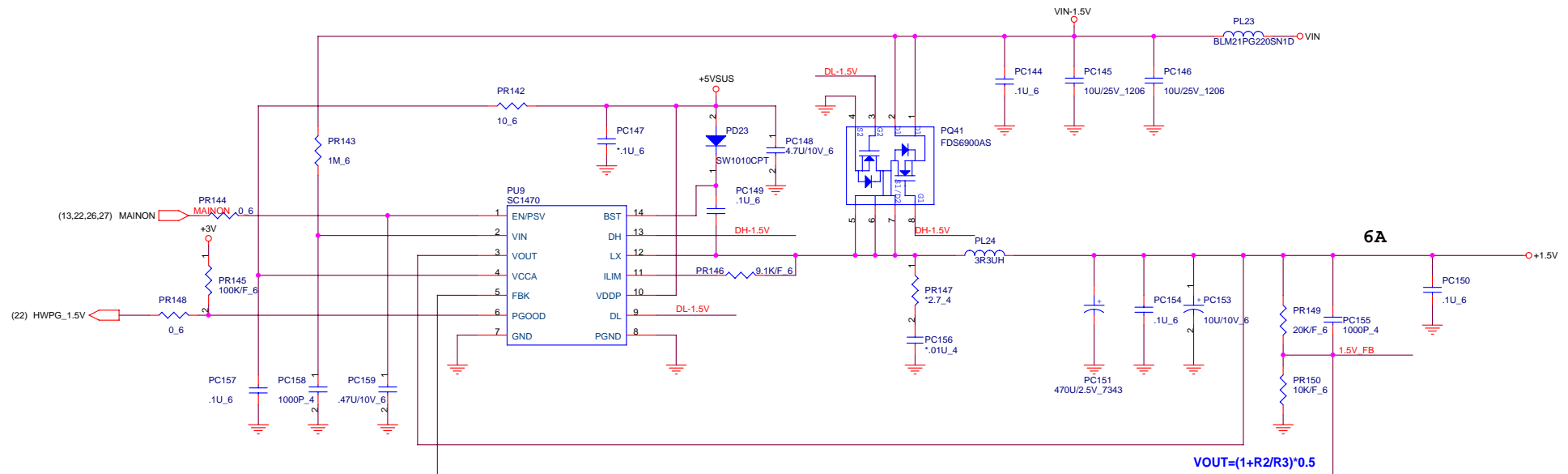
Size	Document Number	Rev
	KB/TP/LED/SWITCH	3B
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Quanta Computer Inc.

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	2.5V/DISCHARGE	3B
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FOR 4S3P CELL-SET HIGH
FOR 3S3P CELL-SET LOW
PR152
CELL-SET 8724CE



Docu
BAT

MODEL:		REV:	CHANGE LIST:			MODEL : BD1 MB		
BD1 MotherBoard		1A	FIRST RELEASE			PAGE	FROM	TO
		2A	<p>Page2. Stuff R136, R427, R432; Unstuff R116, R390, R428, R429, R433, R434</p> <p>Page3. U22 change P/N : AL001032048 --> AL001032030; U21 change P/N : DGG^9000055; R89 change value : 0ohm --> 330ohm; R103 change value : 330ohm --> 8.2Kohm; Delete R441; Add C625, Q38, Q39, Q40, Q41, R529, R530, R531, R532, R535, R533, R534</p> <p>Page5. Stuff R162 for external vga to reverse the PCIE lane</p> <p>Page8. C257 change p/n : CH72201M881 --> CH7221M8854</p> <p>Page10. U5 change P/N : AKE3D8AKA09 --> AKE3D8AKA17; Change C259, C260 stuff for GE sku only; Stuff R214, R455</p> <p>Page11. Add R506 ~ R513, R517, T140 ~ T143; Delete T34, T58, T63</p> <p>Change net : U25.AF19 : MBID1 --> GPIO21; U25.AH18 : MBID0 --> GPIO19; U25.F19 : GPIO12 --> EMAIL_LED#; U25.R4 : GPIO14 --> LID591#; U25.R3 : LAN_DISABLE# --> MBID0; U25.D20 : GPIO25 --> MBID1; U25.AD21 : LID591# --> GPIO35; U25.AE20 : EMAIL_LED# --> GPIO39; U25.B21 : MBID2 --> GPIO27; U25.A21 : GPIO26 --> MBID2; R155.1 : GPIO14 --> LID591#</p> <p>Page12. Delete R127; Add D30; Change net : +3VSUS --> +3V_S5; C248 change p/n : CH72201M881 --> CH7221M8854</p> <p>Page13. C512, C513, C519, C524 change footprint : CC0805 --> CC0603; Unstuff C515; CN22 change footprint : SPS2F-D-SMX-20-X-X-20P-LUV --> 200109MA020G261ZR-20P-LUV</p> <p>Page14. Delete SW1; Add R518, R519, C602, C603; C20, C32 change footprint to CC0603; CN11 change footprint : SV-030105FR004T101XX-4P-EW3 --> SV-030018FR004S100FR-RVS-4P-V</p> <p>Page15. CN31 change footprint : 4IN1-R012-312-LR-44P --> 4IN1-R102-316-XX-44P; U32 change p/n : AJ074120T13 --> AJ084120T08</p> <p>Page16. R490, R492 change value : 10Kohm --> 0ohm; CN9 change footprint : PCI-1CA4A5G1-TC2-LN-84P --> PCI-1CA4A501-TC-RD-84P</p> <p>Page17. U20.M13, U20.N14 net swap; Add R515, R516, R521 ~ R526, R537, C624; Delete T2, T79; Stuff R375; Unstuff R59, R374, R378; U19 change p/n : AKE3C8W0601; U19 change footprint : SOIC8-6_1_27 --> SOIC8-7_9_1_27</p> <p>Page18. Add T147, Delete C28, Q6, R262; Stuff L35, L36; Unstuff R7, R15, R16, R21, R30, R338; R48 change value : 100Kohm --> 0ohm; R50 change value : 1Kohm --> 10Kohm; CN15 change footprint : USB020173MR004SX07ZX-4P-V --> USB-020173MR004G552ZR-4P-R-H; CN29 change footprint : NCARD-1CX43201-LN-RVS-26P --> EXPRESSCARD-1CX43202-BD-26P; CN1.1 change net : BT_POWER --> +3V</p> <p>Page19. Add C581 ~ C601, C604 ~ C623; Add HOLE33, PAD1 ~ PAD3; Delete HOLE2, HOLE16, HOLE19, HOLE32; HOLE7, HOLE8, HOLE11, HOLE15, HOLE18 change p/n : MBEW3001012 --> MBBD1009016; CN26 change p/n : DFHS50FR393 --> DFHS50FR369</p> <p>Page20. U15 change P/N : AL1G0086005 --> AL1G0086013; C356, C361, C363, C388, C389, C397, C398, C399, C401, C402, C403, C405 change footprint to CC0603; Unstuff R303; Stuff R304; CN10 change pin define; Add Q36, Q37, R527, R528; CN32 change footprint : 88076-020XX-20P-RUV --> 88241-20XX-20P-L; Change net (codec digital power) : +3V --> +3VSUS; CN10 change p/n : DFWF04MS079 --> DFHD04MR701; CN10 change pin define</p> <p>Change net : CN32.4 : IRRX2 --> NC; CN32.6 : IRTX2 --> BT_LED#; CN32.8 : IRMODE --> LID591#; CODEC Power : +3V --> +3VSUS</p> <p>Page21. Add T144 ~ T146; Unstuff R283, R287 and Stuff R280, R288 to change the SIO address to 162E</p> <p>Page22. Stuff C309, C310, C311, C322, D16, R241 ~ R244, R246, U30; Unstuff U29, C313; Add D31, D32, R514; Delete T73, T76</p> <p>Change net : U9.24 change net : LPCPD# --> BT_IE#; U9.25 change net : CLKRUN# --> BT_WWW#; U9.153 change net : BT_IE# --> BT_LED#; U9.154 change net : BT_WWW# --> NC; U9.156 change net to LAN_DISABLE#</p> <p>Page23. Add CN33, LED6, LED7, Q42, R520, R536; Delete Q23, R332; Q20, Q21 change to MMBT3904; Q22 change to MMBT3906; CN7 change p/n : DFHD06MS618 --> DFFC06FR212; +3V change to +3VPCU; LED1, LED2 change package; LED3 change p/n : BEAB0006Z07 --> BEGR0080Z07</p> <p>Page24. PQ30, PQ39 change p/n to BAM76520001</p> <p>Page25. PC11, PC14 change P/N : CH2226K1B16 --> CH22206KB16; Stuff PD1, PD12, PQ2, PQ17; Unstuff PR68; PR2.1 change net to VIN-CPU; PL11, PL12 change P/N : CV+60V0MZ33 --> DC+60Q0L001; PL11, PL12 change P/N : CV+60V0MZ33 --> DC+60Q0L001</p> <p>Page26. Add PR135, PR136, R538, R539; PQ6, PQ11 change p/n to BAM76520001; PC24 change value : 4700P --> 5600P; PR26 change value : 62K --> 22K; PR50 change value : 100K --> 39K</p> <p>Page27. PR96 change value : 306_1% --> 604_1%; PR98 change value : 190_1% --> 180_1%</p> <p>Page28. Add CN34, PD20, PF3, PR137; PR94 change value : 6.8K_1% --> 10K_1%; PR95 change value : 10K_1% --> 1K_1%; PR89.1, PD6.2 change net : REFP --> VIN; PL14 change P/N : CV01044MZ18 --> CV-6880MZ01; PQ4 change p/n to BAM76520001; CN21 change footprint</p>			1	2B	3A
			2	2B	3A			
			3	2B	3A			
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			5	2B	3A			
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			28	2B	3A			
			29	2B	3A			

 Quanta Computer Inc.		PROJECT : BD1		APPROVE BY: SAINT LIN		DRAWING BY:DILBERT YU		REV 3B		COVER SHEET 1/3	
Size Document Number Change List --- 1		Rev 3B MB ASSY'S P/N : 31BD1MBXXXX									

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Change List -- 2	3B
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MODEL:		REV:	CHANGE LIST:	MODEL : BD1 MB			
BD1 MotherBoard	3B	<div>h t t p : / / l a p t o p b l u e . v n</div> <div>Page2. Add C647~C653 to reserve.</div> <div>Page8. L14 package change to RC0805.</div> <div>Page15. Add R582 for TI-7412, in TI-7412 Pin-G6 is +3V power.</div> <div>Page19. Add PAD10~PAD12 for EMI.</div> <div>Page20. Del Q37,R528 --- not use.</div> <div>Net name change to Kill_LED from BT_LED</div> <div>Net name change to Kill_LED# from BT_LED#.</div> <div>Page23. Del Q20 for 5IN1 LED.</div> <div>Add R580 and R581to support speaker voltage source can be change to +5V.</div> <div>R520 and R578 change to 12ohm for speaker current (9~10mA).</div> <div>Page29. Add PC168 for EMI</div> <div>change Battery connector for 100W.</div>			PAGE	FROM	TO
					1	3A	3B
					2	3A	3B
					3	3A	3B
					4	3A	3B
					5	3A	3B
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