

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

HAINAN2

CPU : Intel Merom
Chip Set : RS600M & SB600
Remarks : Mobility Platform

Model Name : HAINAN2
PBA Name : MAIN
PCB Code : BA41-XXXXXA
Dev. Step : PR
Revision : 1.0
T.R. Date : 2006.11.16

DRAW	CHECK	APPROVAL

Table of Contents

1. COVER
- 2-7. DIAGRAM & ANNOTATION
8. CLOCK
- 9-11. MEROM
12. THERMAL & FAN
- 13-17. RS600
18. DDR2 SODIMM
19. DDR2 TERMINATION
- 20-23. SB600
24. SB600 TERMINATION
25. SPI ROM
26. LCD
27. CRT
28. 4 IN 1 CARD
29. EXPRESS CARD
30. MINICARD/DMB/BT
- 31-33. AUDIO
34. HDD & ODD
35. MICOM
36. LAN
37. LAN & MDC & USB CONN
38. KBD & TOUCH-PAD CONN
39. CHARGE
40. P3.3V_AUX & P5.0V_AUX
41. P1.2V & VCCP_CORE
42. DDR2 POWER
43. CPU VRM
44. SWITCH POWER
45. POWER SWITCH AND LED
46. POWER STRAPS
- 47-48. TP

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	COVER		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	1	OF 48

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

Power Diagram

KBC3_SUSPWRON

KBC3_PWRON

KBC3_VRON

VCCP3_PWRGD

AC Adapter

Battery DC

VDC

P12.0V_ALWS

P5.0V_ALWS

P1.2V_ALWS

P3.3V_MICOM

P1.8V_AUX

P1.5V

P1.8V

P1.2V

P0.9V

P5.0V_AUX

P5.0V

P3.3V_AUX

P3.3V

P2.5V_LAN

P1.05V
MEROM
NB
SB
(VCCP3_CORE)**CPU_CORE**

MEROM

Rail State	+V*Always	+V*AUX	+V	SUSPWR	PWRON	VRON
Full On	ON	ON	ON	H	H	H
S3	ON	ON	OFF	H	L	L
S4	ON	ON	OFF	H	L	L
S5	ON	OFF	OFF	L	L	L

S5 / S4**S3****S0**

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		POWER DIAGRAM	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	3	OF 48

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

POWER SEQUENCE

VDC
ADT3_SEL
P3.3V_MICOM
KBC3_RST*
P5.0V_ALWS
P1.2V_ALWS
PRTC_BAT

KBC3_PWRSW*

KBC3_SUSPWON

P5V_AUX/P3.3V_AUX/P1.8V_AUX/1.2V_AUX

KBC3_RSMRST*

MEM1_VREF

KBC3_PWRBTN*

CHP3_SLPS5*

CHP3_SLPS3*

KBC3_PWRON

KBC3_CLKPWRGD#

P5.0V

PWRON

P1.8V/P1.5V/P0.9V/P1.2V

KBC3_RUNSCI#

KBC3_VRON

VCCP_CORE

VCCP3_PWRGD

CPU_CORE

VRM3_CPU_PWRGD

KBC3_NBPWRGD

KBC3_SBPWRGD

CPU1_PWRGDCPU

CHP3_ALINK_RST*

PCI3_RST*

CPU1_CPURST*

T1

T2=10mS

T3=40mS

T5=100mS

T16=170mS

T4=120mS

T6=3mS

T14=50mS

T15=30mS

T6=100mS

T8=50mS

T7=15mS

T9=110mS

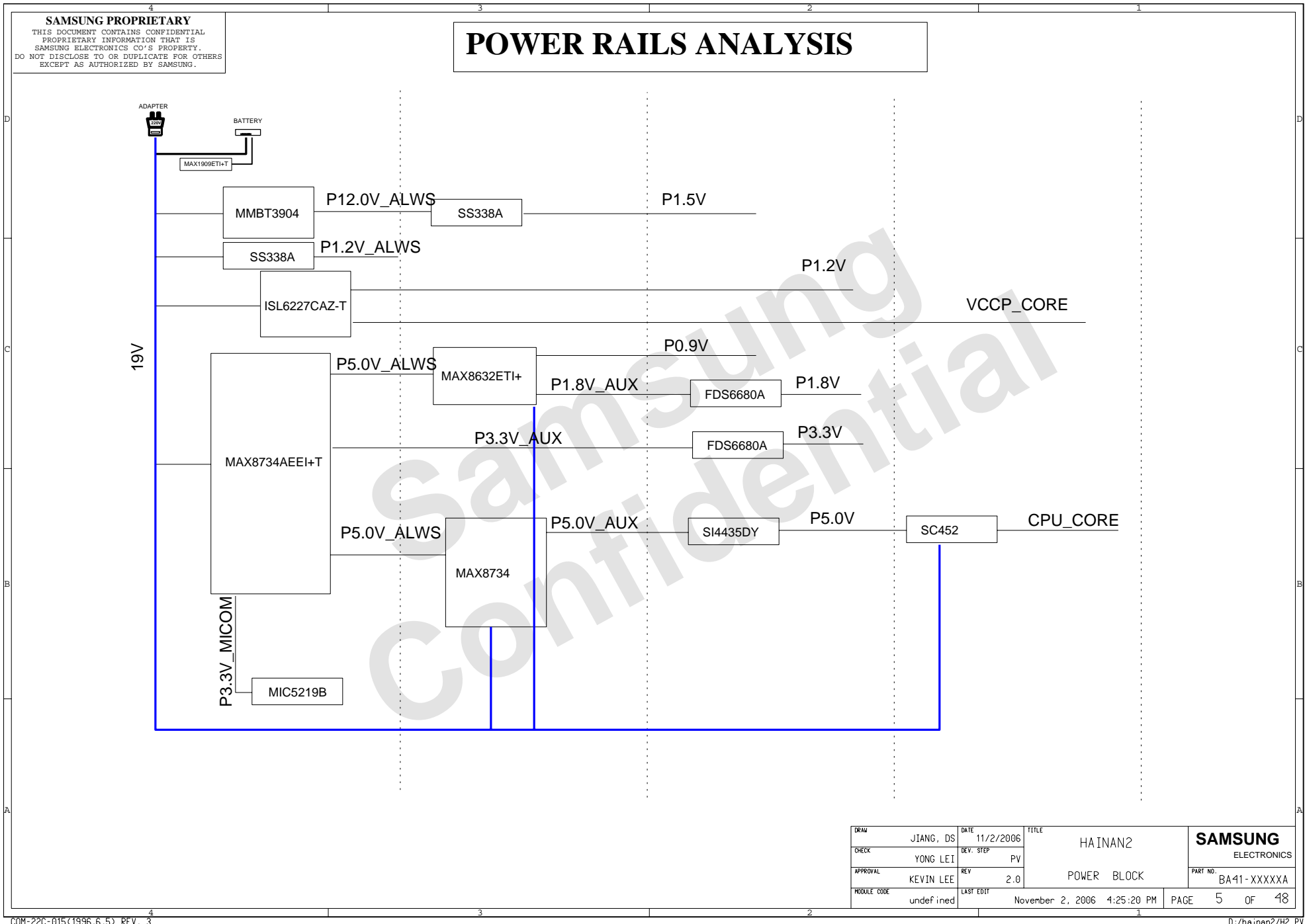
T10=4740mS

T11=80mS

T12=72mS

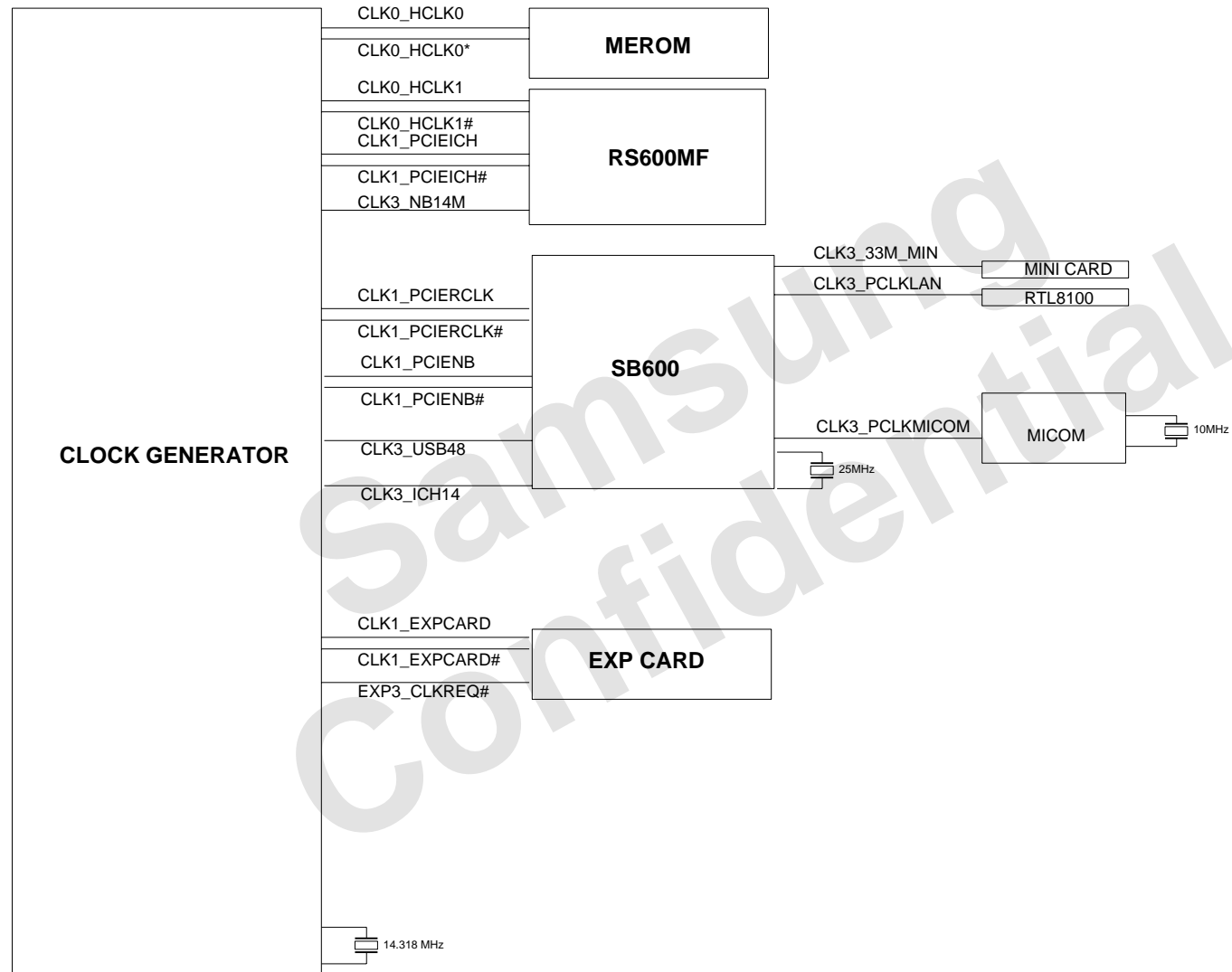
T13=73mS

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	POWER SEQUENCE		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	4 OF 48	



SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		CLOCK DIAGRAM	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	6	OF 48

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
LAN	AD21	1	G
USB	AD30(internal)	-	-
		-	-
Hub to PCI	AD31(internal)	-	-
LPC bridge/IDE/AC97/SMBUS	AD31(internal)	-	-
Internal MAC	AD31(internal)	-	-
AC Link	-	-	-

Voltage Rails

CPU_CORE VCCP_CORE P1.2V	CPU CPU RS600 SB600 RS600 SB600
P1.5V P1.8V P0.9V P5.0V P3.3V P2.5V_LAN	CPU EXPCARD RS600 DDR2 FAN THERMAL CRT SB600 AU6366 MINIPCI ALC262 HDD ODD MICOM USB TOUCH-PAD CPU_CORE SYSTEM POWER RTL8100
P1.8V_AUX P5.0V_AUX	DDR2 P1.8V RS600 THERMAL LCD P1.2V P5.0V
P3.3V_AUX	MDC MICOM MINIPCI EXP-CARD SB600 THERMAL
P12.0V_ALWS P5.0V_ALWS P3.3V_MICOM P1.2V_ALWS	P1.5V P3.3V P1.8V P3.3V DDR2-PWR SB600 MICOM LED P1.8V_ALWS SB600,P1.2V
VDC	Primary DC system power supply (7 to 21V)

2

I C / SMB Address

Devices	Address	Hex	Bus
SB600	Master	-	SMBUS Master
EMCN300(CPU Thermal Sensor)	1001 110X	9Ch	Thermal Sensor
SODIMM0	1010 0000	A0h	-
SODIMM1	1010 001X	A2h	-
ICS95411 (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable

USB PORT Assign

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1,2	SYSTEM PORT B
3	DMB
4	EXPRESS CARD
5	4 IN 1 CARD
6,8	MINI PCIE
7	BT

System Power States

- CHP3_SLPS1* S1, Powered-On-Suspend(POS) : In this state, all clocks(except the 32.768KHz clock) are stopped.
The system context is maintained in system DRAM. Power is maintained to PCI, the CPU, memory controller, memory, and all other critical subsystems.
Note that this state does not preclude power being removed from non-essential devices, such as disk drives. During this state, CPU can be selected for either Deep Sleep or Deeper Sleep.
- CHP3_SLPS3* S3, Suspend-To-RAM(STR) : The system context is maintained in system DRAM, but power is shut off to non-critical circuits.
Memory is retained, and refreshes continue. All clocks stop except RTC clock.
- CHP3_SLPS4* S4, Suspend-To-Disk(STD) : The Context of the system is maintained on the disk. All power is then shut off to the system except for the logic required to resume.
Externally appears same as S5, but may have different wake events.
- CHP3_SLPS5* S5, Soft Off(SOFF) : System context is not maintained. All power is shut off except for the logic required to restart. A full boot is required when waking.

Crystal / Oscillator

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	SB600	Real Time Clock
Crystal	10MHz	MICOM	HD64F2110B
Crystal	14.318MHz	CLOCK-Generator	ICS95461
Crystal	12MHz	AU6366	4 IN 1 CARD
Crystal	25MHz	LAN	LOM

CPU Core Voltage Table IMVP-6

Active Mode		Active/Deeper Sleep Dual Mode Region		Deeper Sleep/Extended Deeper Sleep Dual Mode Region	
VID(6:0)	Voltage	VID(6:0)	Voltage	VID(6:0)	Voltage
0 0 0 0 0 0 0 0	1.5000 V	0 1 0 1 0 0 0 0	1.0000 V	1 0 1 0 0 0 0 1	0.4875 V
0 0 0 0 0 0 0 1	1.4875 V	0 1 0 1 0 0 0 1	0.9875 V	1 0 1 0 0 0 1 0	0.4750 V
0 0 0 0 0 0 1 0	1.4750 V	0 1 0 1 0 0 1 0	0.9750 V	1 0 1 0 0 0 1 1	0.4625 V
0 0 0 0 0 0 1 1	1.4625 V	0 1 0 1 0 1 0 1	0.9625 V	1 0 1 0 0 1 0 0	0.4500 V
0 0 0 0 0 1 0 0	1.4500 V	0 1 0 1 0 1 0 0	0.9500 V	1 0 1 0 1 0 0 1	0.4375 V
0 0 0 0 0 1 0 1	1.4375 V	0 1 0 1 0 1 0 1	0.9375 V	1 0 1 0 1 0 1 0	0.4250 V
0 0 0 0 0 1 1 0	1.4250 V	0 1 0 1 1 0 1 0	0.9250 V	1 0 1 0 1 0 1 1	0.4125 V
0 0 0 0 0 1 1 1	1.4125 V	0 1 0 1 1 0 1 1	0.9125 V	1 0 1 0 1 0 0 0	0.4000 V
0 0 0 0 1 0 0 0	1.4000 V	0 1 1 0 0 0 0 0	0.9000 V	1 0 1 0 1 0 0 1	0.3875 V
0 0 0 0 1 0 0 1	1.3875 V	0 1 1 0 0 0 0 1	0.8875 V	1 0 1 0 1 0 1 0	0.3750 V
0 0 0 0 1 0 1 0	1.3750 V	0 1 1 0 0 0 1 0	0.8750 V	1 0 1 0 1 0 1 1	0.3625 V
0 0 0 0 1 0 1 1	1.3625 V	0 1 1 0 0 0 1 1	0.8625 V	1 0 1 0 1 1 0 0	0.3500 V
0 0 0 0 1 1 0 0	1.3500 V	0 1 1 0 0 1 0 0	0.8500 V	1 0 1 0 1 1 0 1	0.3375 V
0 0 0 0 1 1 0 1	1.3375 V	0 1 1 0 0 1 0 1	0.8375 V	1 0 1 0 1 1 1 0	0.3250 V
0 0 0 0 1 1 1 0	1.3250 V	0 1 1 0 0 1 1 0	0.8250 V	1 0 1 0 1 1 1 1	0.3125 V
0 0 0 0 1 1 1 1	1.3125 V	0 1 1 0 1 0 1 0	0.8125 V	1 1 0 0 0 0 0 0	0.3000 V
0 0 0 1 0 0 0 0	1.3000 V	0 1 1 0 1 0 0 0	0.8000 V	1 1 0 0 0 0 0 1	0.2875 V
0 0 0 1 0 0 0 1	1.2875 V	0 1 1 0 1 0 0 1	0.7875 V	1 1 0 0 0 0 1 0	0.2750 V
0 0 0 1 0 0 1 0	1.2750 V	0 1 1 0 1 0 1 0	0.7750 V	1 1 0 0 0 0 1 1	0.2625 V
0 0 0 1 0 0 1 1	1.2625 V	0 1 1 0 1 0 1 1	0.7625 V	1 1 0 0 0 1 0 0	0.2500 V
0 0 0 1 0 1 0 0	1.2500 V	0 1 1 0 1 1 0 0	0.7500 V	1 1 0 0 0 1 0 1	0.2375 V
0 0 0 1 0 1 0 1	1.2375 V	0 1 1 0 1 1 0 1	0.7375 V	1 1 0 0 0 1 1 0	0.2250 V
0 0 0 1 0 1 1 0	1.2250 V	0 1 1 0 1 1 1 0	0.7250 V	1 1 0 0 0 1 1 1	0.2125 V
0 0 0 1 0 1 1 1	1.2125 V	0 1 1 0 1 1 1 1	0.7125 V	1 1 0 0 1 0 0 0	0.2000 V
0 0 0 1 1 0 0 0	1.2000 V	1 0 0 0 0 0 0 0	0.7000 V	1 1 0 0 1 0 0 1	0.1875 V
0 0 0 1 1 0 0 1	1.1875 V	1 0 0 0 0 0 0 1	0.6875 V	1 1 0 0 1 0 1 0	0.1750 V
0 0 0 1 1 0 1 0	1.1750 V	1 0 0 0 0 0 1 0	0.6750 V	1 1 0 0 1 0 1 1	0.1625 V
0 0 0 1 1 0 1 1	1.1625 V	1 0 0 0 0 0 1 1	0.6625 V	1 1 0 0 1 1 0 0	0.1500 V
0 0 0 1 1 1 0 0	1.1500 V	1 0 0 0 0 1 0 0	0.6500 V	1 1 0 0 1 1 0 1	0.1375 V
0 0 0 1 1 1 0 1	1.1375 V	1 0 0 0 0 1 0 1	0.6375 V	1 1 0 0 1 1 1 0	0.1250 V
0 0 0 1 1 1 1 0	1.1250 V	1 0 0 0 0 1 1 0	0.6250 V	1 1 0 0 1 1 1 1	0.1125 V
0 0 0 1 1 1 1 1	1.1125 V	1 0 0 0 0 1 1 1	0.6125 V	1 1 0 0 1 0 0 0	0.1000 V
0 0 1 0 0 0 0 0	1.1000 V	1 0 0 0 1 0 0 0	0.6000 V	1 1 0 0 1 0 0 1	0.0875 V
0 0 1 0 0 0 0 1	1.0875 V	1 0 0 0 1 0 0 1	0.5875 V	1 1 0 0 1 0 1 0	0.0750 V
0 0 1 0 0 0 1 0	1.0750 V	1 0 0 0 1 0 1 0	0.5750 V	1 1 0 0 1 0 1 1	0.0625 V
0 0 1 0 0 0 1 1	1.0625 V	1 0 0 0 1 0 1 1	0.5625 V	1 1 0 0 1 1 0 0	0.0500 V
0 0 1 0 0 1 0 0	1.0500 V	1 0 0 0 1 1 0 0	0.5500 V	1 1 0 0 1 1 0 1	0.0375 V
0 0 1 0 0 1 0 1	1.0375 V	1 0 0 0 1 1 0 1	0.5375 V	1 1 0 0 1 1 1 0	0.0250 V
0 0 1 0 0 1 1 0	1.0250 V	1 0 0 0 1 1 1 0	0.5250 V	1 1 0 0 1 1 1 1	0.0125 V
0 0 1 0 0 1 1 1	1.0125 V	1 0 0 0 1 1 1 1	0.5125 V	1 1 0 0 1 1 0 0	0.0000 V
		1 0 0 0 1 0 0 0	0.5000 V	1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	0.0000 V
				1 1 0 0 1 1 0 0	0.0000 V
				1 1 0 0 1 1 0 1	0.0000 V
				1 1 0 0 1 1 1 0	0.0000 V
				1 1 0 0 1 1 1 1	0.0000 V
				1 1 0 0 1 0 0 0	0.0000 V
				1 1 0 0 1 0 0 1	0.0000 V
				1 1 0 0 1 0 1 0	0.0000 V
				1 1 0 0 1 0 1 1	

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

CPU Core Voltage Table IMVP-6

Active Mode		Active/Deeper Sleep		Deeper Sleep/Extended Deeper Sleep	
VID(6:0)	Voltage	VID(6:0)	Voltage	VID(6:0)	Voltage
0 0 0 0 0 0 0	1.5000 V	0 1 0 1 0 0 0	1.0000 V	1 0 1 0 0 0 1	0.4875 V
0 0 0 0 0 0 1	1.4875 V	0 1 0 1 0 0 1	0.9875 V	1 0 1 0 0 1 0	0.4750 V
0 0 0 0 0 1 0	1.4750 V	0 1 0 1 0 1 0	0.9750 V	1 0 1 0 1 0 1	0.4625 V
0 0 0 0 0 1 1	1.4625 V	0 1 0 1 1 0 1	0.9625 V	1 0 1 1 0 1 0	0.4500 V
0 0 0 0 1 0 0	1.4500 V	0 1 0 1 1 1 0	0.9500 V	1 0 1 1 1 0 1	0.4375 V
0 0 0 0 1 0 1	1.4375 V	0 1 0 1 1 1 1	0.9375 V	1 0 1 1 1 1 0	0.4250 V
0 0 0 0 1 1 0	1.4250 V	0 1 1 0 0 1 0	0.9250 V	1 0 1 1 1 1 1	0.4125 V
0 0 0 0 1 1 1	1.4125 V	0 1 1 0 1 1 1	0.9125 V	1 0 1 1 1 0 0	0.4000 V
0 0 0 1 0 0 0	1.4000 V	0 1 1 0 1 0 0	0.9000 V	1 0 1 1 1 0 1	0.3875 V
0 0 0 1 0 0 1	1.3875 V	0 1 1 0 1 0 1	0.8875 V	1 0 1 1 1 1 0	0.3750 V
0 0 0 1 0 1 0	1.3750 V	0 1 1 0 1 1 0	0.8750 V	1 0 1 1 1 1 1	0.3625 V
0 0 0 1 0 1 1	1.3625 V	0 1 1 1 0 1 1	0.8625 V	1 0 1 1 1 0 0	0.3500 V
0 0 0 1 1 0 0	1.3500 V	0 1 1 1 0 1 0	0.8500 V	1 0 1 1 1 1 0	0.3375 V
0 0 0 1 1 0 1	1.3375 V	0 1 1 1 0 1 1	0.8375 V	1 0 1 1 1 1 1	0.3250 V
0 0 0 1 1 1 0	1.3250 V	0 1 1 1 1 1 0	0.8250 V	1 0 1 1 1 1 1	0.3125 V
0 0 0 1 1 1 1	1.3125 V	0 1 1 1 1 1 1	0.8125 V	1 1 0 0 0 0 0	0.3000 V
0 0 1 0 0 0 0	1.3000 V	0 1 1 1 1 0 0	0.8000 V	1 1 0 0 0 0 1	0.2875 V
0 0 1 0 0 0 1	1.2875 V	0 1 1 1 1 0 1	0.7875 V	1 1 0 0 0 1 0	0.2750 V
0 0 1 0 0 1 0	1.2750 V	0 1 1 1 1 0 1	0.7750 V	1 1 0 0 0 1 1	0.2625 V
0 0 1 0 0 1 1	1.2625 V	0 1 1 1 1 1 0	0.7625 V	1 1 0 0 1 0 0	0.2500 V
0 0 1 0 1 0 0	1.2500 V	0 1 1 1 1 1 1	0.7500 V	1 1 0 1 0 1 0	0.2375 V
0 0 1 0 1 0 1	1.2375 V	0 1 1 1 1 1 1	0.7375 V	1 1 0 1 0 1 1	0.2250 V
0 0 1 0 1 1 0	1.2250 V	0 1 1 1 1 1 1	0.7250 V	1 1 0 1 1 0 1	0.2125 V
0 0 1 0 1 1 1	1.2125 V	0 1 1 1 1 1 1	0.7125 V	1 1 0 1 1 1 0	0.2000 V
0 0 1 1 0 0 0	1.2000 V	1 0 0 0 0 0 0	0.7000 V	1 1 0 1 1 1 1	0.1875 V
0 0 1 1 0 0 1	1.1875 V	1 0 0 0 0 0 1	0.6875 V	1 1 0 1 1 0 1	0.1750 V
0 0 1 1 0 1 0	1.1750 V	1 0 0 0 0 1 0	0.6750 V	1 1 0 1 1 0 1	0.1625 V
0 0 1 1 0 1 1	1.1625 V	1 0 0 0 1 0 1	0.6625 V	1 1 0 1 1 1 0	0.1500 V
0 0 1 1 1 0 0	1.1500 V	1 0 0 0 1 1 0	0.6500 V	1 1 0 1 1 1 1	0.1375 V
0 0 1 1 1 0 1	1.1375 V	1 0 0 0 1 1 1	0.6375 V	1 1 0 1 1 1 1	0.1250 V
0 0 1 1 1 1 0	1.1250 V	1 0 0 0 1 1 1	0.6250 V	1 1 1 0 0 1 1	0.1125 V
0 0 1 1 1 1 1	1.1125 V	1 0 0 0 1 1 1	0.6125 V	1 1 1 0 1 0 0	0.1000 V
0 1 0 0 0 0 0	1.1000 V	1 0 0 1 0 0 0	0.6000 V	1 1 1 0 1 0 1	0.0875 V
0 1 0 0 0 0 1	1.0875 V	1 0 0 1 0 0 1	0.5875 V	1 1 1 0 1 1 0	0.0750 V
0 1 0 0 0 1 0	1.0750 V	1 0 0 1 0 1 0	0.5750 V	1 1 1 0 1 1 1	0.0625 V
0 1 0 0 0 1 1	1.0625 V	1 0 0 1 1 0 1	0.5625 V	1 1 1 1 0 1 0	0.0500 V
0 1 0 0 1 0 0	1.0500 V	1 0 0 1 1 1 0	0.5500 V	1 1 1 1 0 1 1	0.0375 V
0 1 0 0 1 0 1	1.0375 V	1 0 0 1 1 1 1	0.5375 V	1 1 1 1 1 0 0	0.0250 V
0 1 0 0 1 1 0	1.0250 V	1 0 0 1 1 1 1	0.5250 V	1 1 1 1 1 0 1	0.0125 V
0 1 0 0 1 1 1	1.0125 V	1 0 0 1 1 1 1	0.5125 V	1 1 1 1 1 1 0	0.0000 V
		1 0 0 1 1 1 1	0.5000 V	1 1 1 1 1 1 1	0.0000 V
Active		Deeper Slp			
DPRSLPVR	0	DPRSLPVR	1		
DPRSTP*	1	DPRSTP*	0		
PSI2*	0 or 1	PSI2*	0 or 1		
				**11111111*: 0V power good asserted.	

*Yonah Processor (2.33 GHz / 800 MHz : TBD)

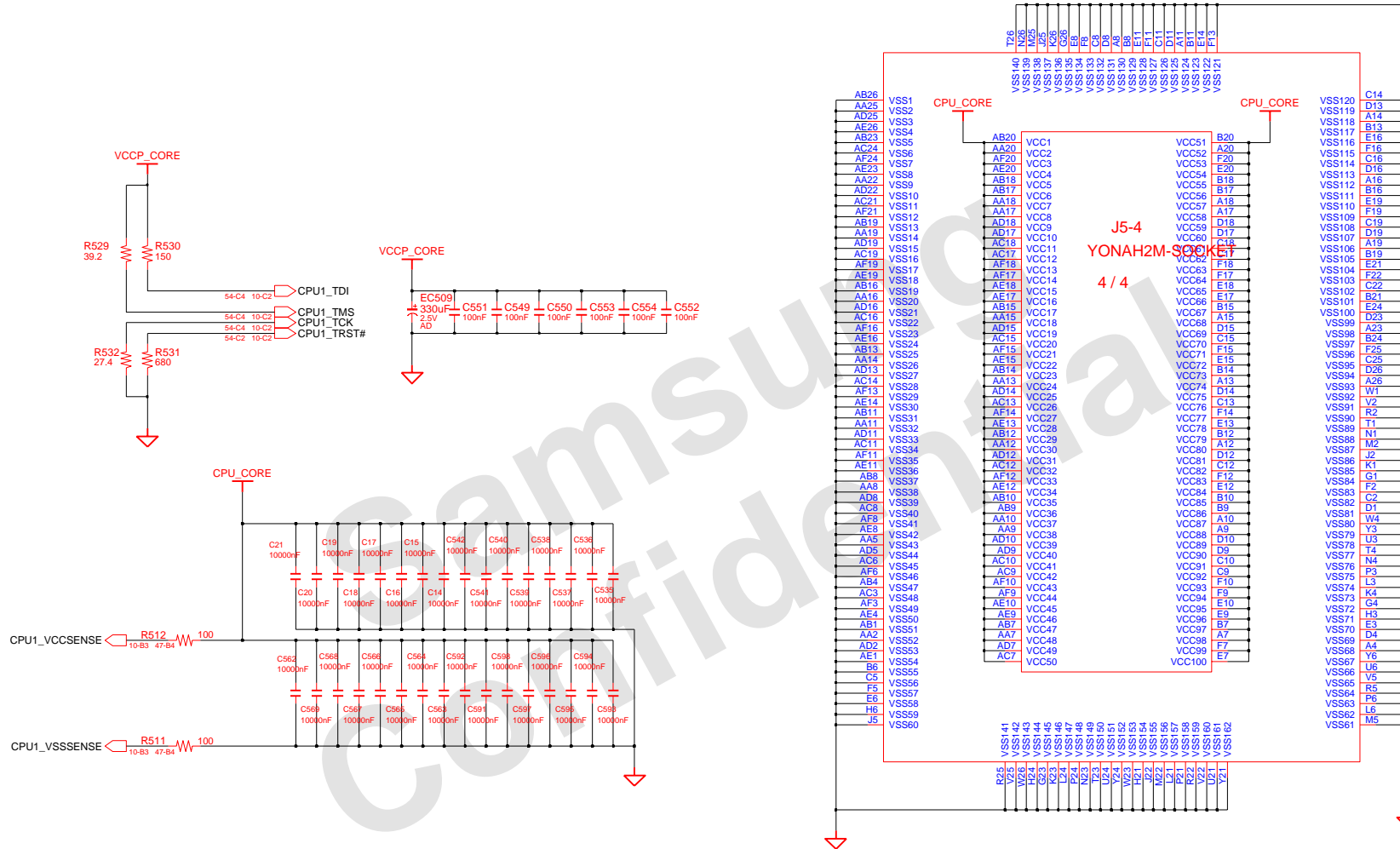
GTLREF : Keep the Voltage divider within 0.5"
of the first GTLREF0 pin with Zo=55ohm trace.
Minimize coupling of any switching signals to this net.

COMP0,2(COMP1,3) should be connected with Zo=27.4ohm(55ohm)
trace shorter than 1/2" to their respective Banias socket pins.

The CPU auto select FSB.
Here just need pull up.

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		MEROM(2/3)	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	10 OF 48	

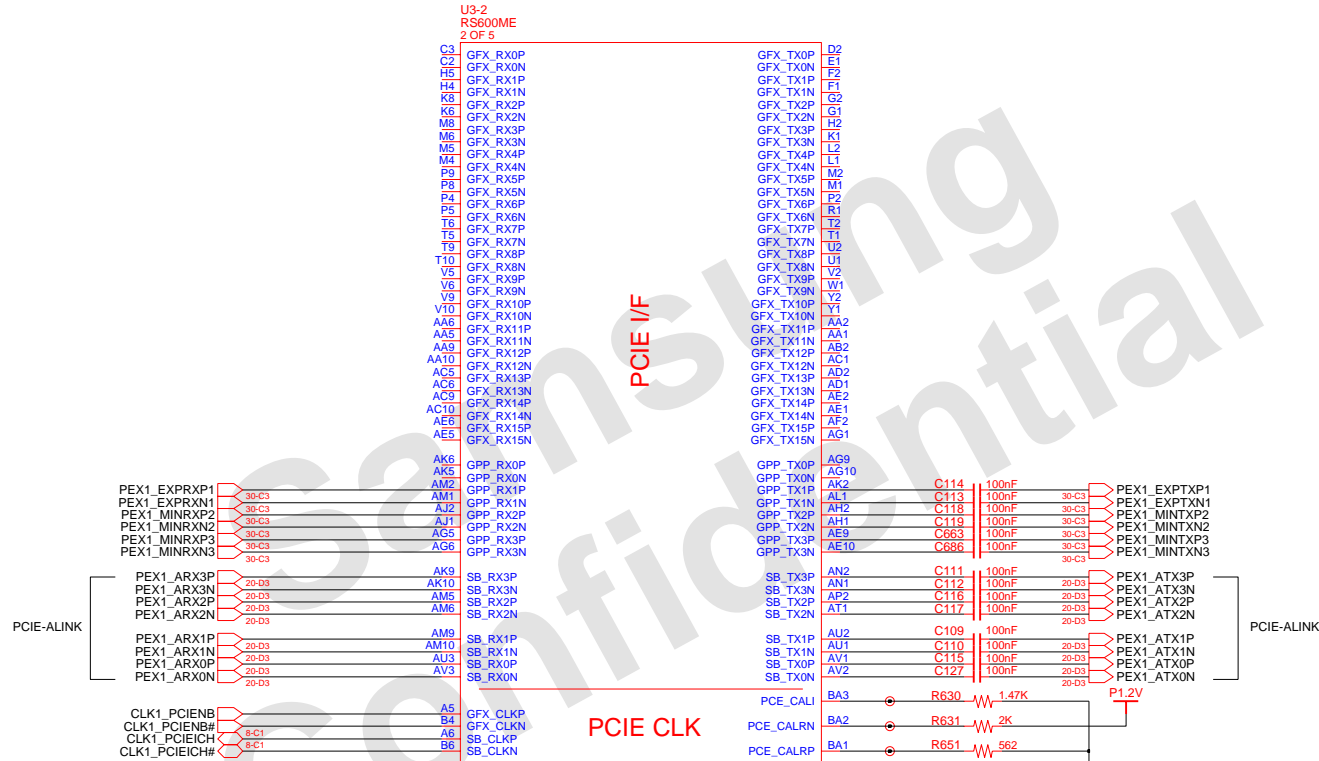
SAMSUNG PROPRIETARY
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



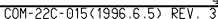
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	MEROM(3/3)		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM			
PAGE	11	OF	48			PART NO. BA41-XXXXXA

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



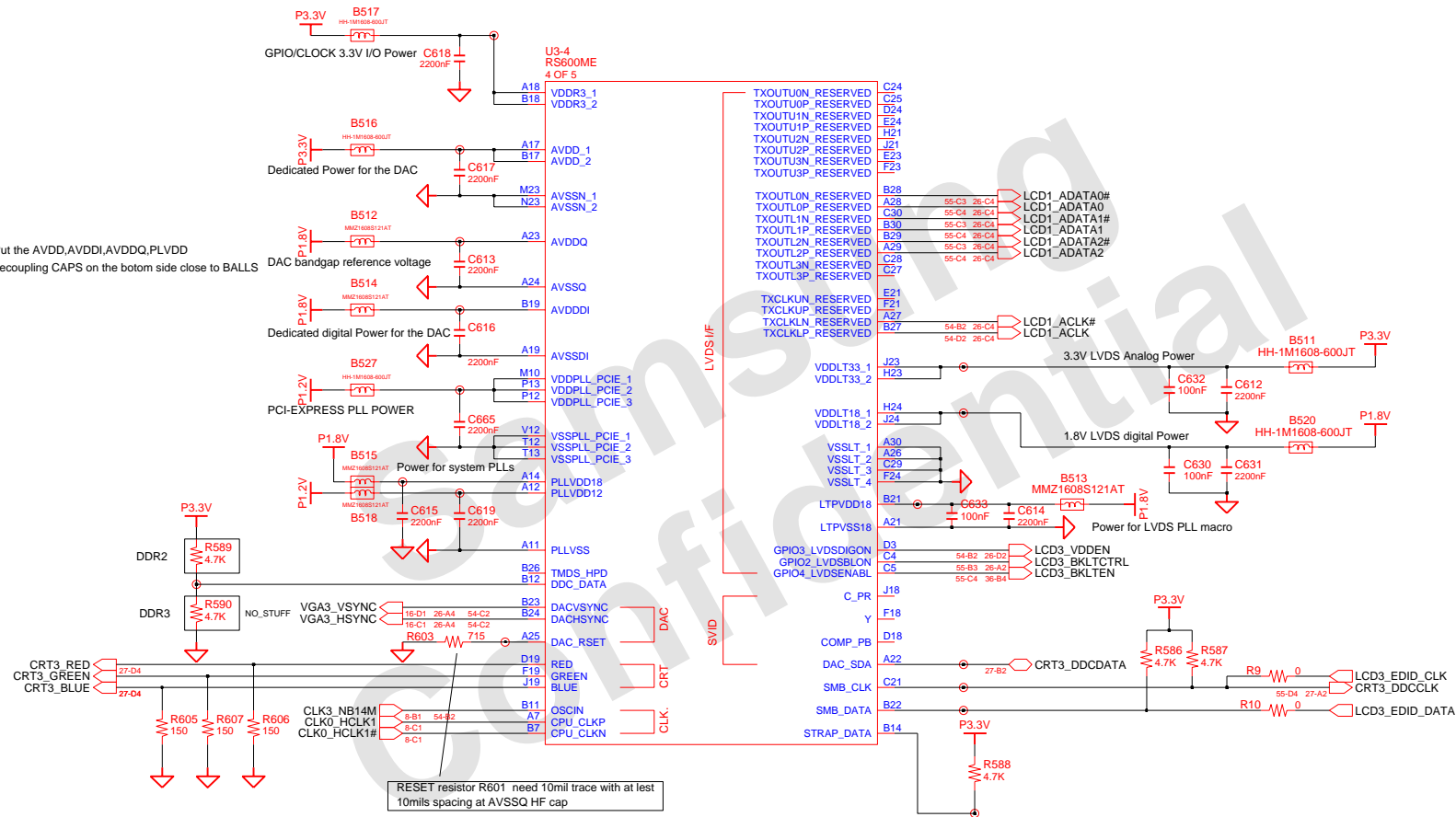
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV		HAINAN2	
APPROVAL	KEVIN LEE	REV	2.0		RS600 (2/5)	
MODULE CODE		LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	14 OF 48	

D:/heinen2/H2_P

SAMSUNG PROPRIETARY

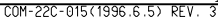
THIS DOCUMENT CONTAINS CONFIDENTIAL
 PROPRIETARY INFORMATION THAT IS
 SAMSUNG ELECTRONICS CO.'S PROPERTY.
 DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
 EXCEPT AS AUTHORIZED BY SAMSUNG.

Put the AVDD,AVDDI,AVDDQ,PLVDD
 decoupling CAPS on the botom side close to BALLS



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV		HAINAN2	
APPROVAL	KEVIN LEE	REV	2.0		RS600 (4/5)	
MODULE CODE		LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	16	OF 48

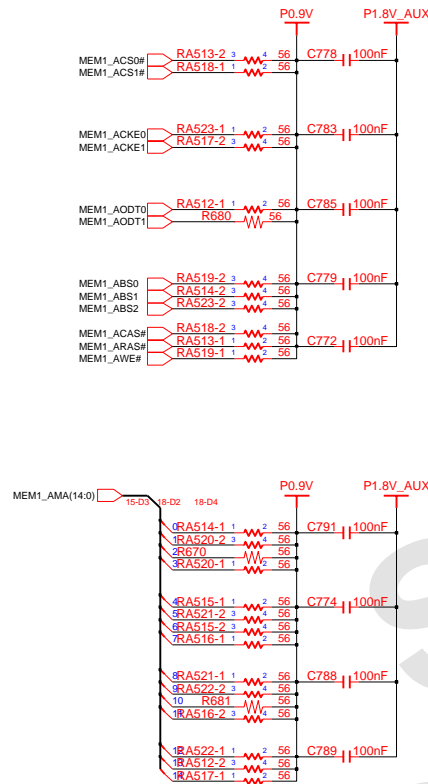




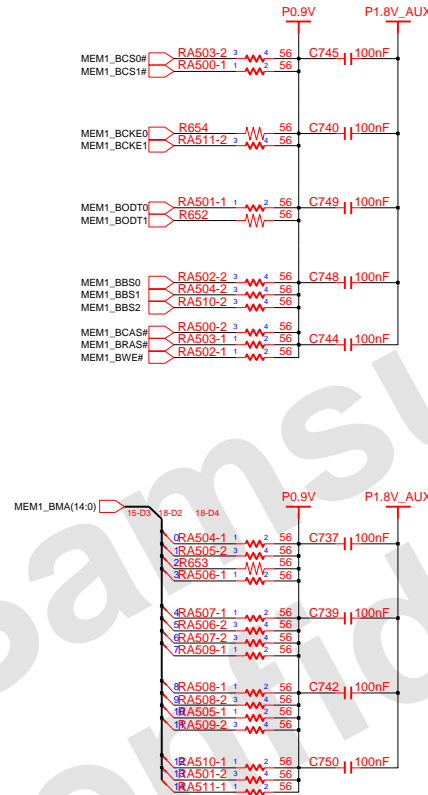
SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

Channel 1

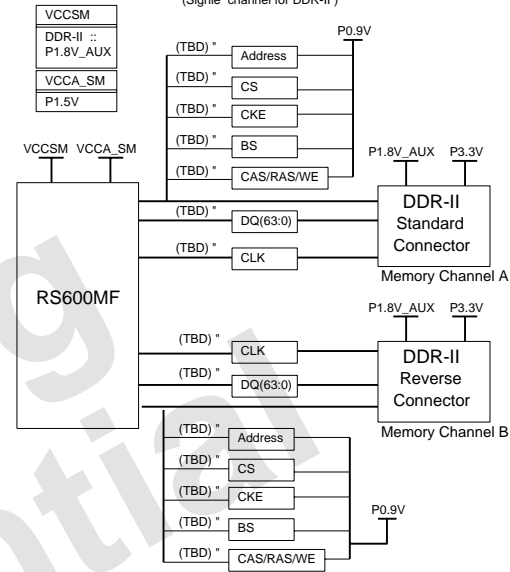


Channel 2

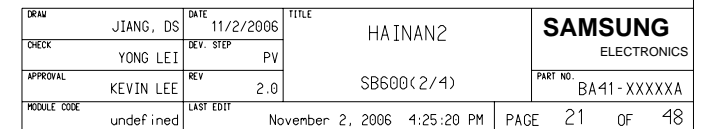


Memory Topology

(Single channel for DDR-II)



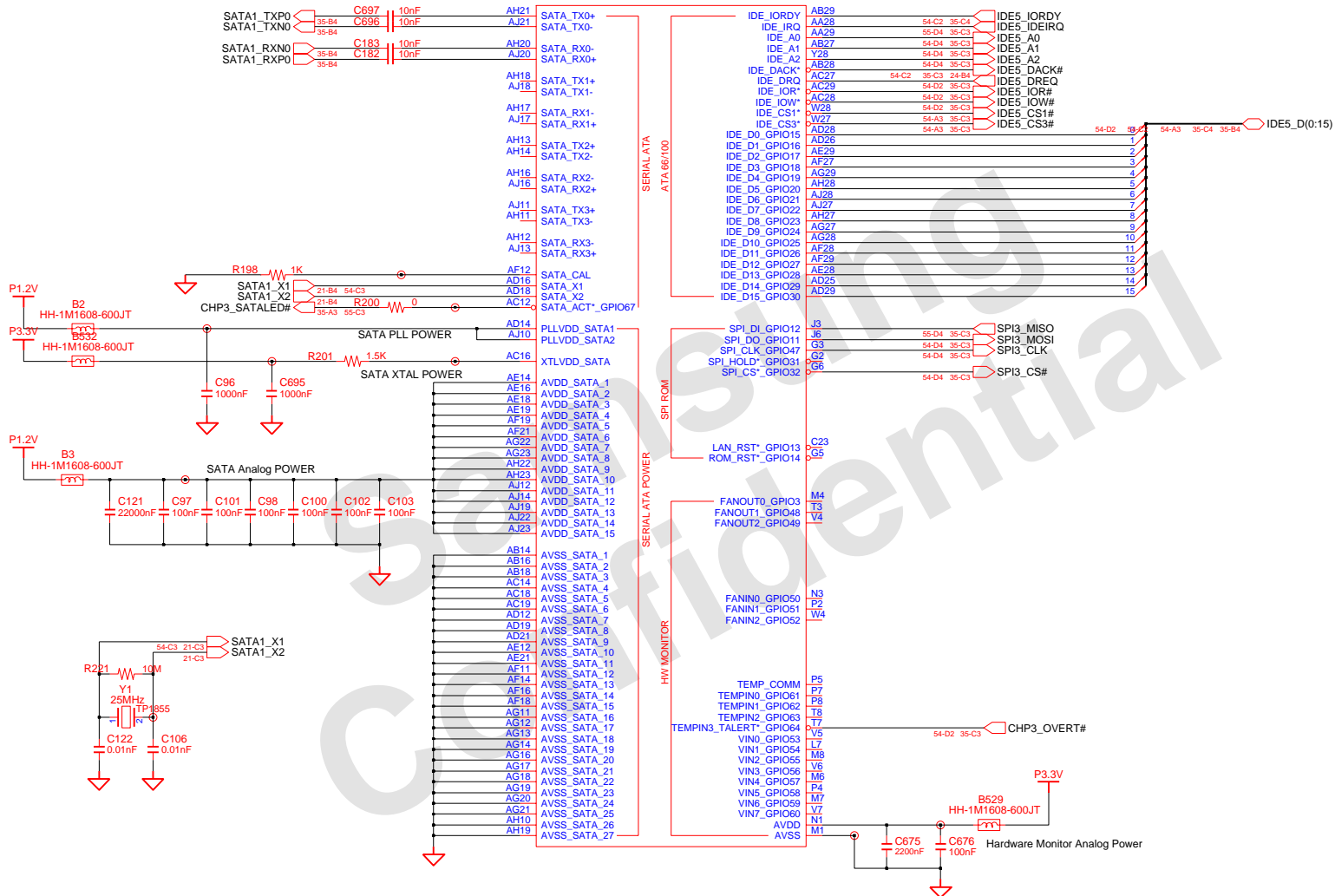
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	DDR2 TERMINATION		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	19	OF 48



SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

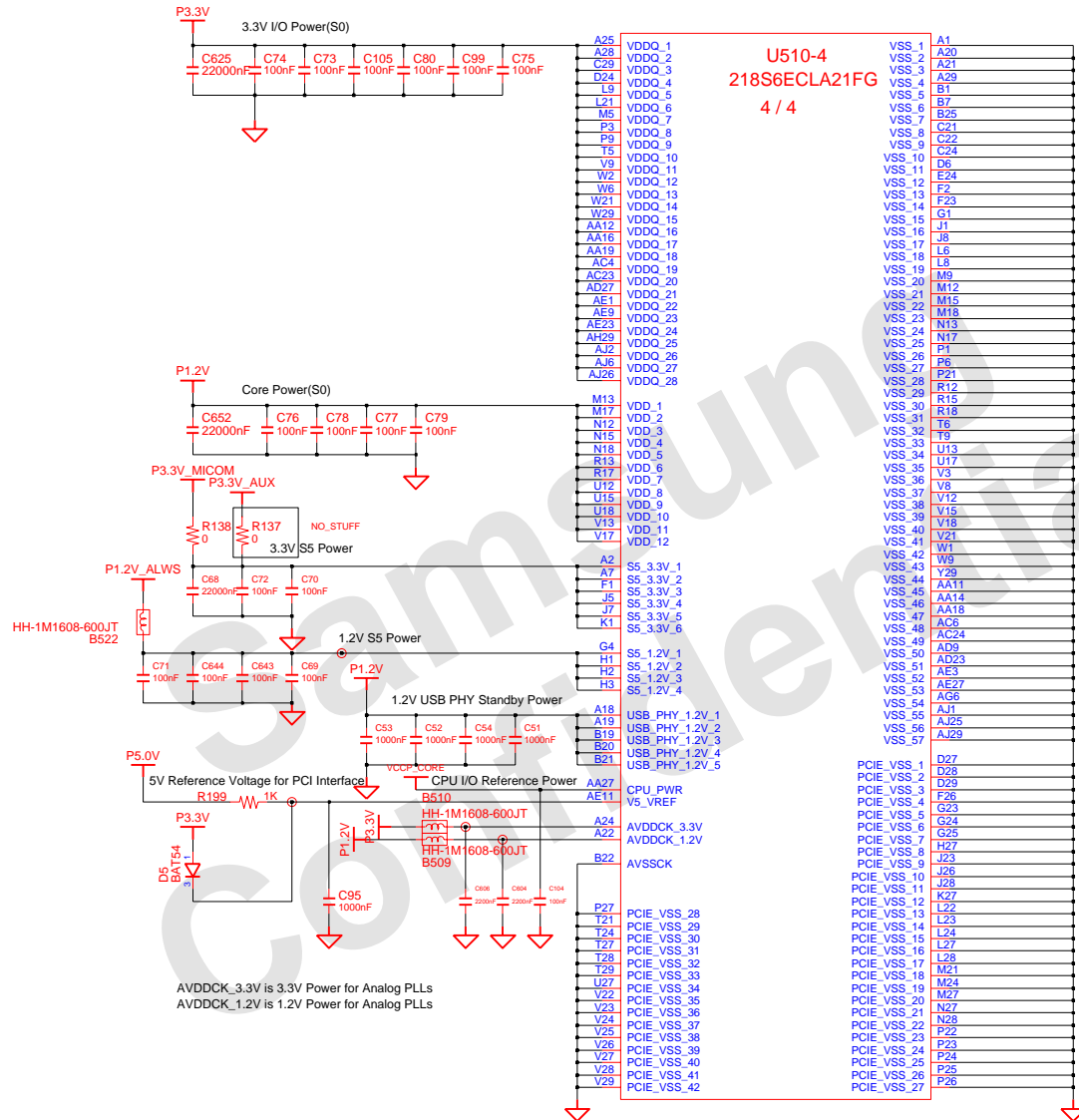
U510-3
218S6ECLA21FG
3 / 4



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		SB600(3/4)	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	22	OF 48

SAMSUNG PROPRIETARY

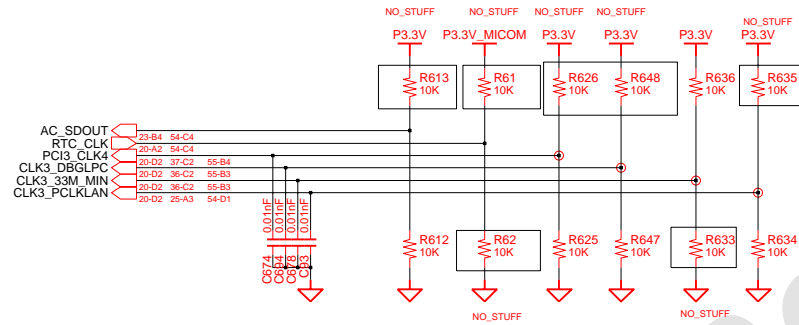
THIS DOCUMENT CONTAINS CONFIDENTIAL
 PROPRIETARY INFORMATION THAT IS
 SAMSUNG ELECTRONICS CO.'S PROPERTY.
 DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
 EXCEPT AS AUTHORIZED BY SAMSUNG.



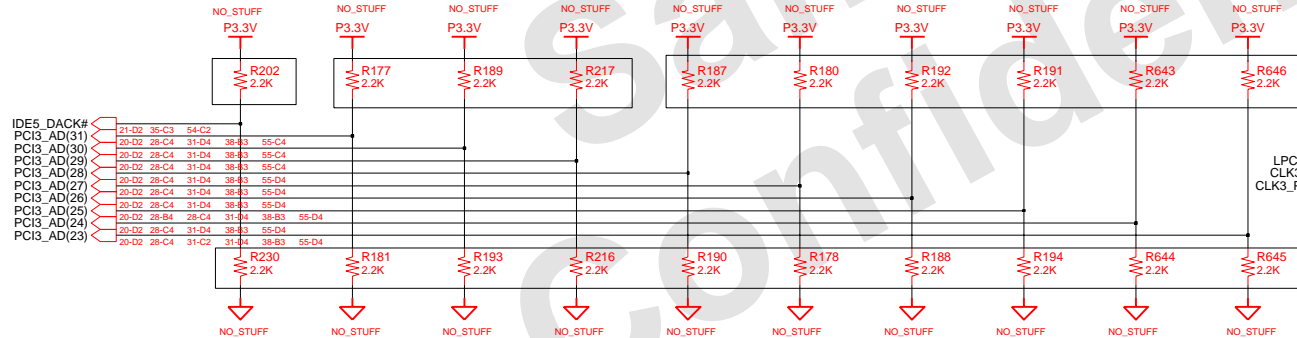
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	SB600(4/4)	PART NO.	BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	23	OF 48

SAMSUNG PROPRIETARY

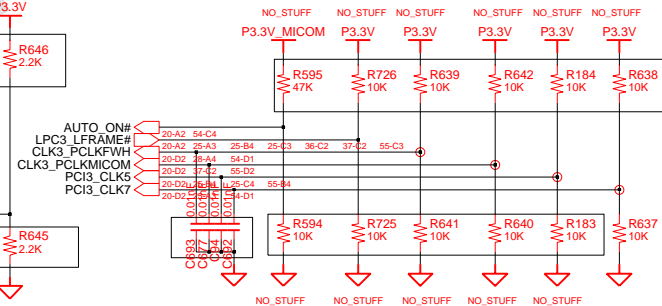
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

REQUIRED SYSTEM STRAPS


	AC_SDOUT	RTC_CLK	PCI3_CLK4	CLK3_DBG LPC	CLK3_33M_MIN, CLK3_PCLKLAN
STRAP HIGH	USE DEBUG STRAPS	INTERNAL RTC DEFAULT	USE INT PLL 48	CPU I/F = K8	ROM TYPE H.H = PCI ROM H.L = SPI ROM L.H = LPC ROM L.L = FWH ROM
STRAP LOW	IGNORE DEBUG STRAPS DEFAULT	EXRERNAL RTC	USE EXT 48MHZ DEFAULT	CPU I/F = P4	DEFAULT

DEBUG STRAPS


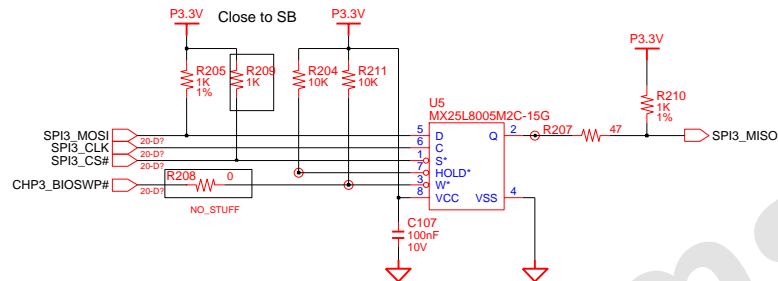
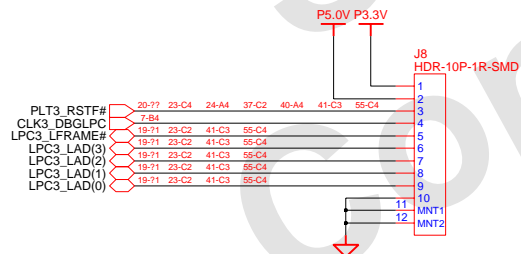
	IDE5_DACK*	PCI3_AD(31)	PCI3_AD(30)	PCI3_AD(29)	PCI3_AD(28)	PCI3_AD(27)	PCI3_AD(26)	PCI3_AD(25)	PCI3_AD(24)	PCI3_AD(23)
STRAP HIGH	RESERVED	RESERVED	RESERVED	RESERVED	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	BOOTF ALL TIMER DISABLED DEFAULT
STRAP LOW					USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	BYPASS EEPROM PCIE STRAPS	BOOTF ALL TIMER ENABLED

RESERVED PORT DEBUG


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	SB600 STRAPS		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	24 OF 48	

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

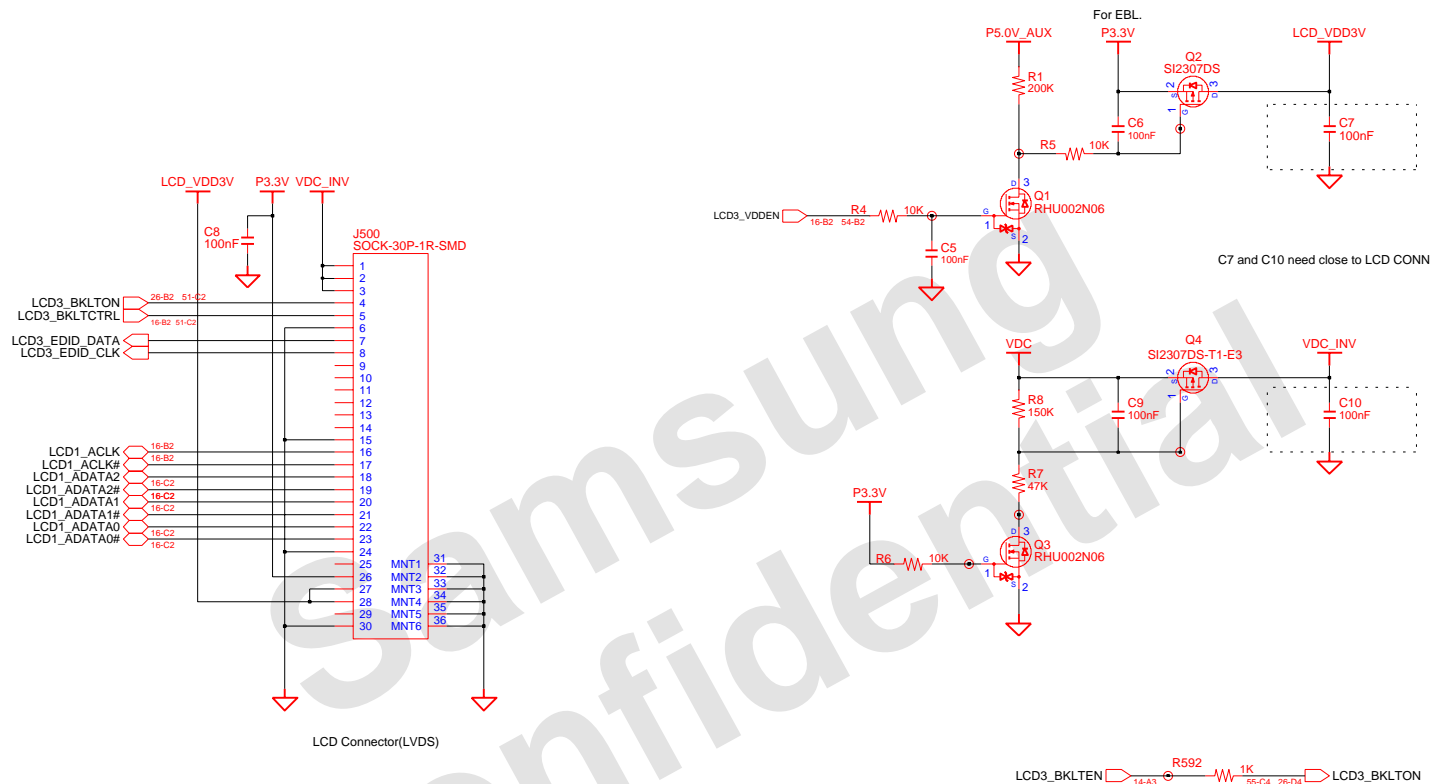
**DEBUG CARD CONN**

- | | | | |
|----|---|----|---------------------------------|
| 02 | VERIFY REAL MODE | 66 | CONFIGURE ADVANCE CACHE REG. |
| 03 | DISABLE NMI | 6A | DISPLAY EXTERNAL CACHE SIZE |
| 04 | GET CPU TYPE | 6C | DISPLAY SHADOW MESSAGE |
| 06 | INIT. SYSTEM H/W | 6E | DISPLAY NON-DISPOSABLE SEGMENT |
| 08 | INIT. CHIPSET REG. | 70 | DISPLAY ERROR MESSAGE |
| 09 | SET IN POST FLAG | 72 | CHECK FOR CONFIGURATION ERROR |
| 0A | INIT CPU.REG | 74 | TEST REAL-TIME CLOCK |
| 0B | CPU CACHE ON | 76 | CHECK FOR KEYBOARD ERROR |
| 0C | INIT.CACHE TO POST | 7C | SETUP HARDWARE INTERRUPT VECTOR |
| 0E | INIT. I/O VALUE | 7E | TEST COPROCESSOR IF PRESENT |
| 0F | ENABLE THE L-BUS IDE | 80 | DISABLE ON-BOARD I/O PORT |
| 10 | INIT. POWER MANAGER | 82 | DETECT AND INSTALL EXT.RS232C |
| 11 | LOAD ALTERNATE REG. | 84 | DETECT AND INSTALL EXT.PARALLEL |
| 13 | PCI BUS MASTER RESET
WITH INITIAL POST VALUE | 86 | RE-INIT. ON-BOARD I/O PORT |
| 14 | INIT. KEYBOARD CONTROLLER | 88 | INIT. BIOS DATA ROM |
| 16 | CHECK CHECKSUM | 8A | INIT.EXTENDED BIOS DATA AREA |
| 18 | 8254 TIMER INIT. | 8C | INIT. FDD CONTROLLER |
| 1A | 8237 DMA CONTROLLER INIT. | 9A | SHADOW OPTION ROMS |
| 1C | RESET INTERRUPT CONTROLLER | 9C | SETUP POWER MANAGEMENT |
| 20 | TEST DRAM REFRESH | 9E | ENABLE H/W INTERRUPT |
| 22 | TEST 8742 KEYBOARD CONTROLLER | A0 | SET TIME OF DAY |
| 24 | SET ES SEGMENT REG. TO 4GB | A4 | INIT. TYPEMATIC RATE |
| 26 | ENABLE A20 | A8 | ERASE F2 PROMPT |
| 28 | AUTO SIZING DRAM | AA | SCAN FOR F2 KEY STROKE |
| 32 | COMPUTE THE CPU SPEED | AC | ENTER SETUP |
| 34 | TESET CMOS RAM | AE | CLEAR IN POST FLAG |
| 38 | SHADOW SYSTEM BIOS ROM | B0 | CHECK FOR ERRORS |
| 3A | AUTO SIZING CACHE | B2 | POST DONE-PREPARE TO BOOT O/S |
| 3C | CONFIGURE ADVANCED CHIPSET REG. | B4 | ONE BEEP |
| 3D | LOAD ALTER REG. WITH CMOS VALUE | B6 | CHECK PASSWORD (OPTION) |
| 42 | INIT. INTERRUPT VECTOR | B7 | ACPI INIT |
| 44 | INIT. BIOS INTERRUPT | BA | DMI INIT |
| 46 | CHECK ROM COPYRIGHT NOTICE | BE | CLEAR SCREEN |
| 47 | INIT. I20 SUPPORT IF INSTALLED | C0 | TRY BOOT WITH INT19 |
| 48 | CHECK VIDEO CONFIGURE AGAINST CMOS | D0 | INTERRUPT HANDLER ERROR |
| 49 | INIT. PCI BUS AND DEVICE | D2 | UNKNOWN INTERRUPT ERROR |
| 4A | INIT. ALL VIDEO BIOS ROM | D4 | PENDING INTERRUPT ERROR |
| 4C | SHADOW VIDEO BIOS ROM | D6 | SHUTDOWN 5 |
| 50 | DISPLAY CPU TYPE AND SPEED | D8 | SHUTDOWN ERROR |
| 52 | TEST KEYBOARD | DA | EXTENDED BLOCK MOVE |
| 54 | SET KEYCLICK IF ENABLED | DC | SHUTDOWN 10 |
| 56 | ENABLE KEYBOARD | 89 | ENABLE NMI |
| 58 | TEST FOR UNEXPECTED INTERRUPTS | 90 | INIT. HDD CONTROLLER |
| 5A | DISPLAY "PRESS SETUP" | 91 | INIT. LOCAL BUS HDD CONTROLLER |
| 5C | TEST RAM BETWEEN 512K AND 640K | 92 | JUMP TO USER PATCH 2 |
| 60 | TEST EXTENDED MEMORY | 94 | DISABLE A20 ADDRESS LINE |
| 62 | TEST EXTENDED MEMORY ADDRESS LINE | 96 | CLEAR HUGE ES SEGMENT REG. |
| 64 | JUMP TO USER PATCH 1 | 98 | SEARCH FOR OPTION ROMS |

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	FWH		PART NO.
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM			BA41-XXXXXA
				PAGE	25	OF 48

SAMSUNG PROPRIETARY

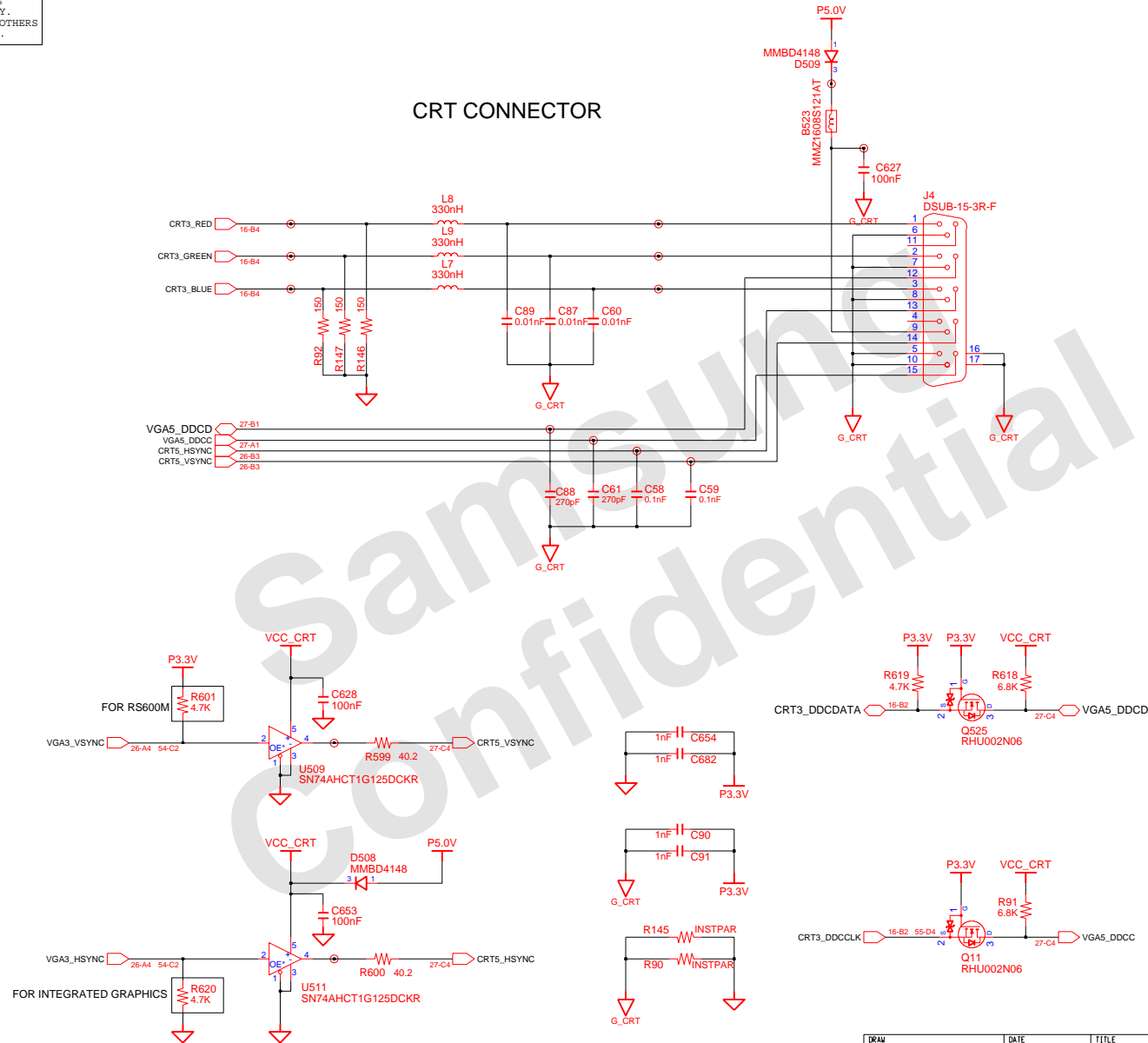
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		LCD	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	26	OF 48

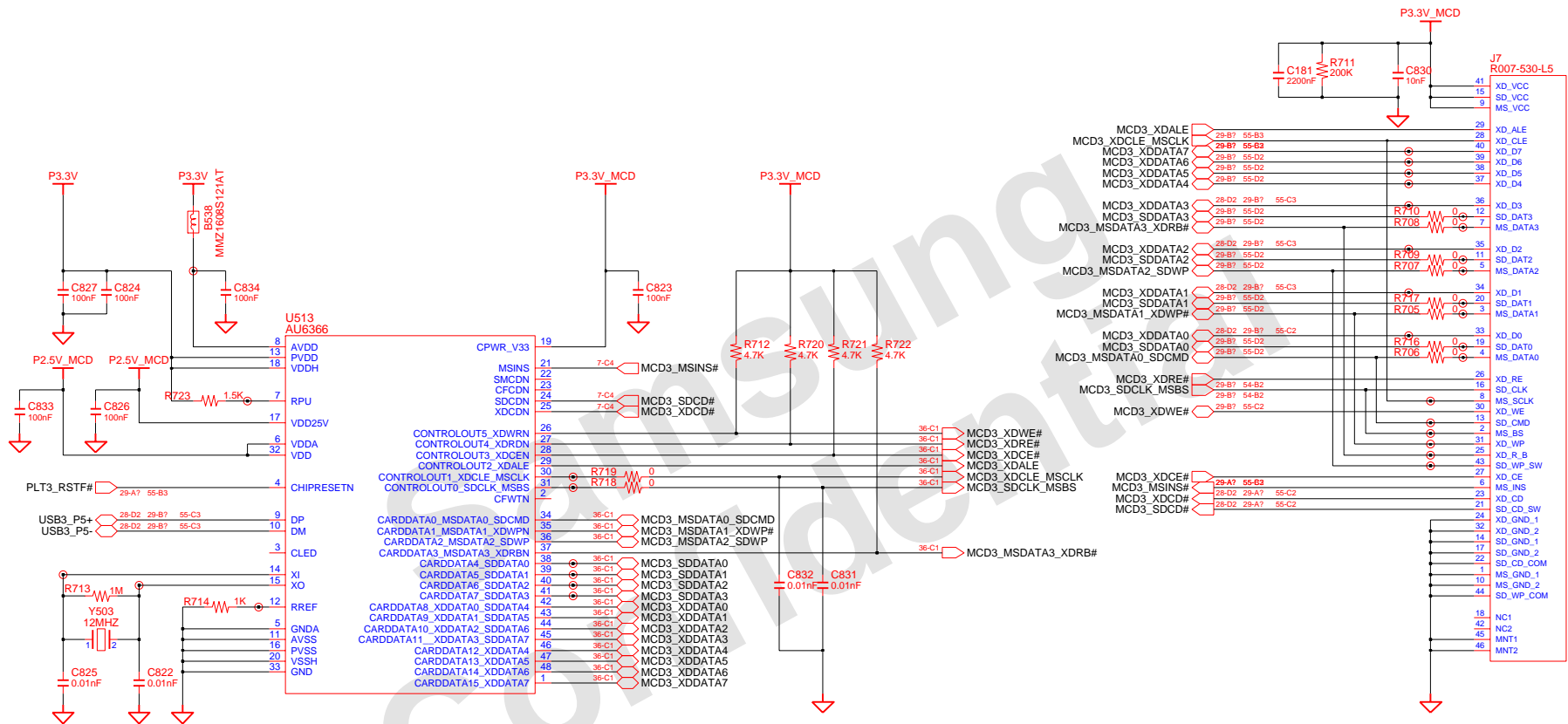
SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

CRT CONNECTOR


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		CRT	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	27	OF 48

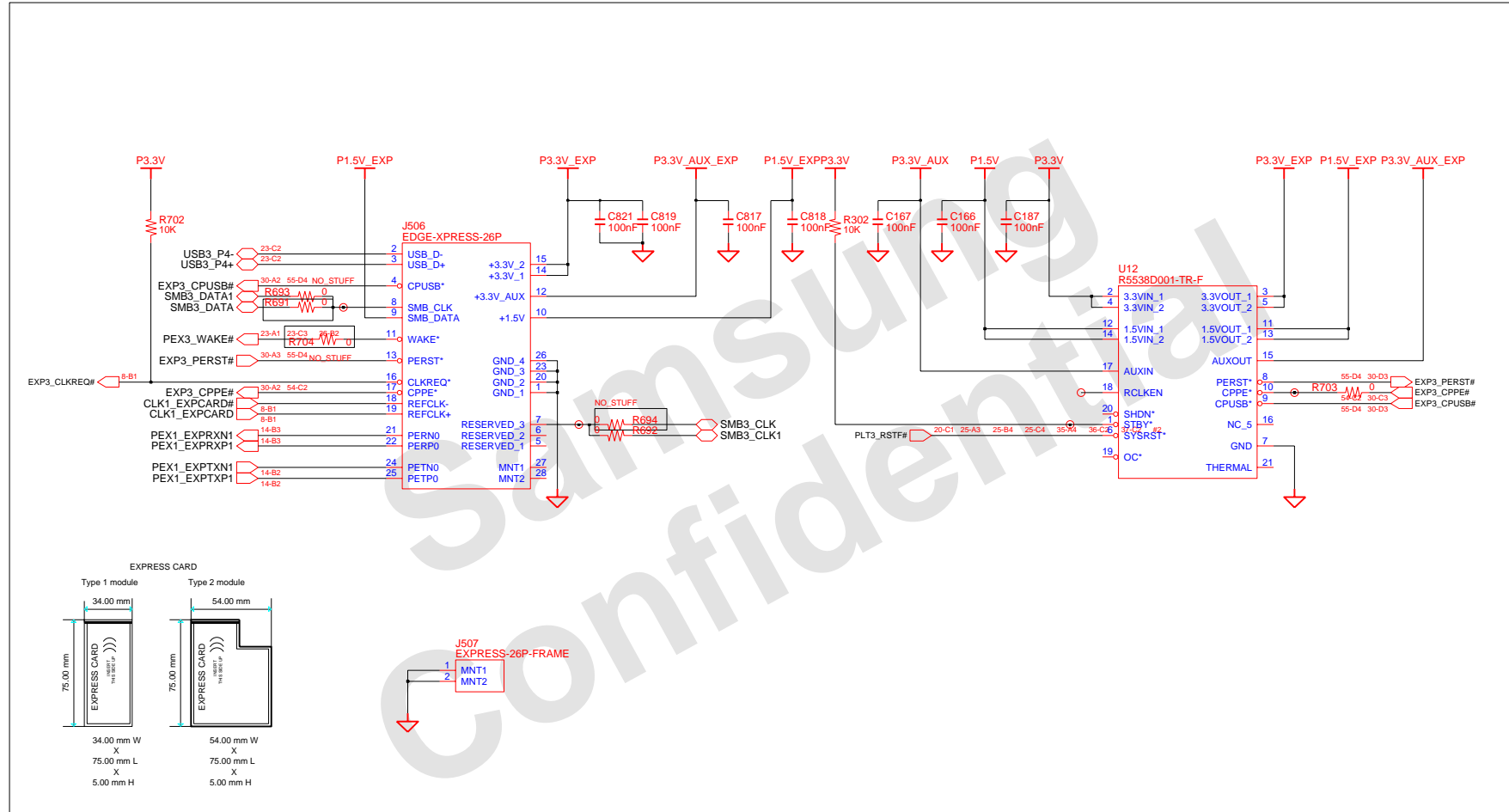
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



DRAM	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2 SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV		
APPROVAL	KEVIN LEE	REV	2.0	4 IN 1 CARD	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE 28 OF 48	

SAMSUNG PROPRIETARY

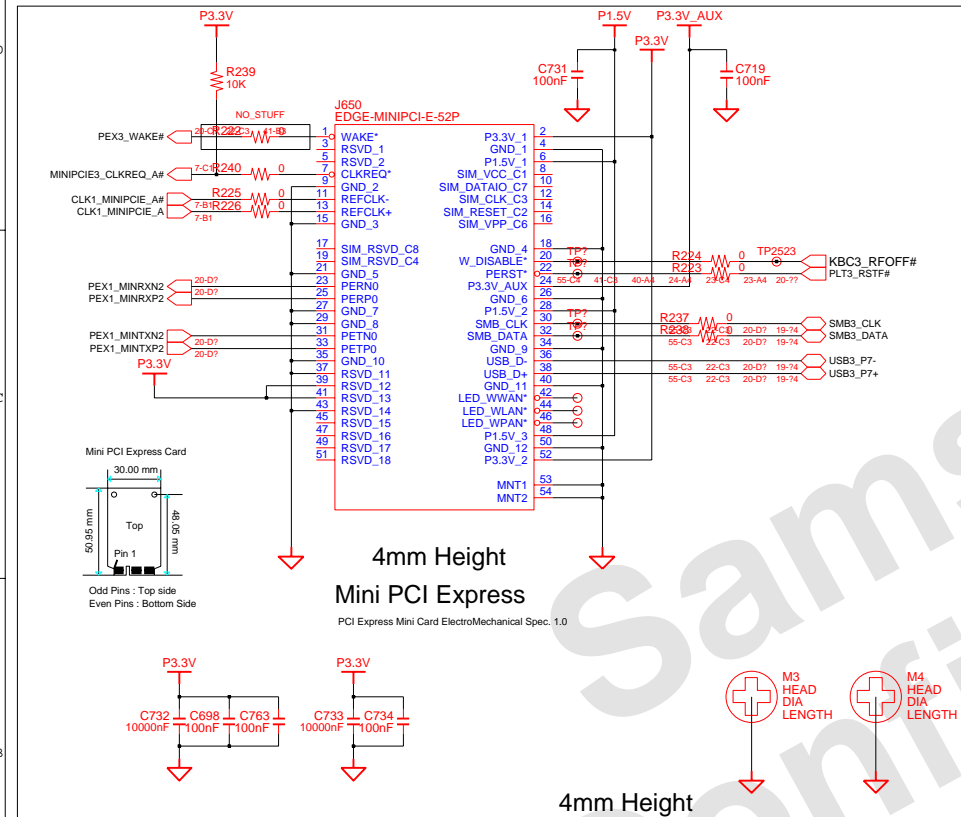
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

EXPCARD


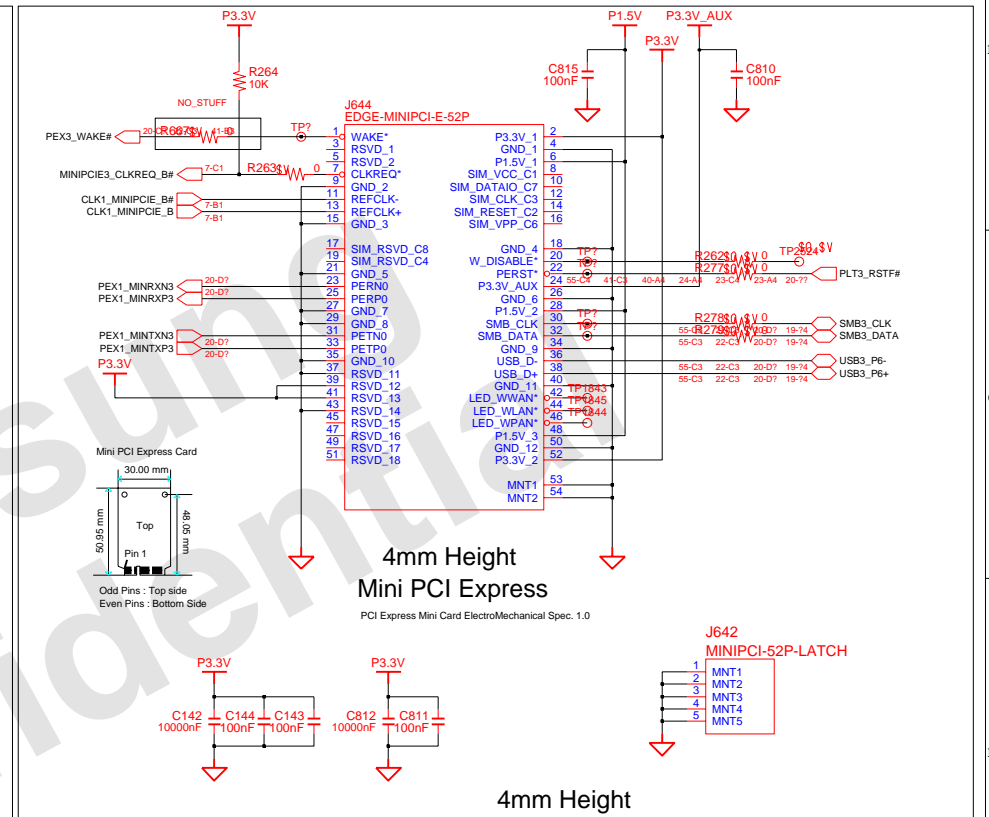
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	EXPRESS CARD		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	29	OF 48

SAMSUNG PROPRIETARY
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

MINICARD

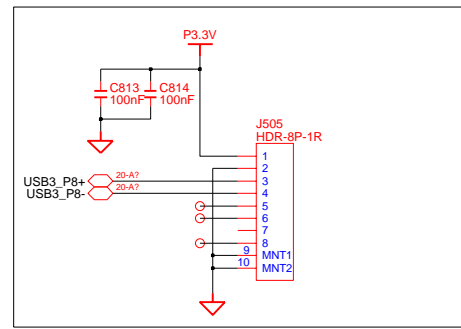
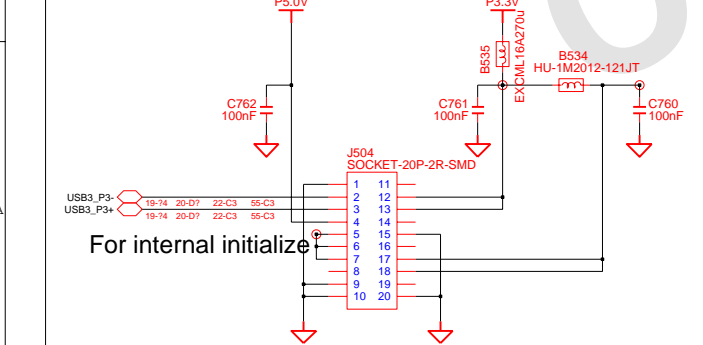


DVB-T



DMB

Bluetooth Interface

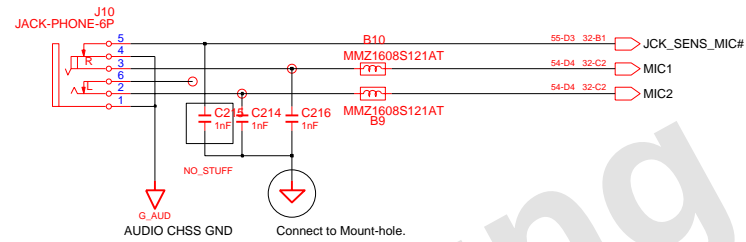


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2 HAINAN2 MINI CARD	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			PART NO. BA41-XXXXXA
APPROVAL	KEVIN LEE	REV	2.0			
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	30	OF 48

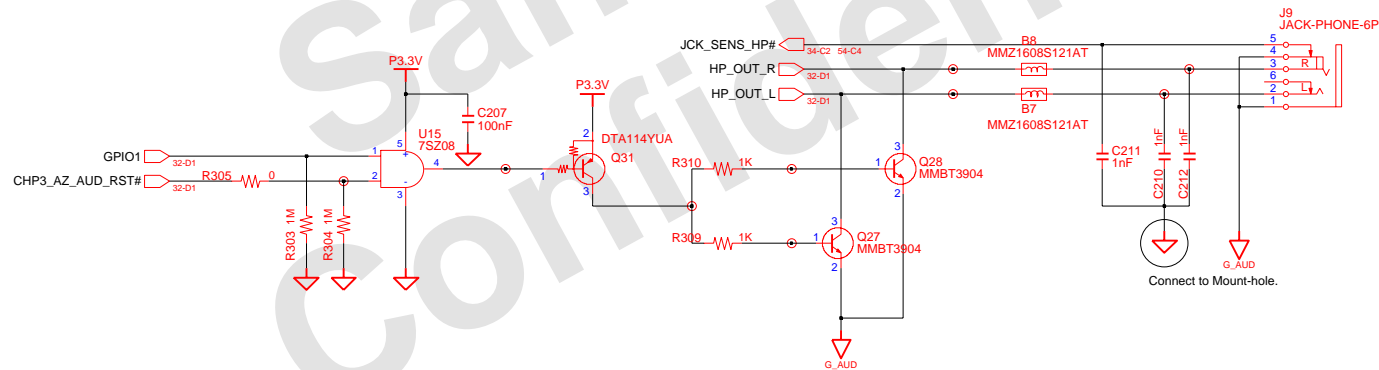
SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

MIC JACK



HEADPHONE

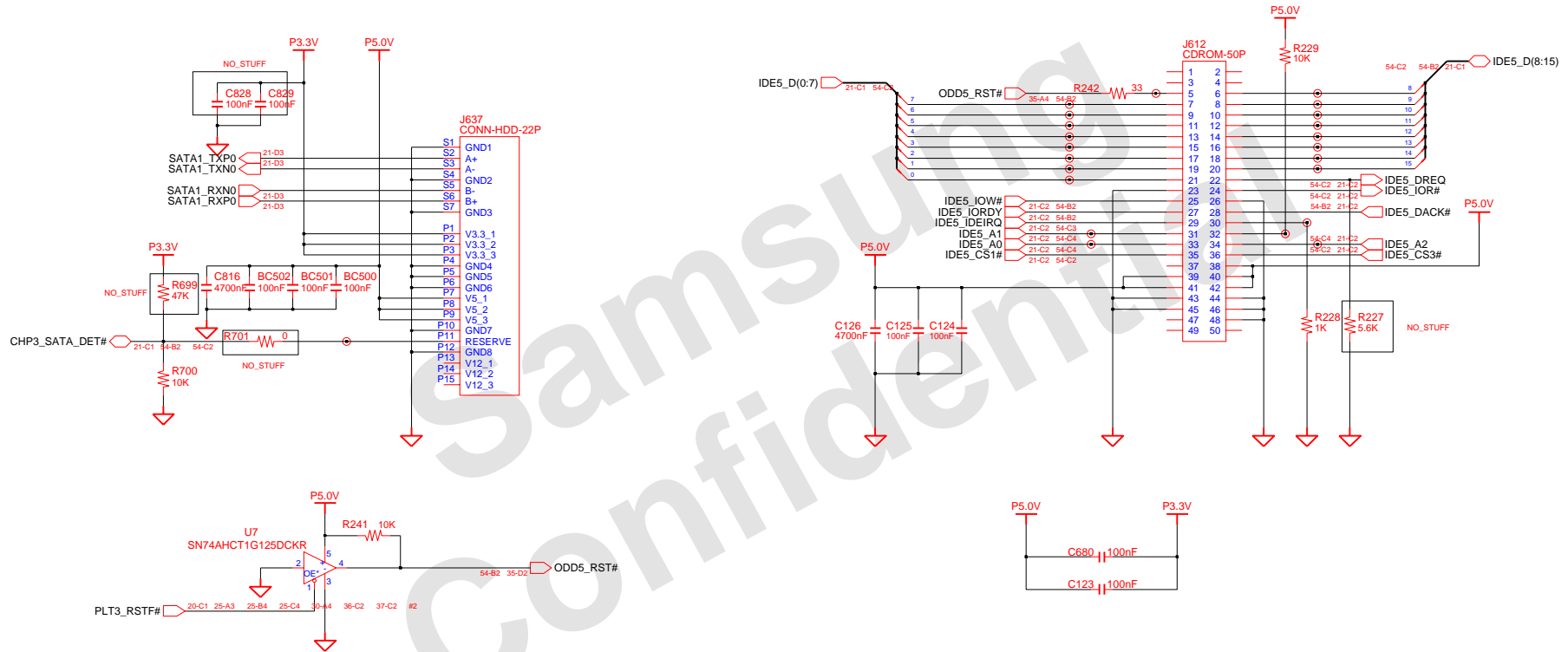


The traces led to Audio Jacks have the width over 10mil

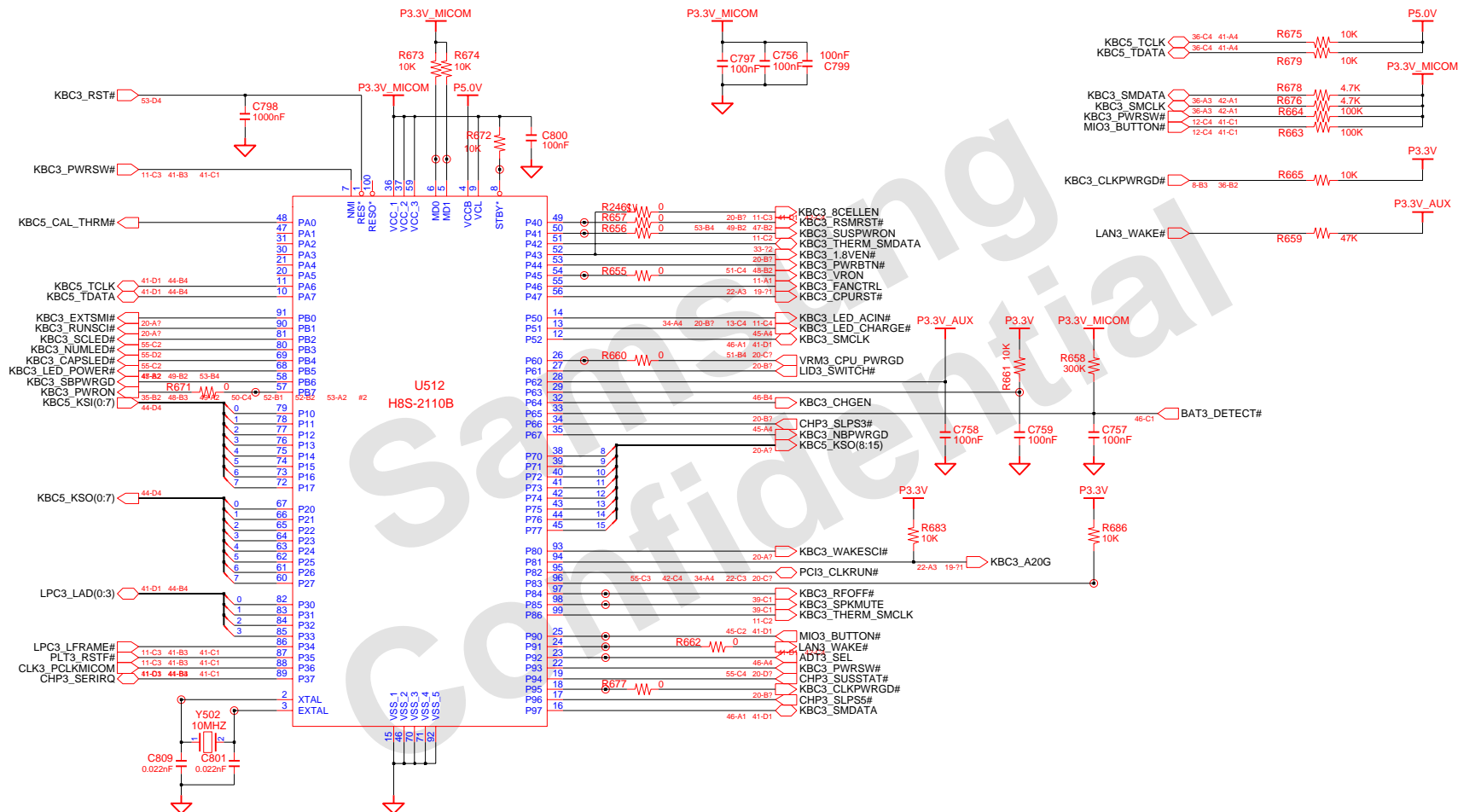
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		AUDIO<3/3>	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	33	OF 48

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
 PROPRIETARY INFORMATION THAT IS
 SAMSUNG ELECTRONICS CO.'S PROPERTY.
 DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
 EXCEPT AS AUTHORIZED BY SAMSUNG.

Main to HDD
Main to ODD


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	HDD & ODD		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	34	OF 48



TP FOR UPDATE MICOM.
P84=>TXD;P85=>RXD
MD0 and MD1 =>GND
P90,P91,P92=>VCC

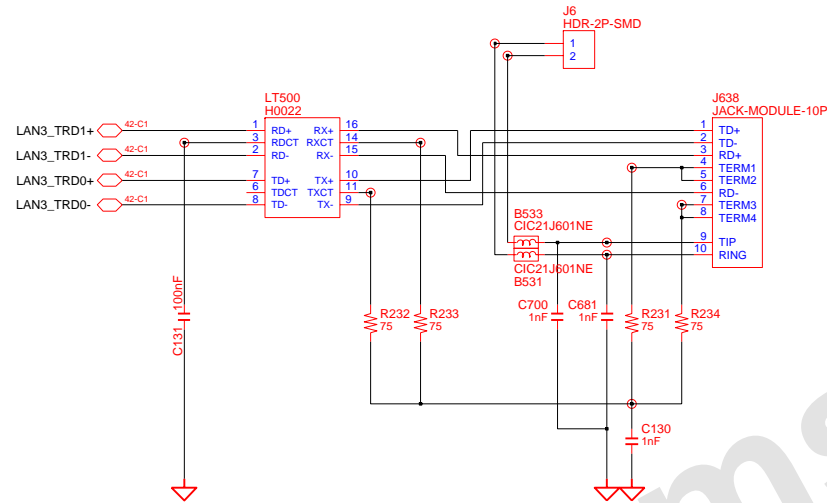
DRAM	JIANG, DS	DATE	11/2/2006	TITLE		SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV	HAINAN2		
APPROVAL	KEVIN LEE	REV	2.0	MICOM		
MODULE CODE		LAST EDIT		PART NO.		
undefined		November 2, 2006 4:25:20 PM		PAGE 35 OF 48		BA41-XXXXXA

DRAM	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2		SAMSUNG ELECTRONICS	
CHECK	YONG LEI	DEV. STEP	PV					
APPROVAL	KEVIN LEE	REV	2.0	LAN			PART NO.	BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT		November 2, 2006	4:25:20 PM	PAGE	36	OF 48

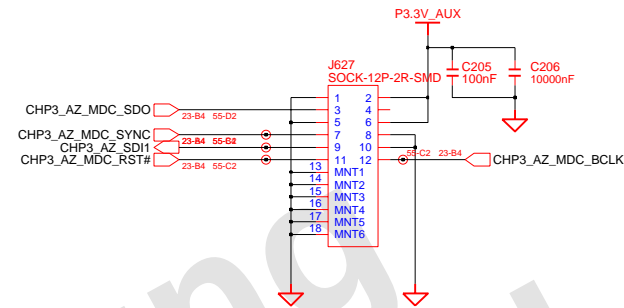
SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

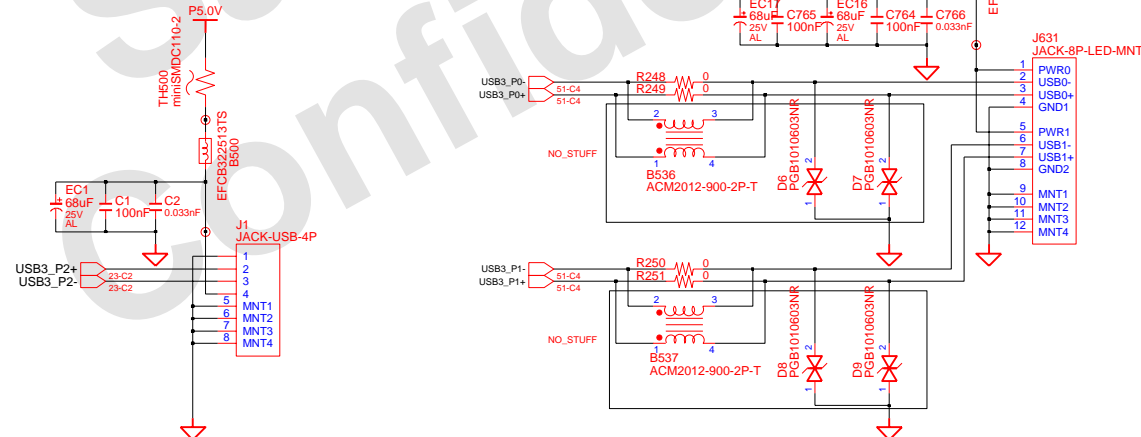
LAN Connector



MDC Connector



USB Connector

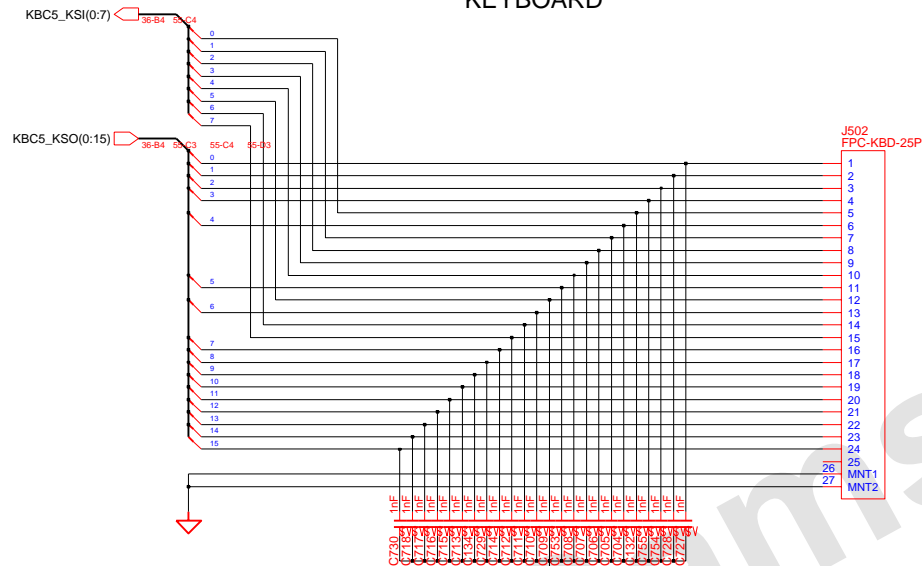


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		LAN & MDC CONN	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	37 OF 48	

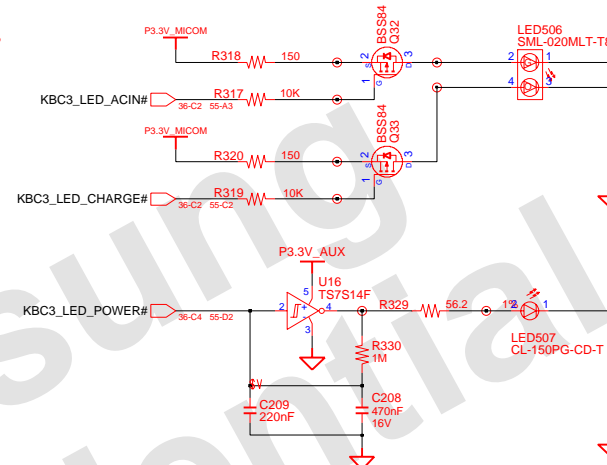
SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

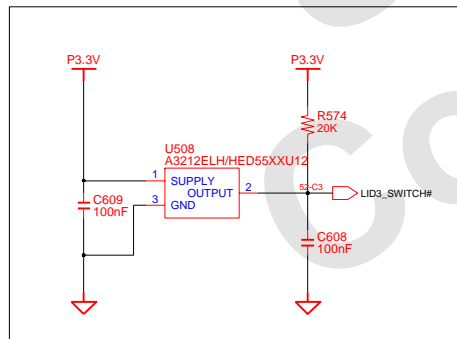
KEYBOARD



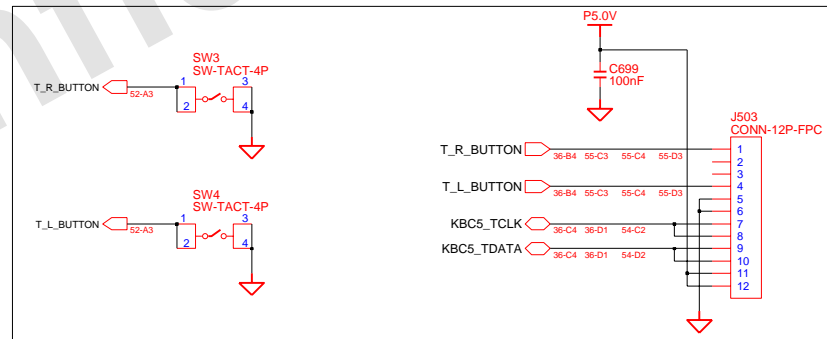
ADAPTERIN/CHARGING LED



LID SWITCH

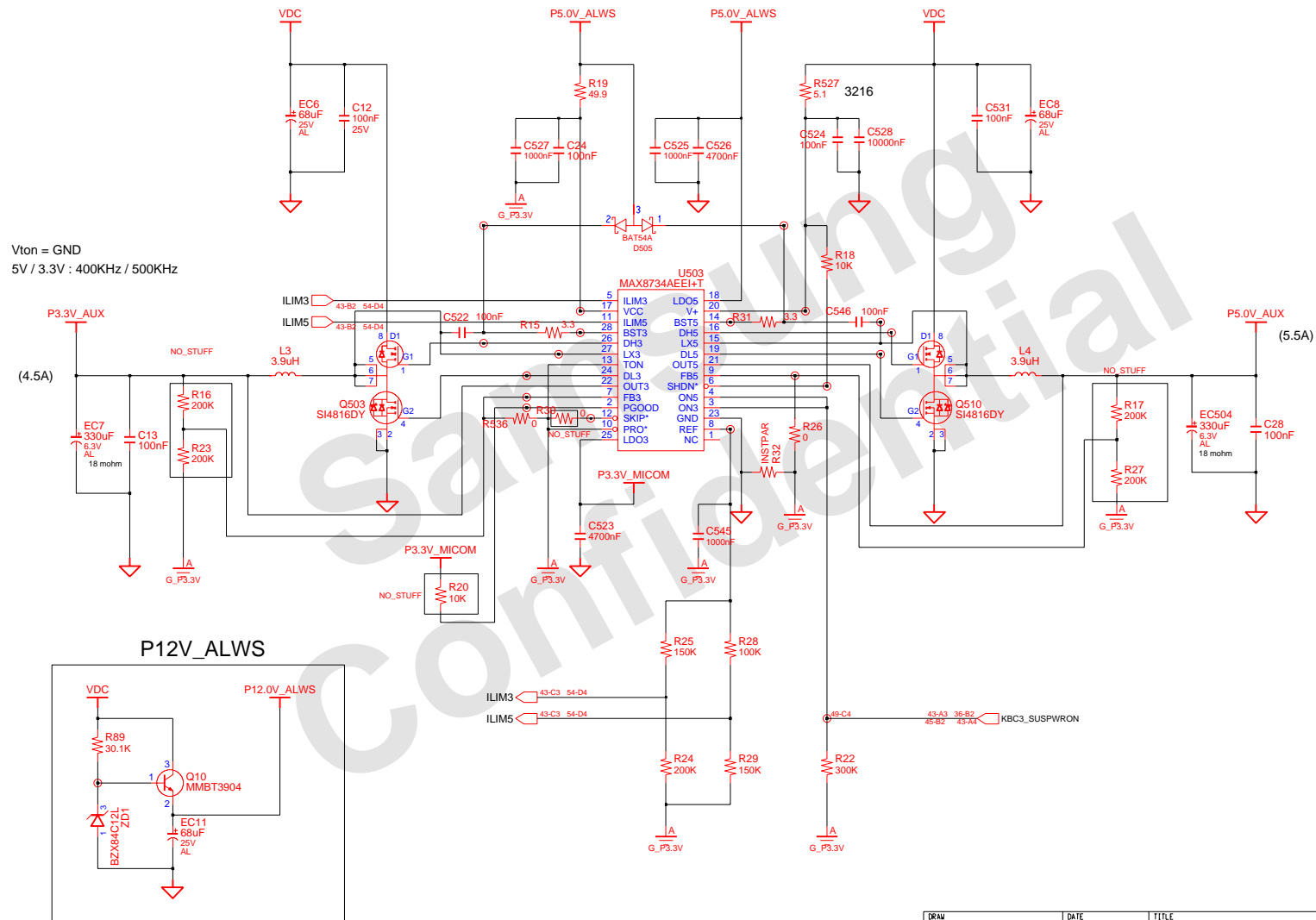


TOUCHPAD



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	BOARD CONN		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	38 OF 48	

V_{ton} = GND
5V / 3.3V : 400KHz / 500KHz

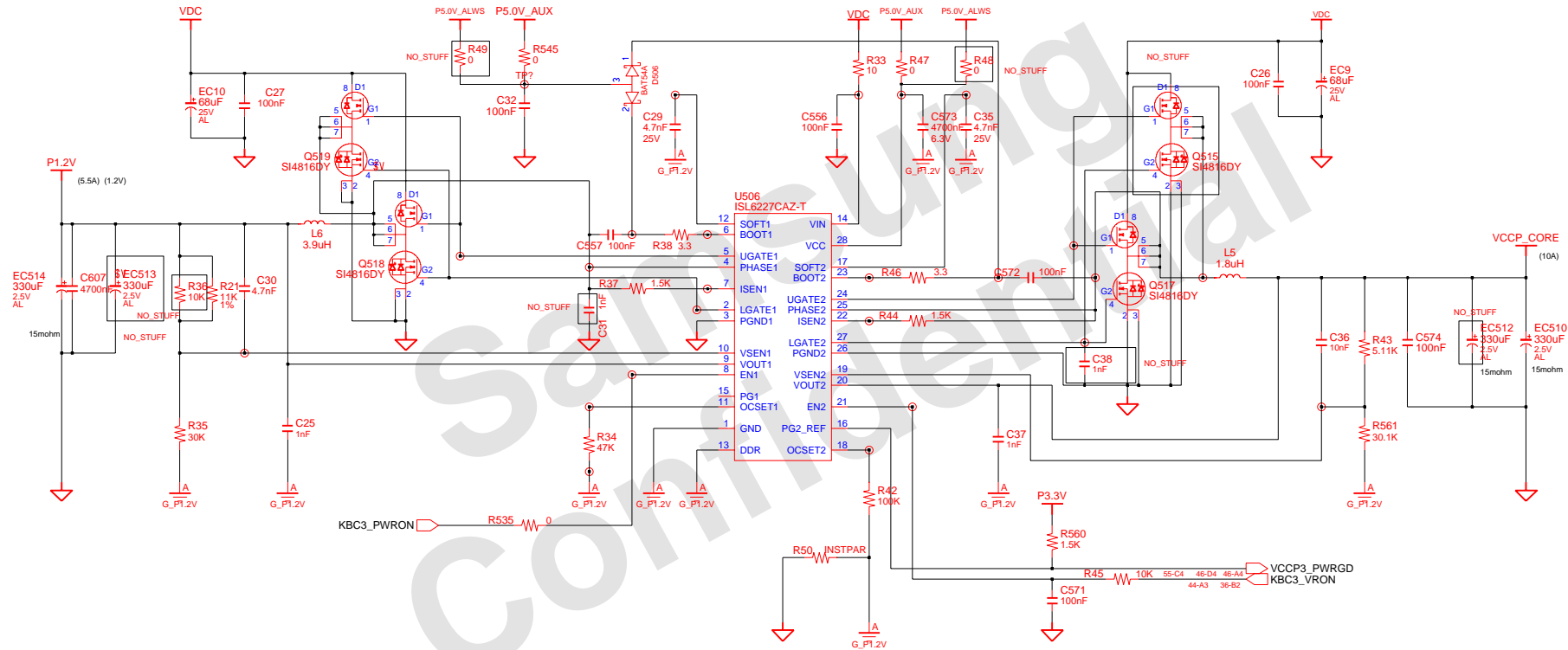


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2		SAMSUNG	
CHECK	YONG LEI	REV.	STEP	PV	P3.3V_AUX & P5V_AUX		ELECTRONICS	
APPROVAL	KEVIN LEE	REV	2.0	PART NO.			BA11-XXXXXA	
MODULE CODE	undefined		LAST EDIT		November 2, 2006	4:25:20 PM	PAGE	40 OF 48

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

P1.2V & VCCP_CORE(1.05V)

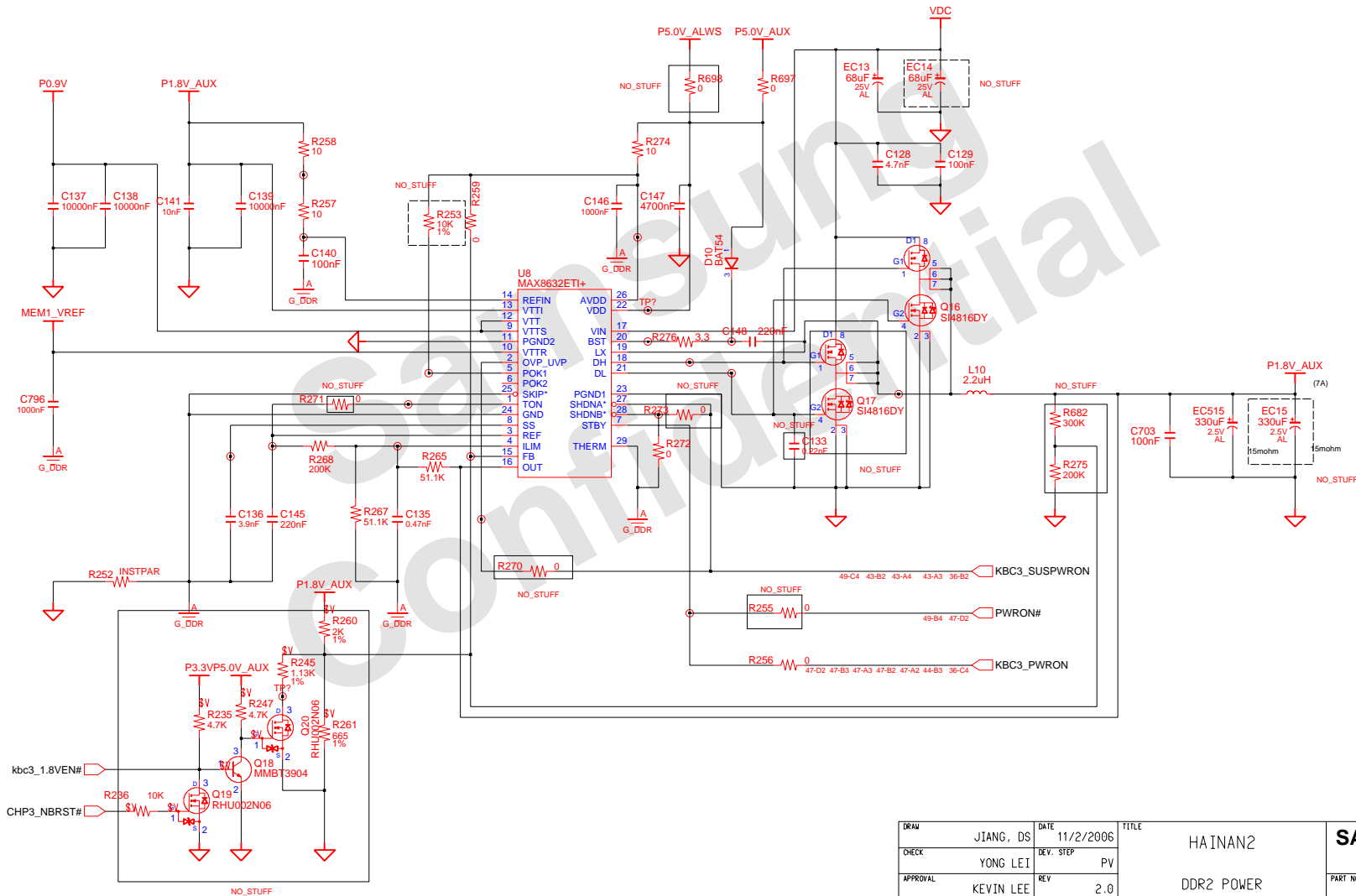


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		P1.2V & VCCP	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	41 OF 48	

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

DDR2 Power

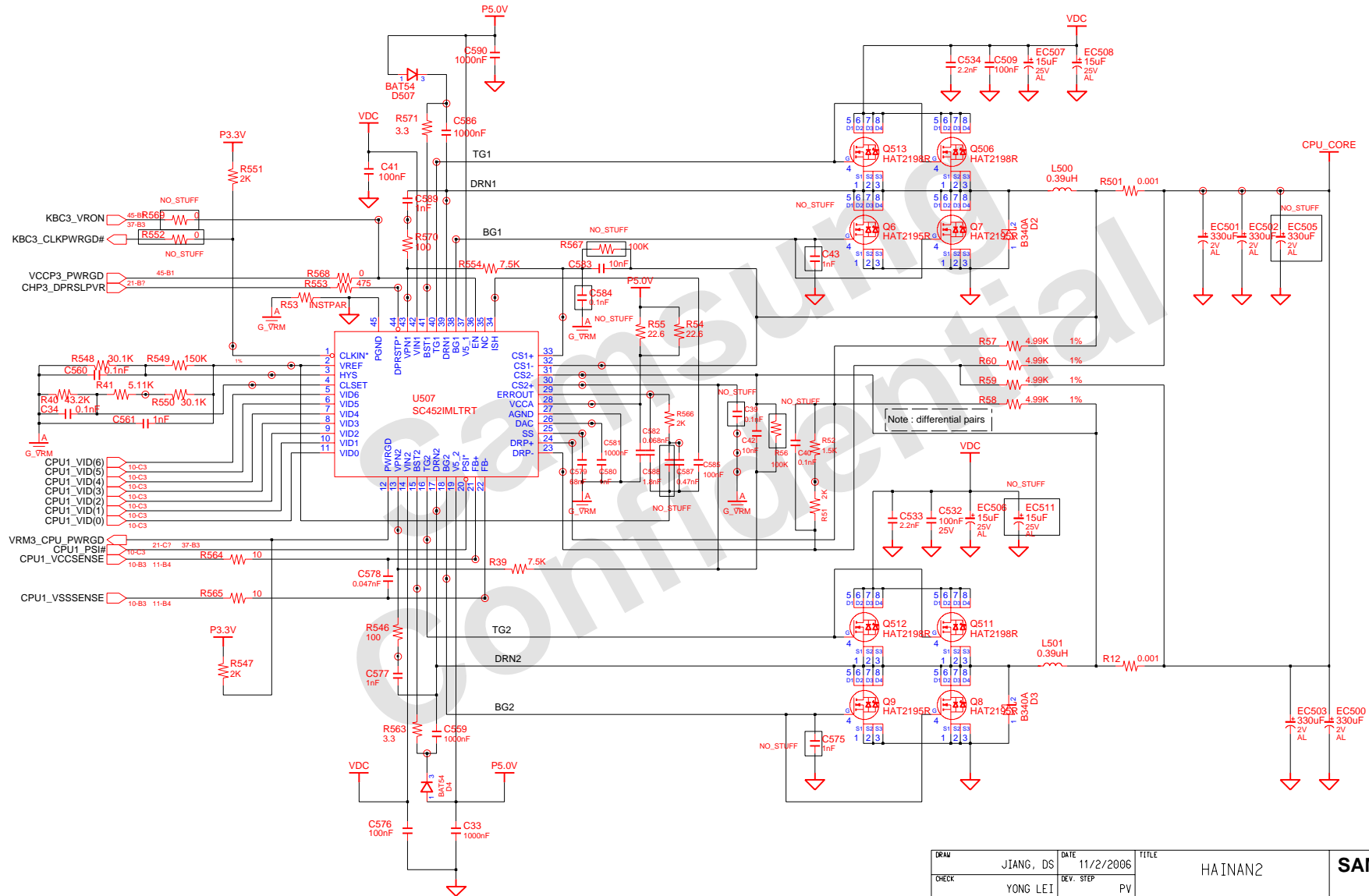


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	DDR2 POWER		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	42 OF 48	

CPU VRM [SEMTECH]

SAMSUNG PROPRIETARY

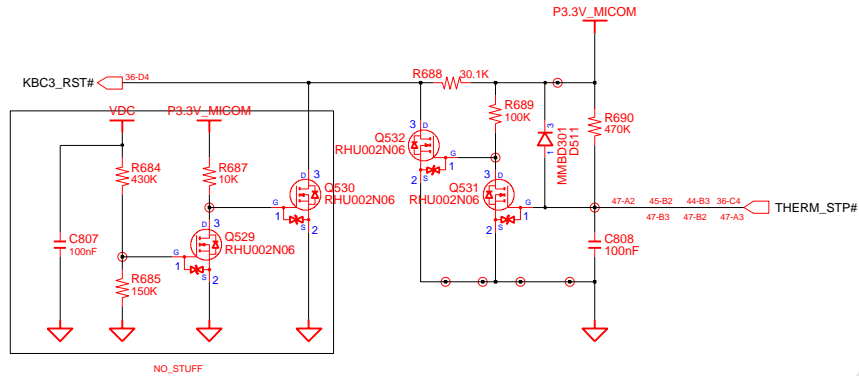
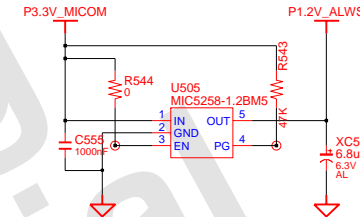
THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



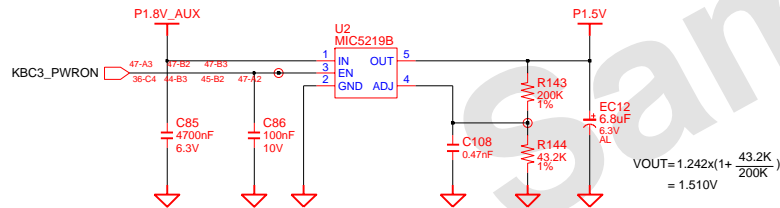
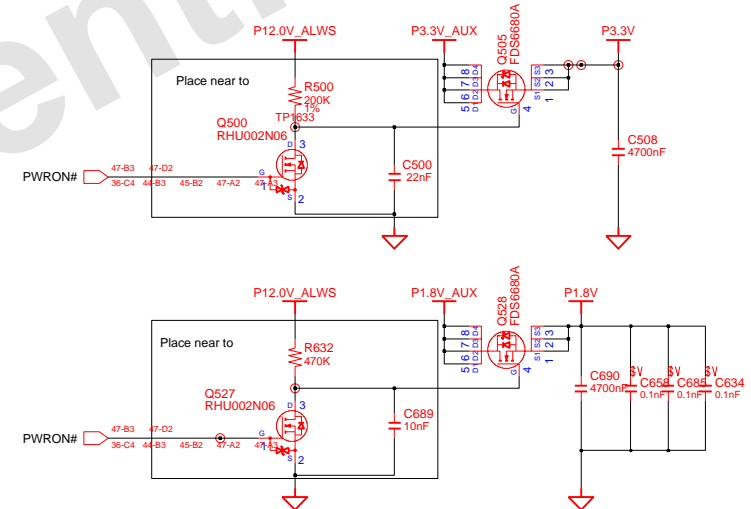
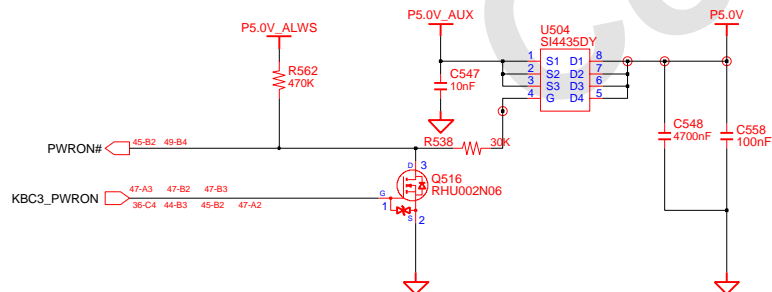
DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV	
APPROVAL	KEVIN LEE	REV	2.0	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	
PAGE 43 OF 48				

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

MICOM RESET

Switched Power On (P3.3V & 1.8V&1.2V_ALWS)

P1.5V POWER

(EBL)
(AVE MAX : 2A)

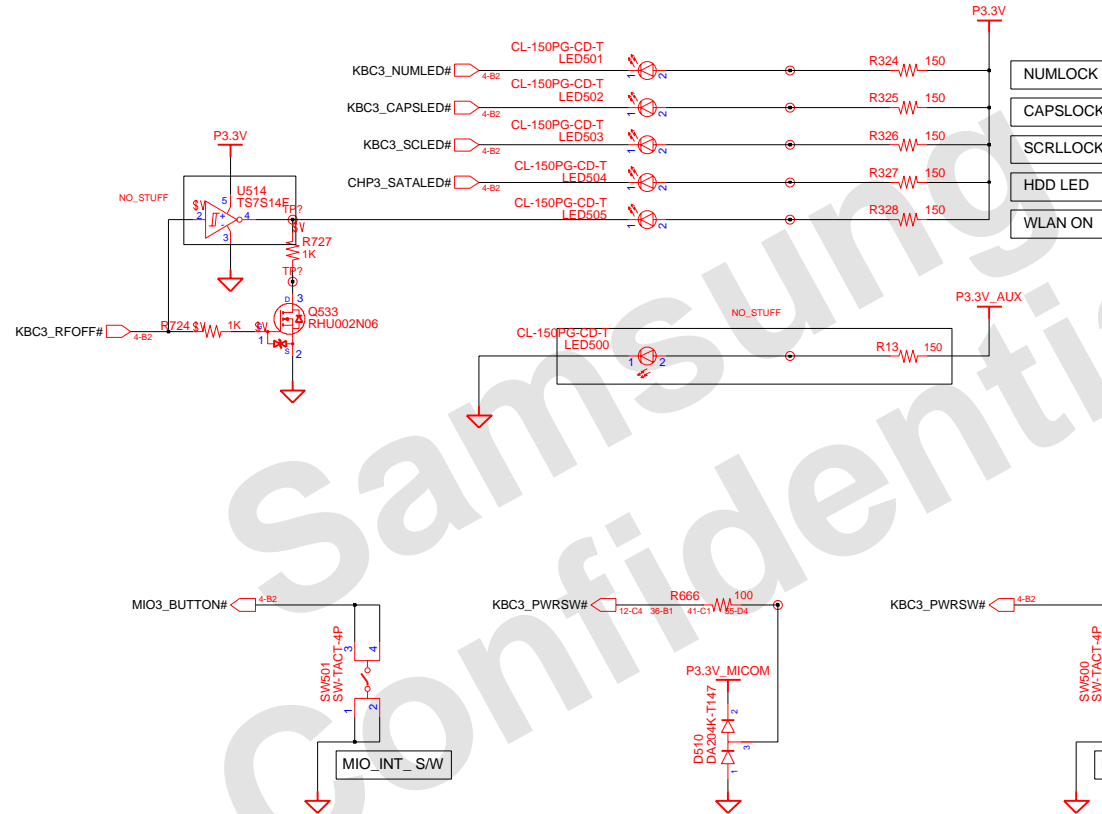

Switched Power On (P5V)


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	SWITCH POWER		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	44 OF 48	

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
 PROPRIETARY INFORMATION THAT IS
 SAMSUNG ELECTRONICS CO.'S PROPERTY.
 DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
 EXCEPT AS AUTHORIZED BY SAMSUNG.

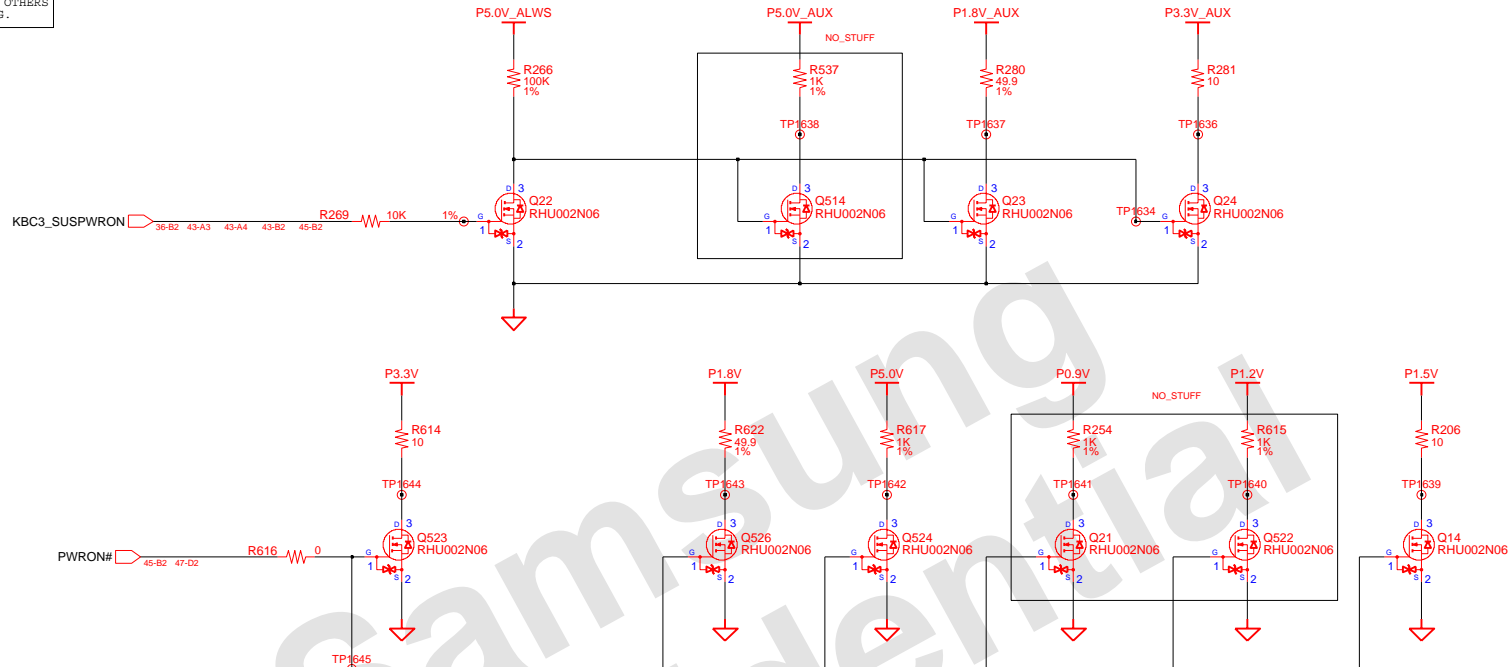
LED 0801-002195



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		POWER SWITCH	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	45	OF 48

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.



BT

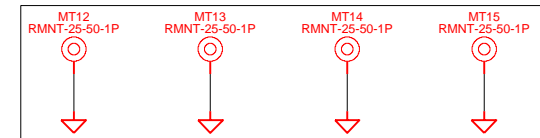
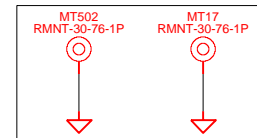
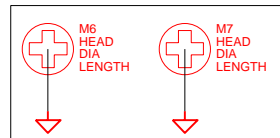
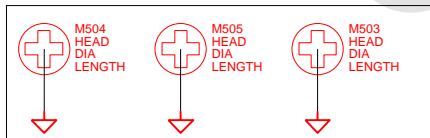
System

DMB

MDC

KBD

Bott with Mainboard



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		POWER STRAPS	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	46 OF 48	

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

TP2027 MIC1
TP2028 MIC2
TP2029 GPIO1
TP2030 ILM3
TP2031 ILM5

TP2020 CPU1_TDI
TP2021 CPU1_TMS
TP2022 PCI3_PAR
TP2023 SATA1_X1
TP2024 SMB3_CLK
TP2025 SPI3_CLK
TP2026 SPI3_CS#

TP1968 CPU1_A20M#

TP1971 CPU1_BSEL0
TP1972 CPU1_BSEL1
TP1973 CPU1_BSEL2

TP1939 PCI3_CBE0#
TP1940 PCI3_CBE1#
TP1941 PCI3_CBE2#
TP1942 PCI3_CBE3#
TP1943 PCI3_GNT1#

TP1945 PCI3_IRDY#
TP1946 PCI3_FERR#
TP1947 PCI3_REQ1#
TP1914 PCI3_RSTF#
TP1915 PCI3_SERR#
TP1916 PCI3_STOP#
TP1917 PCI3_TRDY#

TP1888 KBC5_KSO(0)
TP1889 KBC5_KSO(1)
TP1890 KBC5_KSO(2)
TP1891 KBC5_KSO(3)
TP1892 KBC5_KSO(4)
TP1893 KBC5_KSO(5)
TP1894 KBC5_KSO(6)
TP1895 KBC5_KSO(7)
TP1896 KBC5_KSO(8)
TP1897 KBC5_KSO(9)

TP1898 LCD3_BKLTON
TP1899 LPC3_LAD(0)
TP1900 LPC3_LAD(1)
TP1869 LPC3_LAD(2)
TP1870 LPC3_LAD(3)
TP1871 MCD3_MSINS#

TP2002 CHP3_SPKR

TP2006 CPU1_INTR
TP2007 CPU1_PSI#

TP2011 CPU1_SLP#
TP2012 CPU1_SM#
TP2013 IDE5_CS1#
TP2014 IDE5_CS3#

TP1977 CPU1_FERR#

TP1979 CPU1_INIT#

TP1950 CPU1_TRST#
TP1951 EXP3_CPPE#

TP1918 PEX3_WAKE#
TP1919 PLT3_RST#

TP1952 IDE5_DACK#
TP1953 IDE5_IORDY
TP1954 JCK_SENS_A
TP1955 KBC3_CHGEN
TP1956 KBC3_SMCLK
TP1957 KBC3_TDATA

TP1920 OT_L_BUTTON
TP1921 OT_R_BUTTON
TP1922 VGA3_HSYNC
TP1923 VGA3_VSYNC
TP1924 BAT3_SMCLK#
TP1925 CHP3_OVERT#
TP1910 CHP3_SBPME#
TP1911 CHP3_SERIRQ
TP1912 CHP3_SLPS#
TP1913 CHP3_SLPSS#

TP1980 IDE5_DREQ
TP1981 IDE5_IOR#
TP1982 IDE5_IOW#
TP1983 KBC3_A20G
TP1984 KBC3_RST#
TP1985 KBC3_VRON
TP1986 KBC5_TCLK

TP1958 LCD3_VDDEN
TP1959 MCD3_SDCE#
TP1960 MCD3_XDALE
TP1961 MCD3_XDCE#
TP1962 MCD3_XDCE#
TP1963 MCD3_XDRE#
TP1964 MCD3_XDWE#

TP1902 CPU1_DPSLP#
TP1903 CPU1_IGNNE#
TP1904 CPU1_VID(0)
TP1905 CPU1_VID(1)
TP1906 CPU1_VID(2)
TP1907 CPU1_VID(3)
TP1908 CPU1_VID(4)
TP1909 CPU1_VID(5)
TP1872 CPU1_VID(6)
TP1873 EXP3_CPUSB#
TP1874 EXP3_PERST#
TP1875 IDE5_IDEIRQ
TP1876 KBC3_I_#VEN#
TP1877 KBC3_PWRSW#
TP1878 KBC3_SCLE#
TP1879 KBC3_SMDATA
TP1880 KBC5_KSI(0)
TP1881 KBC5_KSI(1)
TP1882 KBC5_KSI(2)
TP1883 KBC5_KSI(3)
TP1884 KBC5_KSI(4)
TP1885 KBC5_KSI(5)
TP1886 KBC5_KSI(6)
TP1887 KBC5_KSI(7)

TP1996 ODD5_RST#

TP1997 SMB3_DATA
TP1998 SPI3_MISO
TP1999 SPI3_MOSI
TP2000 AUD3_EAPD#

TP2015 RTC_CLK
TP2016 AC_SDOUT
TP2017 AUTO_ON#
TP2018 CPU1_NMI
TP2019 CPU1_TCK

TP2001 CLK3_NB14M

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	TP		PART NO. BA41-XXXXXA
MODULE CODE		LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	47 OF 48	

SAMSUNG PROPRIETARY

THIS DOCUMENT CONTAINS CONFIDENTIAL
PROPRIETARY INFORMATION THAT IS
SAMSUNG ELECTRONICS CO.'S PROPERTY.
DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS
EXCEPT AS AUTHORIZED BY SAMSUNG.

TP2233○PCI3_FRAME#

TP2235○VCCP3_PWRGD
TP2236○AUD3_PCBEEP#
TP2237○BAT3_DETECT#
TP2238○BAT3_SMDATA#
TP2239○CHP3_AZ_SDIO
TP2240○CHP3_BIOSW#
TP2241○CHP3_CPUSTP#

TP2223○CPU1_STPCLK#

TP2224○EXP3_CLKREQ#
TP2225○ITP3_SYSRST#
TP2226○JCK_SENS_HP#
TP2227○KBC3_CPURST#
TP2228○KBC3_EXTSMI#
TP2229○KBC3_FANCTRL
TP2230○KBC3_NBPWRGD
TP2231○KBC3_NUMLED#
TP2232○KBC3_PWRBTN#
TP2207○KBC3_RSMRST#
TP2208○KBC3_RUNSG#
TP2209○KBC3_SBPWRGD
TP2210○KBC5_KSO(10)
TP2211○KBC5_KSO(11)
TP2212○KBC5_KSO(12)
TP2213○KBC5_KSO(13)
TP2214○KBC5_KSO(14)
TP2215○KBC5_KSO(15)

TP2216○LID3_SWITCH#
TP2217○LPC3_LFRAME#

TP2195○CHP3 DPRSLPVR
TP2196○CHP3_SATALED#

TP2197○CPU1_PROCHOT#
TP2198○CPU1_PWRGDCPU
TP2199○CPU1_VCCSENSE
TP2200○CPU1_VSSSENSE
TP2201○JCK_SENS_MCH#
TP2202○KBC3_CAPSLED#
TP2203○KBC3_WAKESC#
TP2204○LCD3_BKLTCTRL
TP2205○LCD3_EDID_CLK

TP2180○CHP3_SATA_DET#

TP2181○CPU1_THRMTRIP#
TP2182○KBC3_CLKPWRGD#
TP2183○KBC3_LED_ACIN#
TP2184○KBC5_CAL_THRM#
TP2185○LCD3_EDID_DATA
TP2186○VRM3_CPU_PWRGD

TP2187○CHP3_ALINK_RST#
TP2188○CHP3_AZ_AUD_SDIO
TP2189○CHP3_AZ_MDC_SDO

TP2192○KBC3_LED_POWER#

TP2158○CHP3_AZ_AUD_RST#

TP2159○CHP3_SBTHRMTRIP#

TP2162○KBC3_LED_CHARGE#
TP2163○KBC3_THERM_SMCLK

TP2164○KBC3_THERM_SMDATA

TP2165○MINIPIC3_CLKREQ_B#

TP2193○PCI3_CLKRUN#
TP2194○PCI3_DEVSEL#

TP2169○CHG_REF
TP2170○G_AUD
TP2171○G_AUD
TP2172○G_AUD
TP2173○G_AUD
TP2174○G_CHG
TP2175○G_CHG
TP2176○G_CHG
TP2177○G_CHG
TP2178○G_CRT
TP2179○G_CRT

TP2126○G_CRT
TP2127○G_CRT
TP2128○G_DDR
TP2129○G_DDR
TP2130○G_DDR
TP2131○G_DDR
TP2132○G_P3.3V
TP2133○G_P3.3V
TP2134○G_P3.3V
TP2135○G_P3.3V
TP2136○LCD_VDD3V
TP2137○LCD_VDD3V

TP2142○LDO_P5.4V
TP2143○LDO_P5.4V

TP2146○MEM1_VREF
TP2147○MEM1_VREF
TP2148○P0.9V
TP2149○P0.9V
TP2150○P0.9V
TP2151○P0.9V
TP2152○P1.2V
TP2153○P1.2V
TP2154○P1.2V
TP2155○P1.2V
TP2156○P1.5V
TP2157○P1.5V
TP2094○P1.5V
TP2095○P1.5V

TP2103○PRTC_BAT

TP2109○P1.5V_EXP
TP2110○P1.5V_EXP
TP2111○P1.5V_EXP
TP2112○P1.8V_AUX
TP2113○P1.8V_AUX
TP2114○P1.8V_AUX
TP2115○P1.8V_AUX
TP2116○P2.5V_LAN
TP2117○P2.5V_LAN
TP2118○P2.5V_LAN
TP2119○P2.5V_LAN

TP2123○P2.5V_MCD
TP2124○P3.3V_AUX
TP2125○P3.3V_AUX
TP2062○P3.3V_AUX
TP2063○P3.3V_AUX

TP2065○P3.3V_EXP
TP2066○P3.3V_EXP

TP2068○P3.3V_MCD
TP2069○P3.3V_MCD
TP2070○P3.3V_MCD
TP2071○P3.3V_MCD
TP2072○P5.0V_AUD
TP2073○P5.0V_AUD
TP2074○P5.0V_AUD
TP2075○P5.0V_AUD
TP2076○P5.0V_AUD
TP2077○P5.0V_AUD

TP2078○P5.0V_AUX
TP2079○P5.0V_AUX
TP2080○PCIE_VDDR

TP2085○P1.2V_ALWS
TP2086○P1.2V_ALWS

TP2088○P5.0V_ALWS
TP2089○P5.0V_ALWS
TP2090○P5.0V_ALWS
TP2091○P5.0V_ALWS
TP2092○P12.0V_ALWS
TP2093○P12.0V_ALWS
TP2032○P12.0V_ALWS
TP2033○P12.0V_ALWS

TP2035○P3.3V_AUX_EXP
TP2037○P3.3V_AUX_EXP
TP2038○VDC
TP2039○VDC
TP2040○VDC
TP2041○VDC

TP2045○VCC_CRT
TP2046○VDC_INV
TP2047○VDC_INV

TP2050○VDD_AMP
TP2051○VDD_AMP
TP2052○VDD_AMP
TP2053○VDD_AMP
TP2054○VDD_AUD
TP2055○VDD_AUD
TP2056○VDD_AUD
TP2057○VDD_AUD
TP2058○VCCP_CORE
TP2059○VCCP_CORE
TP2060○VCCP_CORE
TP2061○VCCP_CORE

REV500
1○
2○ ○3

PCB REVISION CONTROL (ICT)				
NO	CONNECTION	DATE(Y/M/D)	REVISION	STEP
1	N.C.			
2	1-2			
3	2-3			
4	3-1			
5	1-2-3			
6	N.C.			
7	1-2			
8	2-3			
9	3-1			
10	1-2-3			

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	TP		PART NO. BA41-XXXXXA
MODULE CODE		LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	48 OF 48	