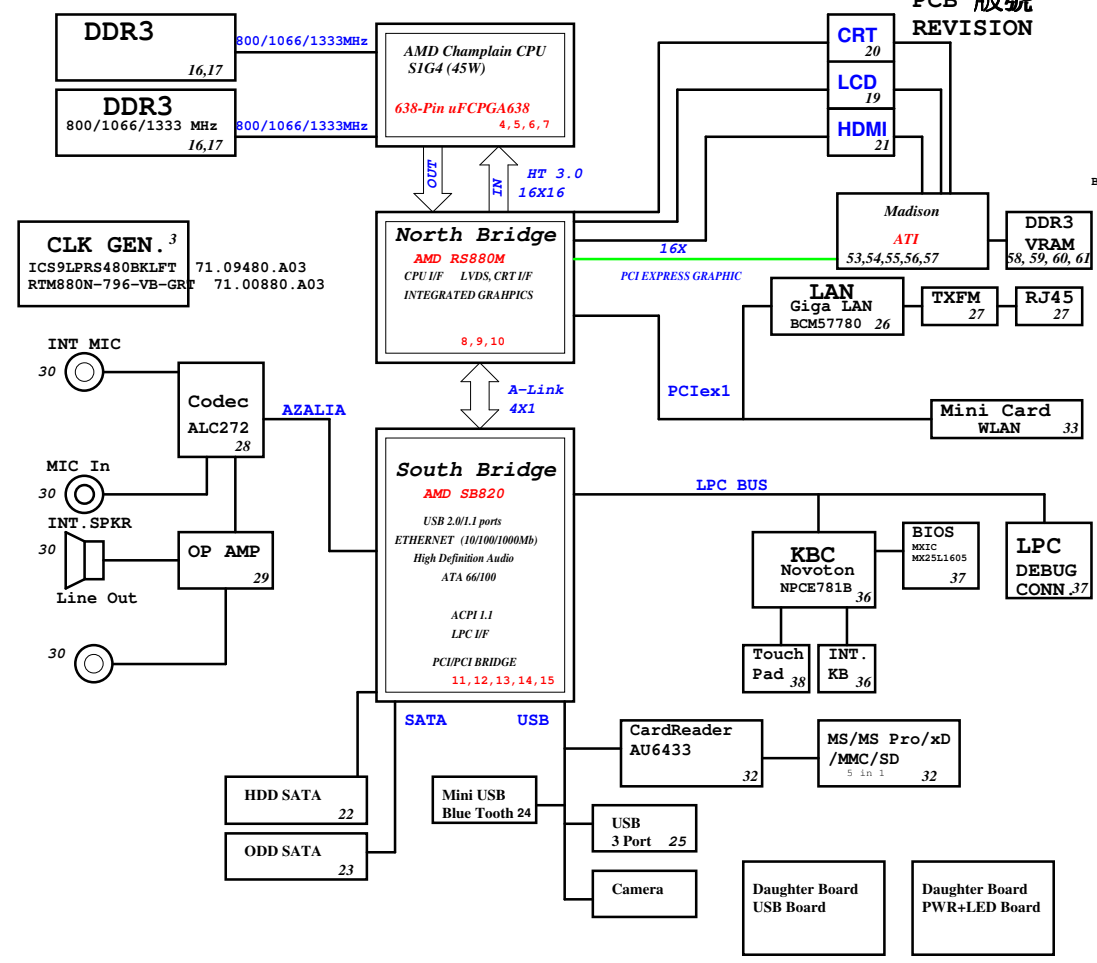


HM42-DN Block Diagram

<http://laptopblue.com/>

Project code: 91.4HD01.001
PCB P/N : 48.4GX01.0SA
PCB 版號 : 09919 SA
REVISION : PCB STACKUP



TOP
VCC
S
S
GND
BOTTOM

SYSTEM DC/DC	
RT8223	46
INPUTS	OUTPUTS
DCBATOUT	5V_S5 (5.5A)
	3D3V_S5 (5A)
SYSTEM DC/DC	
RT8209E	47
5V_S5	1D5V_S3 (1.4A)
RT9026	47
5V_S5	OD75_S3 (1.2A)
SYSTEM DC/DC	
RT8209E	48
INPUTS	OUTPUTS
DCBATOUT	1D1V_S0 (11A)
RT9025	48
3D3V_S5	1D1V_S5 (1.4A)
RT9025	49
5V_S5	1D1V_VGA
RT9161	49
3D3V_S0	2D5V_S0 (200mA)
RT9025	49
1D5V_S3	1D05V_S0 (1.4A)
CHARGER	
BQ24745	50
INPUTS	OUTPUTS
DCBATOUT	CHG_PWR
	18V 6.0A
	UP+5V
	5V 100mA
CPU DC/DC	
ISL6265AHR	45
INPUTS	OUTPUTS
DCBATOUT	VCC_CORE_S0
	0~1.55V 18A
	VDDNB
	0~1.55V 4A

Discrete Madison Hynix

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File: BLOCK DIAGRAM

Size: A3 Document Number: JE40-DN Rev: -3

Date: Friday, March 26, 2010 Sheet: 1 of 80

http://laptopblue.vn/

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緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichang, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
History			
Size	Document Number	Rev	
A3	JE40-DN	-3	
Date:	Friday, March 28, 2019	Sheet	2 of 63

- | Clock chip has internal serial terminations
- | for differential pairs, external resistors are
- | reserved for debug purpose.



NB CLOCKS	RS880M
HT_REFCLKP	100M DIFF
HT_REFCLKN	100M DIFF
REFCLK_P	14M SE (1.1V)
REFCLK_N	vref
GFX_REFCLK	100M DIFF(INOUT)*
GPP_REFCLK	NC or 100M DIFF OUTPUT
GPPSB_REFCLK	100M DIFF

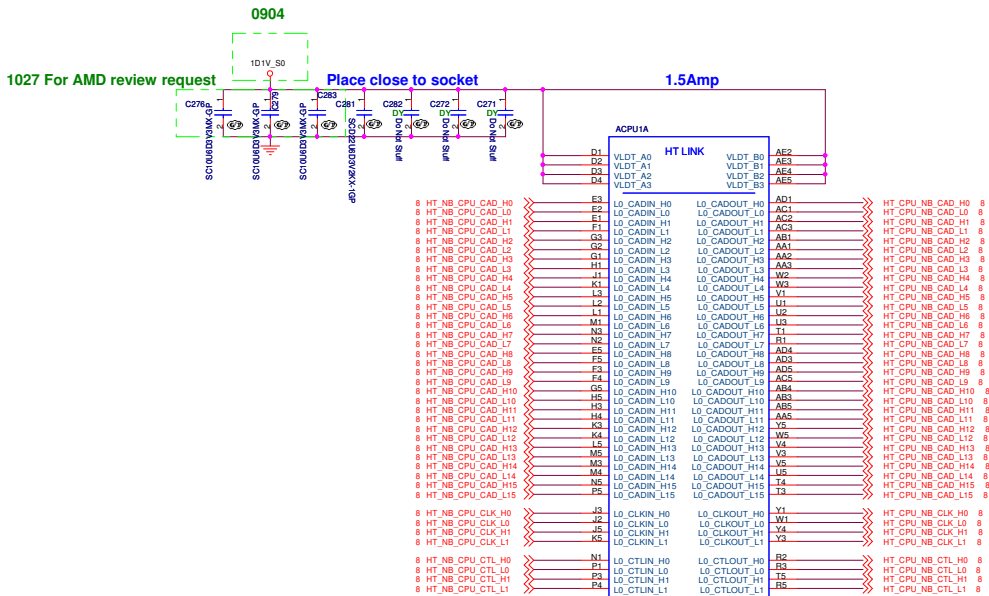
* RS880M can be used as clock buffer to output two PCIE reference clocks
By default, chip will configured as input mode, BIOS can program it to output mode.

Discrete Madison Hynb

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **CLKGEN ICS9LPRS480**

Size A3	Document Number JE40-DN	Rev -3
Date: Friday, March 26, 2010		Sheet 3 of 63



SKT-CPU638P-GP-U2
62.10055.111
2ND = 62.10055.181(ENG 未上)

SKT-BGA638H176

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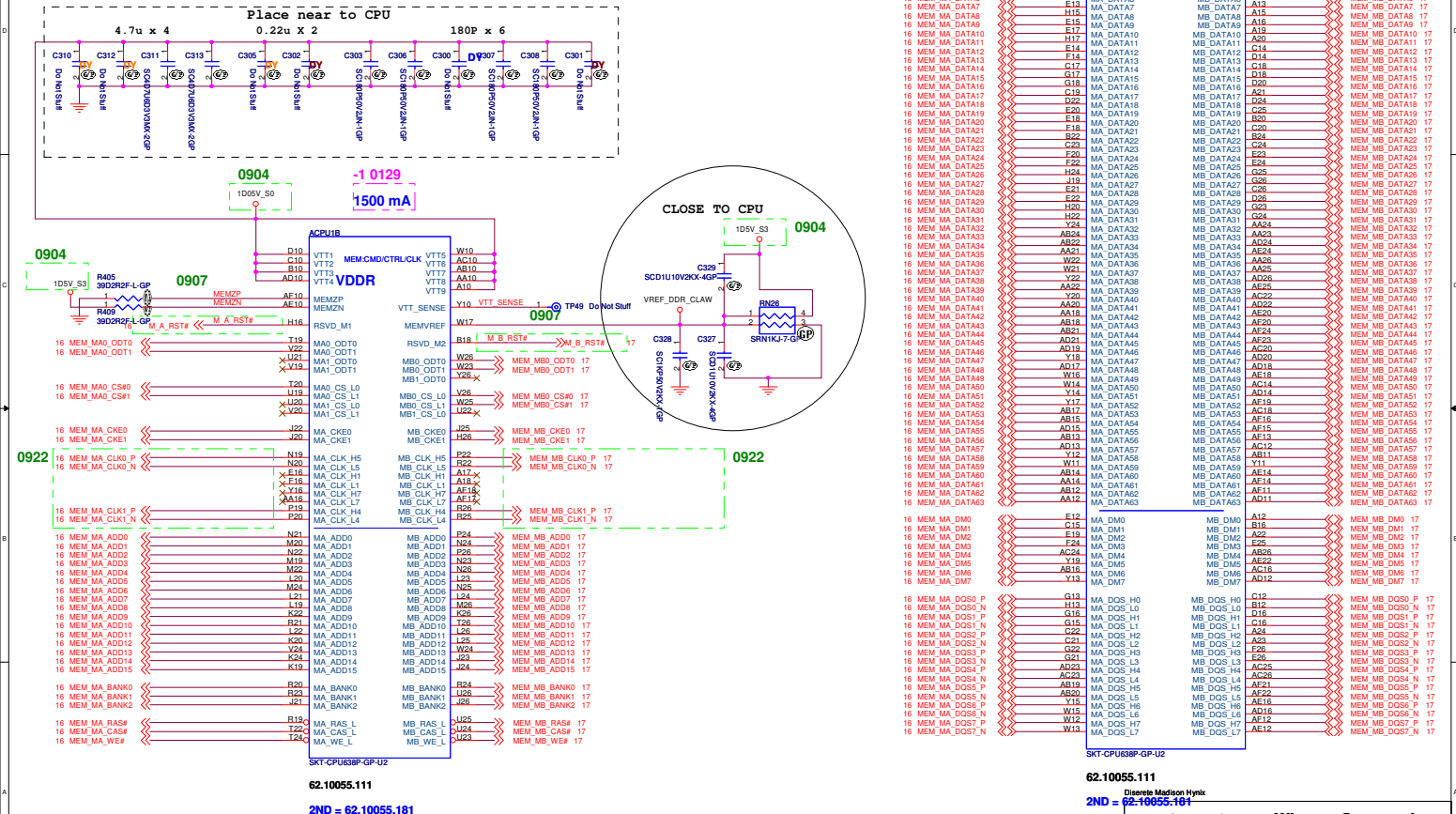
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taipai Hsien 321, Taiwan, R.O.C.

File

CPU HT LINK I/F (1/4)

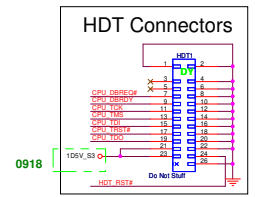
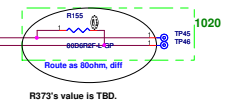
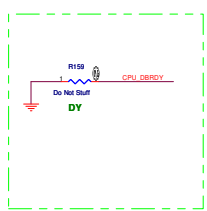
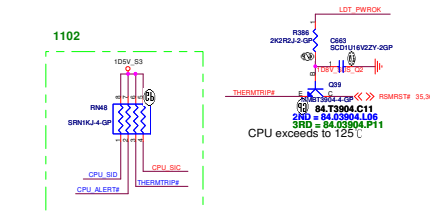
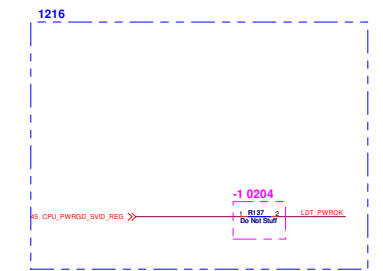
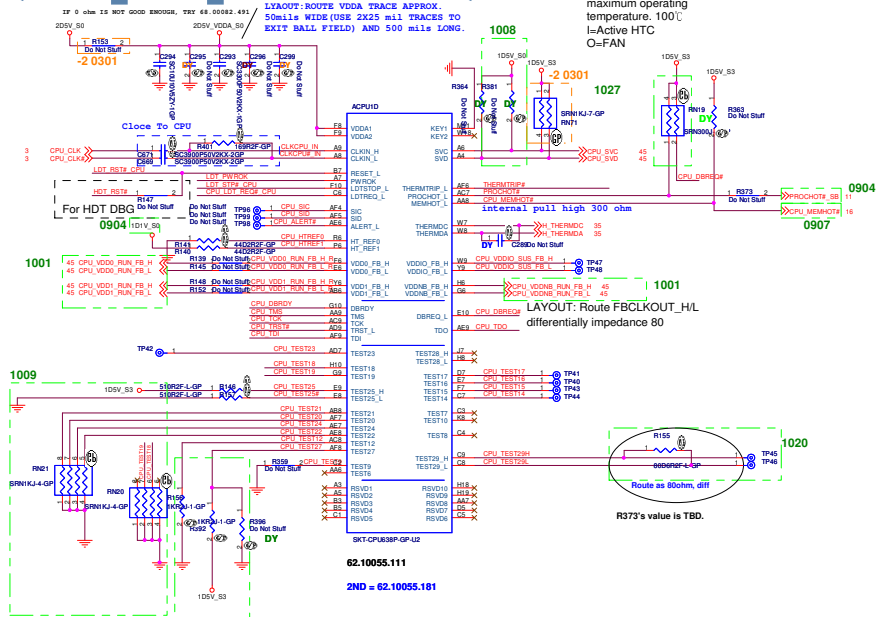
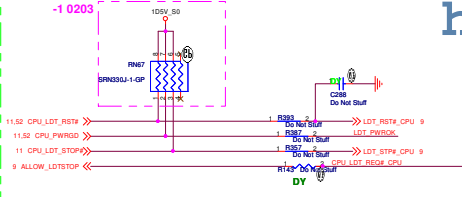
Rev A3 Document Number JE40-DN Rev -3

Date: Friday, March 26, 2010 Sheet 4 of 65



LYAOUT:ROUTE VDDA TRACE APPROX.
50mils WIDE(USE 2X25 mil TRACES TO
EXIT BALL FIELD) AND 500 mils LONG

The Processor has reached a preset maximum operating temperature. 100°C
I=Active HTC
O=FAN



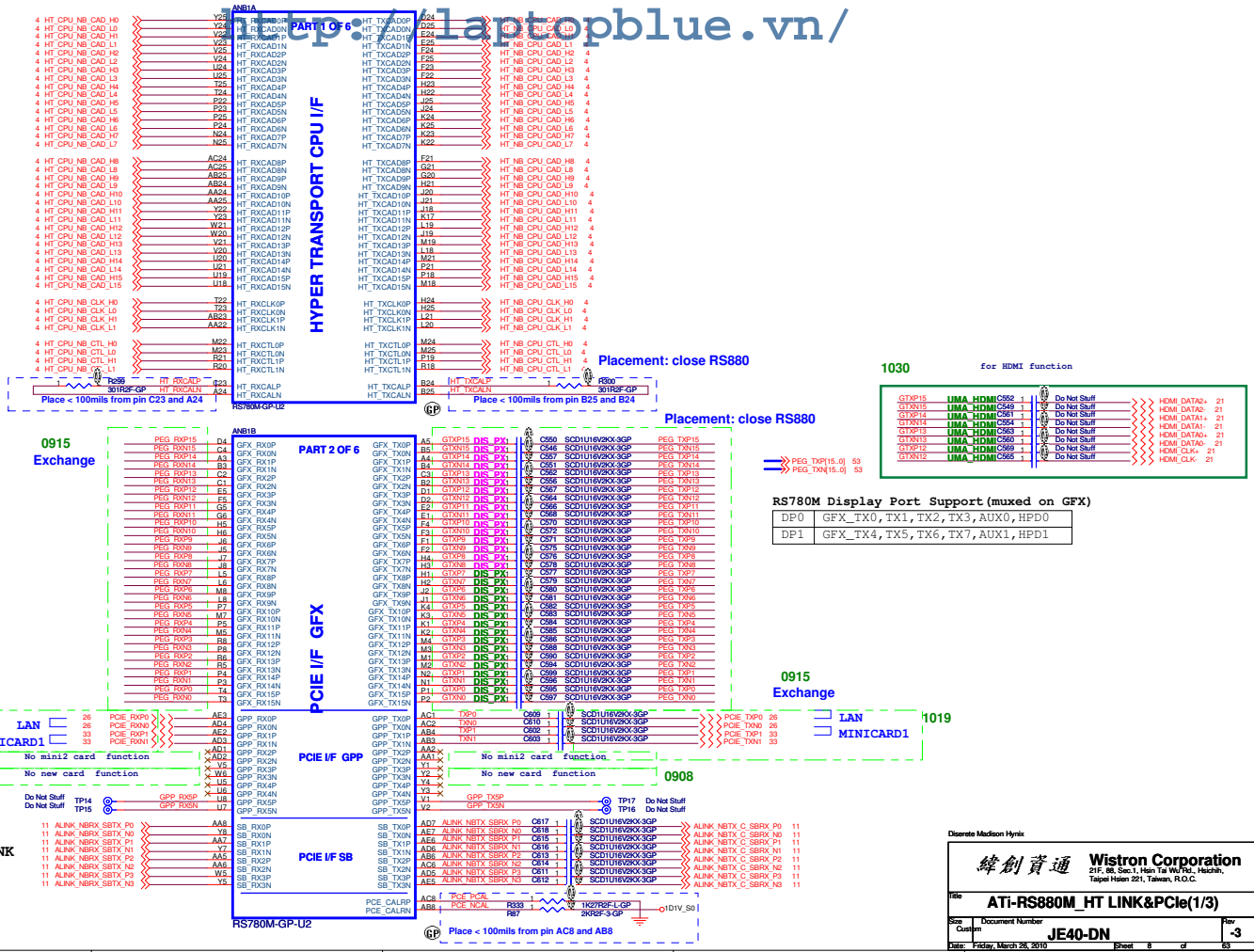
Discrete Madison Hynix

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Ming-shan City, Taipei, R.O.C. Tel: 886-2-2652-1111

Title	CPU_Control&Debug_(3/4)
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Size	Document Number	Rev
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Date: Friday, March 28, 2016	Sheet 6 of 63	

Date: Friday, March 26, 2010 Sheet 6 of 63





1027 For AMD review request

0.6A per ANT Rev1.1, Page3

0.45A per ANT Rev1.1, Page3

1027 For AMD review request

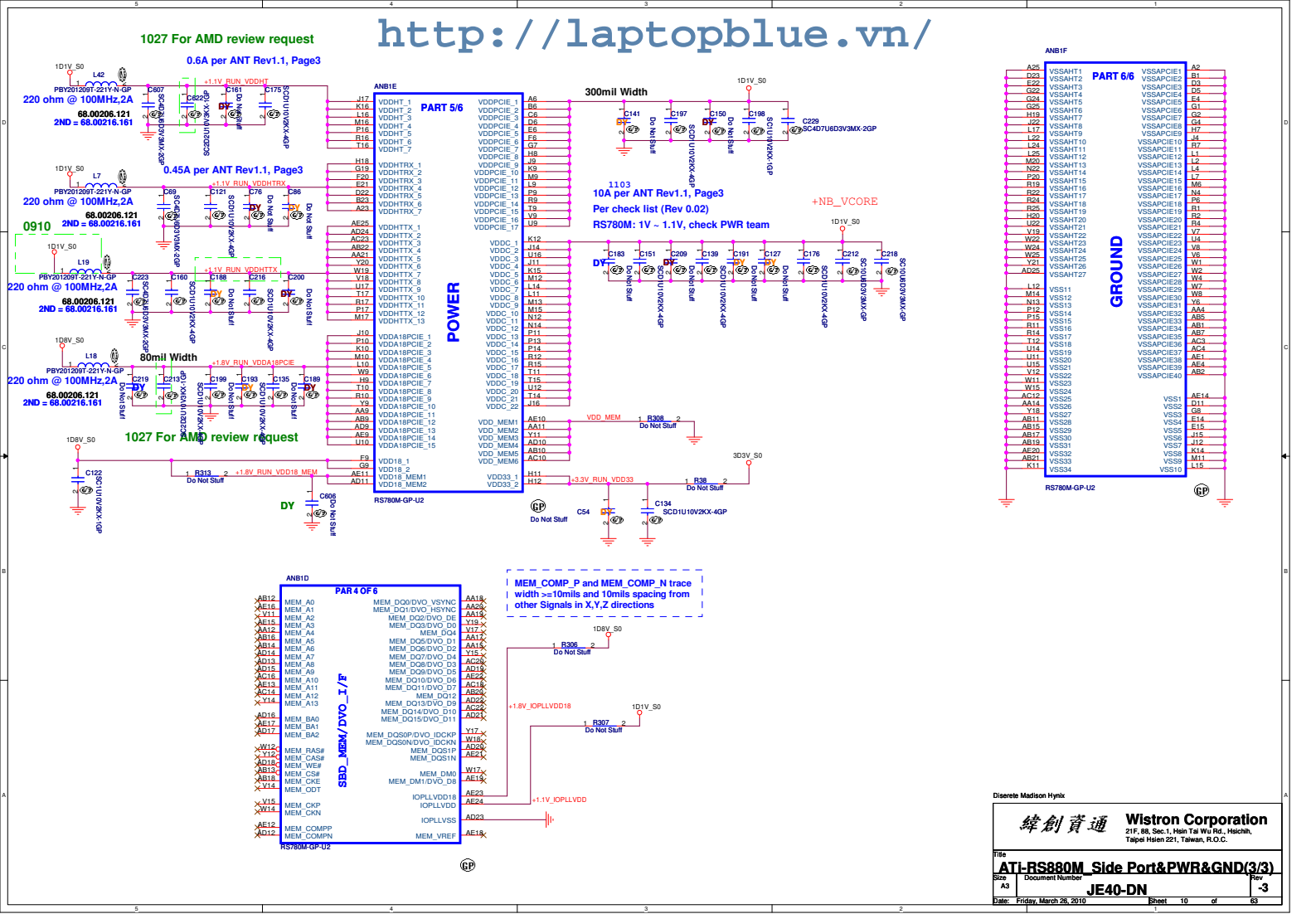
MEM_COMP_P and MEM_COMP_N trace width >=10mils and 10mils spacing from other Signals in X,Y,Z directions

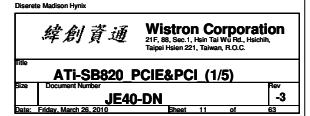
Discrete Madison Hynix

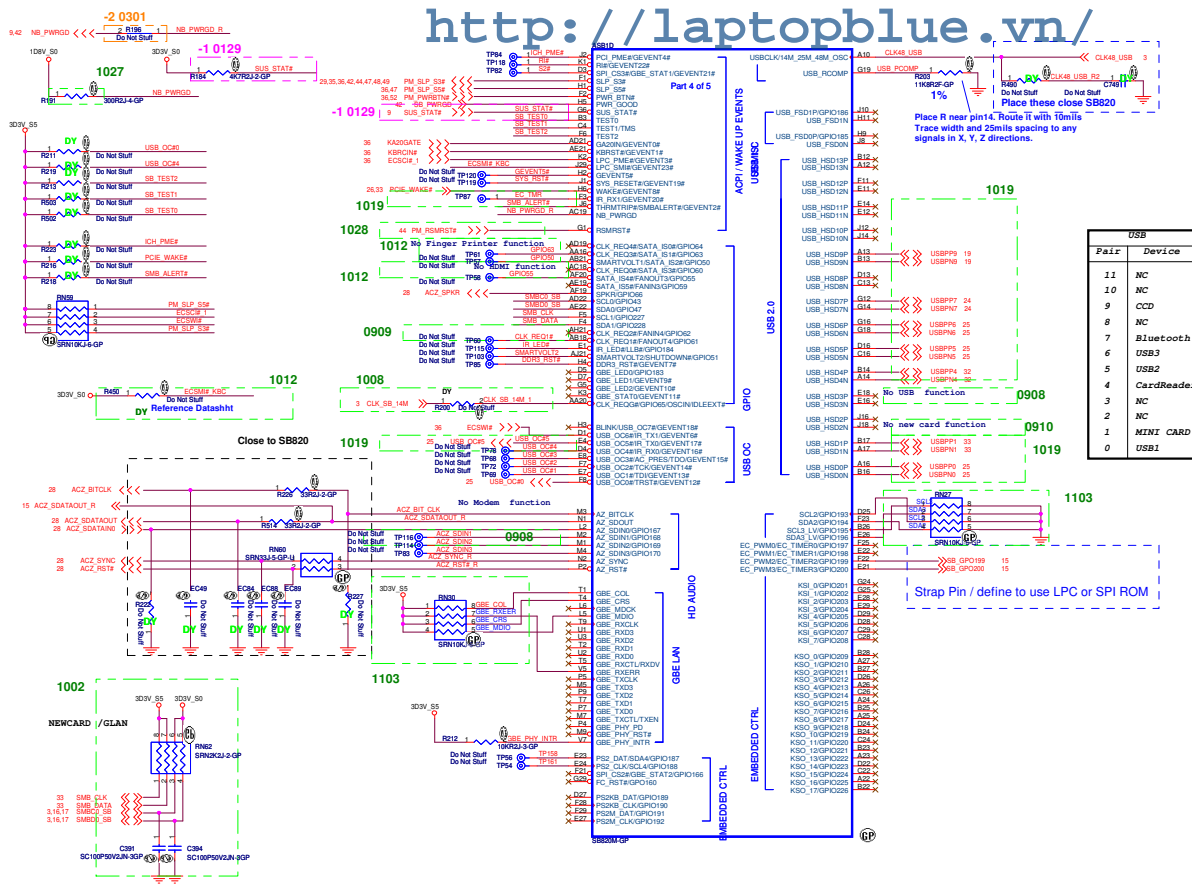
緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taipei Hsien 221, Taiwan, R.O.C.

File
AT1-RS880M_Side Port&PWR&GND(3/3)
Size A3 Document Number
Date: Friday, March 26, 2010 JE40-DN
Sheet 10 of 30



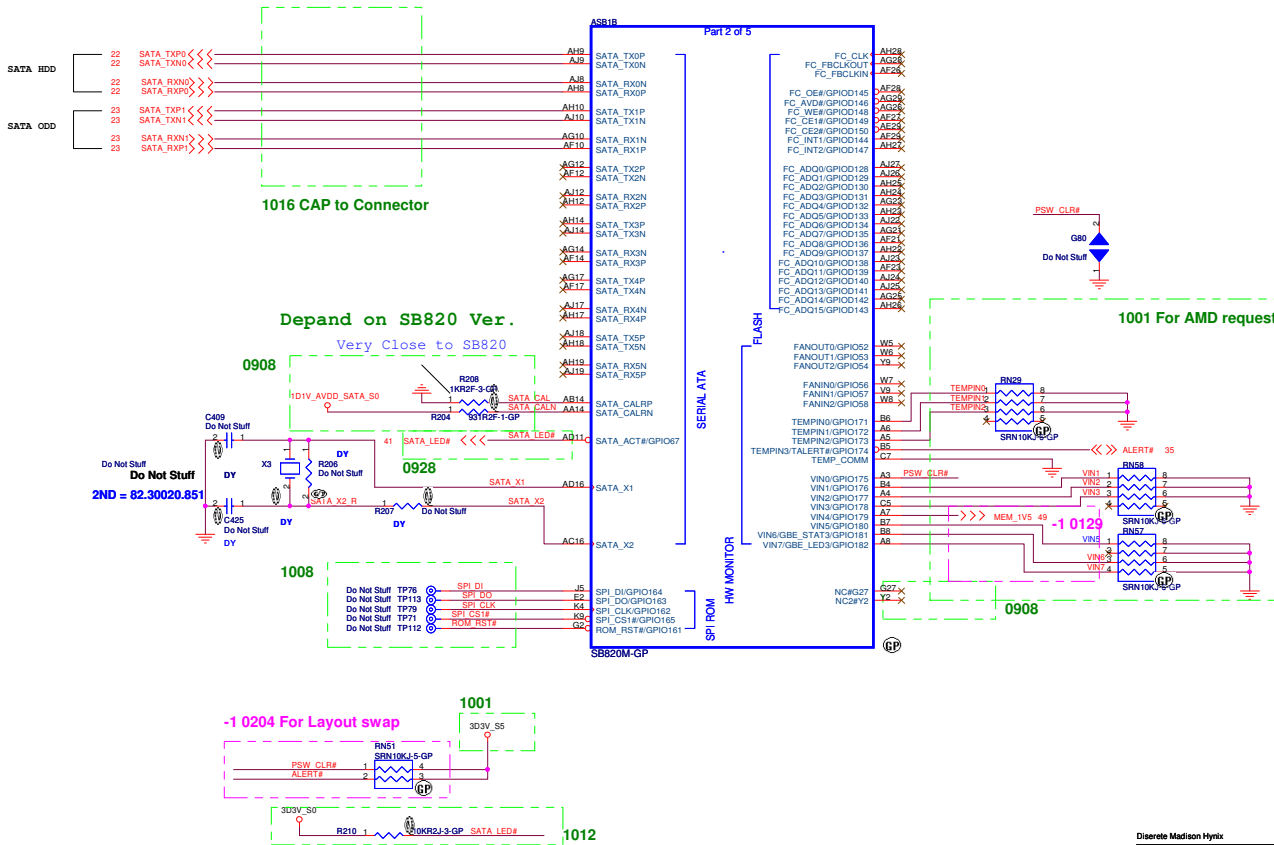




Discrete Madison Hynix

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichlin,
Taipei Hsien 221, Taiwan, R.O.C.

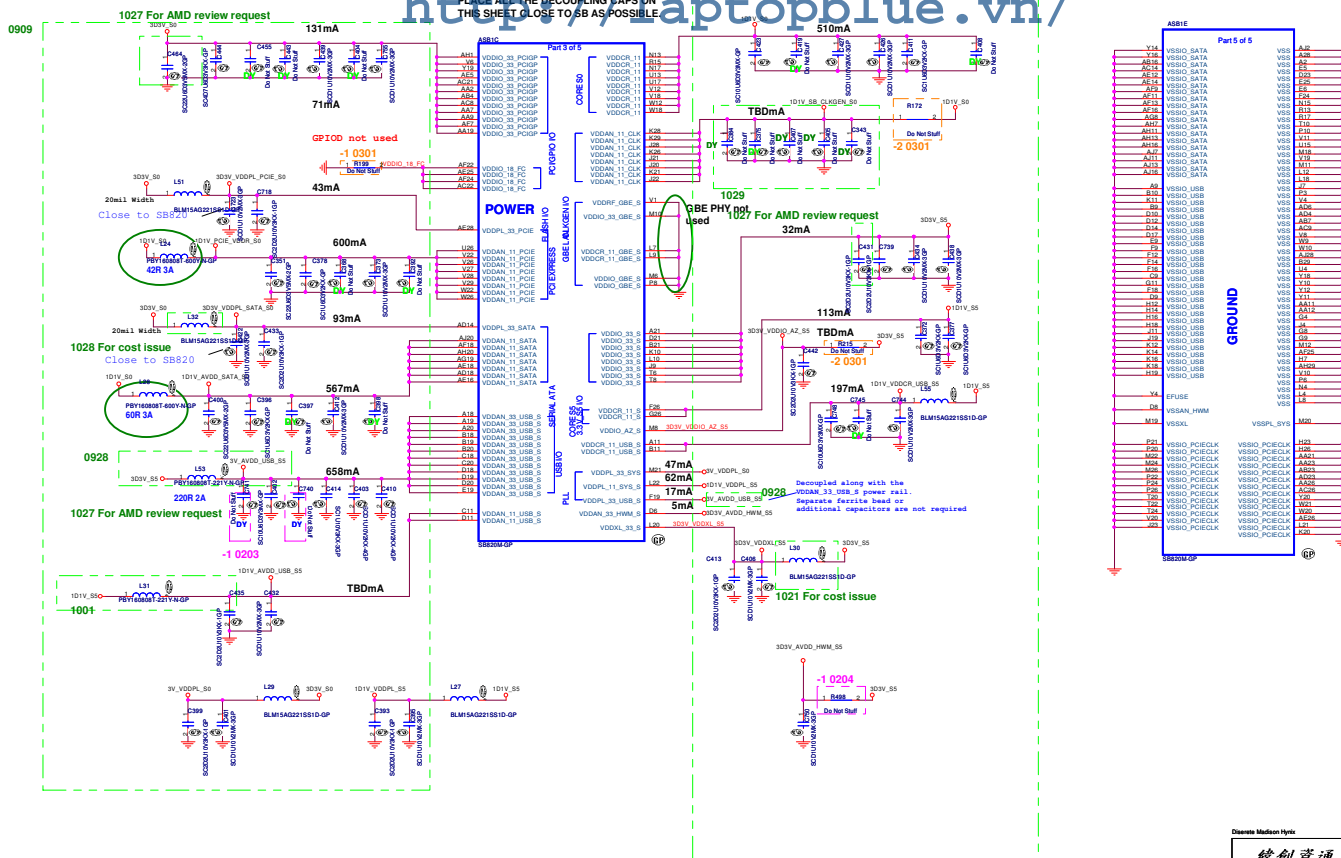
File			
ATI-SB820 USB&GPIO (2/5)			
Size	Document Number	Rev	
Custom	JE40-DN	-3	
Date:	Friday, March 25, 2010	Sheet	12 of 53



Discrete Madison Hyinx

緯創資通 Wistron Corporation
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Taipei Hsien 221, Taiwan, R.O.C.

File
Size A3
Date: Friday, March 28, 2019
Document Number
JE40-DN
Sheet 13 of 63
Rev -3



Discrete Median Hori

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Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

ATI-SB820 POWER&GND (4/5)					
Size	Document Number				Rev
Custom	JE40-DN				-
<small> Access - Address Memory Size 512KB Max Bus Width 1.4 # of Banks 83 Max Data Rate 1.0GBps Max Power 1.0W Max Temp 70°C Max Voltage 1.5V </small>					

6 CPU_MEMHOT# <<< R237 1 0x Do Not Stuff M.B. EVENT#

```
VREF_DDR_MEM
```

DDR_VREF_DQ

Title				DDRIII SO-DIMM SKT 1			
Size		Document Number				Rev	
Custom		JE40-DN				*3	
Date: Friday, March 26, 2010				Sheet 16 of 53			

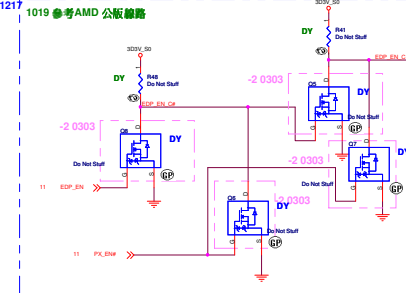
1001

STANDARD TYPE

[illegible]



Title			
DDRIII SO-DIMM SKT 2			
Size	Document Number		Rev
Custom	JE40-DN		-3
Date:	Friday, March 26, 2010	Sheet 17 of	63



Truth Table

Function	SEL
A_N to NB_1	L
A_N to NB_2	H

CRT

E	S	YA	YB	YC	YD	Function
H	X	Hb-Z	Hb-Z	Hb-Z	Hb-Z	Disable
L	L	IA0	IB0	IC0	ID0	$S=0$
L	H	IA1	IB1	IC1	ID1	$S=1$

Function Table

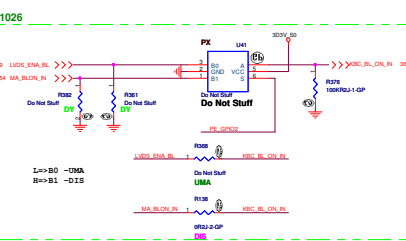
Input (B)	Function
L	B_1 Connected to A
H	B_1 Connected to A

H = HIGH Logic Level L = LOW Logic Level

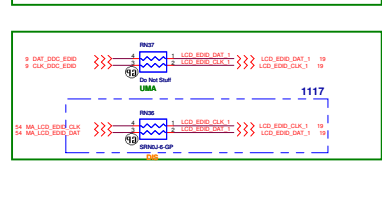
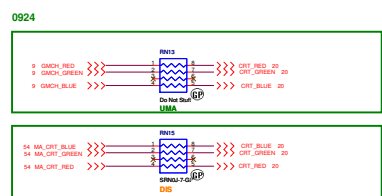
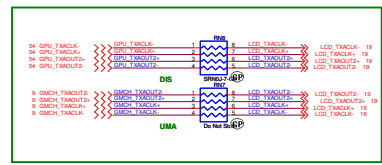
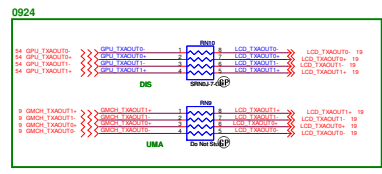
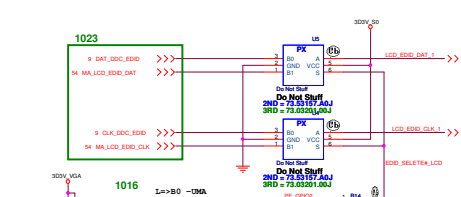
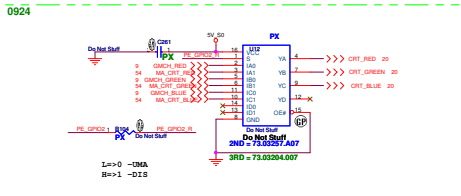
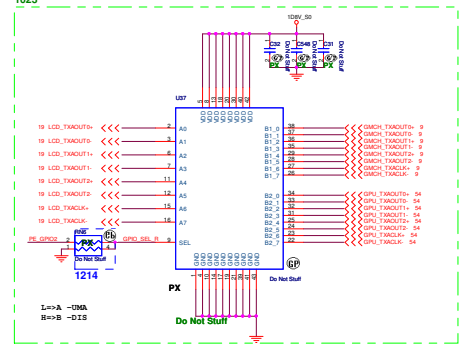
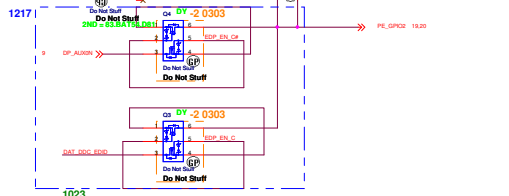
Function Table

Input (B)	Function
L	B_1 Connected to A
H	B_1 Connected to A

H = HIGH Logic Level L = LOW Logic Level



<http://laptopblue.vn/>



LCD/INVERTER/CCD CONN

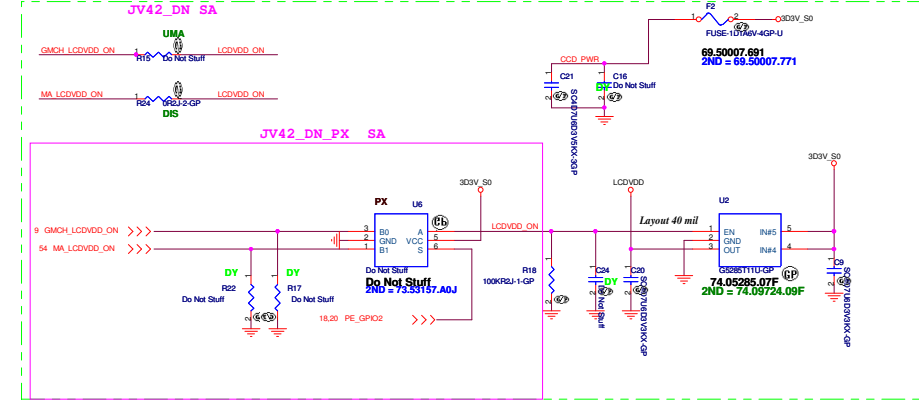
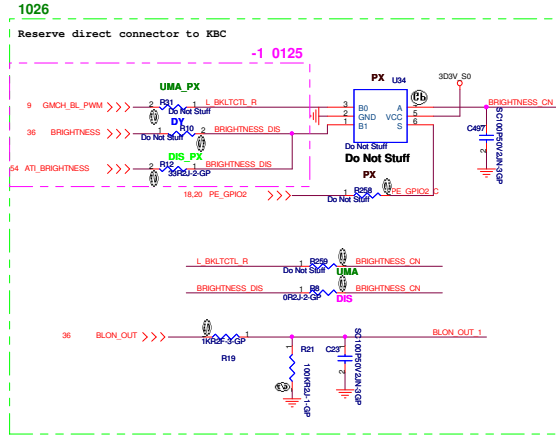
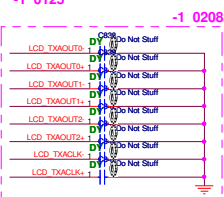
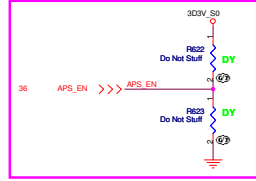
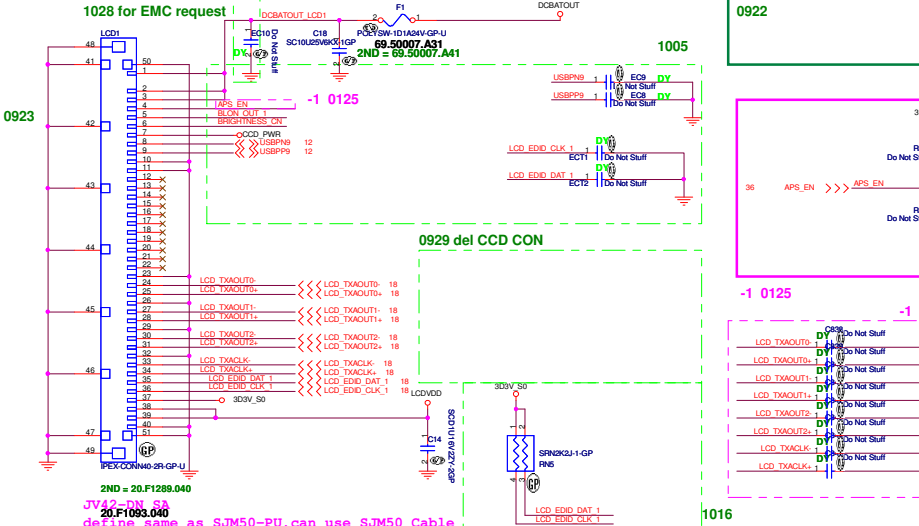
<http://laptopblue.vn/>

Change PX

0922

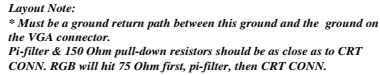
Inverter Pin		
Pin	Symbol	
1	Vin	
2	Vin	
3	Brightness	
4	BLON	
5	GND	
6	GND	

CCD Pin		
Pin	Symbol	
1	CCD_PWR	
2	USB-	
3	USB+	
4	GND	
5	GND	



Discrete Medicon Hydris

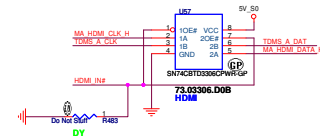
緯創資通 Wistron Corporation		
21F, 8th, Sec. 1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsin 321, Taiwan, R.O.C.		
File	LCD CONN	
Size	Document Number	Rev
Custom	JE40-DN	-3
Date	Friday, March 26, 2010	Sheet 19 of 63

[illegible]

H = High-Low Case L = Low-Low Case

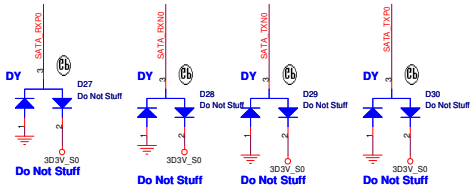
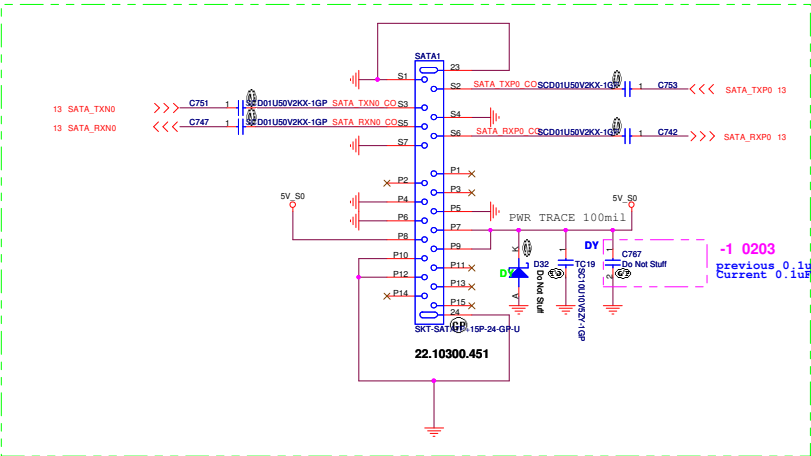


Title			CRT Connector		
Size	Document Number				Rev
	JE40-DN				-3
Date: Friday, March 26, 2010			Sheet	20	of 63



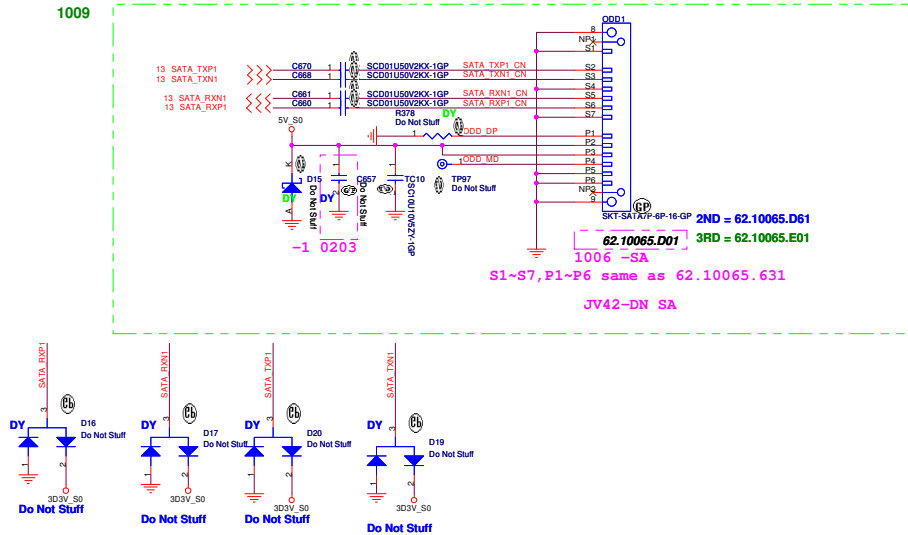
SATA Connector

0923



ODD Connector

1009



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緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Year	Journal	Volume	Page
1. The Effect of Temperature on the Rate of Reaction of Hydrogen Peroxide with Potassium Iodide	John Doe	2018	Journal of Chemical Education	95	1234
2. Synthesis and Characterization of a New Polymeric Material	Jane Smith	2019	Macromolecules	52	5678
3. Kinetic Study of the Reaction Between Sulfur Dioxide and Hydrogen Sulfide	Michael Brown	2020	Chemical Kinetics and Catalysis	101	9876
4. Investigation of the Catalytic Activity of Various Metals in the Decomposition of Hydrogen Peroxide	Sarah White	2021	Applied Catalysis A: General	400	1122
5. Thermodynamic Properties of Aqueous Solutions of Sodium Sulfate	David Black	2022	Journal of Solution Chemistry	51	3456

ODD

Size

Document Number

lev

JE40-DN

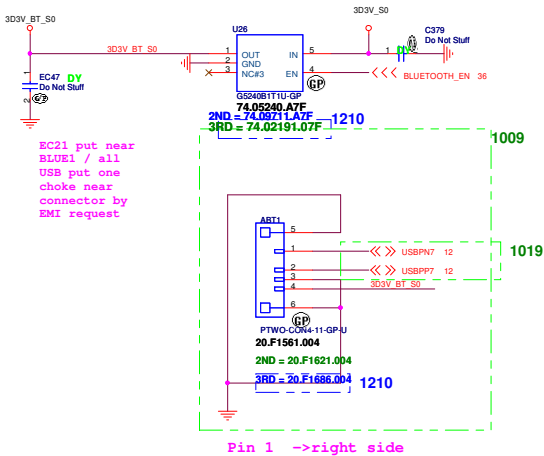
Date: Friday, March 26, 2010

Sheet 23

3

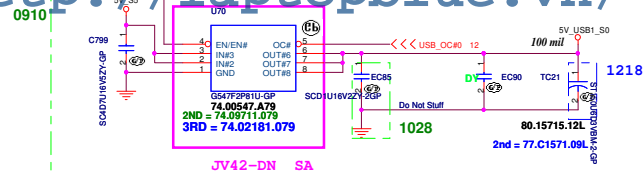
BLUETOOTH MODULE

1.5A / High Active Voltage 2V

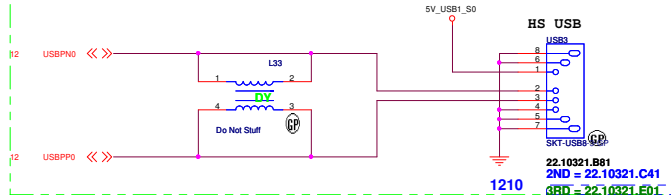


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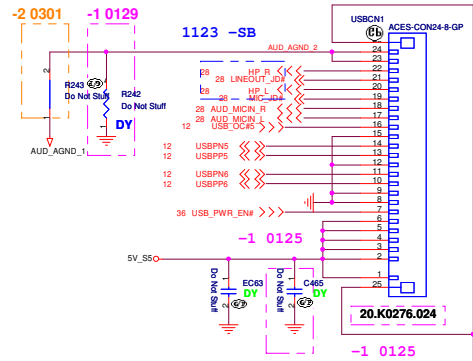
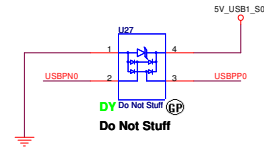
緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinshih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
BLUETOOTH			
Size	Document Number		Rev
	JE40-DN		-3
Date:	Friday, March 26, 2010	Sheet 24 of	63



1028 for EMC request



```
Pin1 -> right side
22.10218.T51 Pin1 -> left side (JV42-DN)
change Net sequence
```



Eng stuff 20.K0216.024 Pin1 -> left side
PD change to 20.K0276.024 Pin1 -> left side
so do not swap net

0910

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,

Taipei Hsien 221, Taiwan, R.O.C.

Title

USB

Size

Document Number

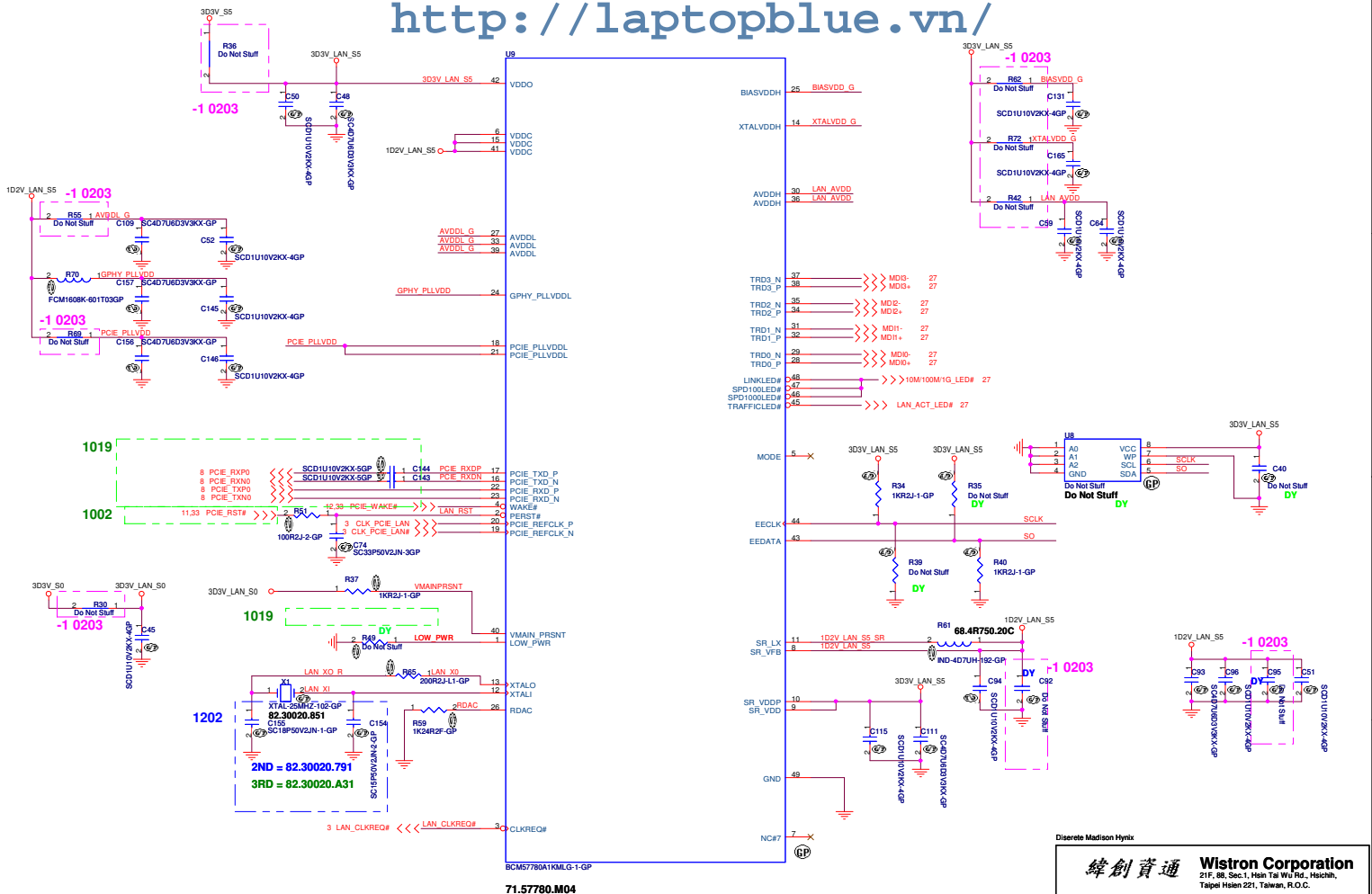
Rev

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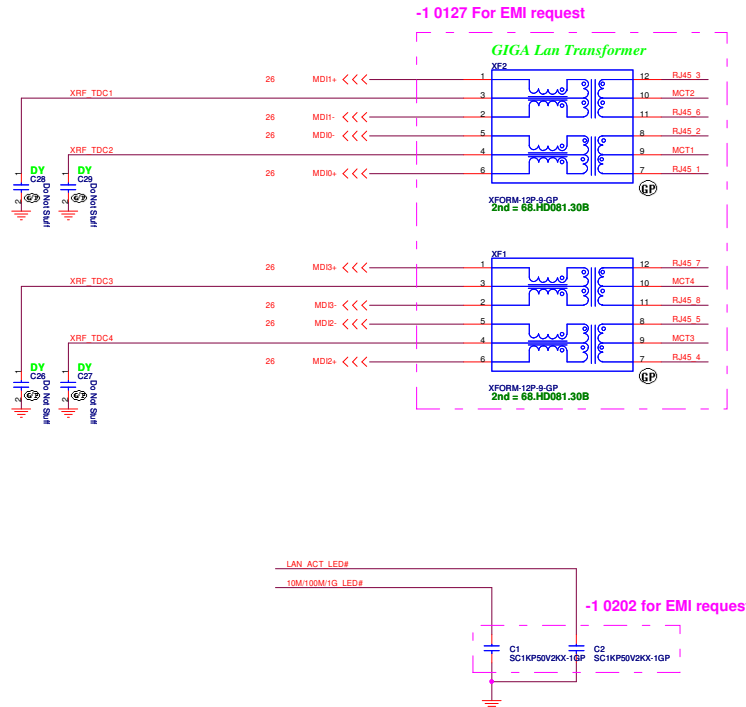
Q-DN



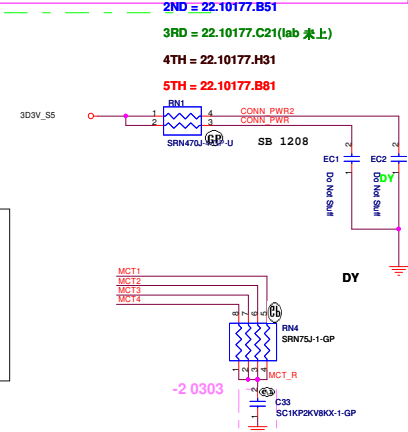
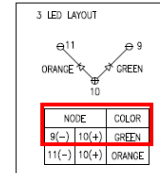
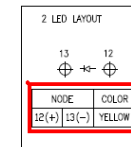
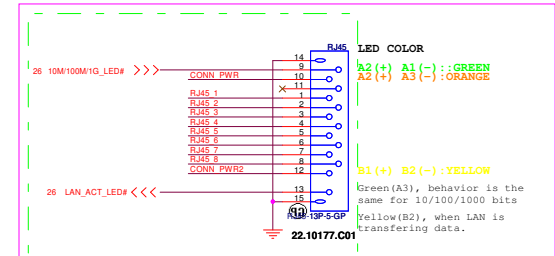
63



1. route on bottom as differential pairs.
2. Tx+/Tx- are pairs. Rx+/Rx- are pairs.
3. No vias, No 90 degree bends.
4. pairs must be equal lengths.
5. 6mil trace width, 12mil separation.
6. 36mil between pairs and any other trace.
7. Must not cross ground moat, except RJ-45 moat.



0923



Discrete Madison Hymn

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title

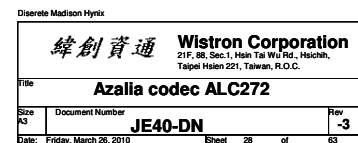
LAN CONN

Size	Document Number
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JE40-DN

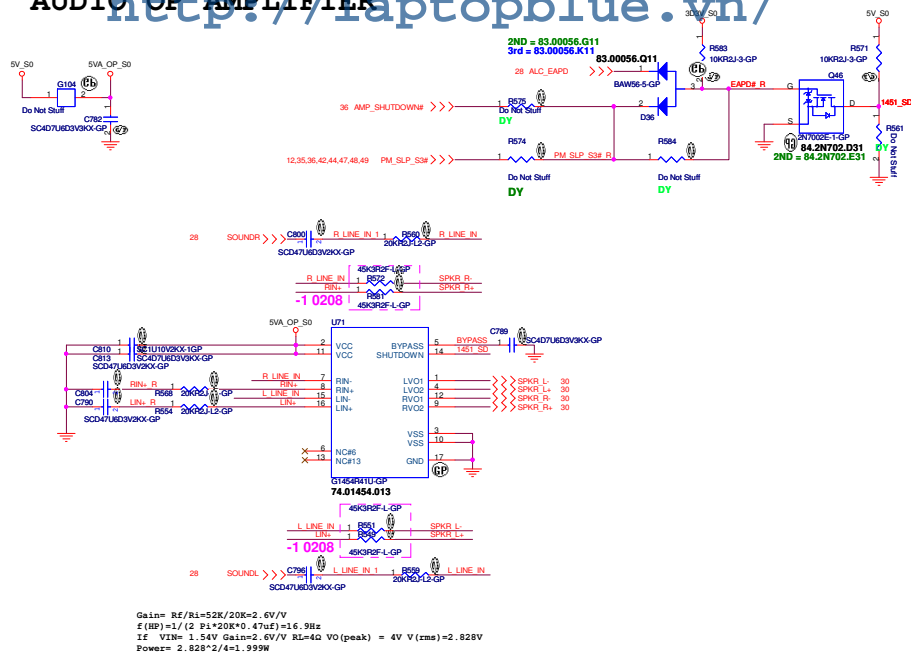
Date: Friday, March 26, 2010

Sheet 27 of 63



AUDIO OF AMPLIFIER

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Discrete Medium Hybrid

緯創資通

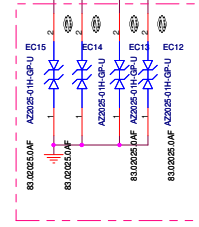
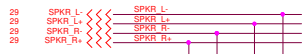
Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Heichih,
 Taipei Hsien 221, Taiwan, R.O.C.

AUDIO AMP		
JE40-DN		
Rev	Document Number	Rev
3		-3
Date: Friday, March 26, 2010	Sheet 29 of 63	

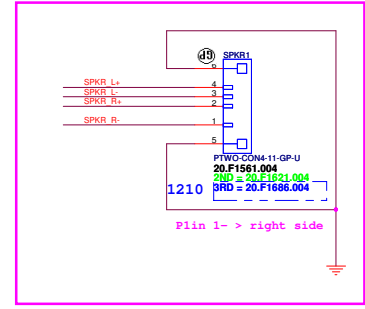
Internal Speaker



0910



-1 0125 for EMI request

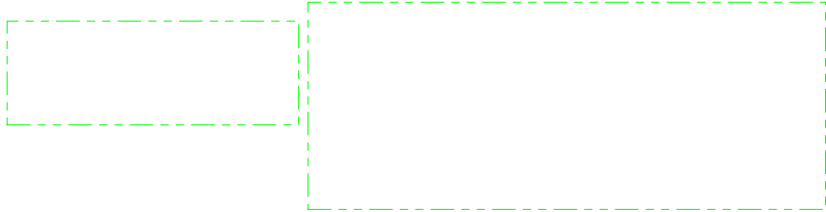


Pin 1 -> right side

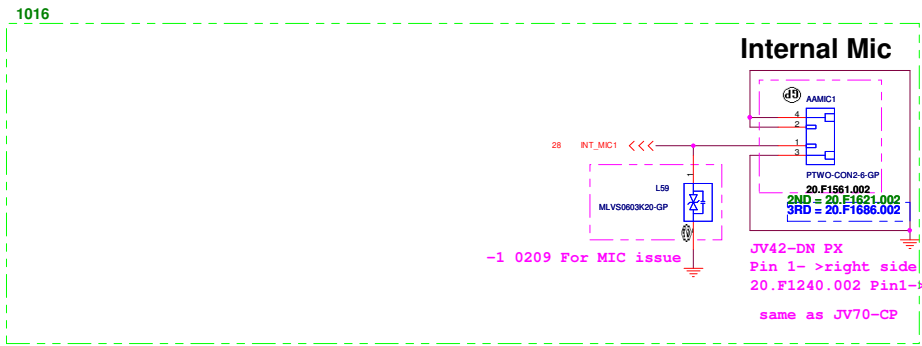
JV42-DN SA

0910

LINE OUT



0911



Internal Mic

-1 0209 For MIC issue

JV42-DN PX
Pin 1- >right side
20.F1240.002 Pin1->left side
same as JV70-CP

1210

Discrete Madison Hyinx

緯創資通		Wistron Corporation	
21F, 88, Sec 1, Hsin Tai Wu Rd., Hsinchu, Taipei Hsin 221, Taiwan, R.O.C.			
File			
Size			
Document Number			
Date: Friday, March 28, 2019			
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Rev -3			

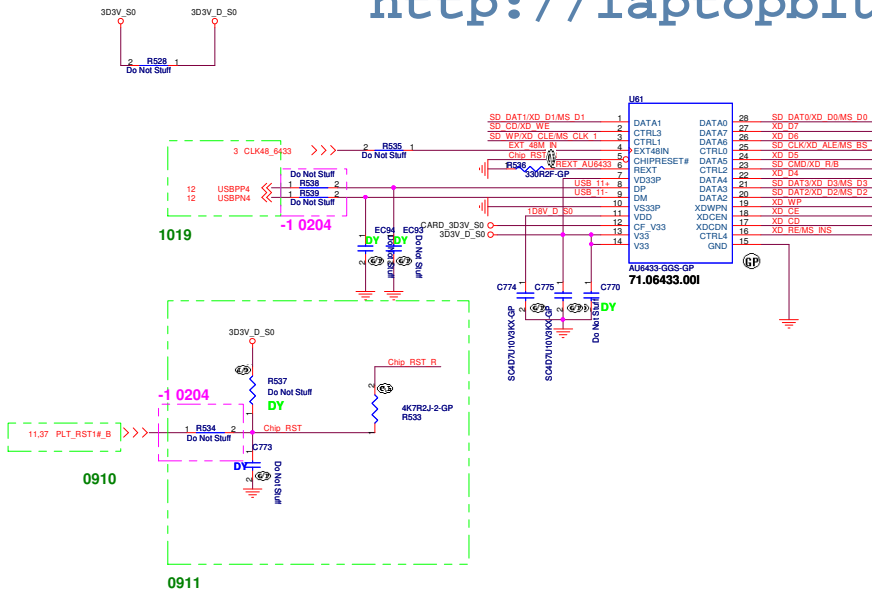
AUDIO JACK
JE40-DN

http://laptopblue.vn/

No Modem Function

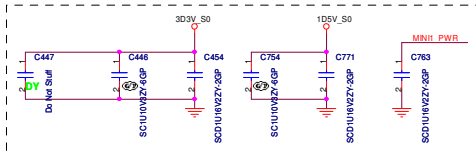
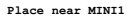
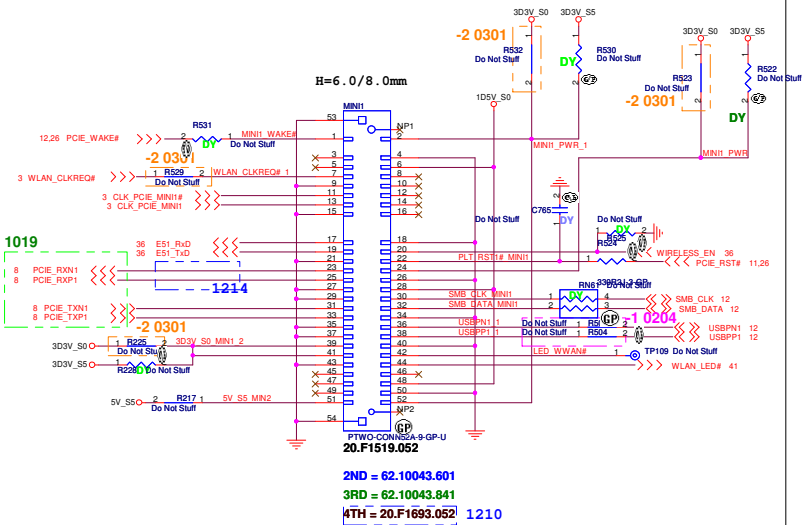
Discrete Madison Hyrix

緯創資通		Wistron Corporation	
		21F., 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		MDC	
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No Mini Card Function (Robson2 and 3G)

0923



Discrete Madison Hynix

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Year	Journal	Volume	Page
...

MINI CARD

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Discrete Madison Hynix

緯創資通

Wistron Corporation

21F, 88, Sec 1, Hsin Tai Wu Rd., Hsichih,
Taippei Hsien 221, Taiwan, R.O.C.

Title

NEW CARD

Size

Document Number

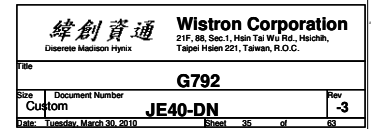
Rev

JE40-DN

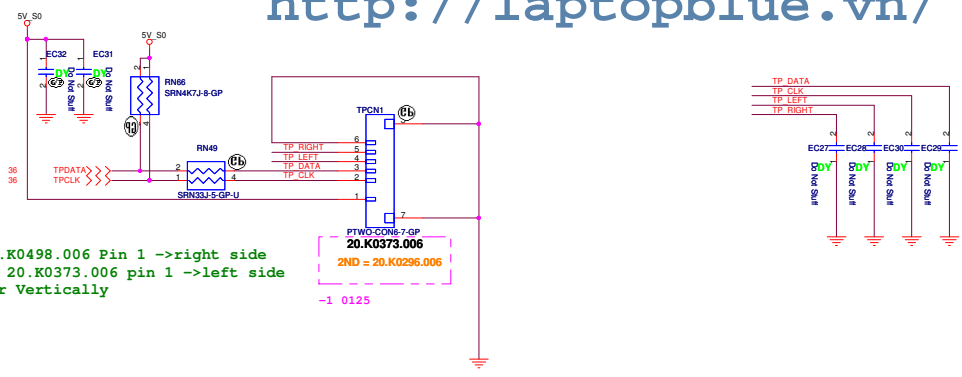
-3

Date: Friday, March 26, 2010

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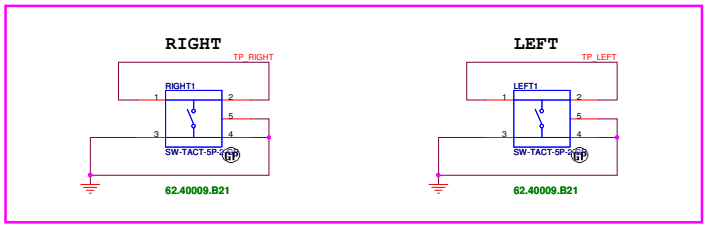




Eng stuff 20.K0498.006 Pin 1 -->right side
PD change to 20.K0373.006 pin 1 -->left side
so net mirror Vertically

PTWO CONA-7-GP
20.K0373.006
2ND = 20.K0296.006
-1 0125

-1 0205



No Finger printer Function

緯創資通

Wistron Corporation

21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

NONE BOARD

Size

A3

Document Number

JE40-DN

Date

Friday, March 26, 2010

Rev

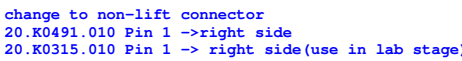
-3


Sheet

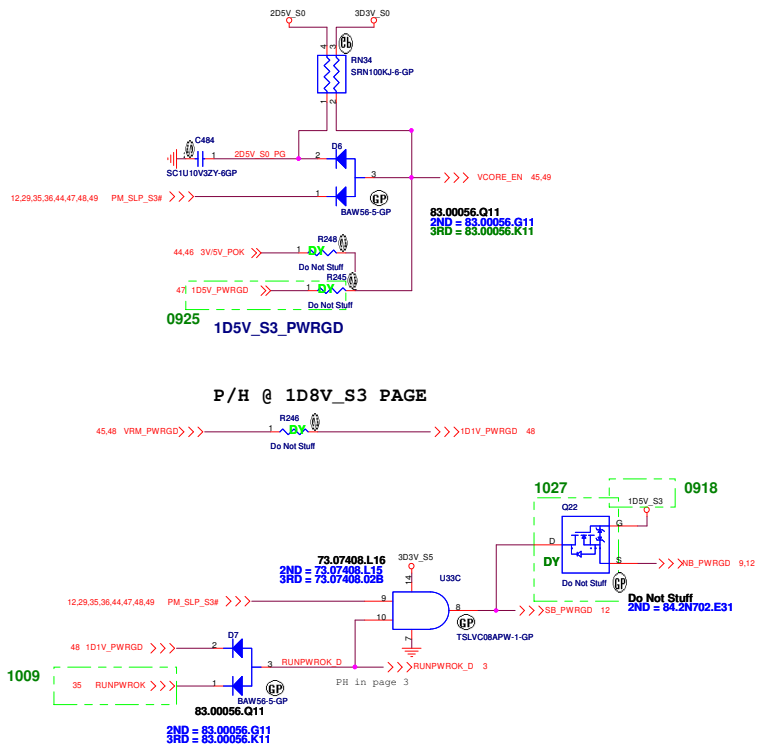
39

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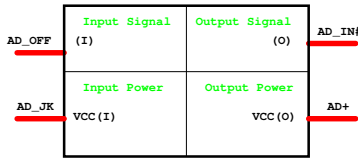
63



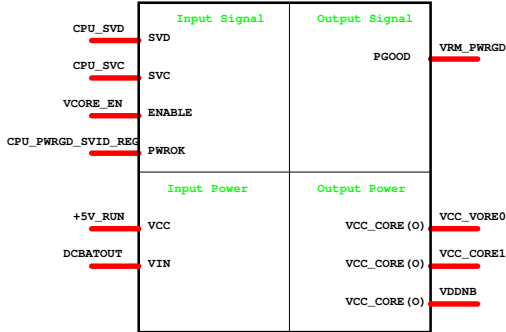
 緯創資通 Wistron Corporation 21F, No.1, Hsin Tai Wu Rd., Hsuehchi, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
LED		
Size A3	Document Number JE40-DN	Rev -3
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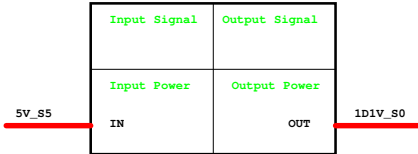
Adapter



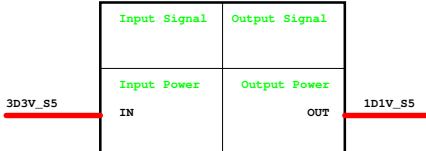
CPU_CORE ISL6265HRTZ



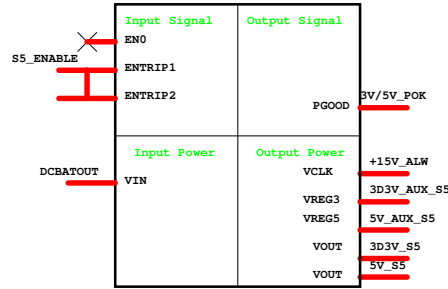
1D1V LDO RT8209E



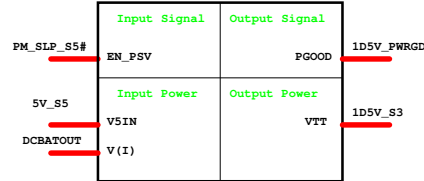
1D1V LDO RT9025



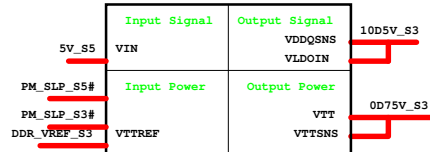
DCDC 5V/3D3V(RT8223)



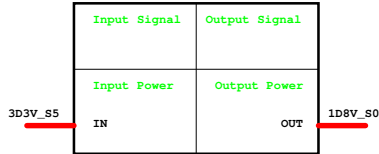
DCDC 1D5V(RT8209E)



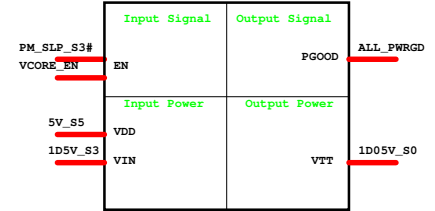
0D75V LDO RT9026



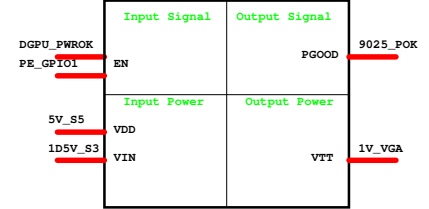
1D8V LDO RT8015A



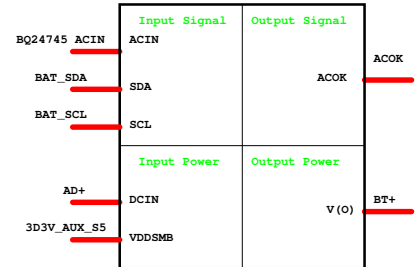
DCDC 1D05V_S0(RT9025)



DCDC 1V_VGA(RT9025)



CHARGER BQ24745



Discrete Madison Hynix

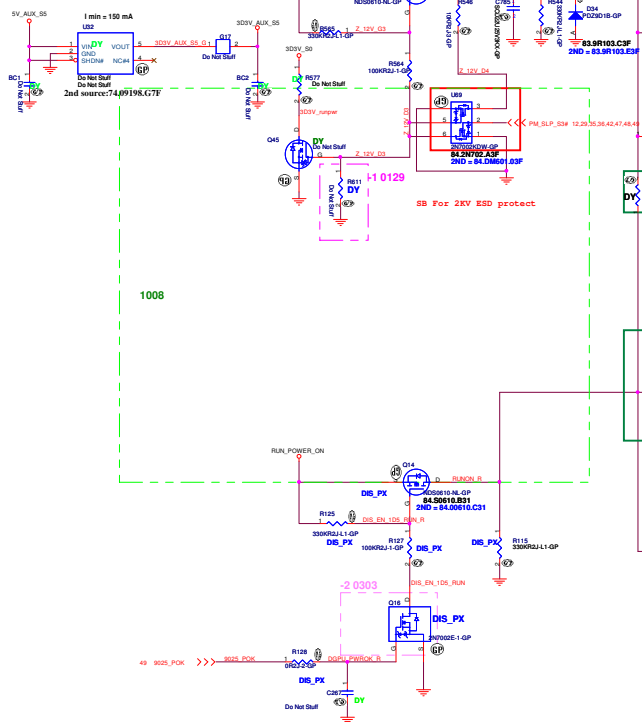
緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsinchu,
Taippei Hsien 321, Taiwan, R.O.C.

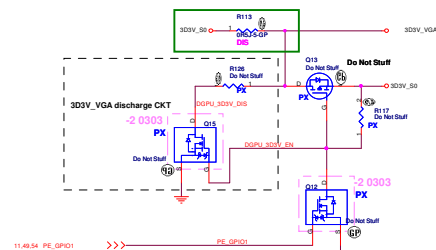
Power Block Diagram			
File	Document Number	Rev	
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Aux Power

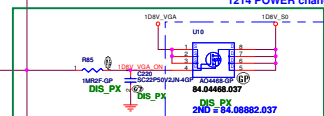
3D3V AUX S5



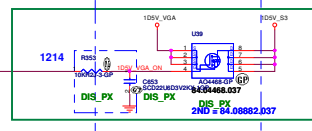
+3VS to 3.3V_DELAY Transfer



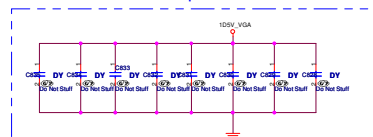
1214 POWER change BOM



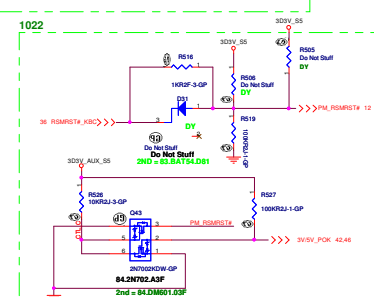
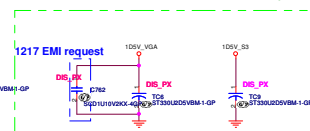
1214 [



1217 RF request



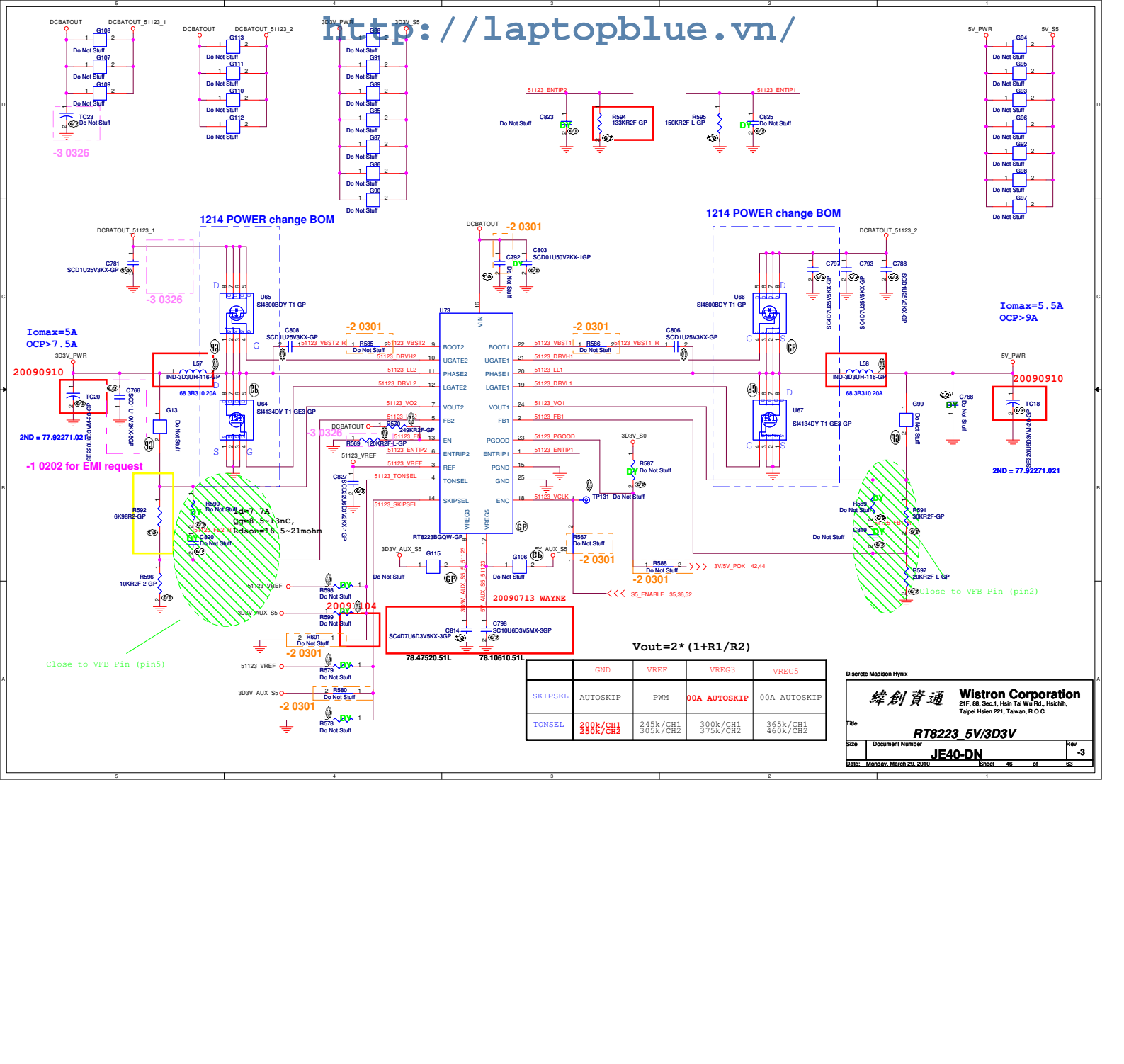
Please closed U77 for Park issue






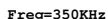
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
CPU Vcore(ISL6265HR)			
Size	Document Number		Rev
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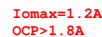
	GND	VREF	VREG3	VREG5
SKIPSEL	AUTOSKIP	PWM	00A AUTOSKIP	00A AUTOSKIP
TONSEL	200k/CH1 250k/CH2	245k/CH1 305k/CH2	300k/CH1 375k/CH2	365k/CH1 460k/CH2

Discrete Madison Hynix			
	Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsinchi, Taipei Hsien 221, Taiwan, R.O.C.		
Title			
RT8223 5V/3D3V			
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$$V_{out} = 0.75V * (R1 + R2) / R2$$

RT9026 for 0D75V_S3



2ND = 74.02997.B79

0917

Discrete Madison Hynix

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **RT8209E 1D5V**

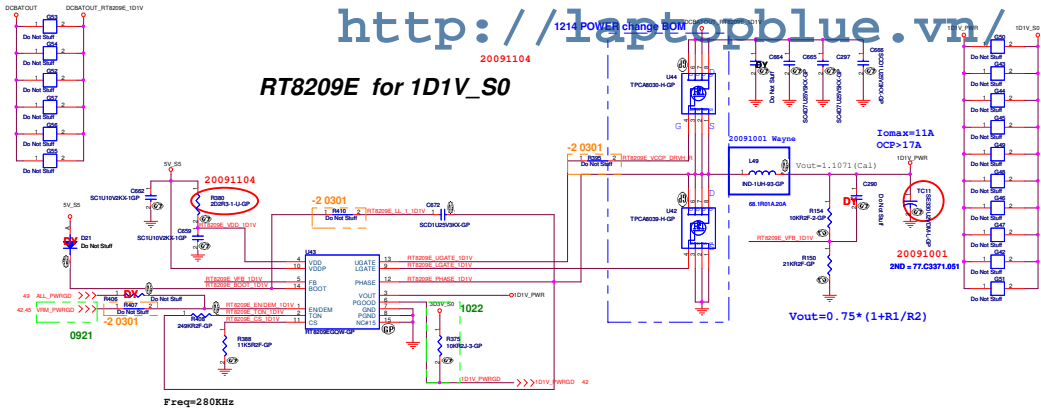
Size	Document Number
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JE40-DN

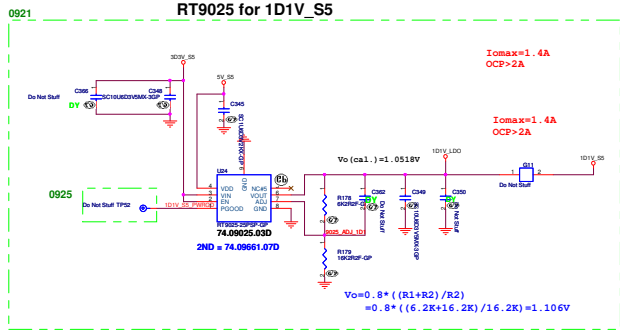
Date: Monday, March 29, 2010

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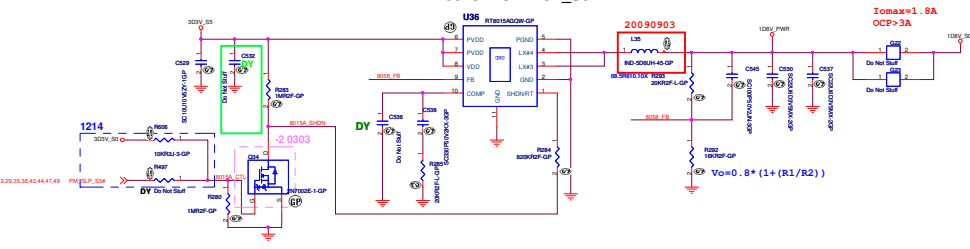
RT8209E for 1D1V_S0

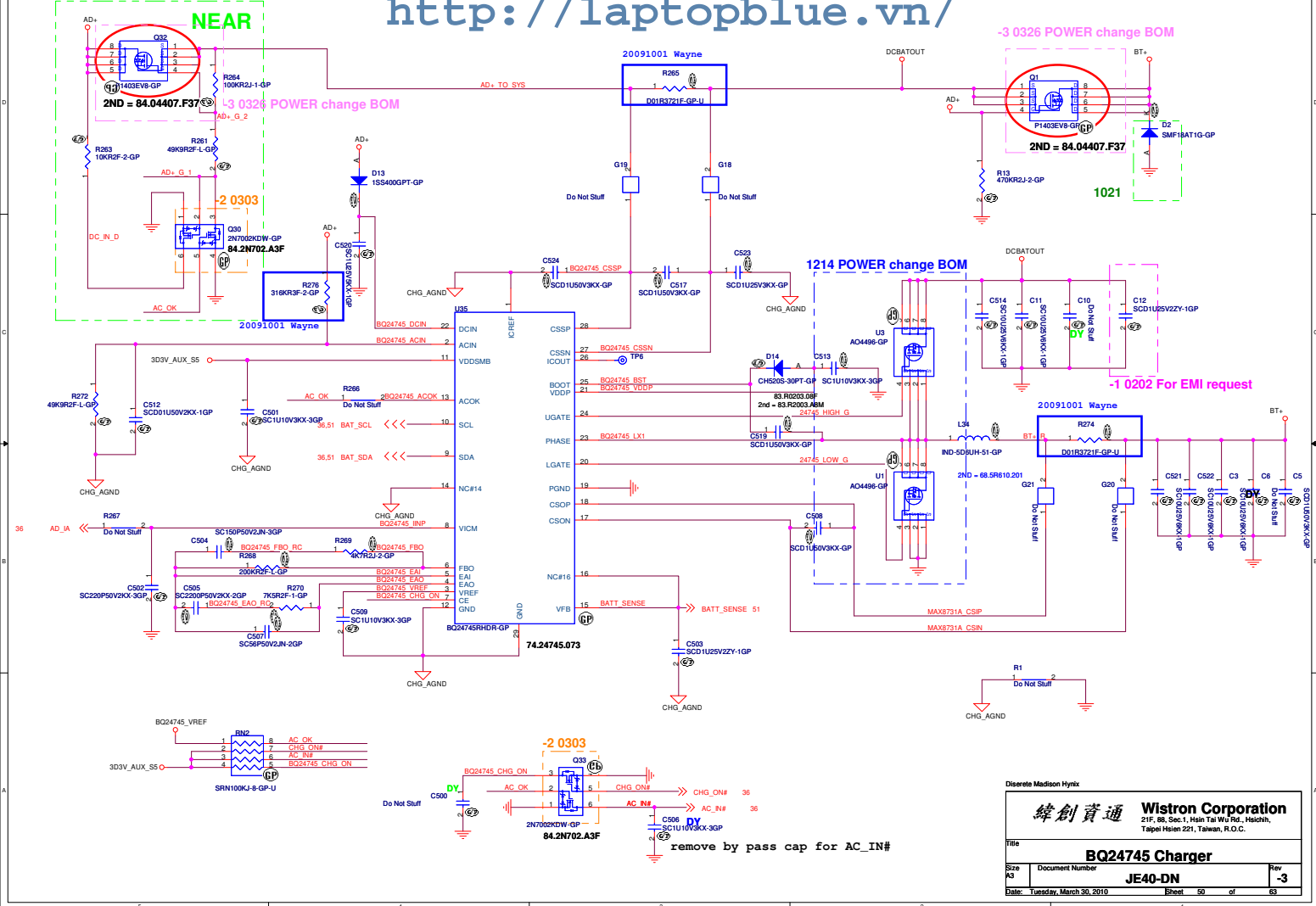


RT9025 for 1D1V_S5



RT8015A for 1D8V_S0





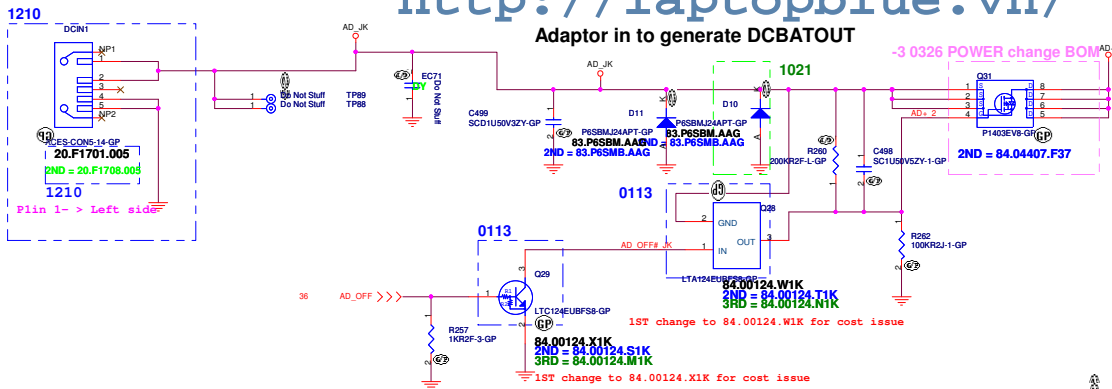
Discrete Madison Hymn:

緯創資通

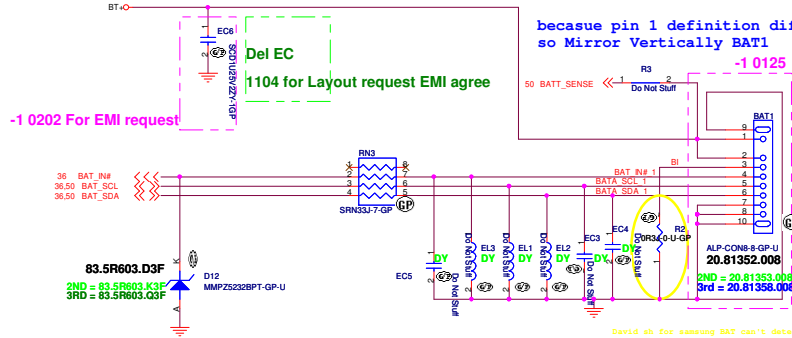
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

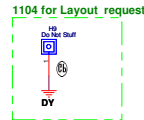
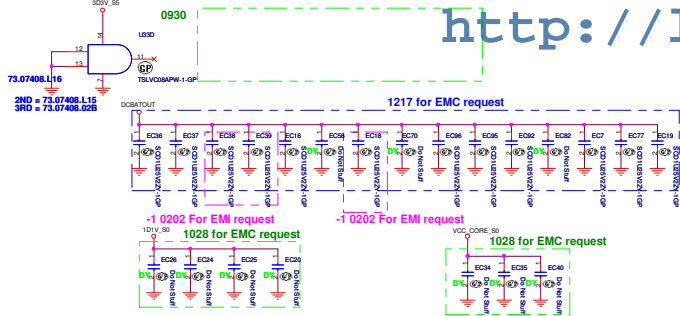
Title			
BQ24745 Charger			
Size A3	Document Number		Rev
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Adaptor in to generate DCBATOUT

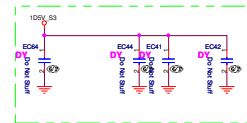


BATTERY CONNECTOR

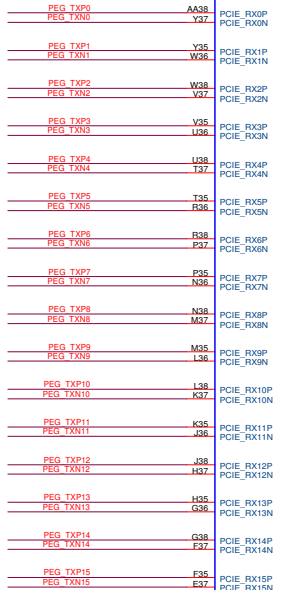




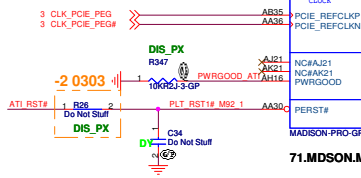
1028 for EMC request



8 PEG_TXP[15..0] << PEG_TXP[15..0]
8 PEG_TXN[15..0] << PEG_TXN[15..0]

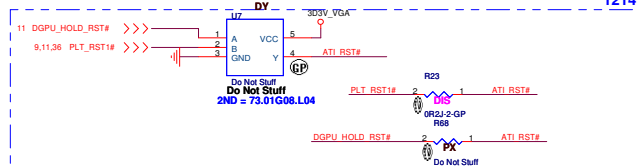


PCI EXPRESS INTERFACE

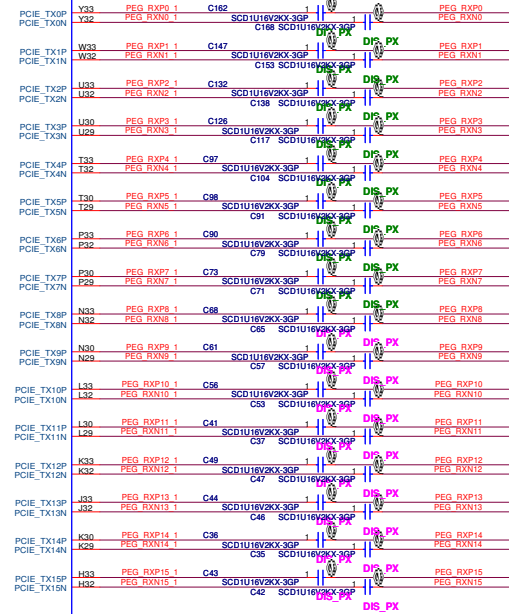


0908
PCI_ECALRP M
PCI_ECALRN M
R64 1K2R2F-L-GP
R63 2K2R2F-3-GP

1214



8 PEG_RXP[15..0] << PEG_RXP[15..0]
8 PEG_RXN[15..0] << PEG_RXN[15..0]



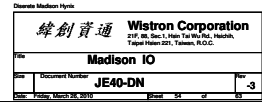
Discrete Madison Hynix

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title **Madison PCIE**

Size Document Number **JE40-DN** Rev **-3**

Date: Friday, March 28, 2019 Sheet 53 of 53



Discrete Median Hori

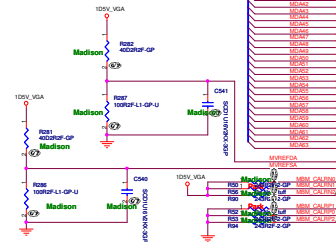
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

File				Madison POWER			
Size	Document Number						Rev
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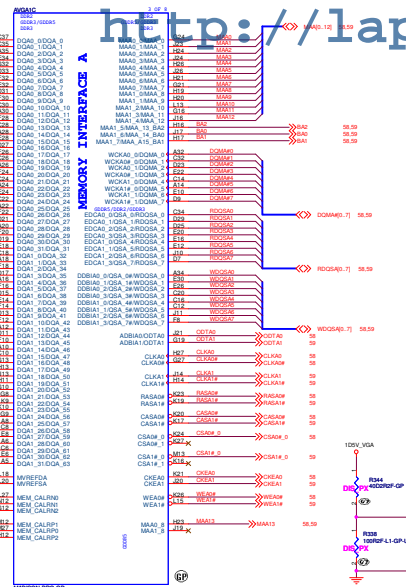


For SSTL-1.8/SSTL-2/DRI/GDDR1: 0.5 * VDDRI.
For DOR3/GDDR3/GDDR4/GDDR5: 0.7 * VDDRI.

DIVIDER RESISTORS	GDDR5	DOR3	DOR3
MYREF	1.5V	1.8/1.5V	1.5V
MYREF TO PWR	40.2R	50.2R	40.2R
MYREF TO GND	100R	100R	100R

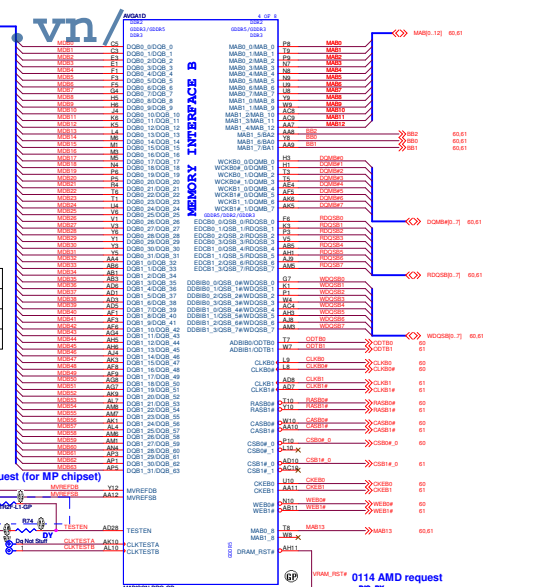
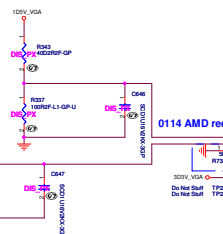


Madison: MEM_CALRP[0,2] signals are used
Park: MEM_CALRP1 and MEM_CALRP1 are used



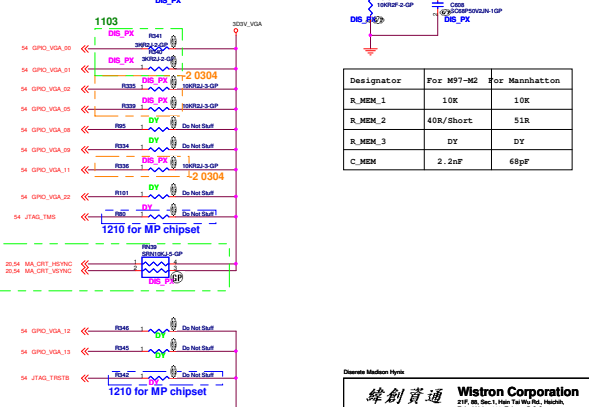
For SSTL-1.8/SSTL-2/DRI/GDDR1: 0.5 * VDDRI.
For DOR3/GDDR3/GDDR4/GDDR5: 0.7 * VDDRI.

DIVIDER RESISTORS	GDDR5	DOR3	DOR3
MYREF	1.5V	1.8/1.5V	1.5V
MYREF TO PWR	40.2R	50.2R	40.2R
MYREF TO GND	100R	100R	100R



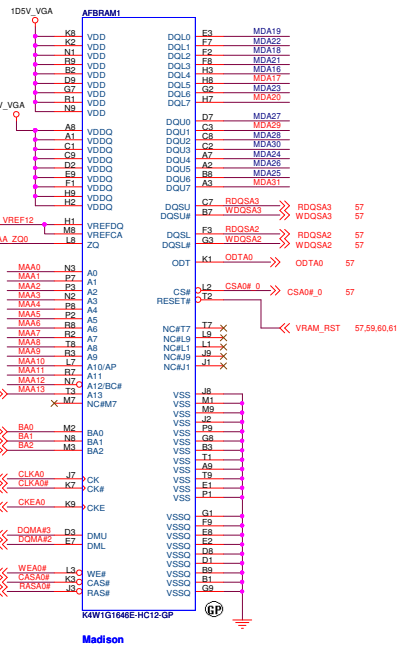
STRAPS	PIN	DESCRIPTION	RECOMMENDED SETTINGS S: DO NOT RETAIL RESISTOR I: RETAIL RESISTOR X: DESIRED (DEPENDENT) N/A: NOT APPLICABLE
TX_PWRN_ENB (Internal PD)	GP100	FOR FULL TX OUTPUT SWING Transmitter Power Savings Enable = 50k Tx output swing = Full Tx output swing	X
TX_DEMUR_EN (Internal PD)	GP101	Transmitter De-emphasis Enable = 0: 2x de-emphasis disabled = 1: 2x de-emphasis enabled	X
RESERVED	GP108	RESERVED	0
BIF_VGA_DIS	GP109	VGA ENABLED	0
RESERVED	GP1021	RESERVED	0
BIOS_ROM_EN	GP1022_ROMCS	ENABLE EXTERNAL BIOS ROM	0
VIP_DEVICE_STRAP_ENA (Internal PD)	GP1013,12,11	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT If BIOS_ROM_EN=1, then Config[3:0] Defines the ROM type If BIOS_ROM_EN=0, then Config[3:0] Defines the primary memory aperture size	X X X
RSVD	V2SYNC		0
RSVD	R2SYNC		0
AUD[1] AUD[0] (Internal PD)	VGA_HSYNC VGA_VSYNC	AUD[1:0] 0:No audio function 1:Audio for Displayport and HDMI (1 if adapter is detected) 10:Audio for Displayport only 11:Audio for both Displayport and HDMI	X X

AMD RESERVED CONFIGURATION STRAPS			
ALLOW FOR PULLED PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET			
R2SYNC, GENERIC1, GPIO2, GPIO21			
If BIOS_ROM_EN (GPIO22) = 0		If BIOS_ROM_EN (GPIO22) = 1	
Size of the primary memory aperture	GPIO[13,12,11]	Manufacturer	Part Number
128MB	x000	ST	M25P05A
256MB	x001	Microelectronics	M25P10A
512MB	x010		M25P20
1GB	x		M25P40
2GB	x		M25P80
4GB	x		
		Chingis (Formerly PMC)	Pw2SLV012A Pw2SLV010A



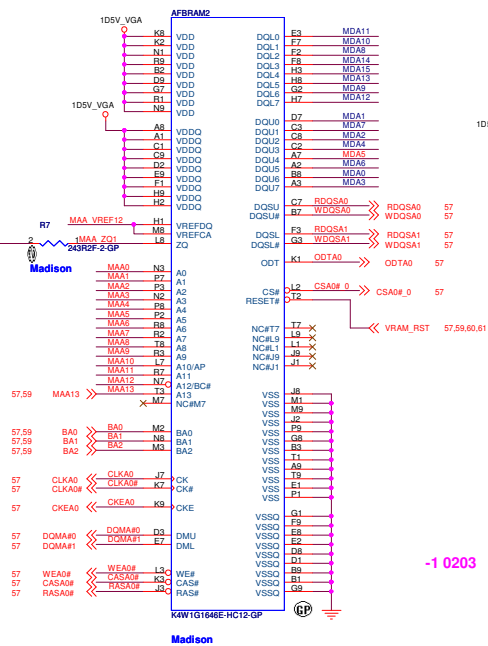
Designator	For M97-M2	For Mannheim
R_MEM_1	10K	10K
R_MEM_2	40R/Short	51R
R_MEM_3	DY	DY
C_MEM	2.2nF	68pF

Wistron Corporation
2/F, No. 101, Hsinchu Road, Hsinchu, Taiwan 30010, Taiwan, R.O.C.
Madison Memory / Straps
JE40-DN
3

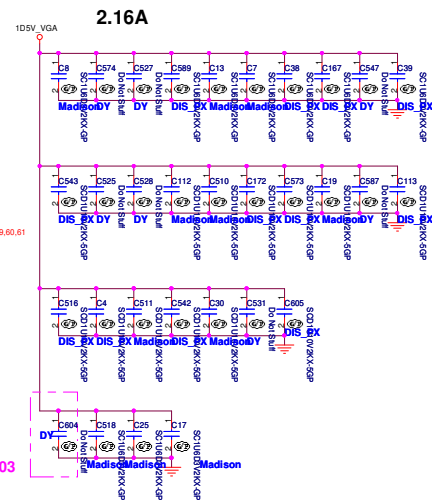


72.41164.H0U

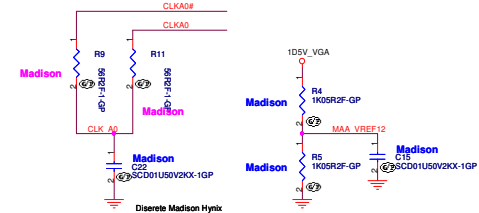
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SAMSUNG: 72.41164.H0U
HYNIX:   72.51G63.C0U
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72.41164.H0U

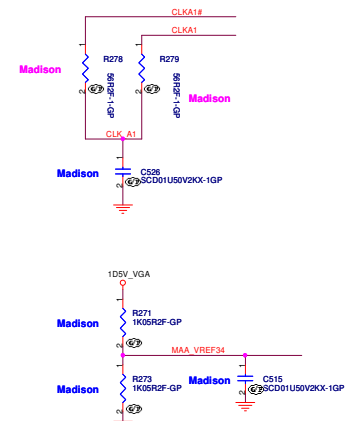
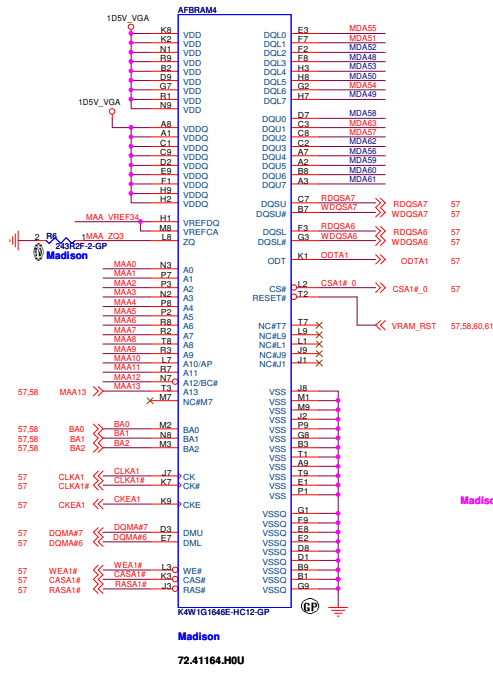


-1 0203



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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
VRAM(1/4)			
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		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsuehlin, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
VRAM(2/4)			
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72.41164.H0U



72.41164.H0U



HYNIX: 72.51G63.C0U



Dierette Madison Hynix		緯創資通 Wistron Corporation 21F, 8B, Sec.1, Hsin Tai Wu Rd., Heichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Size A3		VRAM(3/4) JE40-DN	
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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
RT8209E VGA CORE			
Size	Document Number	Rev	
Custom	JV42-DN	-3	
Date: Monday, March 29, 2010	Sheet 62	of	63

NB
RS780-->RS880M
71.RS780.M02-->71.RS880.M02

SB
SB820M-1-GP
71.SB820.00U-->71.SB820.M03

SATA_CALRP
A11: 80 歐姆 1% resistor to GND
A12: TBD 歐姆 1% resistor to GND

SATA_CALRN
A11:A11: 931 歐姆 1% resistor to VDDAN_11_SATA
A12: TBD 歐姆 1% resistor to VDDAN_11_SATA.

VGA
Madison--> PN:71.MDSON.M01
Park--> PN:71.0PARK.M04

VRAM
Samsung-->VRAM FBRAM1~8 PN:VR.1GB0B.006
Hynix--> VRAM FBRAM1~8 PN:VR.1GB0G.004
ATI--> VRAM FBRAM1~8 PN: VR.1GB0T.002

CRT
UMA-->L1-->2R 0603
C62-->47U/6.3V
DIS-->L1-->Bead(L1 68.00217.711 L1608-UH38 SBK160808T-221Y-N-GP)
C62-->DY
PX--> 133 歐姆 (R58 63.10334.1DL R402H16 133R2F-GP)

R605(MAD/PARK/PX)
Madison-->R605 64.15005.6DL R402H16150R2F-L-GP
Park -->R605 64.15005.6DL R402H16150R2F-L-GP
PX -->R605 64.15005.6DL R402H16150R2F-L-GP

HDMI
UMA -->R612 R613 R618 R619 R620 R621 R624 R625 64.71505.6DL R402H16715R2F-2-GP

R432(MAD/PARK)
Madison-->15KR2F 64.15025.6DL
Park-->R432 64.68115.6DL R402H16 6.81KR2F

BOM
1st -> All SKU

R250
Madison-->R250 64.32425.6DL R402H16 32K4R2F
Park -->R250 64.32425.6DL R402H16 32K4R2F
PX -->R250 64.32425.6DL R402H16 32K4R2F

Discrete Madison Hynix

緯創資通

Wistron Corporation

21F, 88, Sec 1, Hsin Tai Wu Rd., Hsinshih, Taipei Hsien 221, Taiwan, R.O.C.

Title

NOTE

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