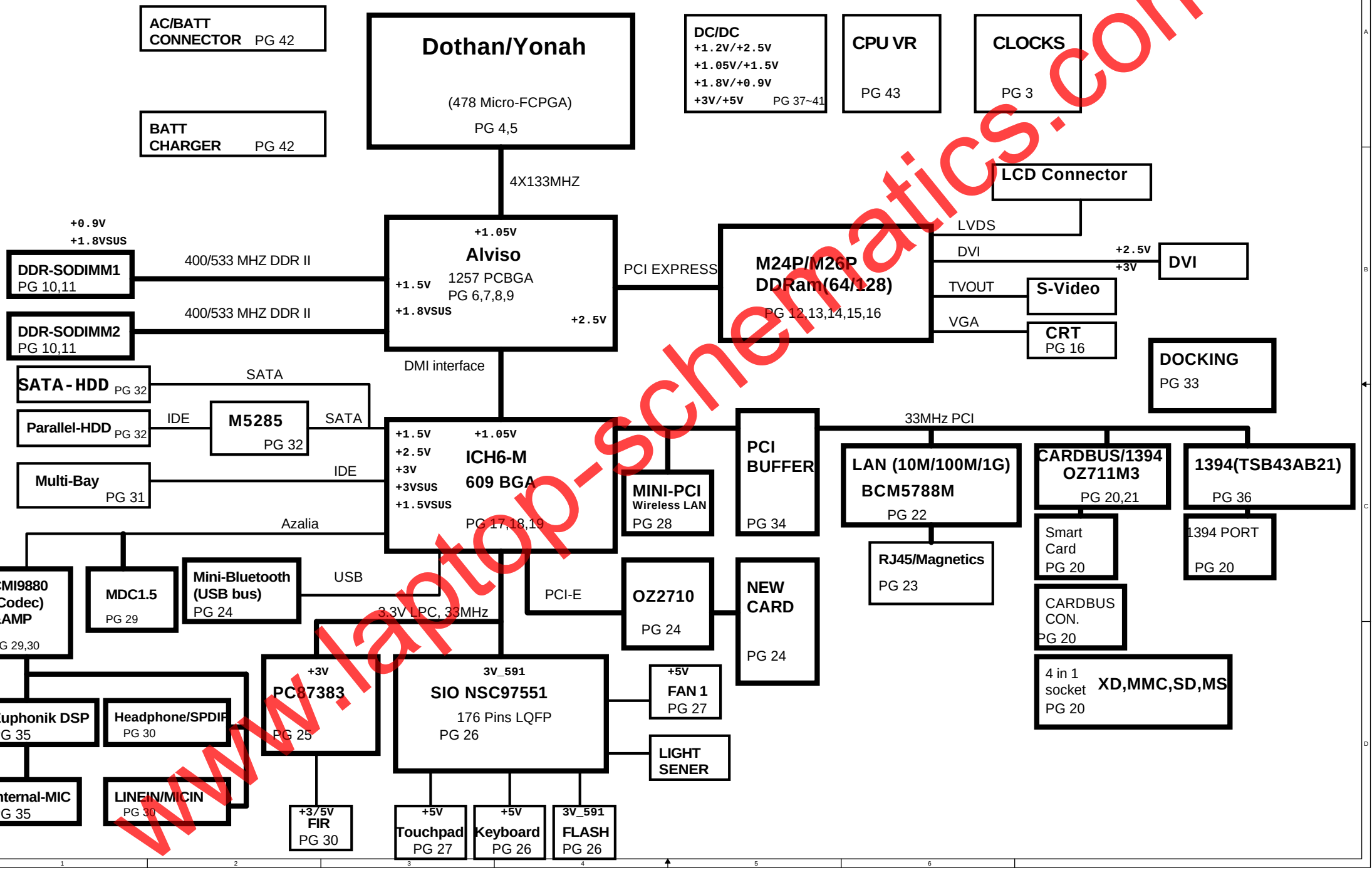


Check again

Voltage Rails		ON S0~S1	ON S3	ON S4	ON S5	Control signal
12VOUT		X	X	X	X	
3V_591		X	X	X	X	
5VPCU		X	X	X	X	
+3V_S5		X	X	X	X	S5_ON
3V_LAN		X	X	X	X	
+1.5V_S5		X	X	X	X	S5_ON
+1.8VSUS		X	X			SUSON
+3VSUS		X	X			SUSD
+5VSUS		X	X			SUSD
SMDDR_VTERM	DDR Termination voltage	X	X			MAINON
SMDDR_VREF		X				MAINON
VGA_PCIE_1.2V		X				MAINON
VCC_CORE	Core voltage for Processor	X				VR_ON
+VCCP	1.05V rail for Processor I/O	X				MAINON
+1.5V		X				MAINON
+1.8V		X				MAIND
+2.5V		X				MAIND
+3V		X				MAIND
+5V						MAIND
+12V		X				MAINON
+3VRUN		X				PCI_Switch_Power_ON
+5VRUN		X				PCI_Switch_Power_ON

Change History

5/28
1.System_DVI DET function move in EZ port , So,Del Q47,R557
2.Addition AND gate for DOCKING Power Good AND DockingIN Singal combine Circuit
3.Addition Power led circuit for system
4.Change D34 AND D35 + -
5.Addition LID Switch and LID connector
6.Addition RC Delay for PCIE1.2V
7.Change EC Three GPIO port same to ZL2
5/31
1.Change C145 PCB Footprint to 3528
2.Combine USB and bluetooth connector to 19pin connector 87212-1900
3.Change PCBFootprint 88216-1200 to 88213-1200
4.Change USB connector bypass C to 0805 10u
5.Adujst 80pin connector 3 singal
6/1
1.Update power all circuit for GND name
2.Addition OR to PRST
3.Change IDE RST

6/2
1.Change ICH-6 USB Port
2.Del CDR,CDL,CDGND Singal and DEL prevent CDR,CDL,CDGND noise circuit
6/4
1.U49,U50, Form 3VRUN change to +3V AND CHANGE MINPCI connector to PCI BUS,And addition PCI_SWRST # AND PCI_SWRST1#
2.Change BT_POWER NAME
3.Change VOIP AGND

6/7
1.Change VOIP AGND TO AGND2 for Layout

External PCI Devices

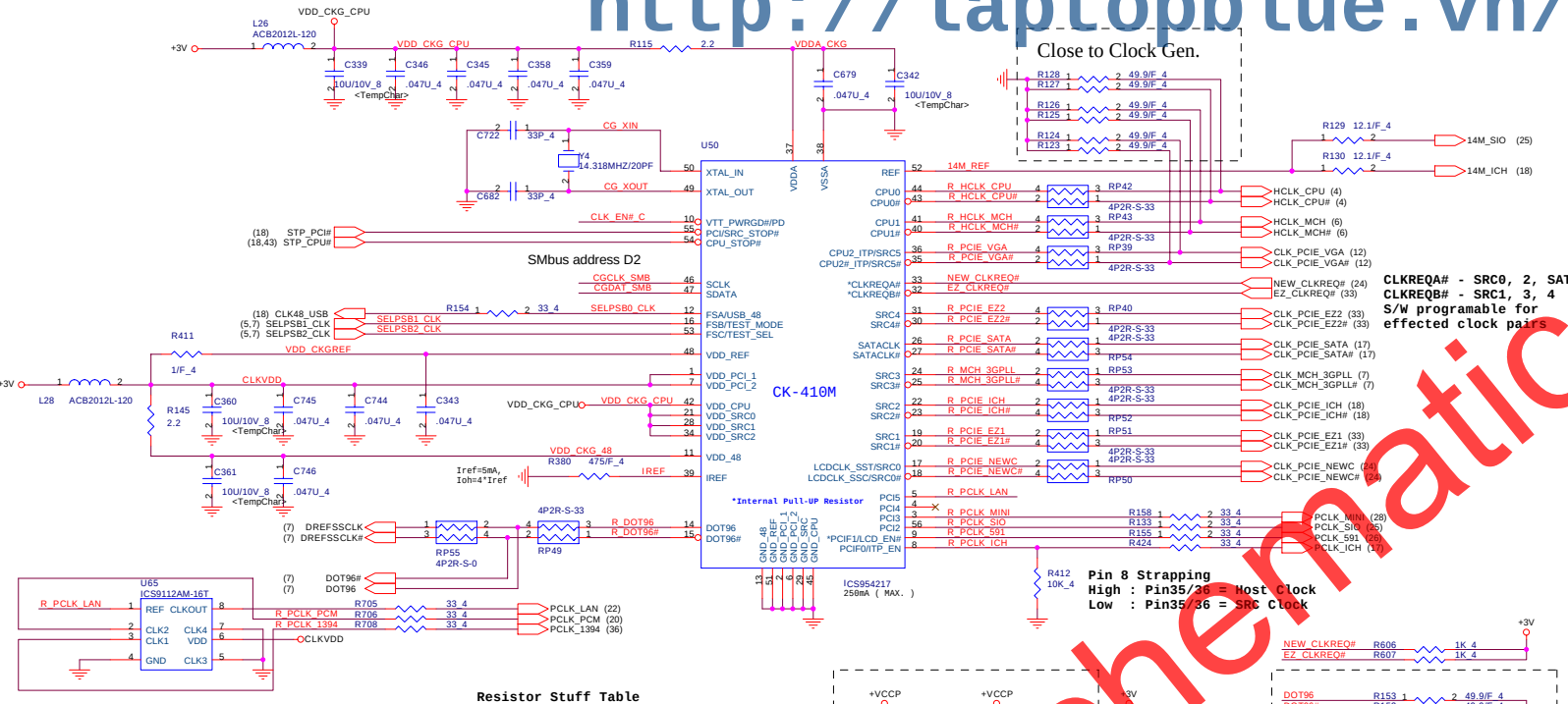
Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus+Smart Card	AD25	1	PIRQC/B
Mini-PCI	AD19	2	PIRQB/D
LAN	AD22	0	PIRQA
1394	AD23	3	PIRQD

EC SM Bus1 address

Device
Smart Battery
THERMAL SENSOR
LIGHT SENER
VOIP FLASH ROM

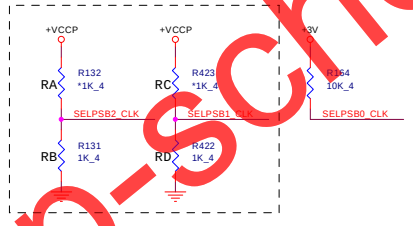
ICH6-M SM Bus address

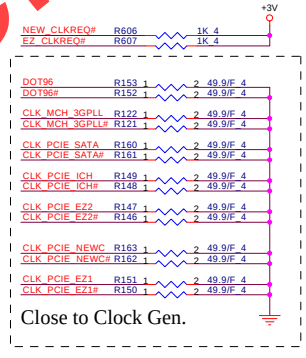
Device	
SODIMM	**1010 000X b**
Clock Gen	**1101 001x b**

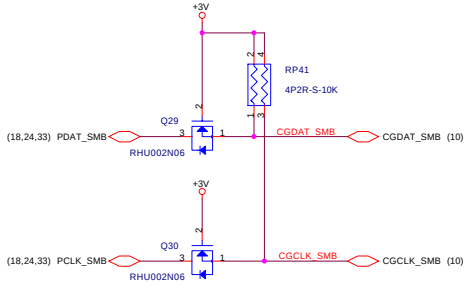


Resistor Stuff Table

	RA	RB	RC	RD
Dothan A 400	V	X	X	V
Dothan A 533	X	V	X	V
Dothan B	X	X	X	X







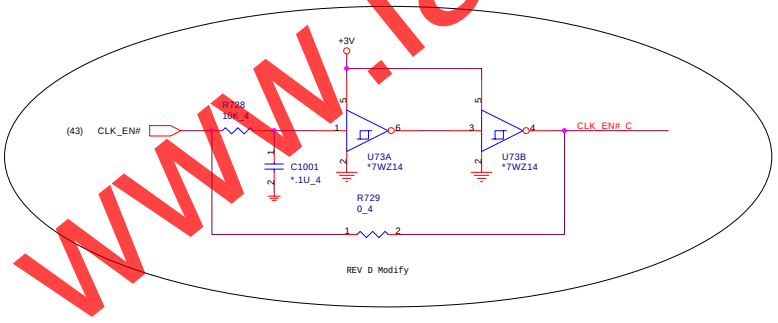
Clock Gen. Frequency Selection Table

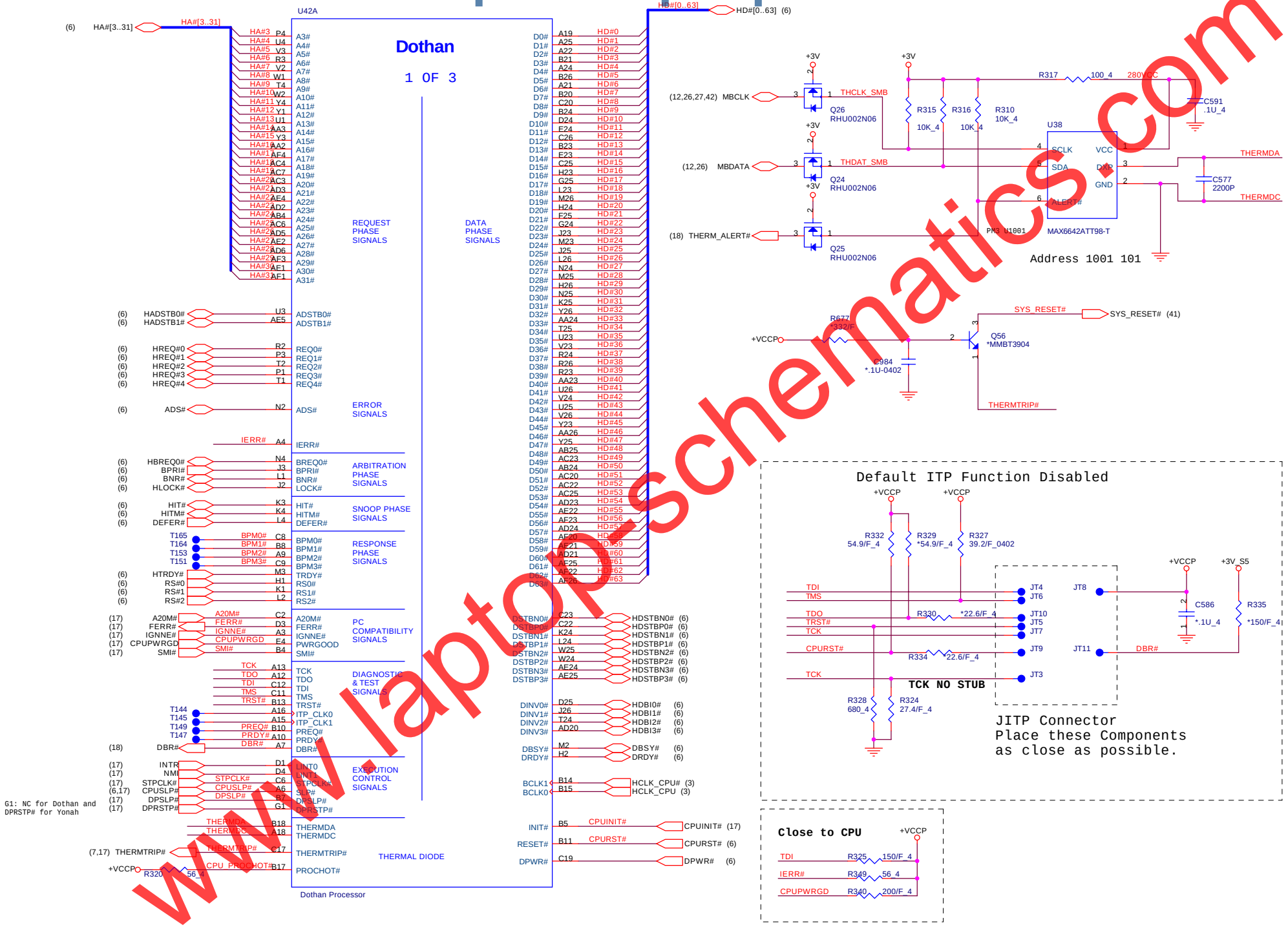
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

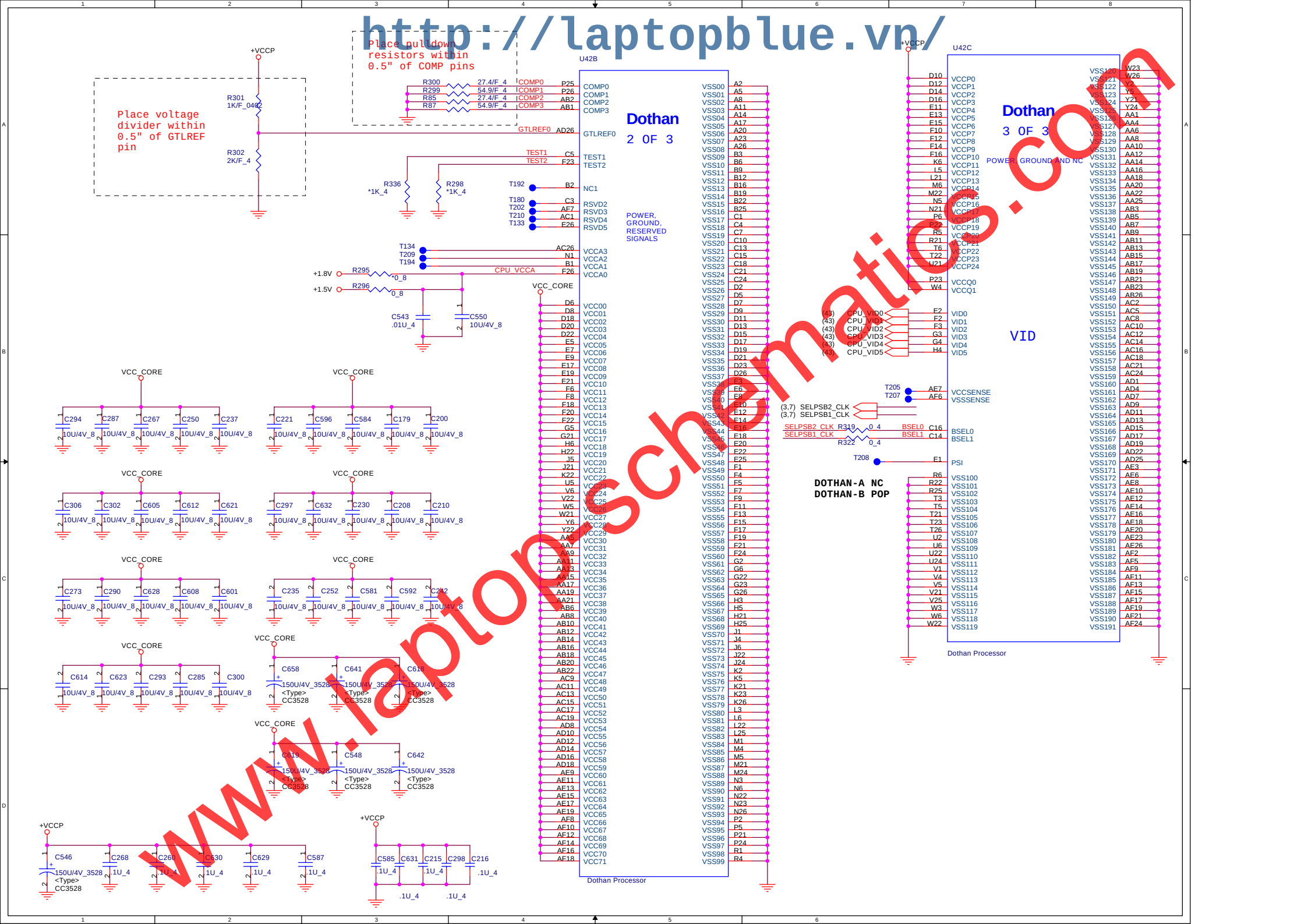
DOTHAN BSEL Output Value

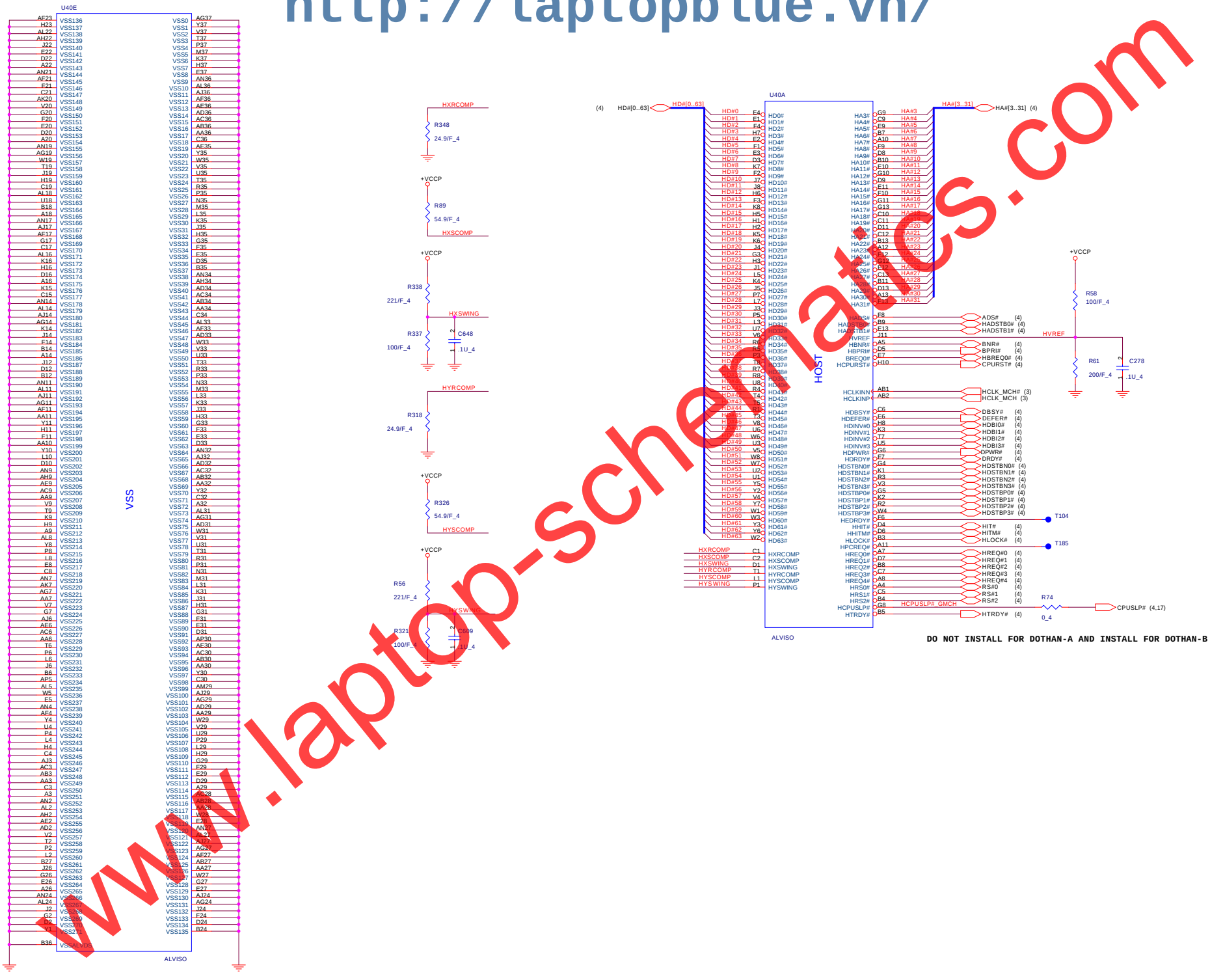
FSB Frequency	DOTHAN A-Step		DOTHAN B-Step	
	BSEL1	BSEL0	BSEL1	BSEL0
400 MHz	0	0	0	1
533 MHz	0	1	0	0

These are for backdrive issue









CFG[0:2]=100 FOR FSB 533
CFG[0:2]=101 FOR FSB 400

U40C

DMI
DMIRXN0
DMIRXN1
DMIRXN2
DMIRXN3
DMIRXP0
DMIRXP1
DMIRXP2
DMIRXP3
DMITXN0
DMITXN1
DMITXN2
DMITXN3
DMITXP0
DMITXP1
DMITXP2
DMITXP3

DDR MUXING
SM_CK0
SM_CK1
SM_CK2
SM_CK3
SM_CK4
SM_CK5
SM_CK0#
SM_CK1#
SM_CK2#
SM_CK3#
SM_CK4#
SM_CK5#
SM_CKE0
SM_CKE1
SM_CKE2
SM_CKE3
SM_CS0#
SM_CS1#
SM_CS2#
SM_CS3#
SM_OCDCOMP0
SM_OCDCOMP1
SM_ODT0
SM_ODT1
SM_ODT2
SM_ODT3
SMRCOMPN
SMRCOMPP
SMVREF0
SMVREF1
SMXSLEWIN
SMXSLEWOUT
SMYSLEWIN
SMYSLEWOUT

CFG/RSVD
CFG0
CFG1
CFG2
CFG3
CFG4
CFG5
CFG6
CFG7
CFG8
CFG9
CFG10
CFG11
CFG12
CFG13
CFG14
CFG15
CFG16
CFG17
CFG18
CFG19
CFG20
RSVD21
RSVD22
RSVD23
RSVD24
RSVD25
RSVD26
RSVD27

PM
BM_BUSY#
EXT_TS0#
EXT_TS1#
THRMTRIP#
PWROK
RSTIN#

LCK
DREF_CLKN
DREF_CLKP
DREF_SSCLKN
DREF_SSCLKP

NC
NC1
NC2
NC3
NC4
NC5
NC6
NC7
NC8
NC9
NC10
NC11

+VCCP
R60 4.7K_4
R72 1K_4
R67 1K_4
R66 *1K_4
SELPSB1_CLK (3,5)
SELPSB2_CLK (3,5)
FOR DDR533
Low=DMIx2
High=DMIx4
R68 *1K_4
R83 1K_4
R75 1K_4
FOR CPU533

PM_EXTTS#0
PM_EXTTS#1
PM_BMBUSY# (18)
R323 0_4
R303 100_4
THERMTRIP# (4,17)
IMVP_PWRGD (18)
PLTRST# (17,18,24,25,26,31,33)
DOT96# (3)
DOT96 (3)
DREFSSCLK# (3)
DREFSSCLK (3)

U40F

MISC
CLK_MCH_3GPLL (3)
CLK_MCH_3GPLL# (3)
SDVOCTRL_DATA
SDVOCTRL_CLK
GCLKN
GCLKP

TV
TVDAC_A
TVDAC_B
TVDAC_C
TV_REFSET
TV_IRTNA
TV_IRTNB
TV_IRTNC

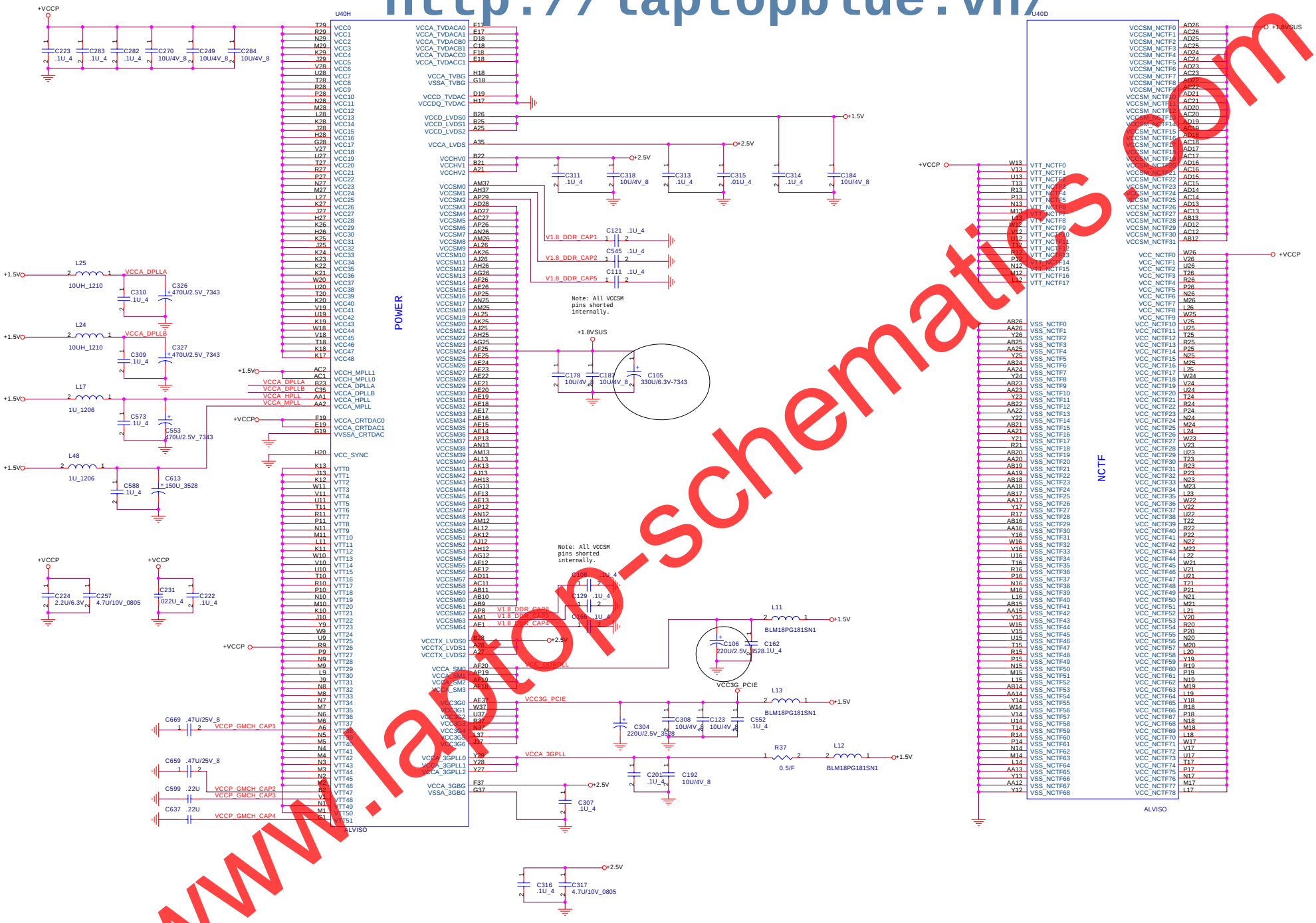
VGA
DDCCLK
DDCDATA
BLUE
BLUE#
GREEN
GREEN#
RED
RED#
VSYNC
HSYNC
REFSET

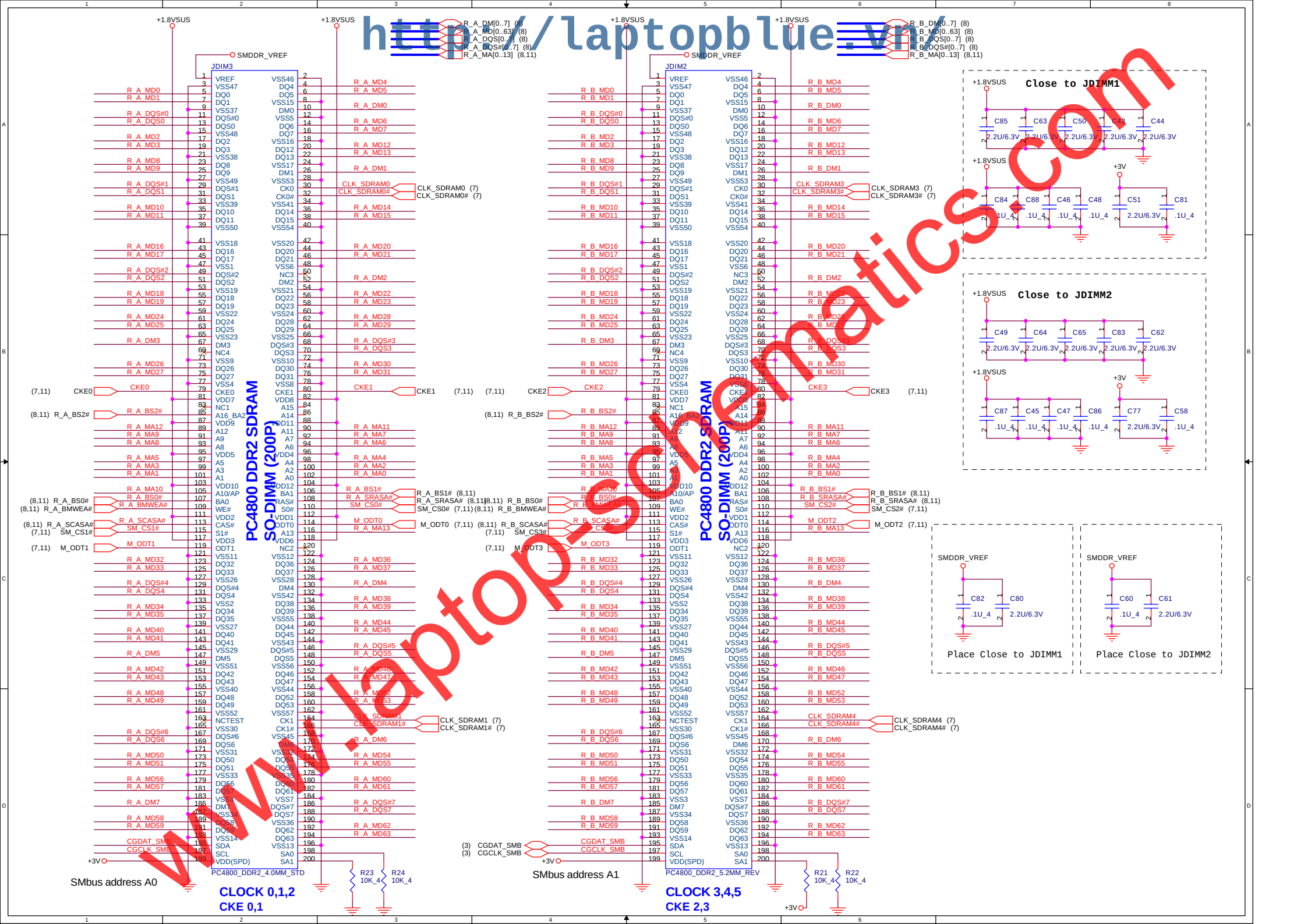
LVDS
LBKLT_CTRL
LBKLT_EN
LCTLA_CLK
LCTLB_DATA
LDDC_CLK
LDDC_DATA
LVDD_EN
LIBG
LVBG
LVREFH
LVREFL
LACLKN
LACLKP
LBCLKN
LBCLKP
LADATAN0
LADATAN1
LADATAN2
LADATAP0
LADATAP1
LADATAP2
LBDATAN0
LBDATAN1
LBDATAN2
LBDATAP0
LBDATAP1
LBDATAP2

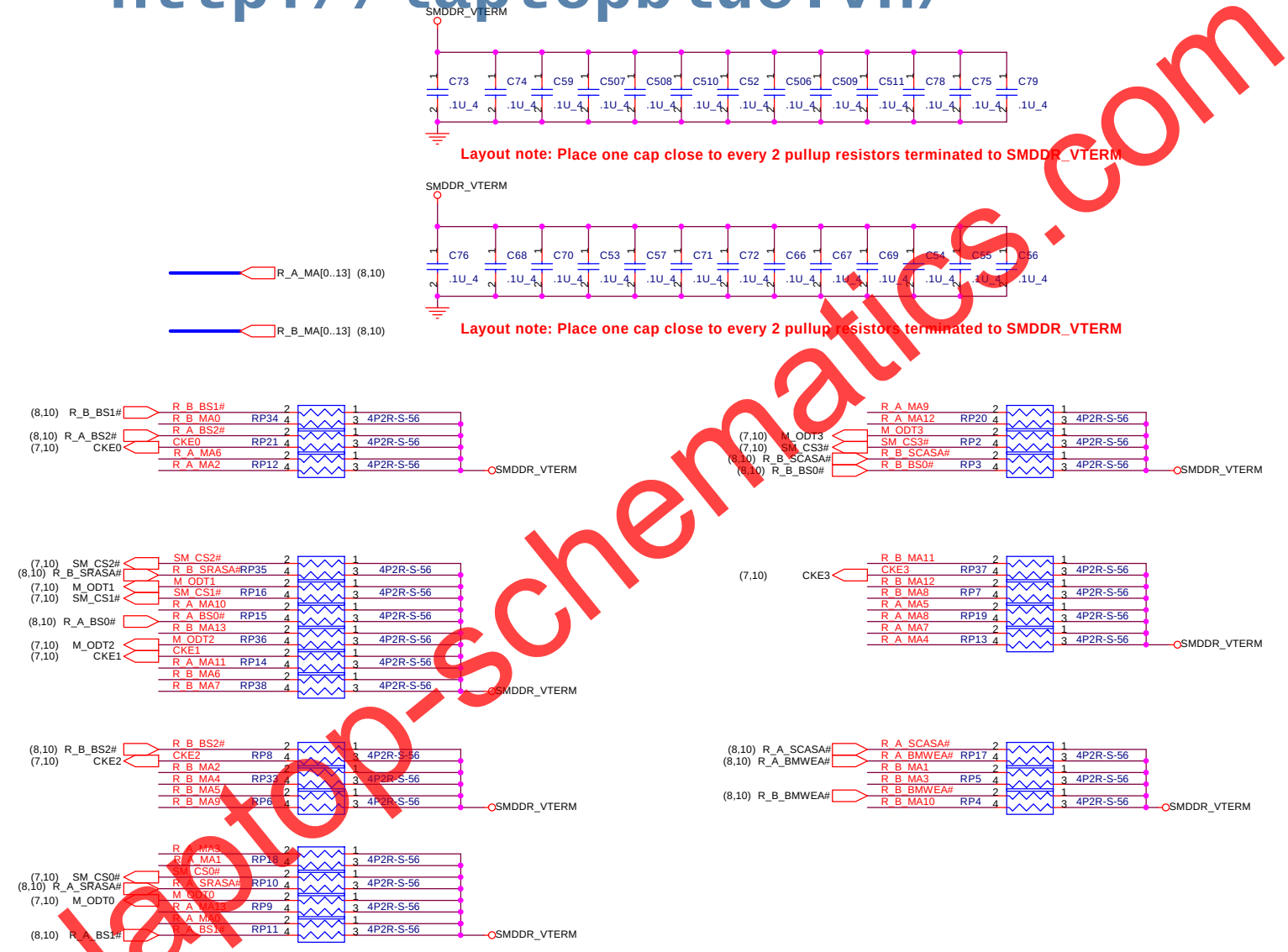
PCI-EXPRESS GRAPHICS
EXP_COMPI
EXP_ICOMPO
EXP_RXN0 ... EXP_RXN15
EXP_RXP0 ... EXP_RXP15
EXP_TXN0 ... EXP_TXN15
EXP_TXP0 ... EXP_TXP15

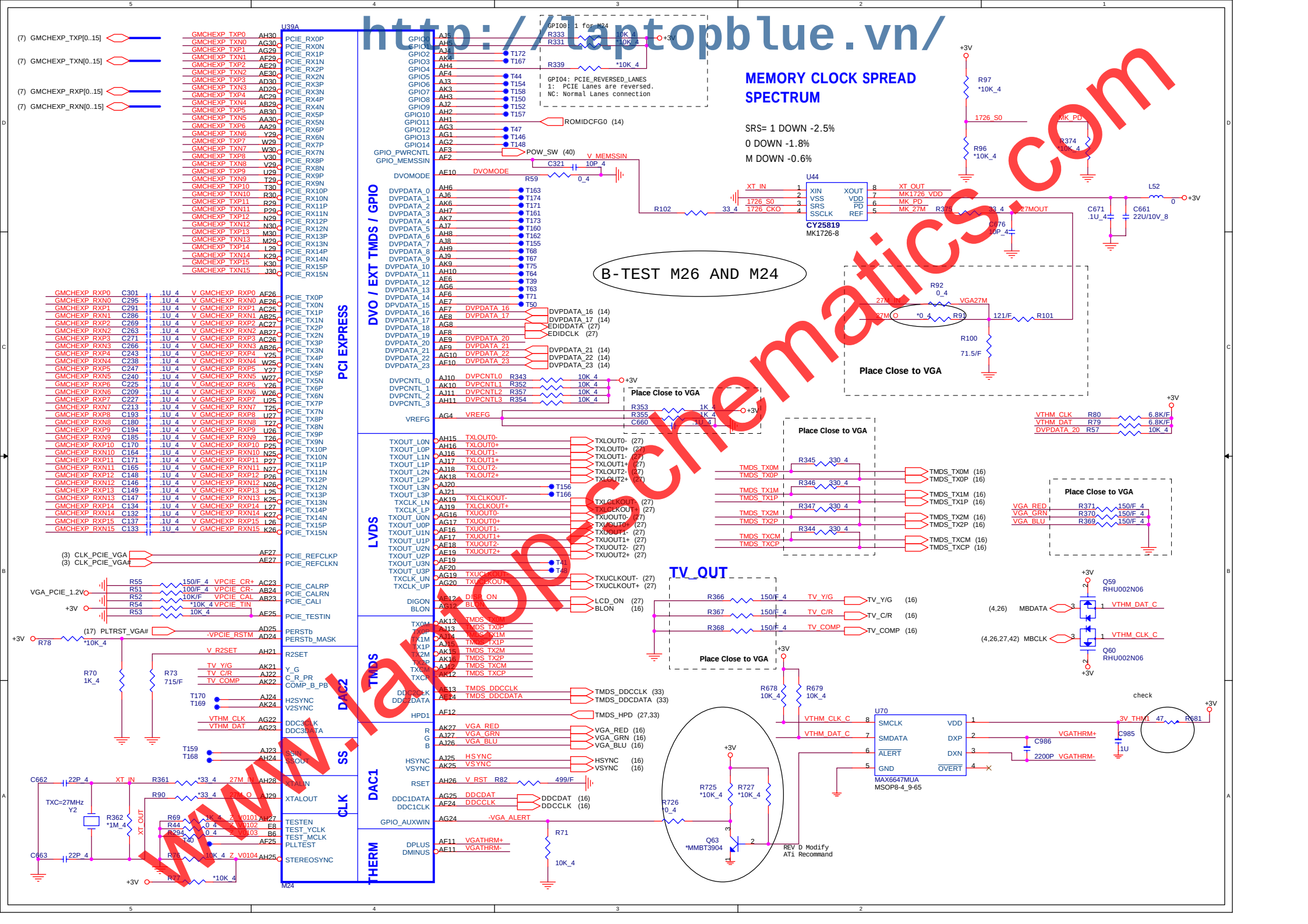
GMCHEXP_TXP[15..0] (12)
GMCHEXP_TXN[15..0] (12)
GMCHEXP_RXP[15..0] (12)
GMCHEXP_RXN[15..0] (12)
VCC3G_PCIE
R88 24.9/F_4
ALVISO

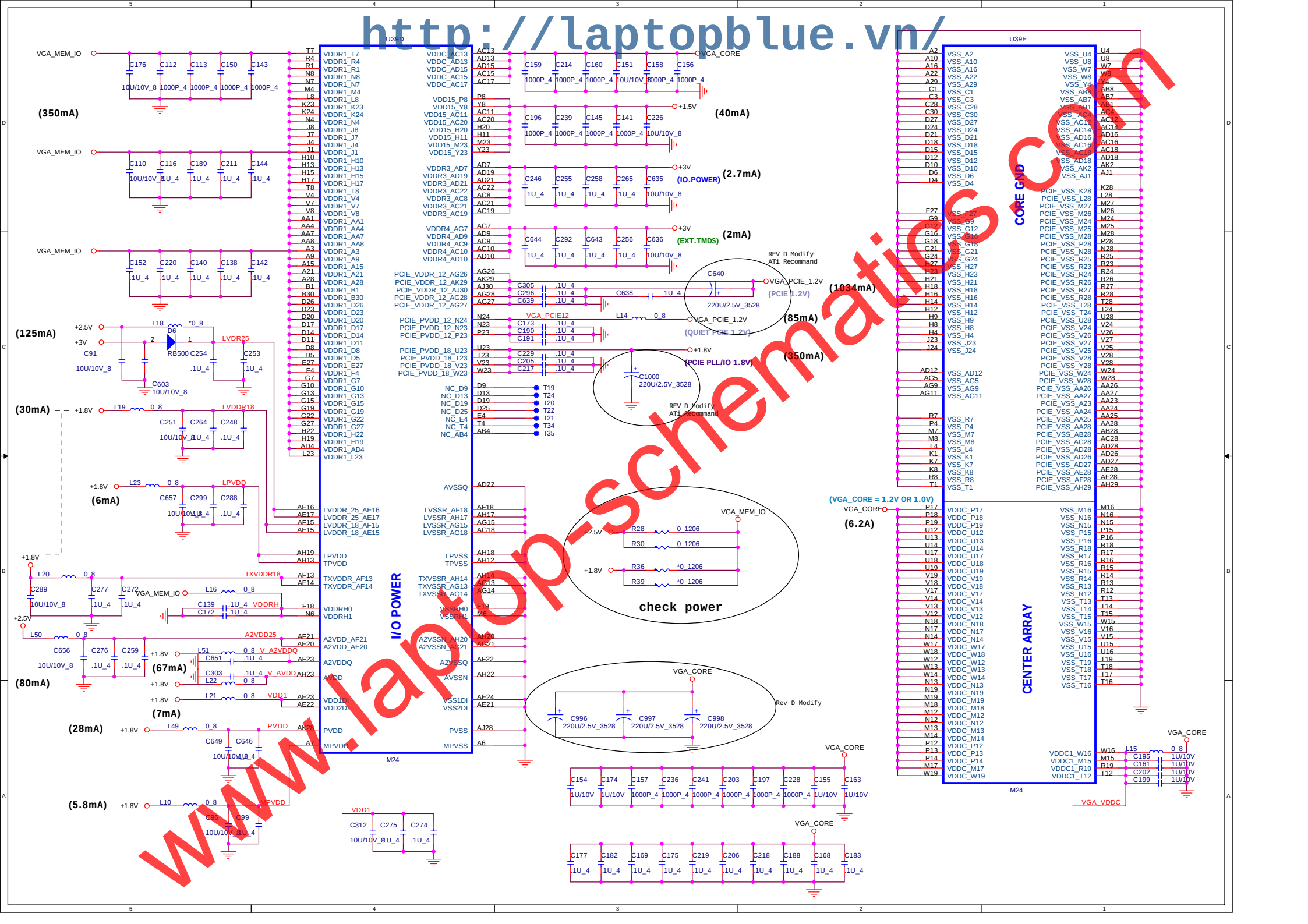
+2.5V
R62 10K_4
R63 10K_4
+1.8VSUS
R45 40.2/F_4
R46 40.2/F_4
Route as short as possible.
It's point to point, 55ohm trace, keep as short as possible.
R41 80.6/F_4
R43 80.6/F_4
R47 100/F
R297 100/F
C167 .1U_4
C566 2.2U/6.3V
Place Close to MCH

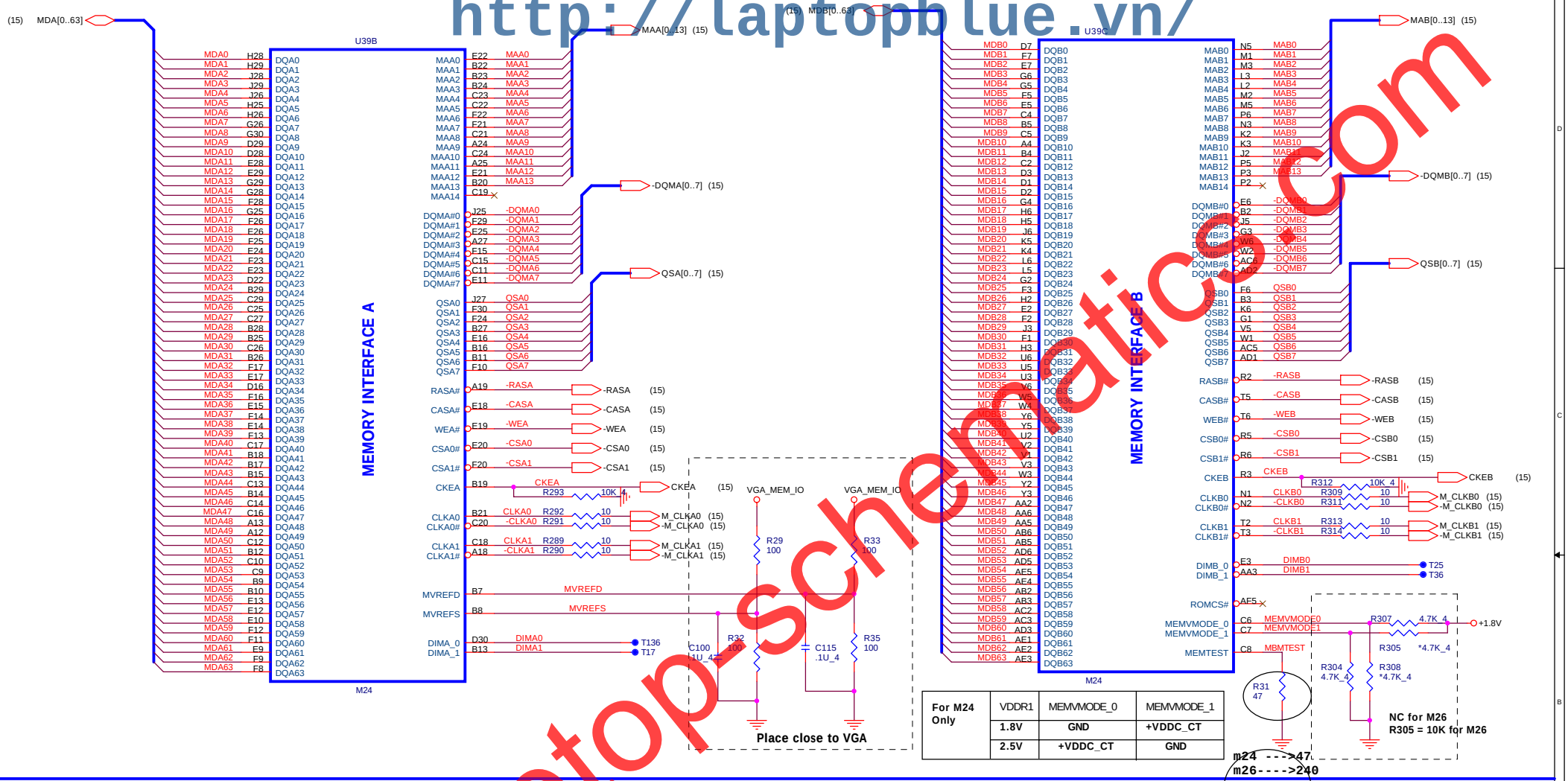



http://laptopblue.vn
www.laptop-schematics.com
JDIM3
JDIM2
PC4800 DDR2 SDRAM SO-DIMM (200P)
PC4800_DDR2_4.0MM_STD
PC4800_DDR2_5.2MM_REV
SMDDR_VREF
+1.8VSUS
+3V
R_A_DM[0..7] (8)
R_A_MD[0..63] (8)
R_A_DQS[0..7] (8)
R_A_DQS#[0..7] (8)
R_A_MA[0..13] (8,11)
R_B_DM[0..7] (8)
R_B_MD[0..63] (8)
R_B_DQS[0..7] (8)
R_B_DQS#[0..7] (8)
R_B_MA[0..13] (8,11)
CLK_SDRAM0 (7)
CLK_SDRAM0# (7)
CLK_SDRAM1 (7)
CLK_SDRAM1# (7)
CLK_SDRAM3 (7)
CLK_SDRAM3# (7)
CLK_SDRAM4 (7)
CLK_SDRAM4# (7)
CKE0
CKE1
CKE2
CKE3
M_ODT0 (7,11)
M_ODT1 (7,11)
M_ODT2 (7,11)
M_ODT3 (7,11)
R_A_BS0# (8,11)
R_A_BS1# (8,11)
R_A_BS2# (8,11)
R_A_BMWEA# (8,11)
R_A_SRASA# (8,11)
R_A_SCASA# (8,11)
SM_CS0# (7,11)
SM_CS1# (7,11)
R_B_BS0# (8,11)
R_B_BS1# (8,11)
R_B_BS2# (8,11)
R_B_BMWEA# (8,11)
R_B_SRASA# (8,11)
R_B_SCASA# (8,11)
SM_CS2# (7,11)
SM_CS3# (7,11)
CGDAT_SMB (3)
CGCLK_SMB (3)
R23 10K_4
R24 10K_4
R21 10K_4
R22 10K_4
SMbus address A0
SMbus address A1
CLOCK 0,1,2
CKE 0,1
CLOCK 3,4,5
CKE 2,3
Close to JDIMM1
Close to JDIMM2
Place Close to JDIMM1
Place Close to JDIMM2
C85 C63 C50 C43 C44 2.2U/6.3V
C84 C88 C46 C48 C51 C81 .1U_4 2.2U/6.3V
C49 C64 C65 C83 C62 2.2U/6.3V
C87 C45 C47 C86 C77 C58 .1U_4 2.2U/6.3V
C82 C80 .1U_4 2.2U/6.3V
C60 C61 .1U_4 2.2U/6.3V





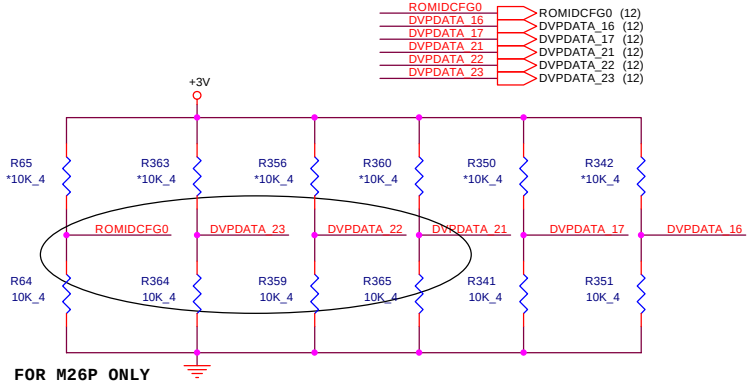


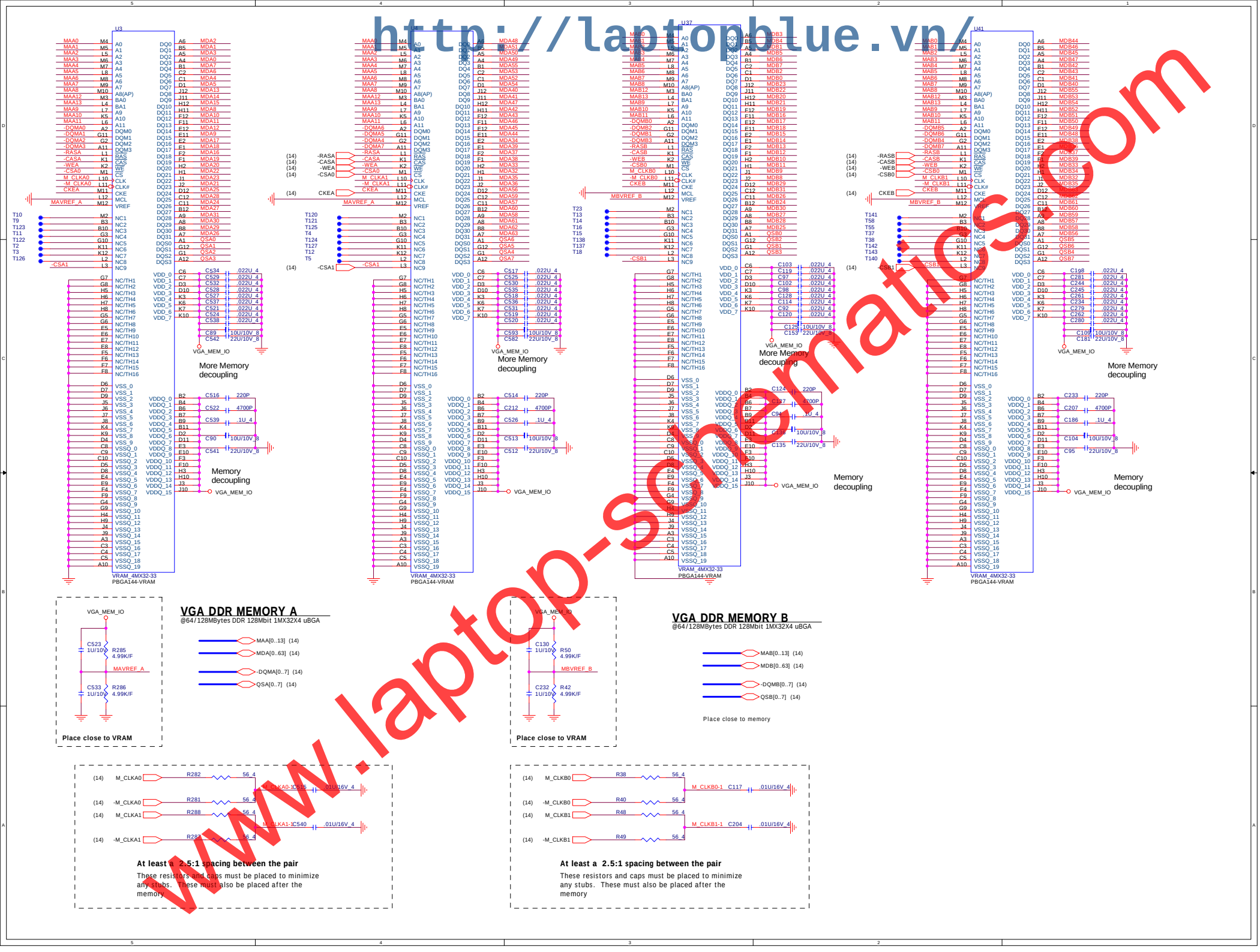


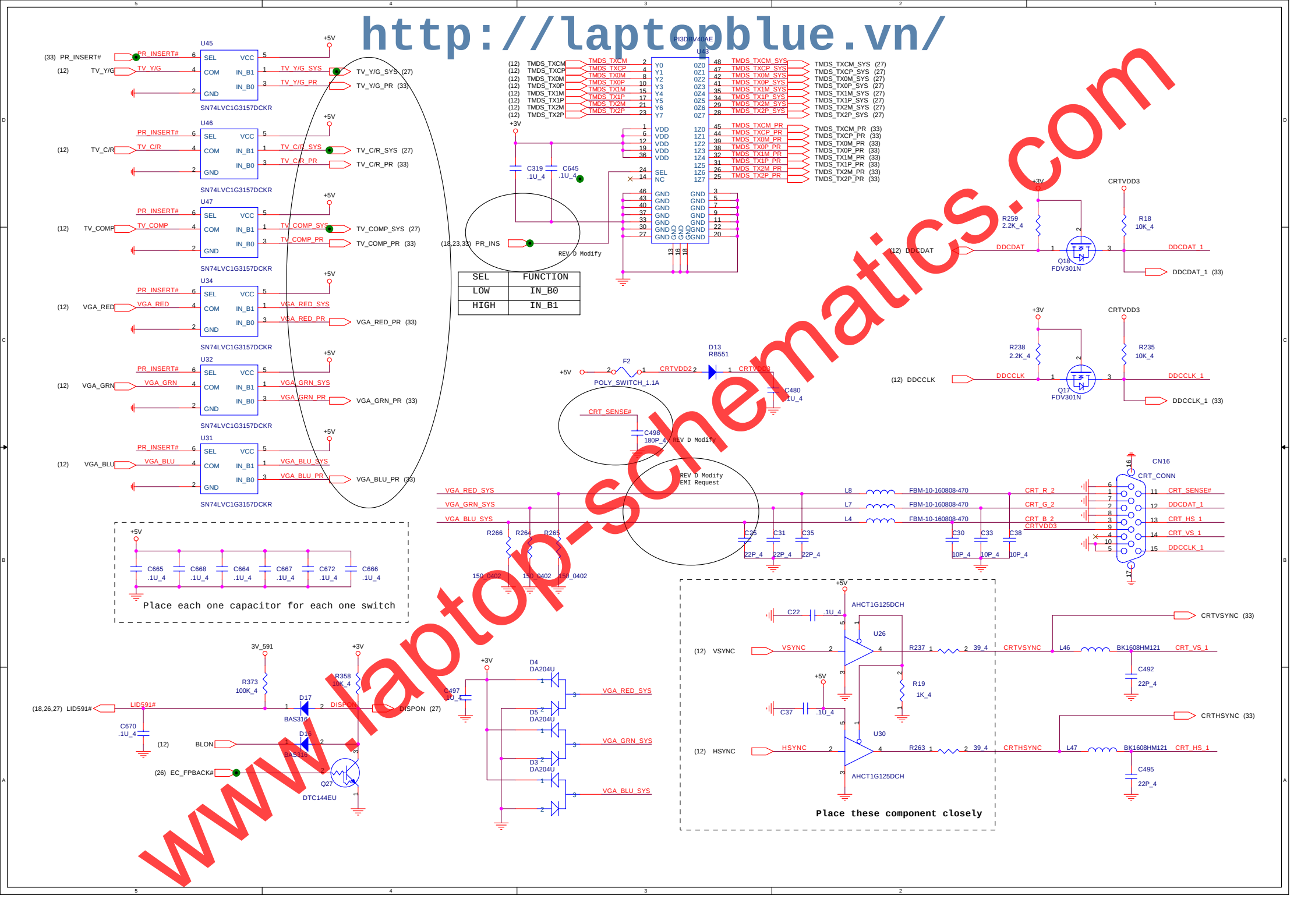
For M24 Only	VDDR1	MEMVMODE_0	MEMVMODE_1
	1.8V	GND	+VDDC_CT
	2.5V	+VDDC_CT	GND

STRAPS PIN

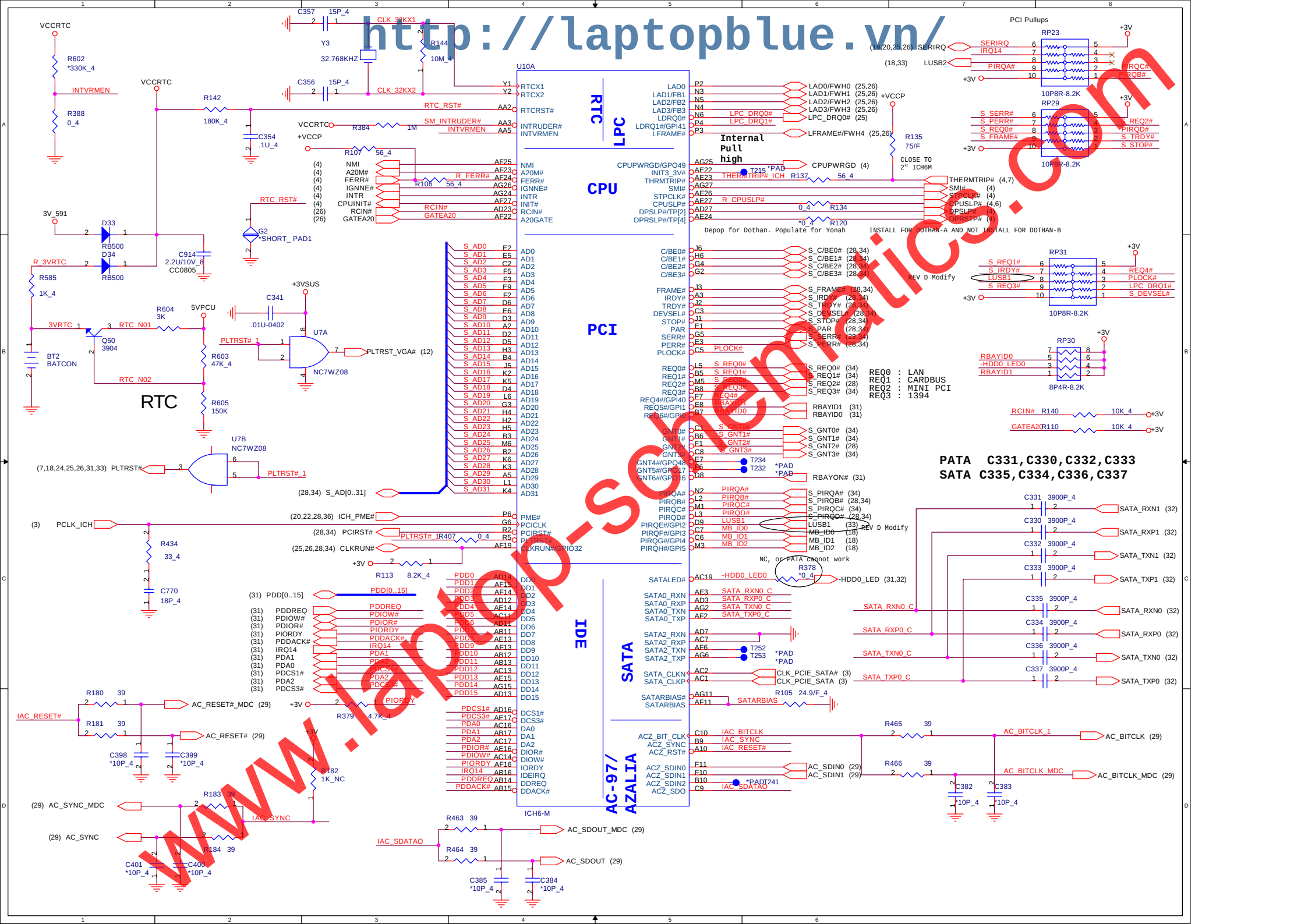
GPIO(9,13:11) INT P/D	ROMIDCFG **0x0x: No ROM, CHG_ID=0** 0x1x: No Rom, CHG_ID=1 1000: Parallel ROM, Chip ID'S from ROM 1000: Parallel ROM, Chip ID'S from ROM
DVPDATA_21~23 MEM TYPE	DVPDATA_21: 0=4Mx32 1=8Mx32 DVPDATA_22: 0=128M 1=64M DVPDATA_23: 0=Hynix 1=Samsung

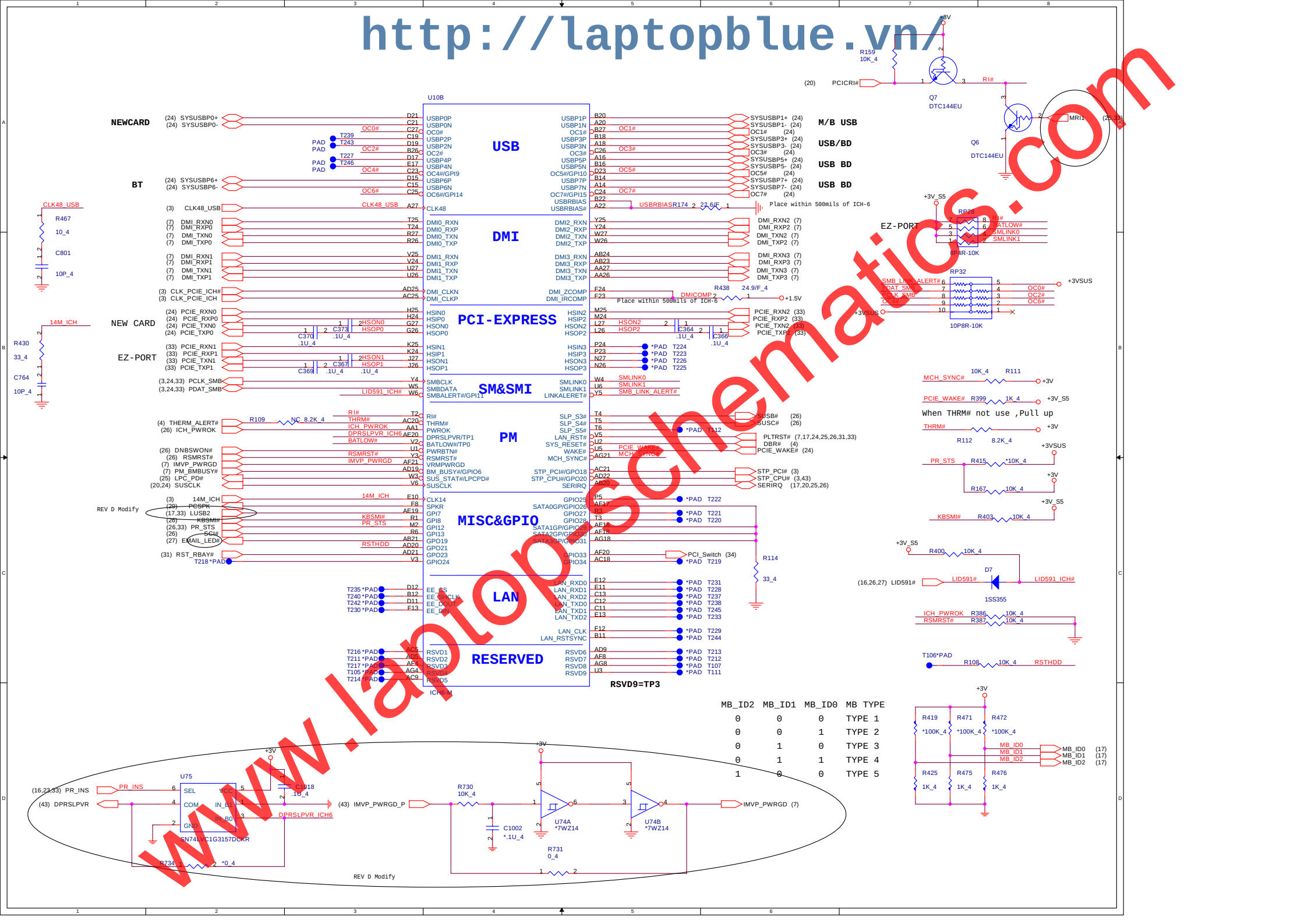


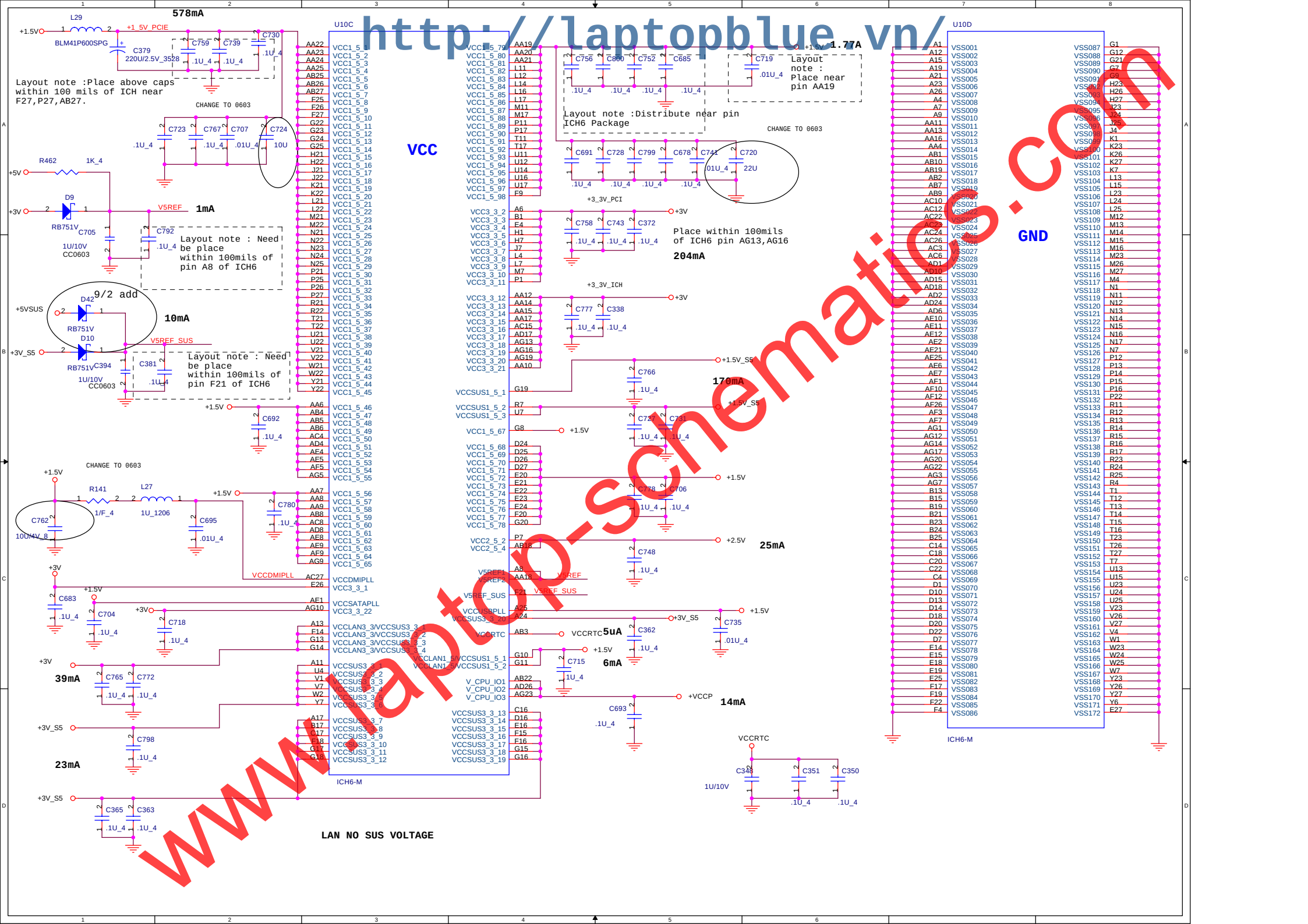


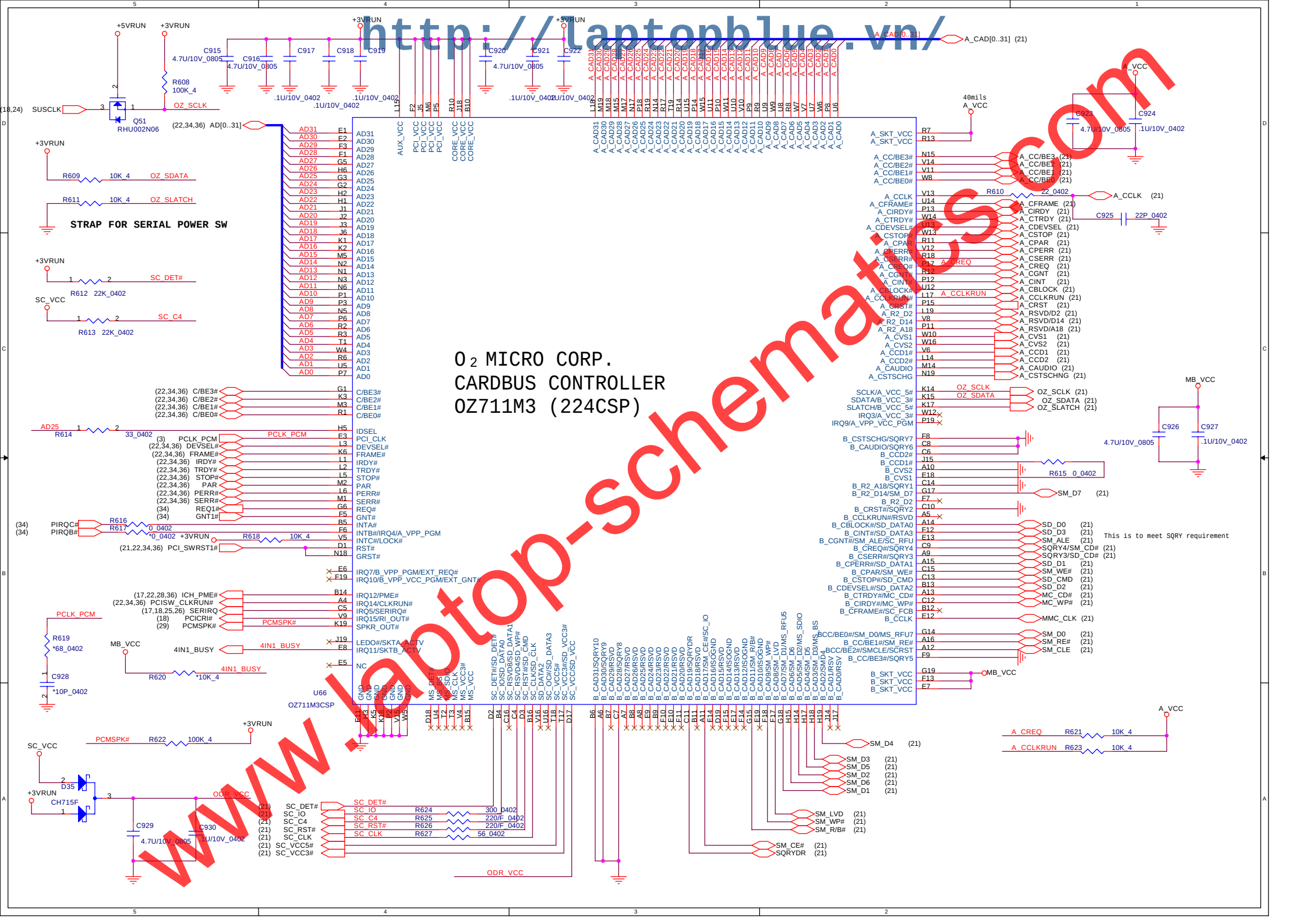


SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1


http://laptopblue.vn/
www.laptop-schematics.com
U10A
RTC
LPC
CPU
PCI
IDE
SATA
AC-97/ AZALIA
ICH6-M
RTC
PCI Pullups
Internal Pull high
REV D Modify
INSTALL FOR DOTHAN-A AND NOT INSTALL FOR DOTHAN-B
REQ0 : LAN
REQ1 : CARDBUS
REQ2 : MINI PCI
REQ3 : 1394
PATA C331,C330,C332,C333
SATA C335,C334,C336,C337



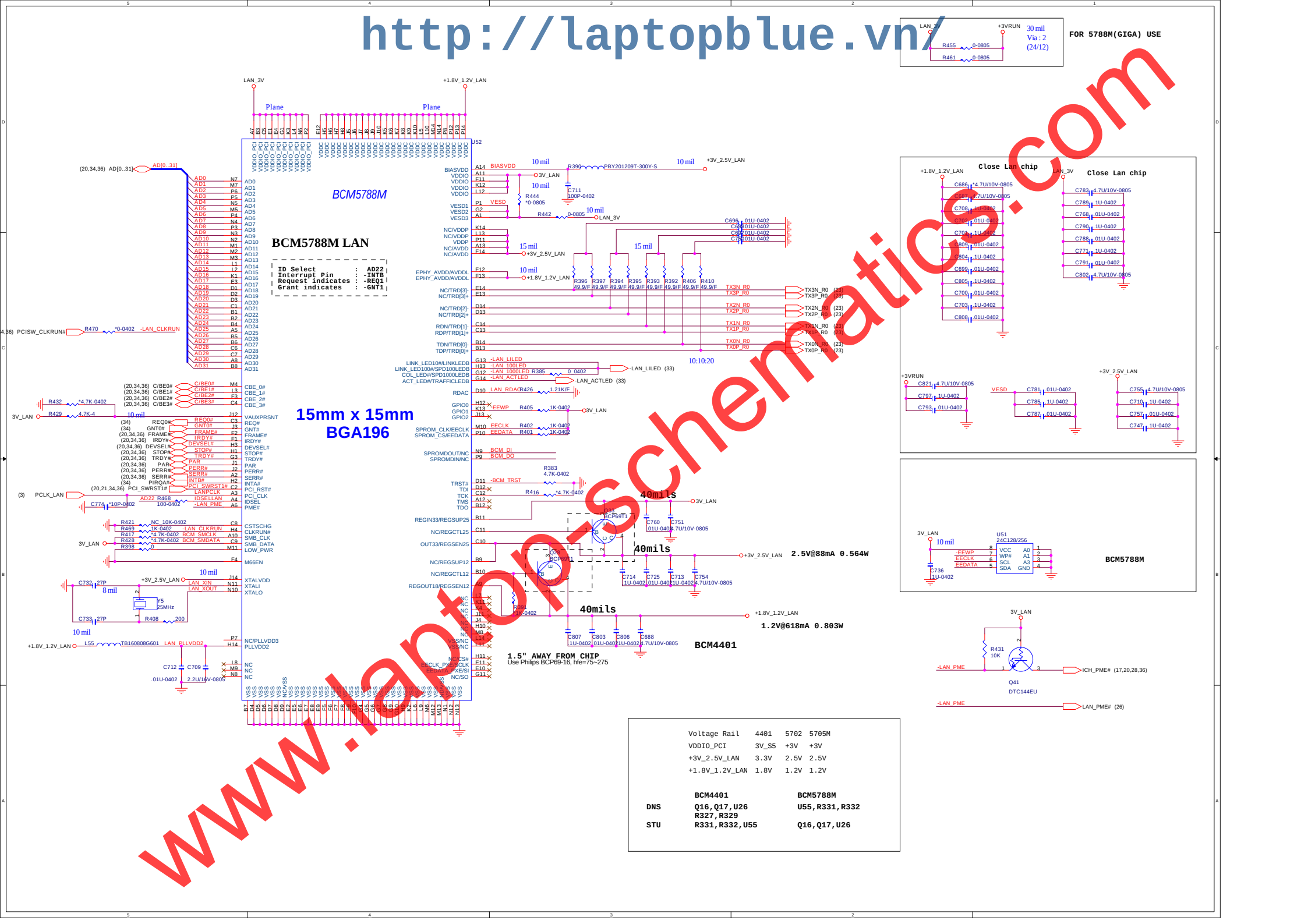

http://laptopblue.vn/
www.laptop-schematics.com
578mA
L29
BLM41P600SPG
+1_5V_PCIE
C379
220U/2.5V_3528
C759
C739
C730
.1U_4
Layout note :Place above caps within 100 mils of ICH near F27,P27,AB27.
CHANGE TO 0603
C723
C767
C707
C724
10U
R462
1K_4
+5V
D9
RB751V
+3V
V5REF
1mA
C705
1U/10V
CC0603
C792
Layout note : Need be place within 100mils of pin A8 of ICH6
9/2 add
D42
+5VSUS
10mA
D10
+3V_S5
V5REF_SUS
C394
C381
Layout note : Need be place within 100mils of pin F21 of ICH6
+1.5V
C692
CHANGE TO 0603
R141
L27
1/F_4
1U_1206
C762
10U/4V_8
C695
.01U_4
C780
VCCDMIPLL
+3V
C683
C704
C718
39mA
C765
C772
23mA
C798
C365
C363
+3V_S5
LAN NO SUS VOLTAGE
U10C
VCC
ICH6-M
VCC1_5_1
VCC1_5_2
VCC1_5_3
VCC1_5_4
VCC1_5_5
VCC1_5_6
VCC1_5_7
VCC1_5_8
VCC1_5_9
VCC1_5_10
VCC1_5_11
VCC1_5_12
VCC1_5_13
VCC1_5_14
VCC1_5_15
VCC1_5_16
VCC1_5_17
VCC1_5_18
VCC1_5_19
VCC1_5_20
VCC1_5_21
VCC1_5_22
VCC1_5_23
VCC1_5_24
VCC1_5_25
VCC1_5_26
VCC1_5_27
VCC1_5_28
VCC1_5_29
VCC1_5_30
VCC1_5_31
VCC1_5_32
VCC1_5_33
VCC1_5_34
VCC1_5_35
VCC1_5_36
VCC1_5_37
VCC1_5_38
VCC1_5_39
VCC1_5_40
VCC1_5_41
VCC1_5_42
VCC1_5_43
VCC1_5_44
VCC1_5_45
VCC1_5_46
VCC1_5_47
VCC1_5_48
VCC1_5_49
VCC1_5_50
VCC1_5_51
VCC1_5_52
VCC1_5_53
VCC1_5_54
VCC1_5_55
VCC1_5_56
VCC1_5_57
VCC1_5_58
VCC1_5_59
VCC1_5_60
VCC1_5_61
VCC1_5_62
VCC1_5_63
VCC1_5_64
VCC1_5_65
VCCDMIPLL
VCC3_3_1
VCCSATAPLL
VCC3_3_22
VCCLAN3_3/VCCSUS3_3_1
VCCLAN3_3/VCCSUS3_3_2
VCCLAN3_3/VCCSUS3_3_3
VCCLAN3_3/VCCSUS3_3_4
VCCSUS3_3_1
VCCSUS3_3_2
VCCSUS3_3_3
VCCSUS3_3_4
VCCSUS3_3_5
VCCSUS3_3_6
VCCSUS3_3_7
VCCSUS3_3_8
VCCSUS3_3_9
VCCSUS3_3_10
VCCSUS3_3_11
VCCSUS3_3_12
VCC3_3_2
VCC3_3_3
VCC3_3_4
VCC3_3_5
VCC3_3_6
VCC3_3_7
VCC3_3_8
VCC3_3_9
VCC3_3_10
VCC3_3_11
VCC3_3_12
VCC3_3_13
VCC3_3_14
VCC3_3_15
VCC3_3_16
VCC3_3_17
VCC3_3_18
VCC3_3_19
VCC3_3_20
VCC3_3_21
VCCSUS1_5_1
VCCSUS1_5_2
VCCSUS1_5_3
VCC1_5_67
VCC1_5_68
VCC1_5_69
VCC1_5_70
VCC1_5_71
VCC1_5_72
VCC1_5_73
VCC1_5_74
VCC1_5_75
VCC1_5_76
VCC1_5_77
VCC1_5_78
VCC2_5_2
VCC2_5_4
V5REF1
V5REF2
V5REF_SUS
VCCUSBPLL
VCCSUS3_3_20
VCCRTC
VCCLAN1_5/VCCSUS1_5_1
VCCLAN1_5/VCCSUS1_5_2
V_CPU_IO1
V_CPU_IO2
V_CPU_IO3
VCCSUS3_3_13
VCCSUS3_3_14
VCCSUS3_3_15
VCCSUS3_3_16
VCCSUS3_3_17
VCCSUS3_3_18
VCCSUS3_3_19
+3_3V_PCI
+3_3V_ICH
Layout note :Distribute near pin ICH6 Package
Layout note : Place near pin AA19
1.77A
C756
C800
C752
C685
C719
.01U_4
C691
C728
C799
C678
C741
C720
01U_4
22U
CHANGE TO 0603
Place within 100mils of ICH6 pin AG13,AG16
204mA
C758
C743
C372
C777
C338
170mA
C766
C727
C731
C778
C706
25mA
C748
5uA
C362
C735
6mA
C715
C693
14mA
VCCRTC
C348
C351
C350
1U/10V
U10D
GND
ICH6-M
VSS001
VSS002
VSS003
VSS004
VSS005
VSS006
VSS007
VSS008
VSS009
VSS010
VSS011
VSS012
VSS013
VSS014
VSS015
VSS016
VSS017
VSS018
VSS019
VSS020
VSS021
VSS022
VSS023
VSS024
VSS025
VSS026
VSS027
VSS028
VSS029
VSS030
VSS031
VSS032
VSS033
VSS034
VSS035
VSS036
VSS037
VSS038
VSS039
VSS040
VSS041
VSS042
VSS043
VSS044
VSS045
VSS046
VSS047
VSS048
VSS049
VSS050
VSS051
VSS052
VSS053
VSS054
VSS055
VSS056
VSS057
VSS058
VSS059
VSS060
VSS061
VSS062
VSS063
VSS064
VSS065
VSS066
VSS067
VSS068
VSS069
VSS070
VSS071
VSS072
VSS073
VSS074
VSS075
VSS076
VSS077
VSS078
VSS079
VSS080
VSS081
VSS082
VSS083
VSS084
VSS085
VSS086
VSS087
VSS088
VSS089
VSS090
VSS091
VSS092
VSS093
VSS094
VSS095
VSS096
VSS097
VSS098
VSS099
VSS100
VSS101
VSS102
VSS103
VSS104
VSS105
VSS106
VSS107
VSS108
VSS109
VSS110
VSS111
VSS112
VSS113
VSS114
VSS115
VSS116
VSS117
VSS118
VSS119
VSS120
VSS121
VSS122
VSS123
VSS124
VSS125
VSS126
VSS127
VSS128
VSS129
VSS130
VSS131
VSS132
VSS133
VSS134
VSS135
VSS136
VSS137
VSS138
VSS139
VSS140
VSS141
VSS142
VSS143
VSS144
VSS145
VSS146
VSS147
VSS148
VSS149
VSS150
VSS151
VSS152
VSS153
VSS154
VSS155
VSS156
VSS157
VSS158
VSS159
VSS160
VSS161
VSS162
VSS163
VSS164
VSS165
VSS166
VSS167
VSS168
VSS169
VSS170
VSS171
VSS172


http://laptopblue.vn/
www.laptop-schematics.com
O2 MICRO CORP.
CARDBUS CONTROLLER
OZ711M3 (224CSP)
STRAP FOR SERIAL POWER SW
OZ711M3CSP
U66
This is to meet SQRY requirement

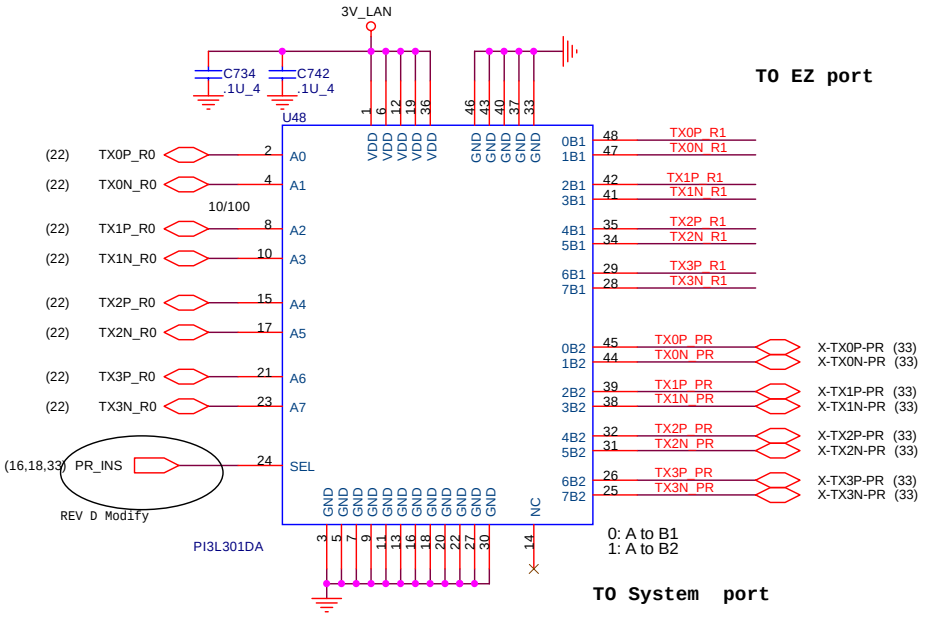
Card	XD	MMC/SD	MS
SQRY3	1	0	1
SQRY4	0	1	1
MC_CD#	0	0	0

4 IN1 CARD READER (XD,MMC/SD,MS)

This is to meet SQRY requirement



Lan Switch

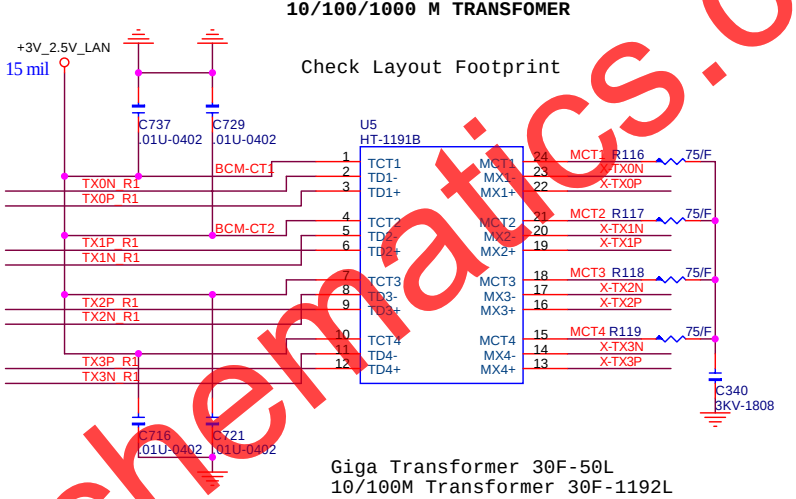


TO EZ port

TO System port

10/100/1000 M TRANSFOMER

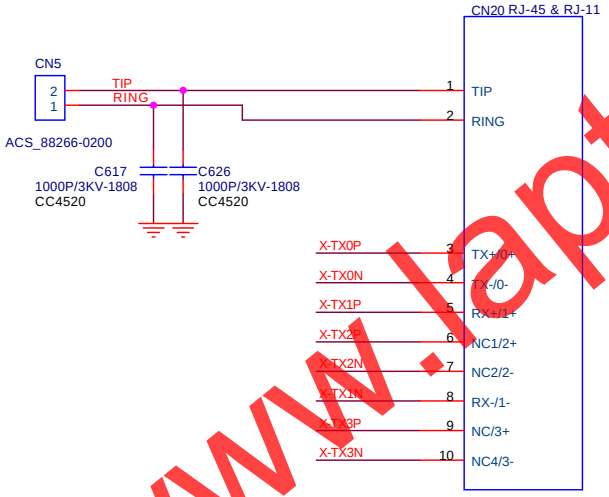
Check Layout Footprint



Giga Transformer 30F-50L
10/100M Transformer 30F-1192L

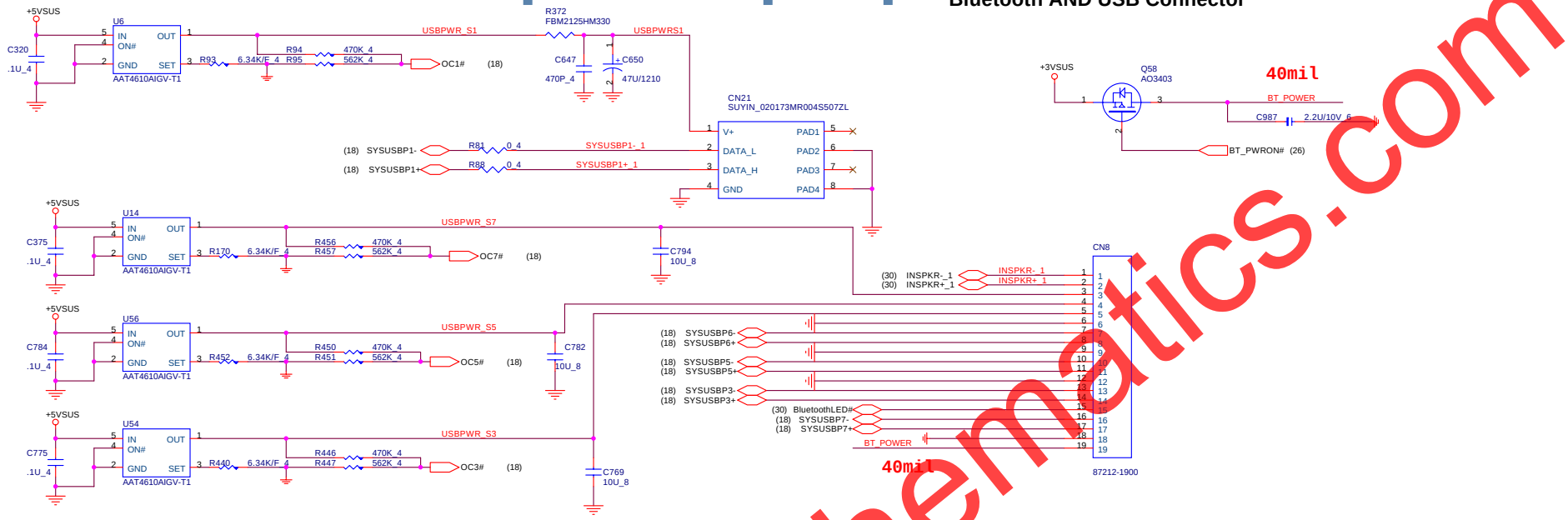
LAN and RJ11 Jack

Check Layout Footprint

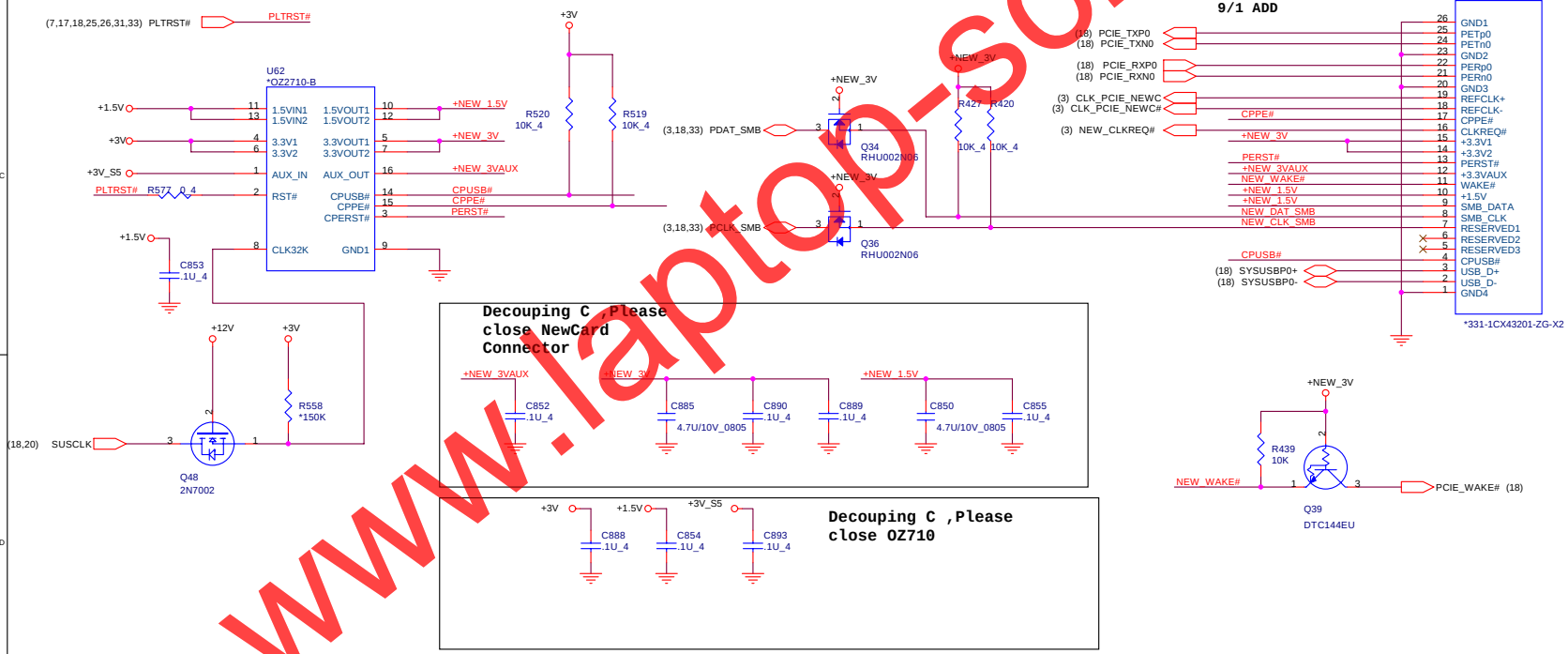


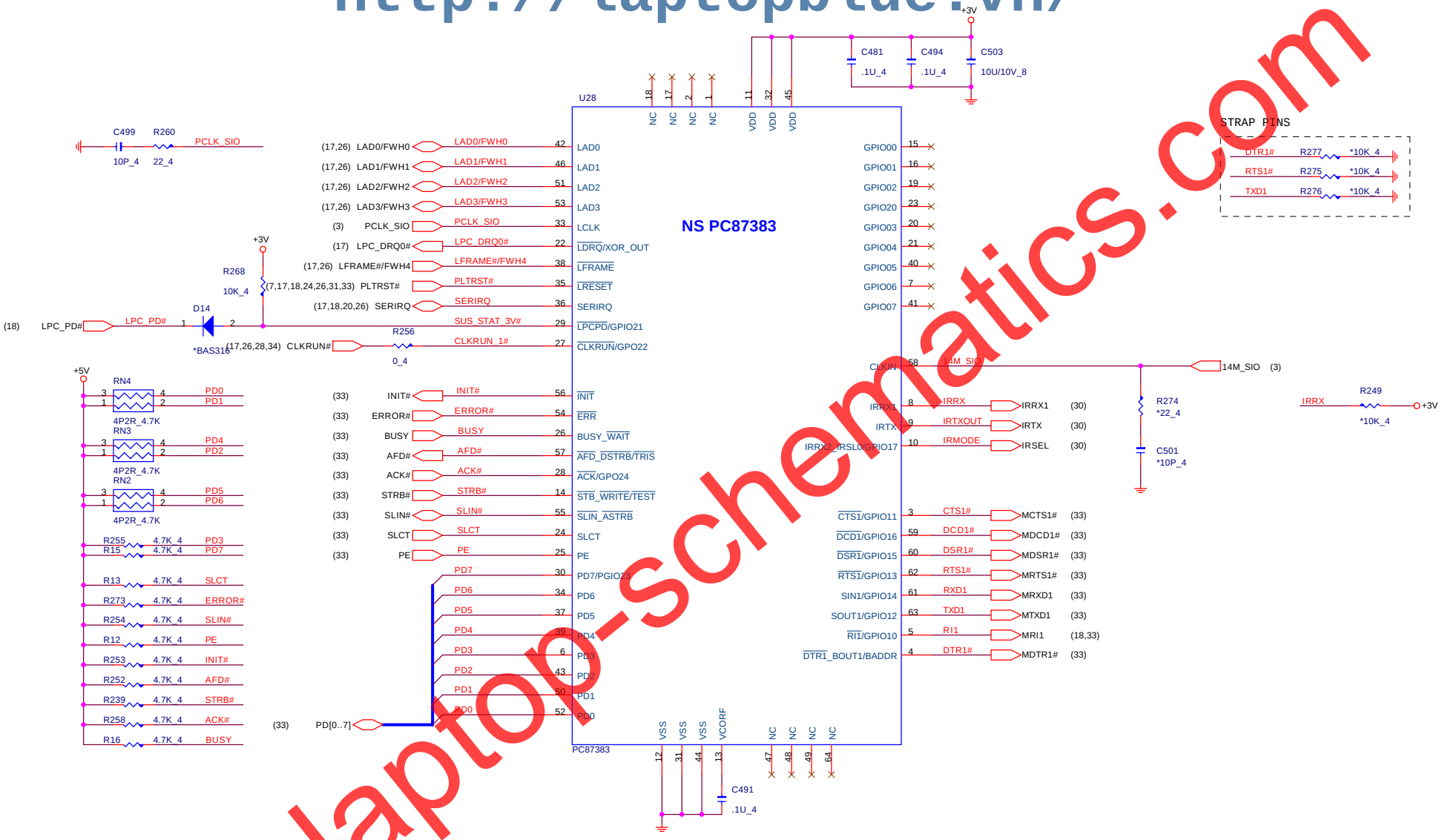
USB Connector and USB board

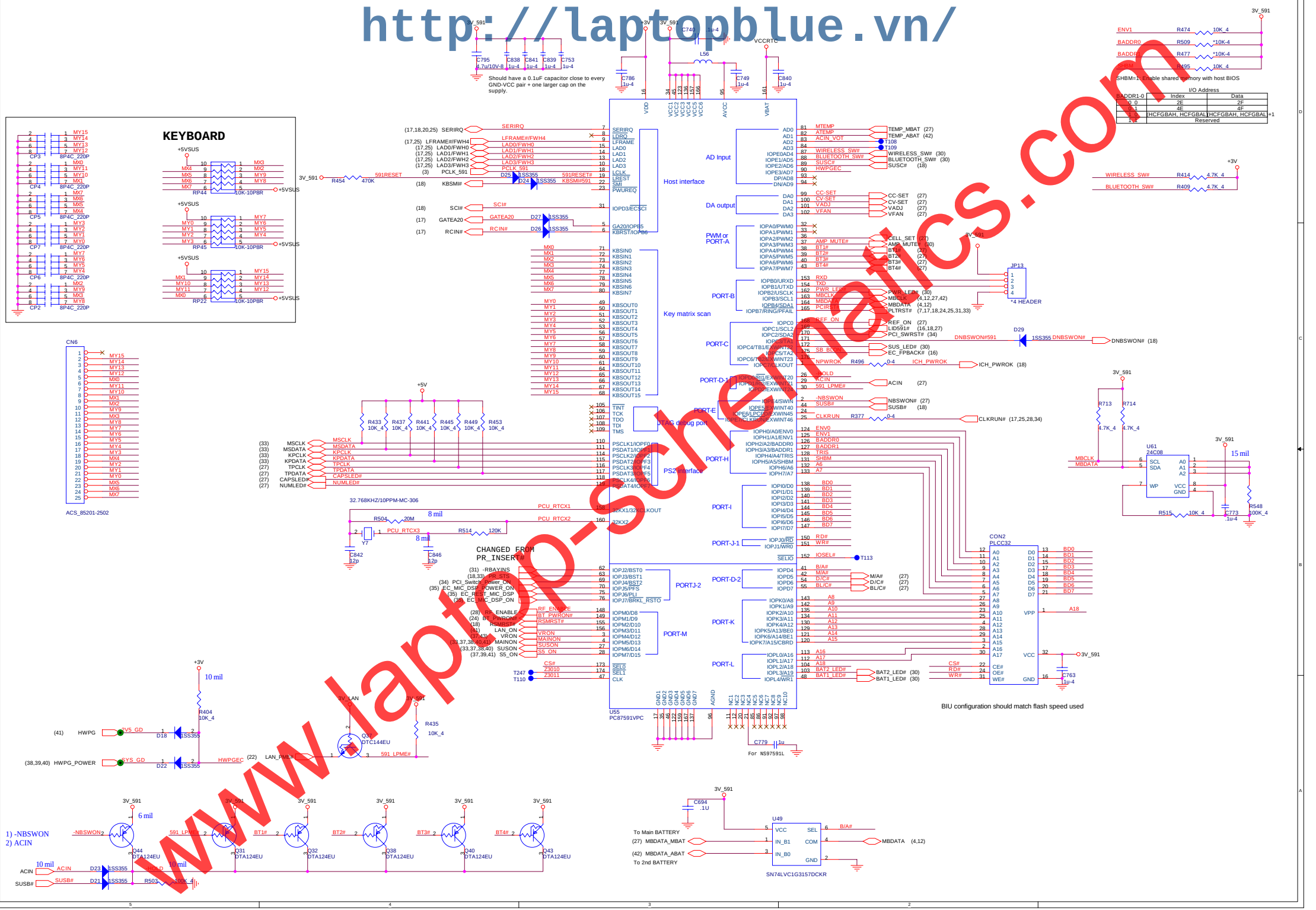
Bluetooth AND USB Connector



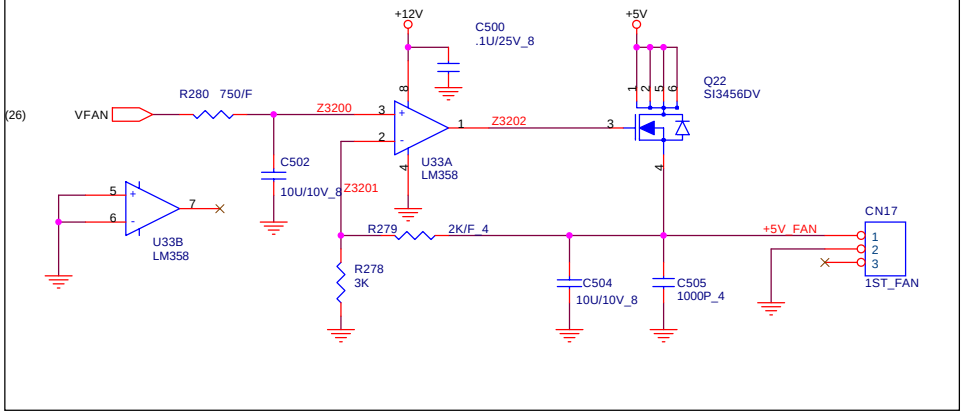
NewCard



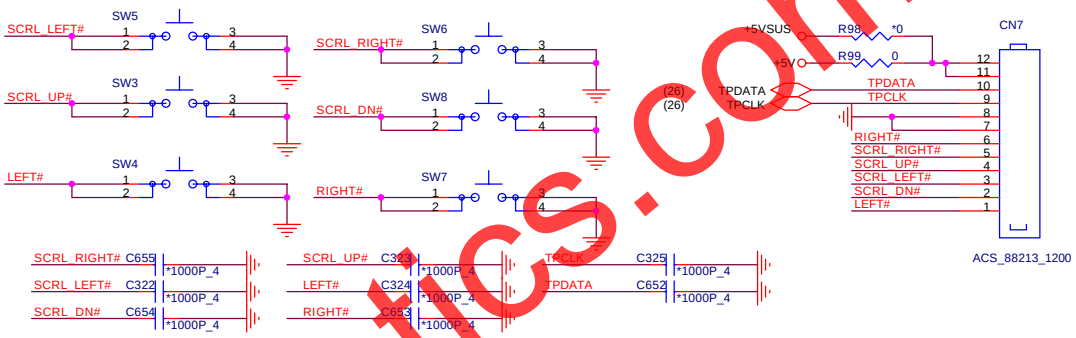




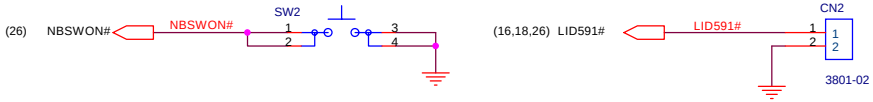
1st FAN OUT CONNECTOR



TouchPad Switch and T/P Module Connector

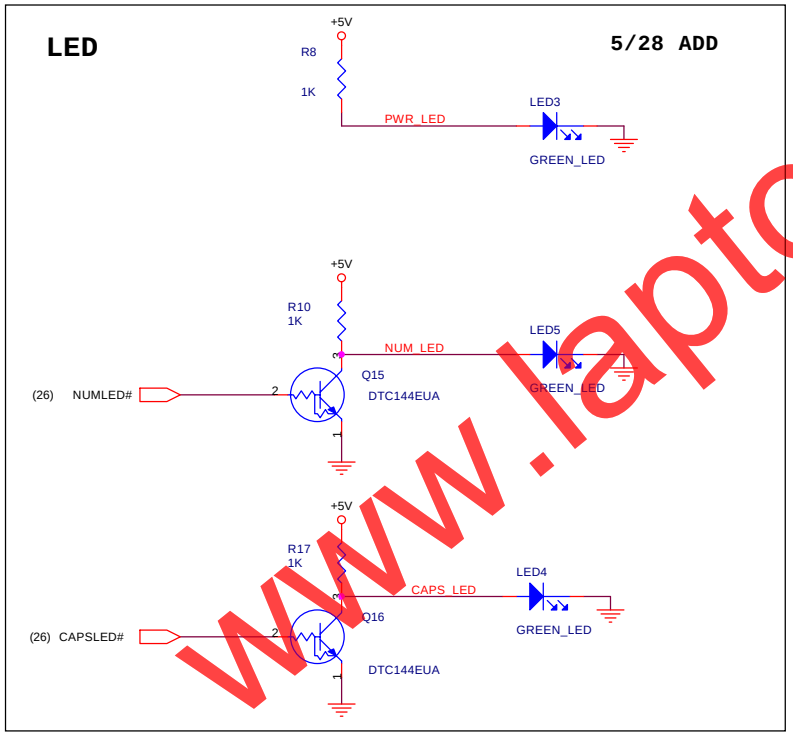


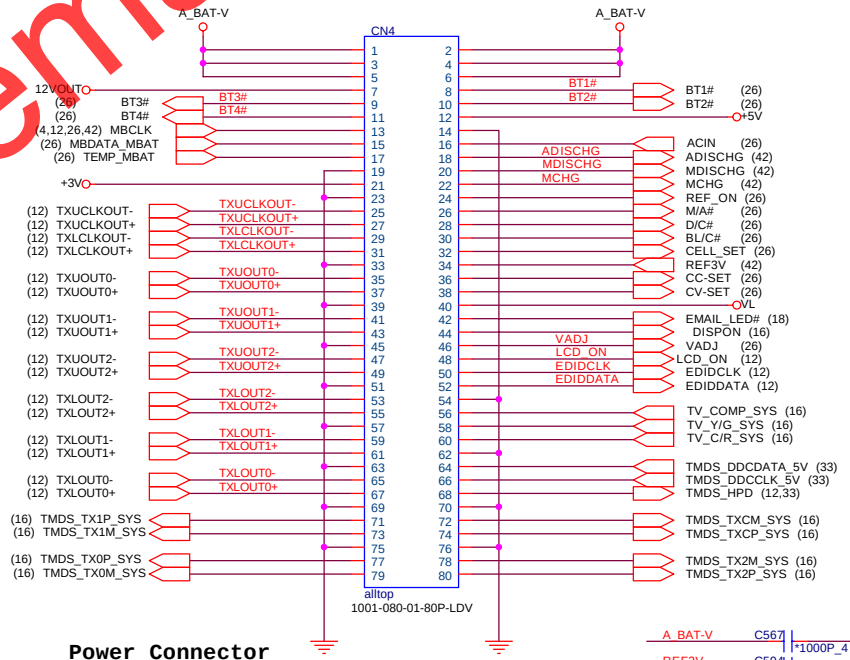
Power Switch



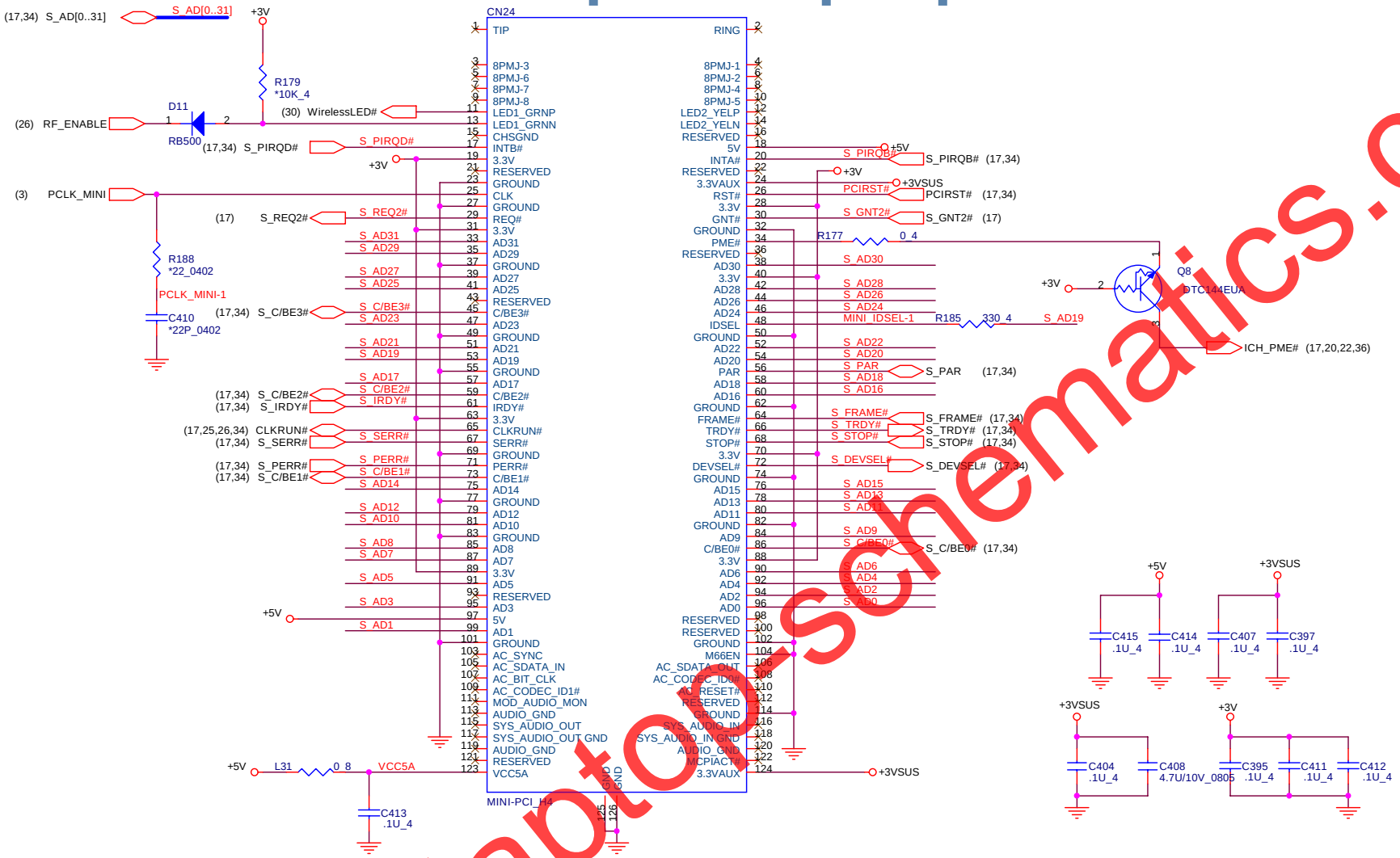
LED

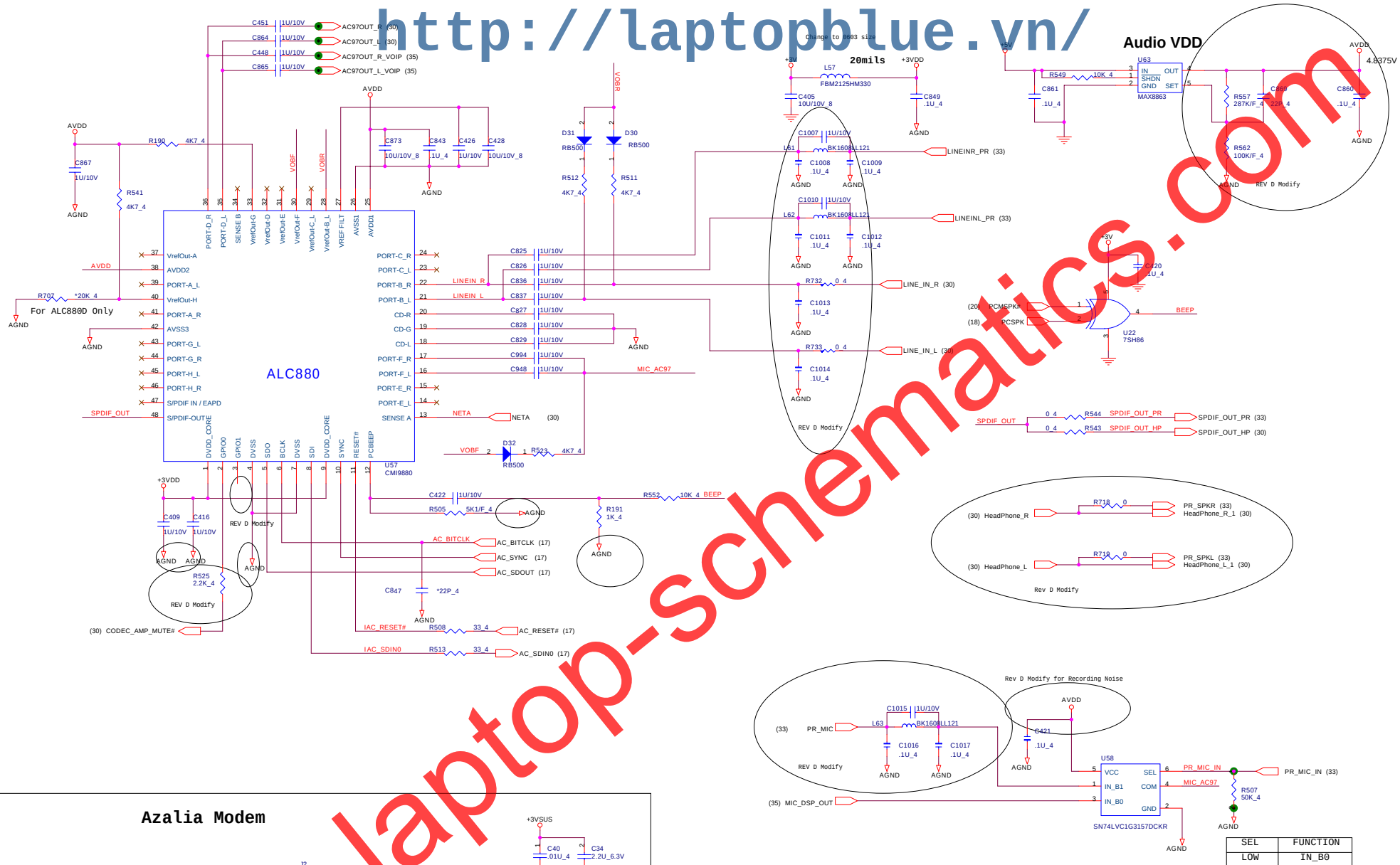
5/28 ADD



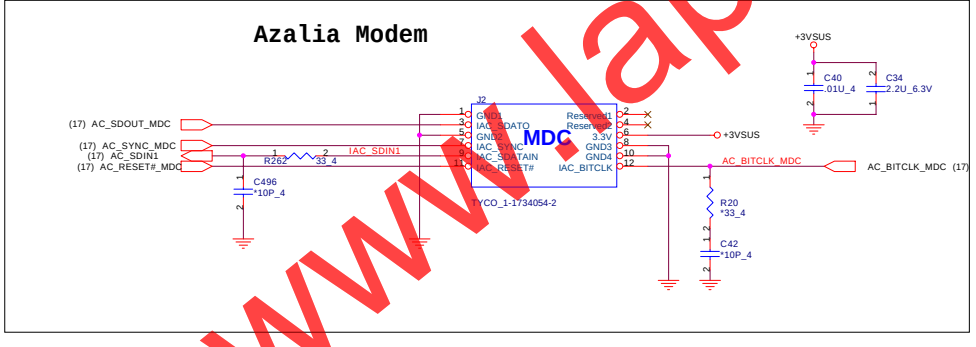


Power Connector

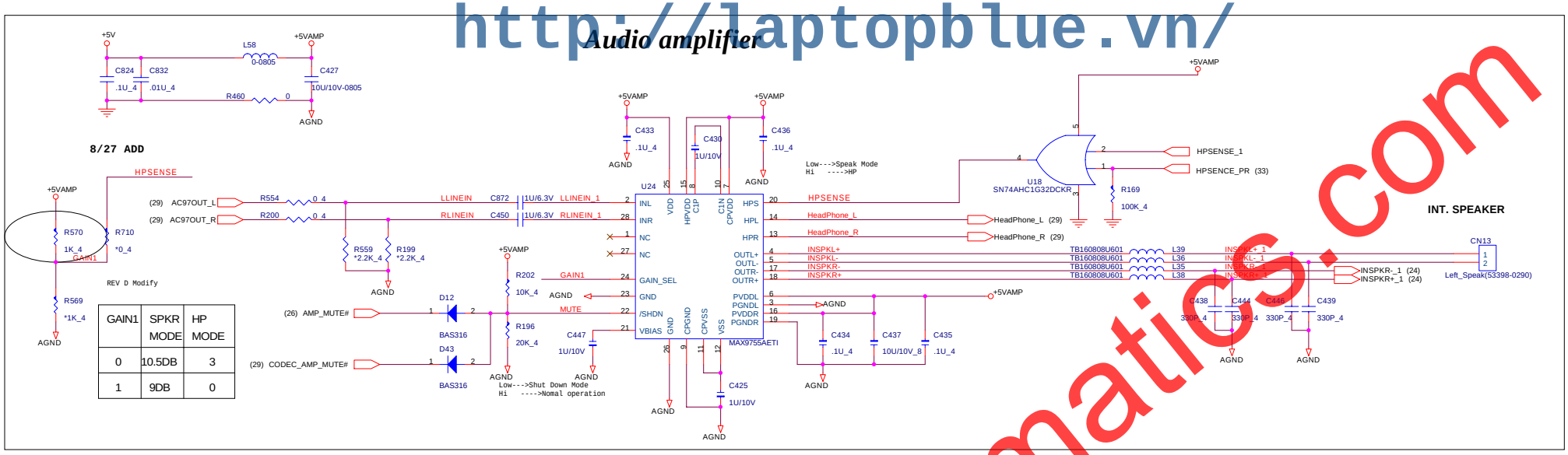




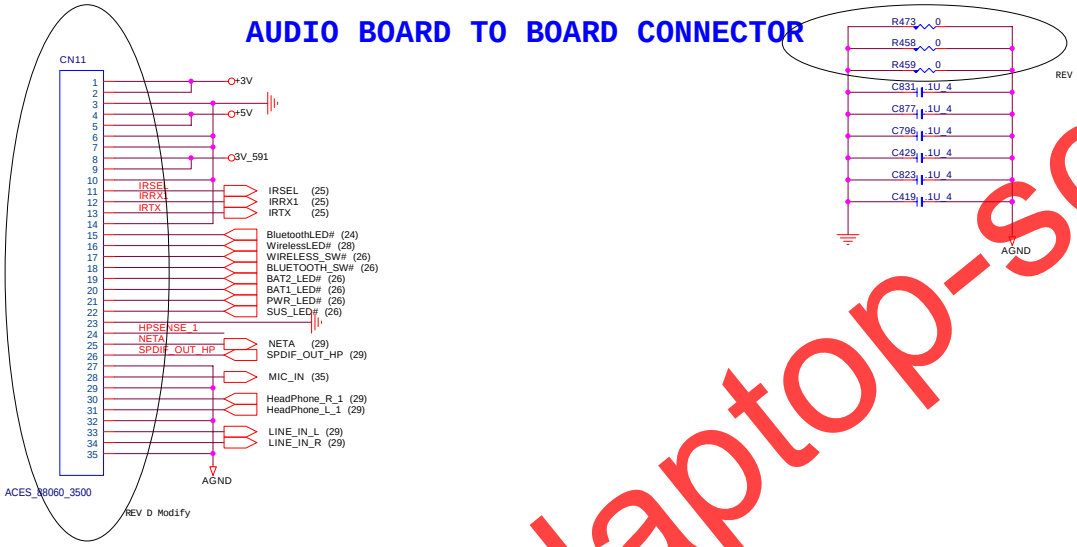
SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



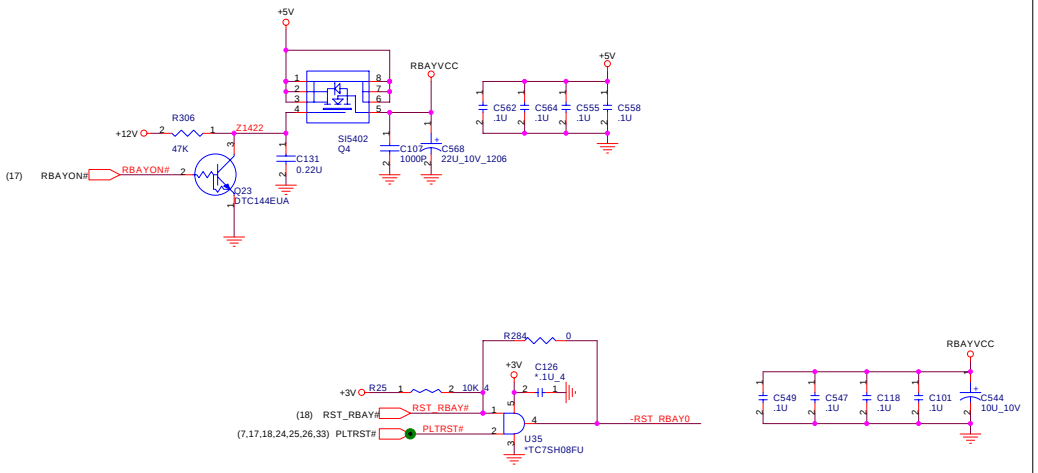
Audio amplifier



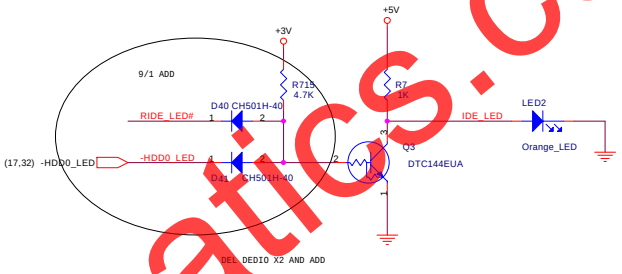
AUDIO BOARD TO BOARD CONNECTOR



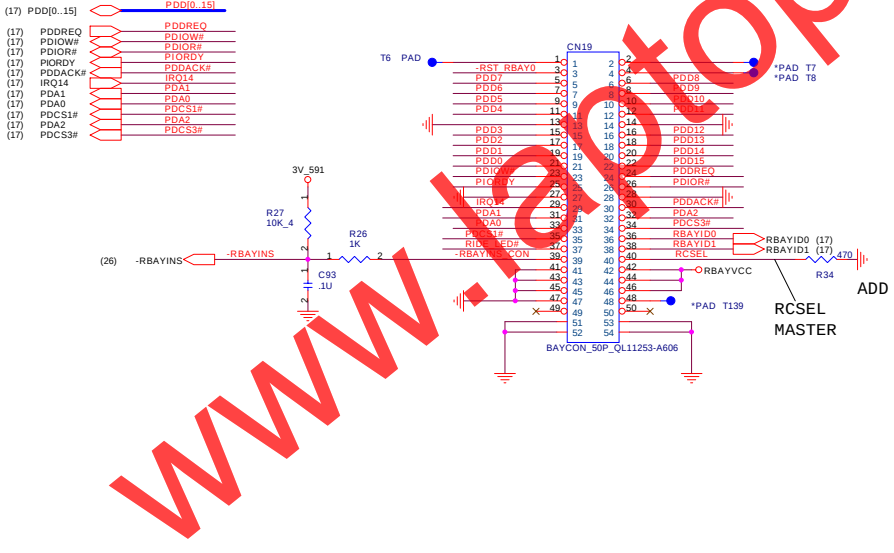
SWAP BAY POWER CONTROL& RESET



IDE LED CONTROL LOGIC

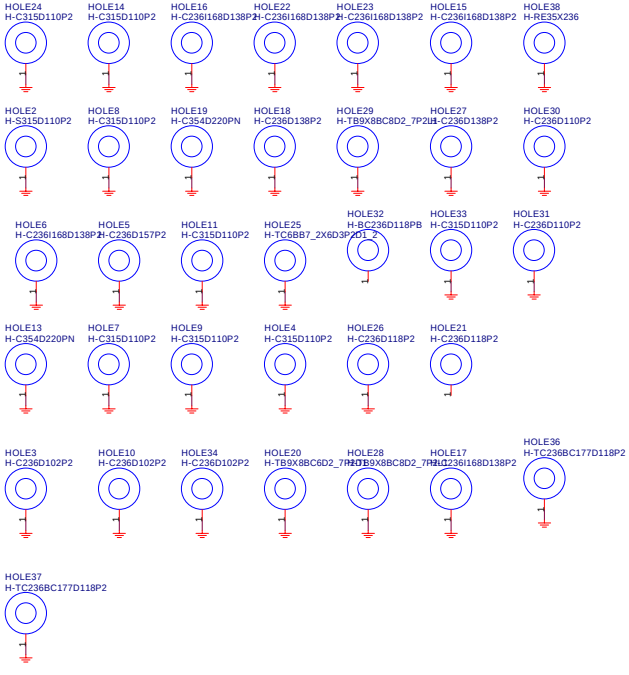


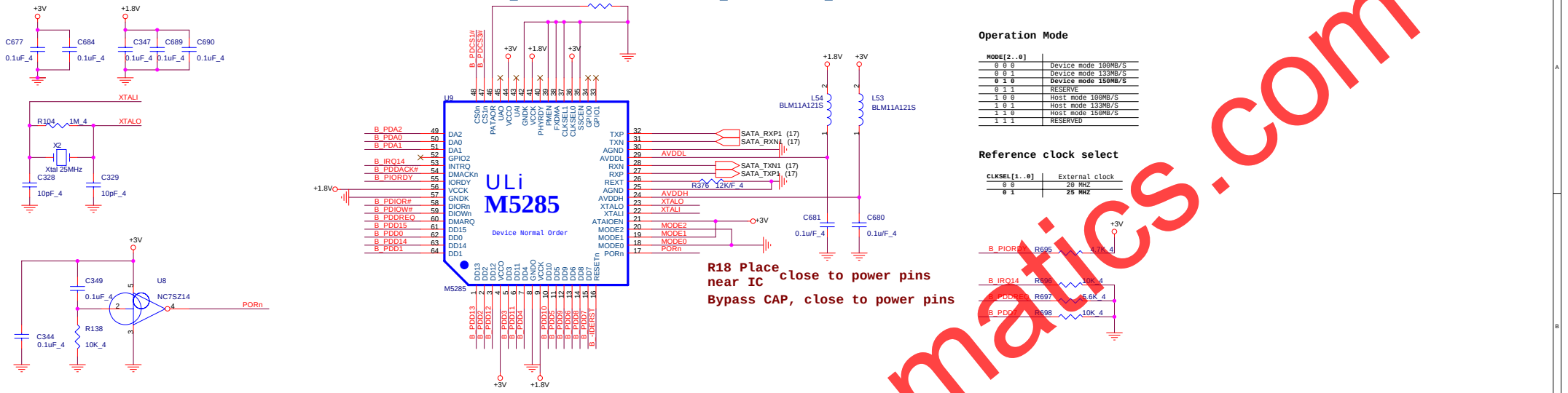
Multi-Bay Connector

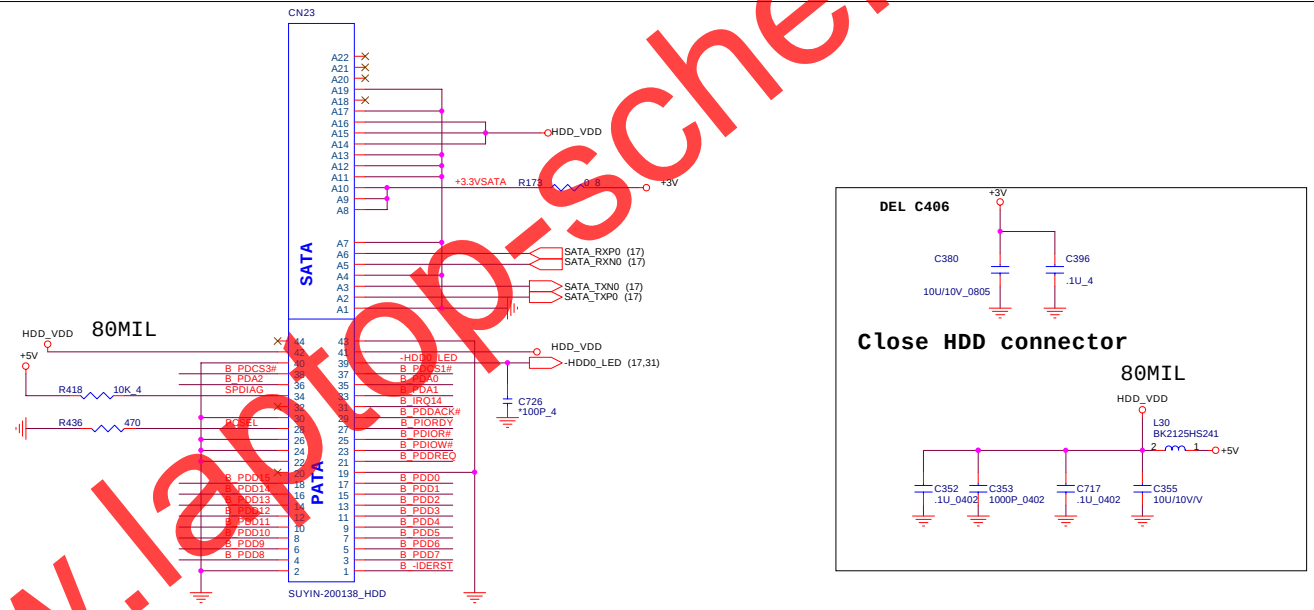


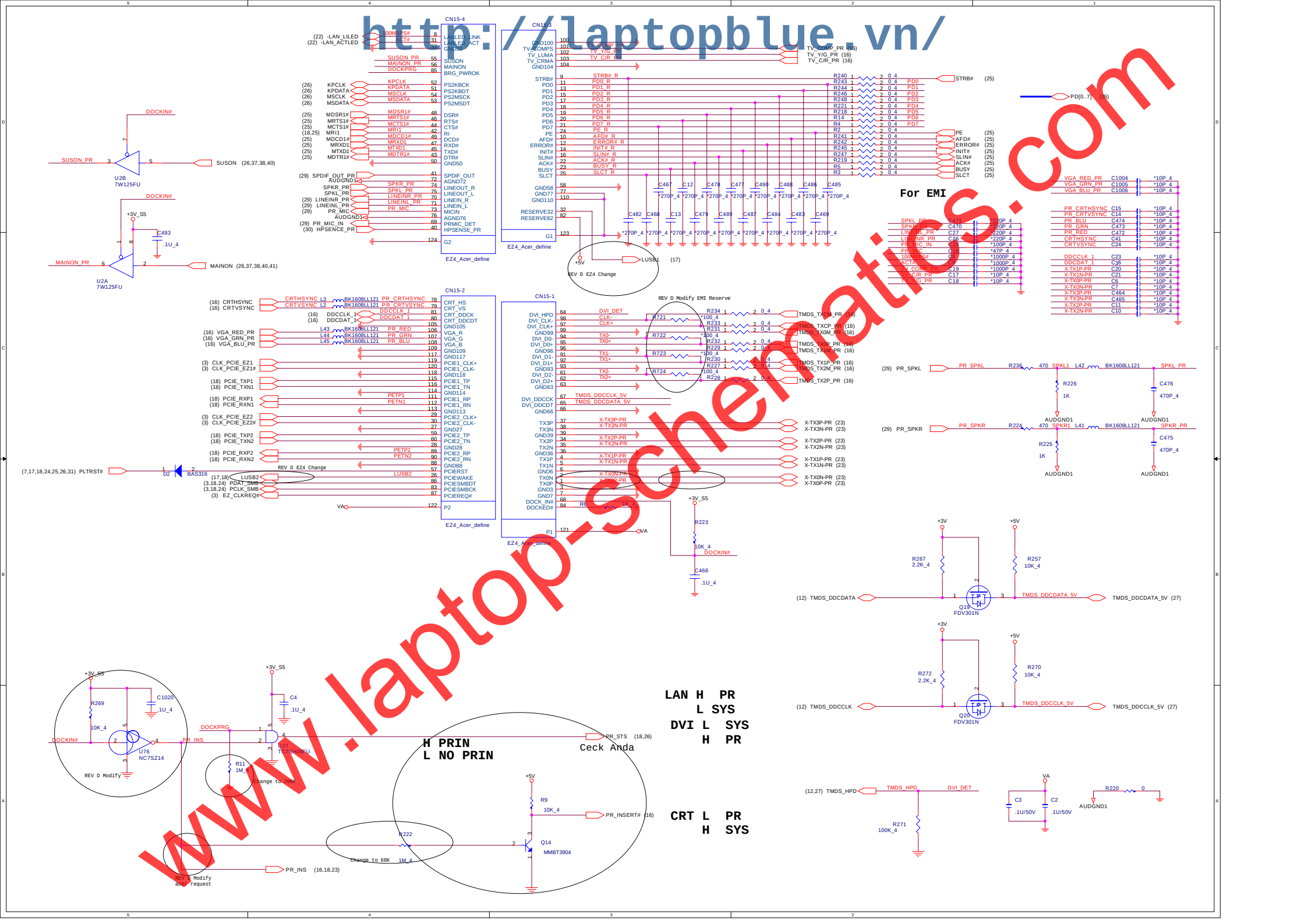
BAY ID STATUS

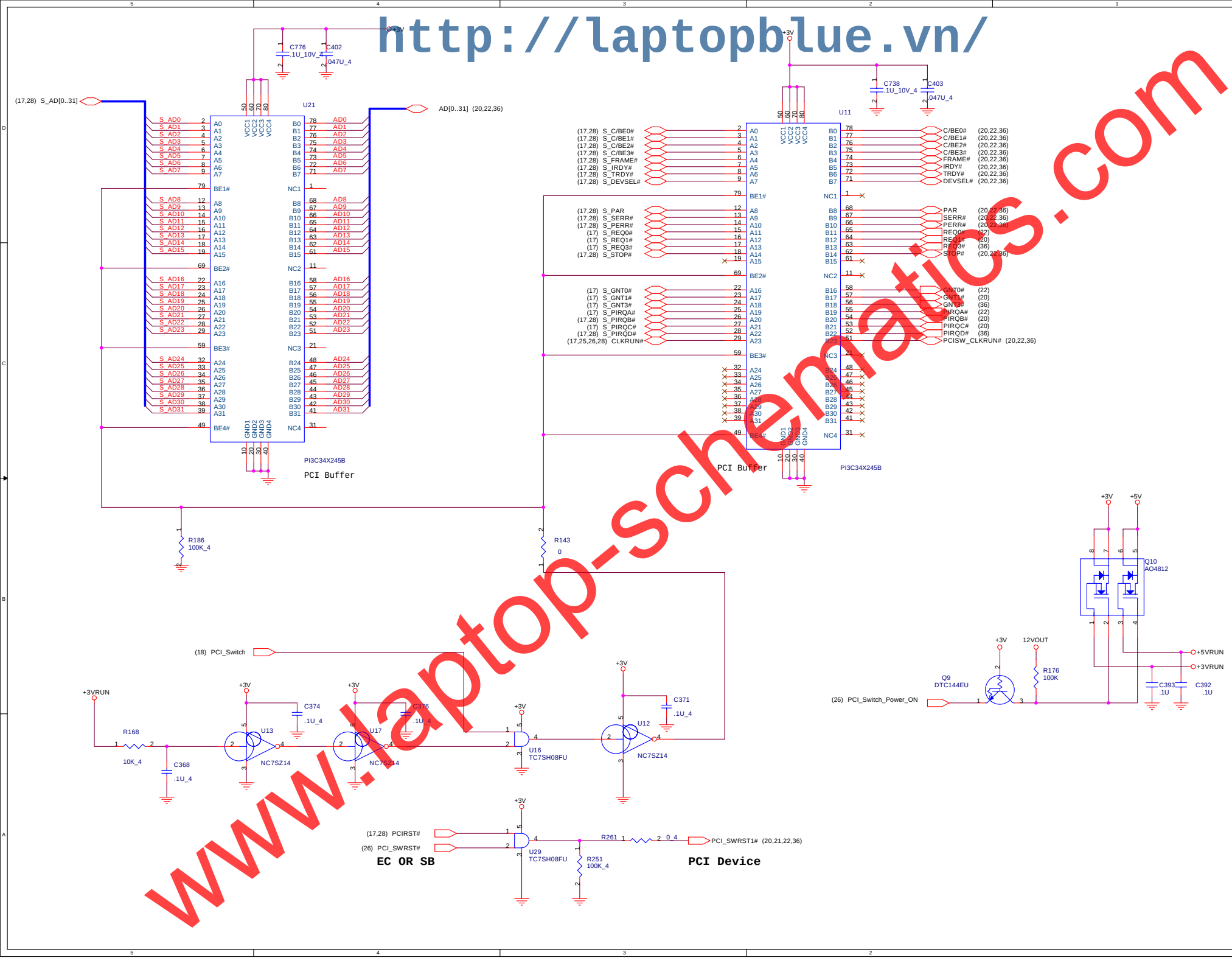
RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	1	HDD
1	0	CD/DVD

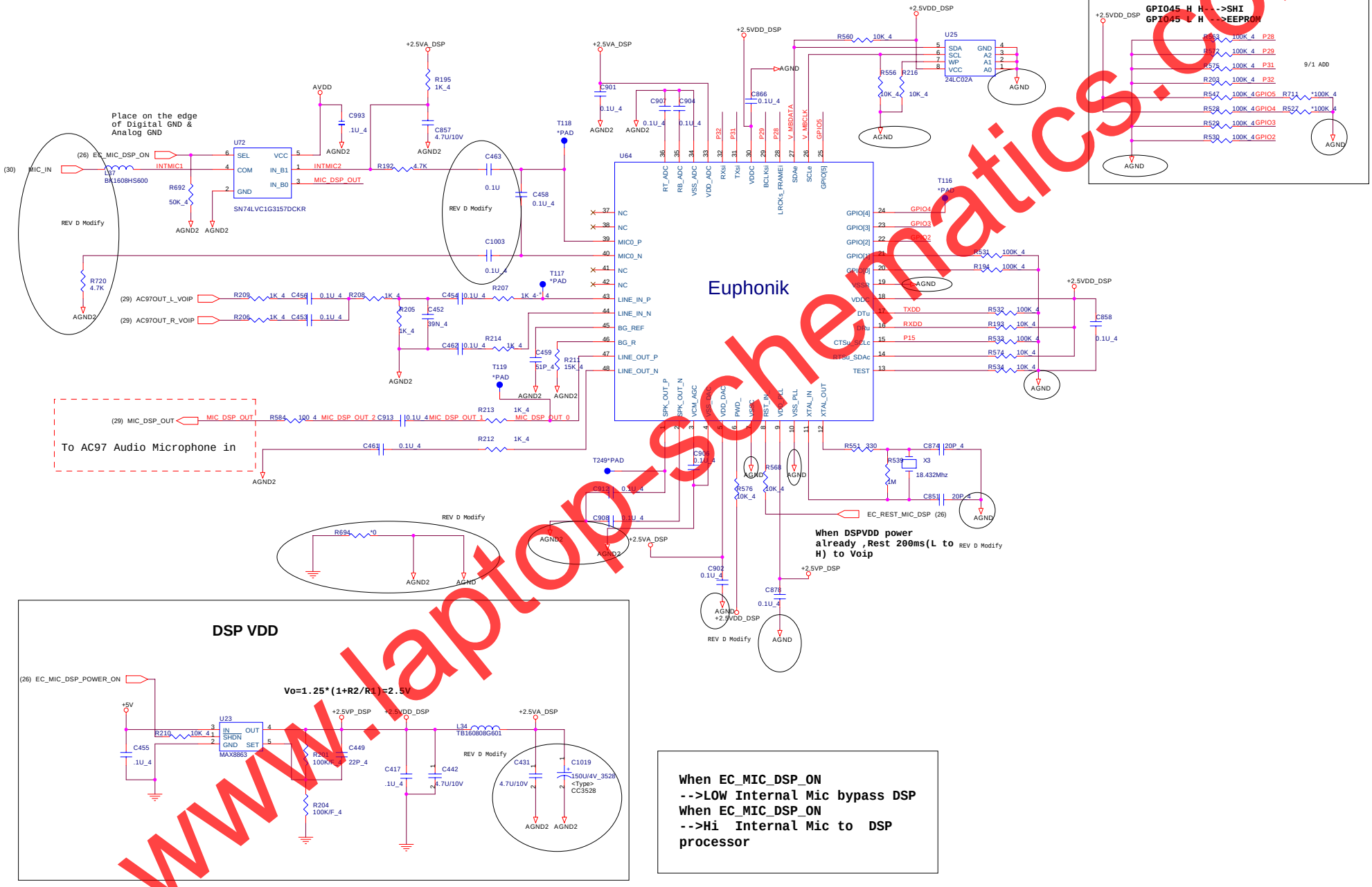












IEEE-1394

Used for vccp
(Pin20,35,48,62,78)

Used for vcc3
(Pin15,27,39,51,59,72,88,100)

Used for AVCC
(Pin1,2,107,108,120)

ID Select	: AD23
Interrupt Pin	: PIRQD#
Request indicates	: REQ3#
Grant indicates	: GNT3#

U68 TSB43AB21PDT

PHY PORT 0 · BIAS CURRENT · CRYSTAL · FILTER · EEPROM BUS · PHY PORT 1 · POWER CLASS · PCI INTERFACE

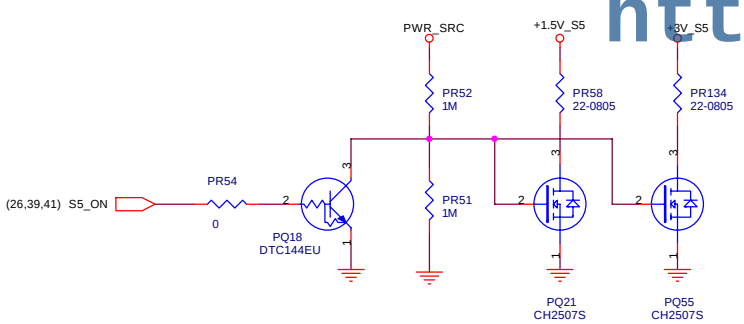
For ATE

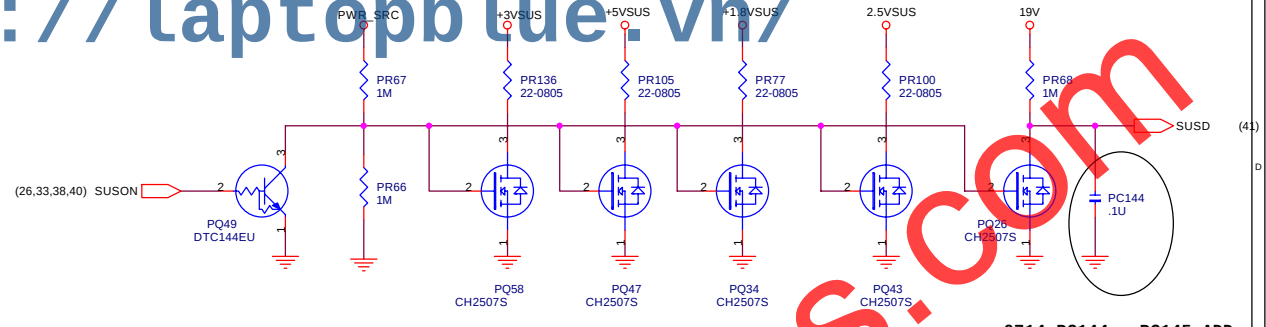
1394 EEPROM

U69 NM24C02-WMN6T

Q61 DTC144EU

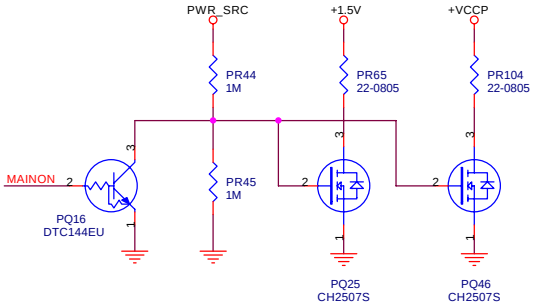
CONN-1394

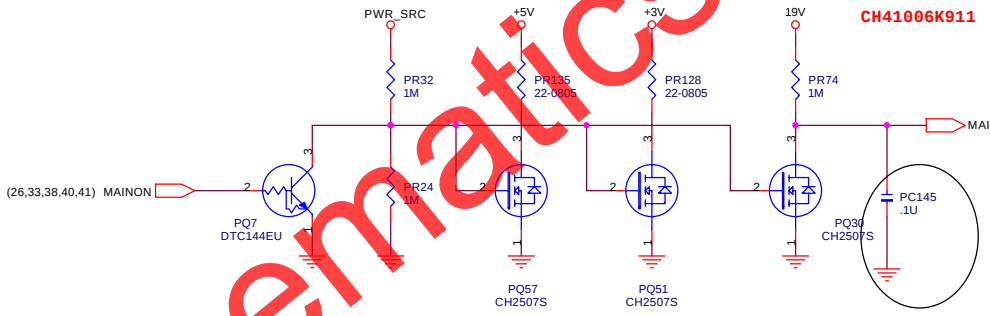


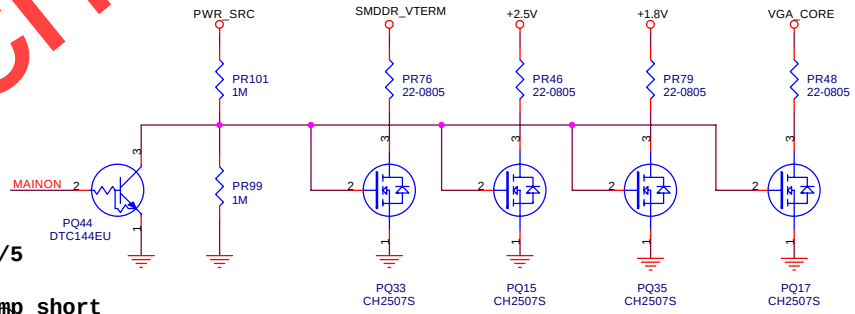


0714 PC144 , PC145 ADD

CH41006K911 0.1U



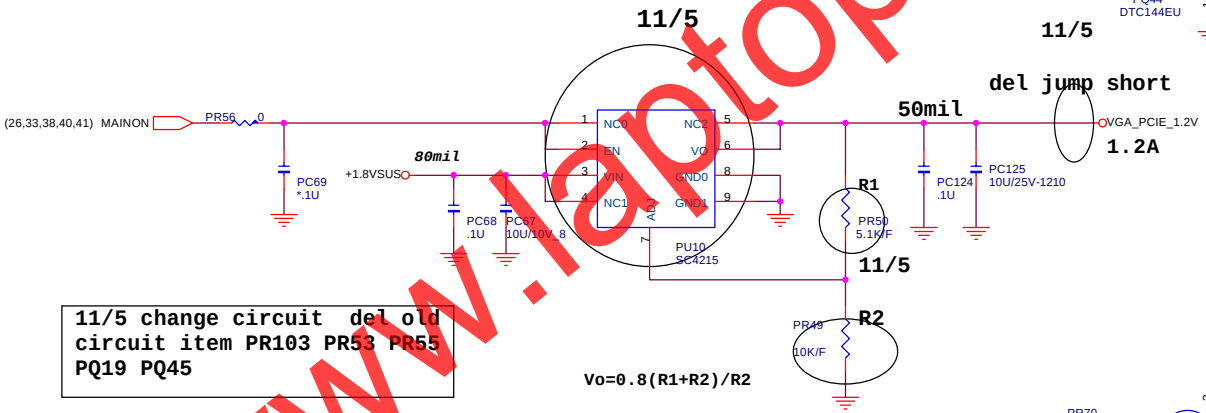




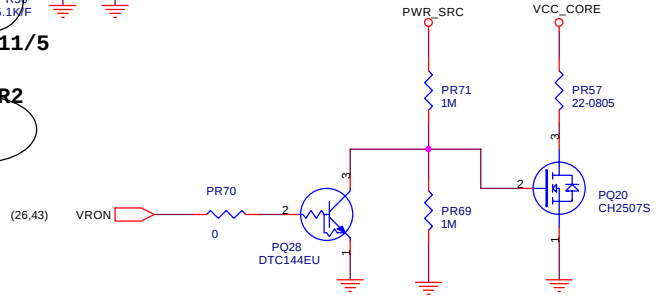
11/5

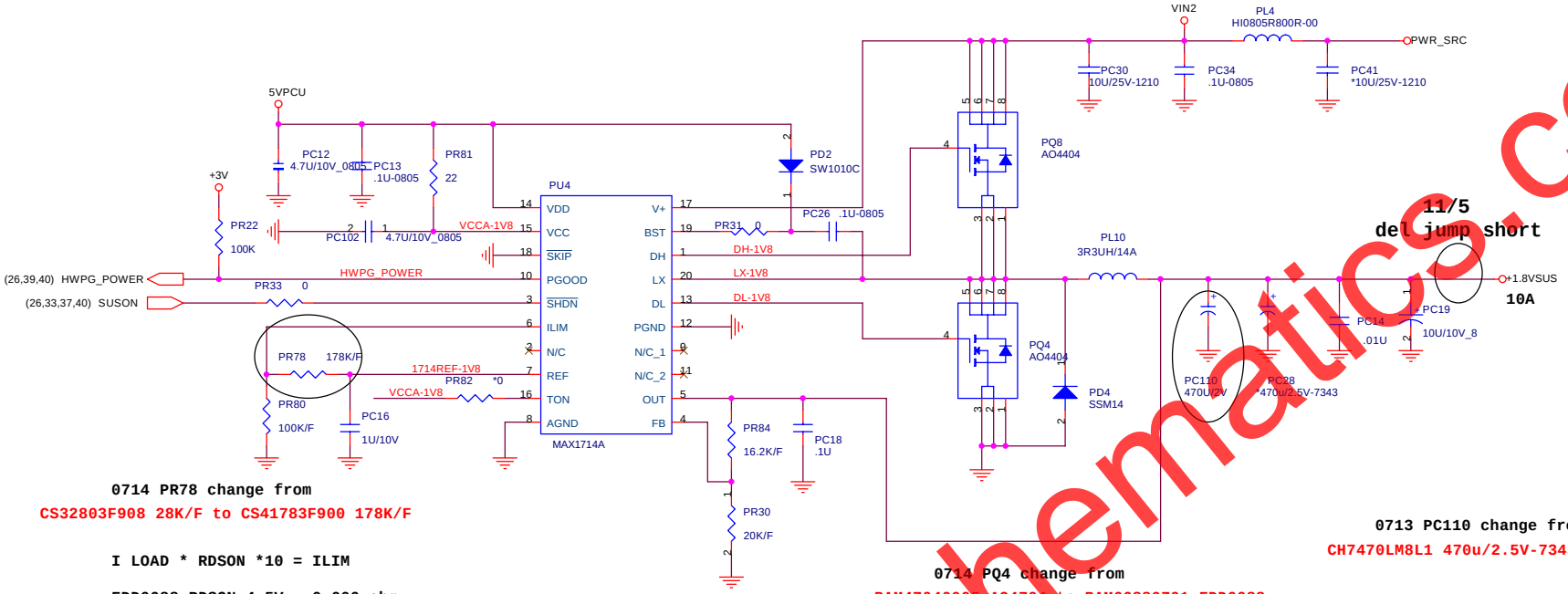
11/5

del jump short



11/5 change circuit del old circuit item PR103 PR53 PR55 PQ19 PQ45





11/5
del jump short

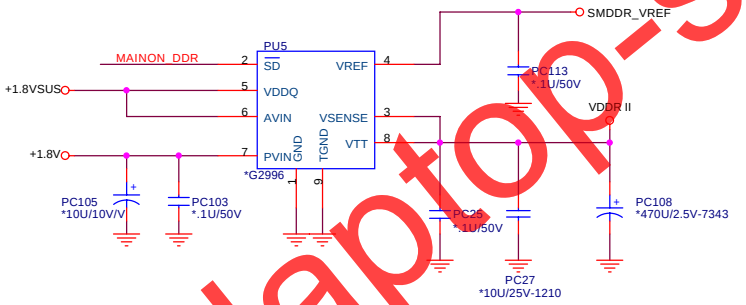
0714 PR78 change from
CS32803F908 28K/F to CS41783F900 178K/F

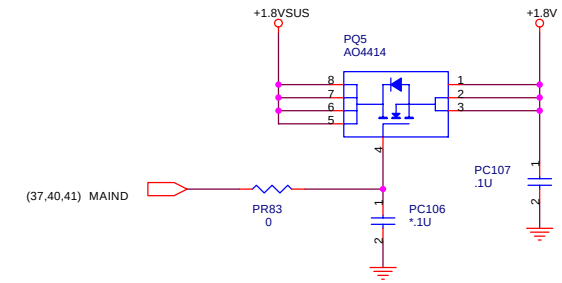
I LOAD * RDSON *10 = ILIM

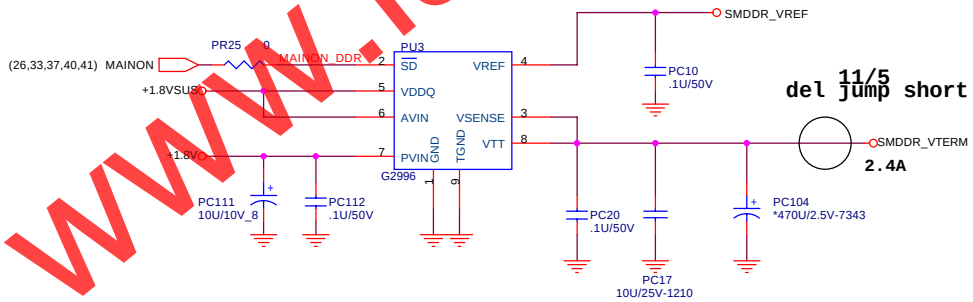
FDD6688 RDSON 4.5V = 0.006 ohm
12 * 0.006 *10 = 0.72V (ILIM)

0714 PQ4 change from
BAM47040005 AO4704 to BAM66880Z01 FDD6688
0714 PD4 change from
NC to BC000014Z01 SSM14

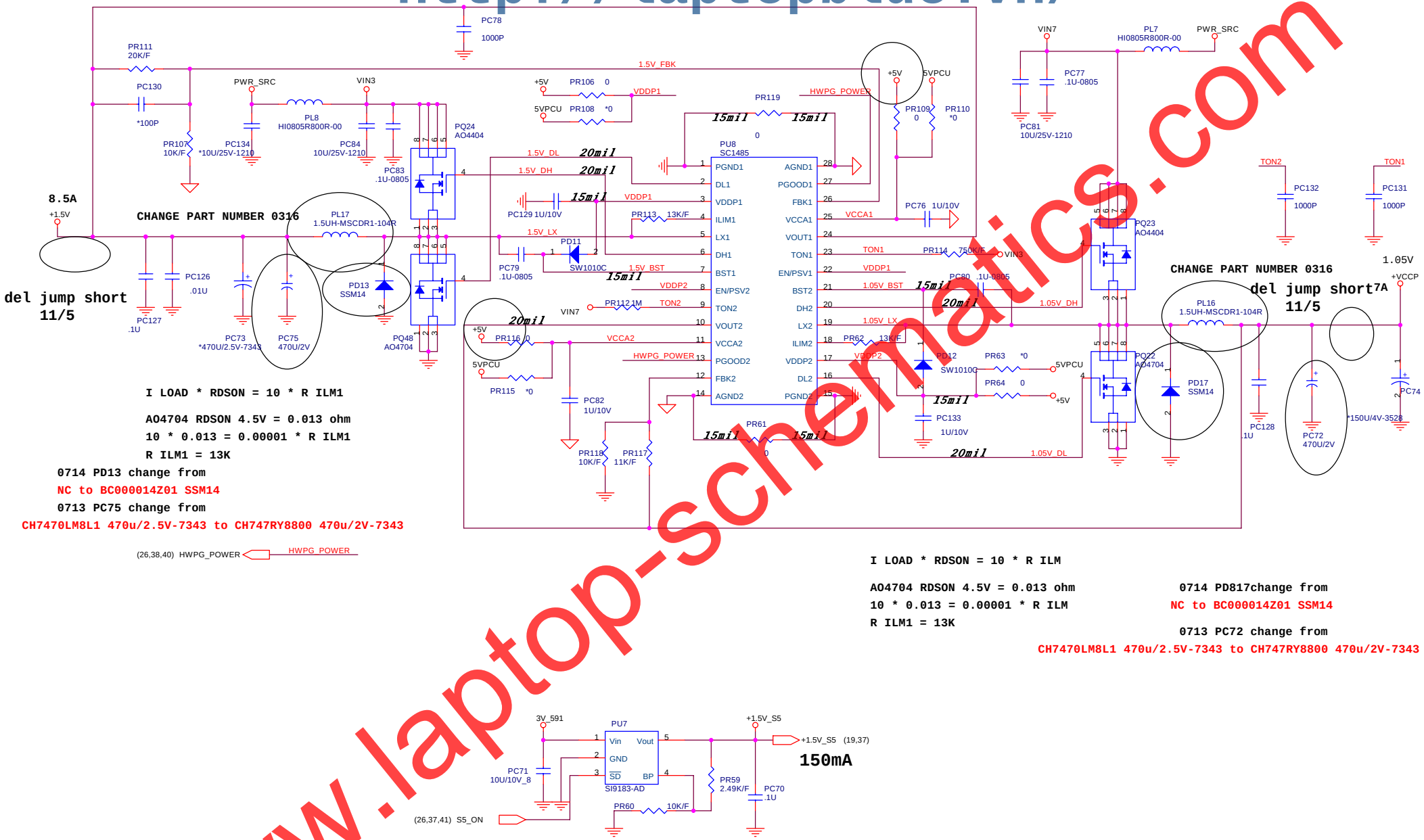
0713 PC110 change from
CH7470LM8L1 470u/2.5V-7343 to CH747RY8800 470u/2V-7343







11/5
del jump short



I LOAD * RDSON = 10 * R ILM1

AO4704 RDSON 4.5V = 0.013 ohm
10 * 0.013 = 0.00001 * R ILM1
R ILM1 = 13K

0714 PD13 change from
NC to BC000014Z01 SSM14
0713 PC75 change from
CH7470LM8L1 470u/2.5V-7343 to CH747RY8800 470u/2V-7343

(26,38,40) HWPG_POWER HWPG_POWER

I LOAD * RDSON = 10 * R ILM

AO4704 RDSON 4.5V = 0.013 ohm
10 * 0.013 = 0.00001 * R ILM
R ILM1 = 13K

0714 PD817change from
NC to BC000014Z01 SSM14
0713 PC72 change from
CH7470LM8L1 470u/2.5V-7343 to CH747RY8800 470u/2V-7343

0714 PR37 change from
CS21823F902 1.82K/F to CS27683F909 7.68K/F
0714 PL13 change from
CV-15A0MZ05 1R5 to CV-33E0MZ01 3R3
0714 PQ11 change from
BAM44040012 AO4404 to BAM60300Z11 FDD6030L
0714 PQ37 change from
BAM47040005 AO4704 to BAM66880Z01 FDD6688

L / RL(DCR) = Cqe * Rqe

3R3 DCR = 0.0043
3.3u / 0.0043 = 0.1u * Rqe
Rqe = 7.68K

L / RL(DCR) = Cqe * Rqe

3R8 DCR = 0.0130
3.8u / 0.0130 = 0.1u * Rqe
Rqe = 2.94K

del jump short
11/5

del jump short
11/5

1.2V/1.0V
VGA_CORE
12A

4A

0714 PC118 change from
NC to CH747RY8800 470u/2.5V-7343

0713 PC57 change from
CH7470LM8L1 470u/2.5V-7343 to CH747RY8800 470u/2V-7343

0714 PD8 change from
NC to BC000014Z01 SSM14

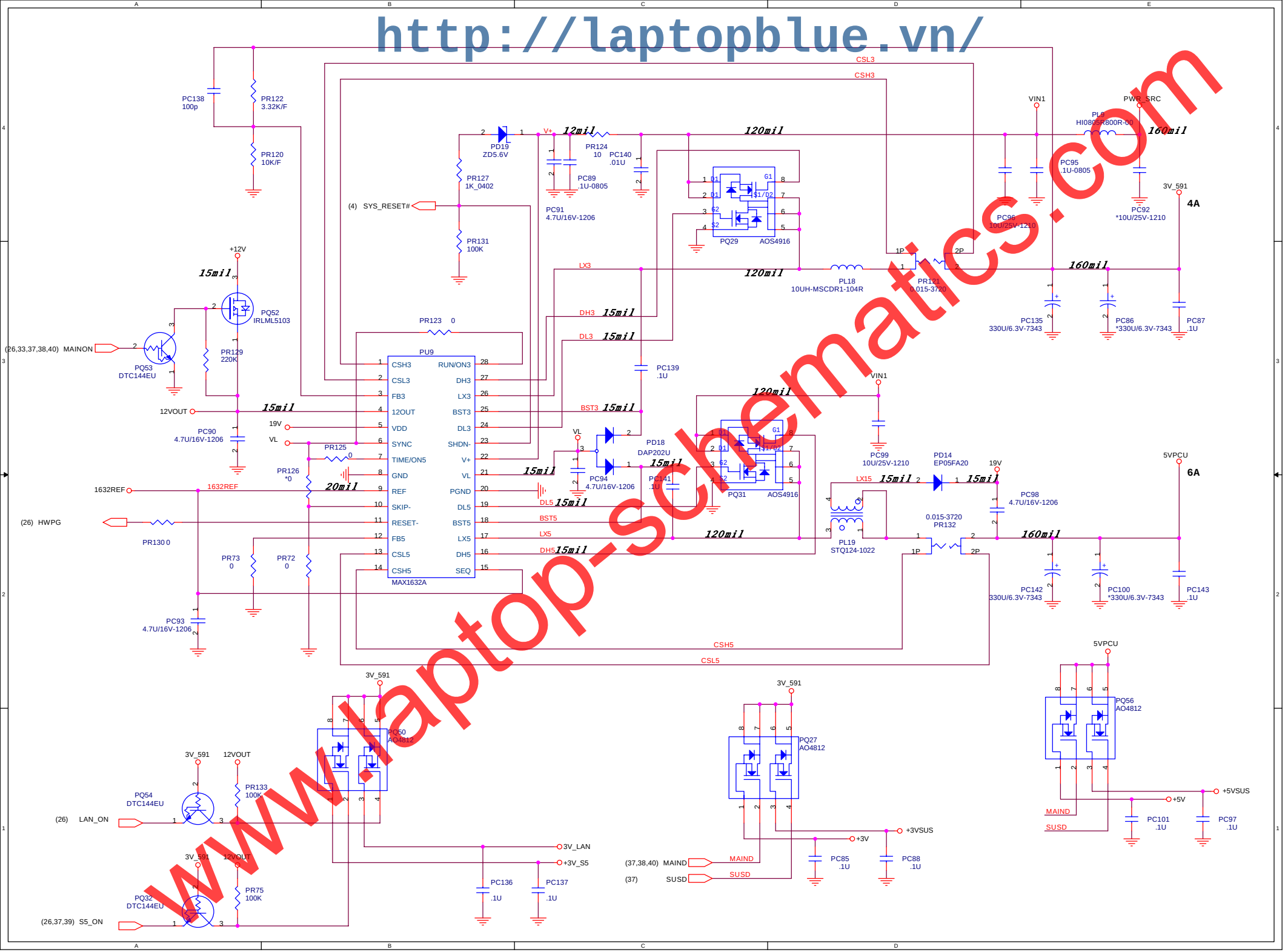
I LOAD * DCR *10 = ILIM

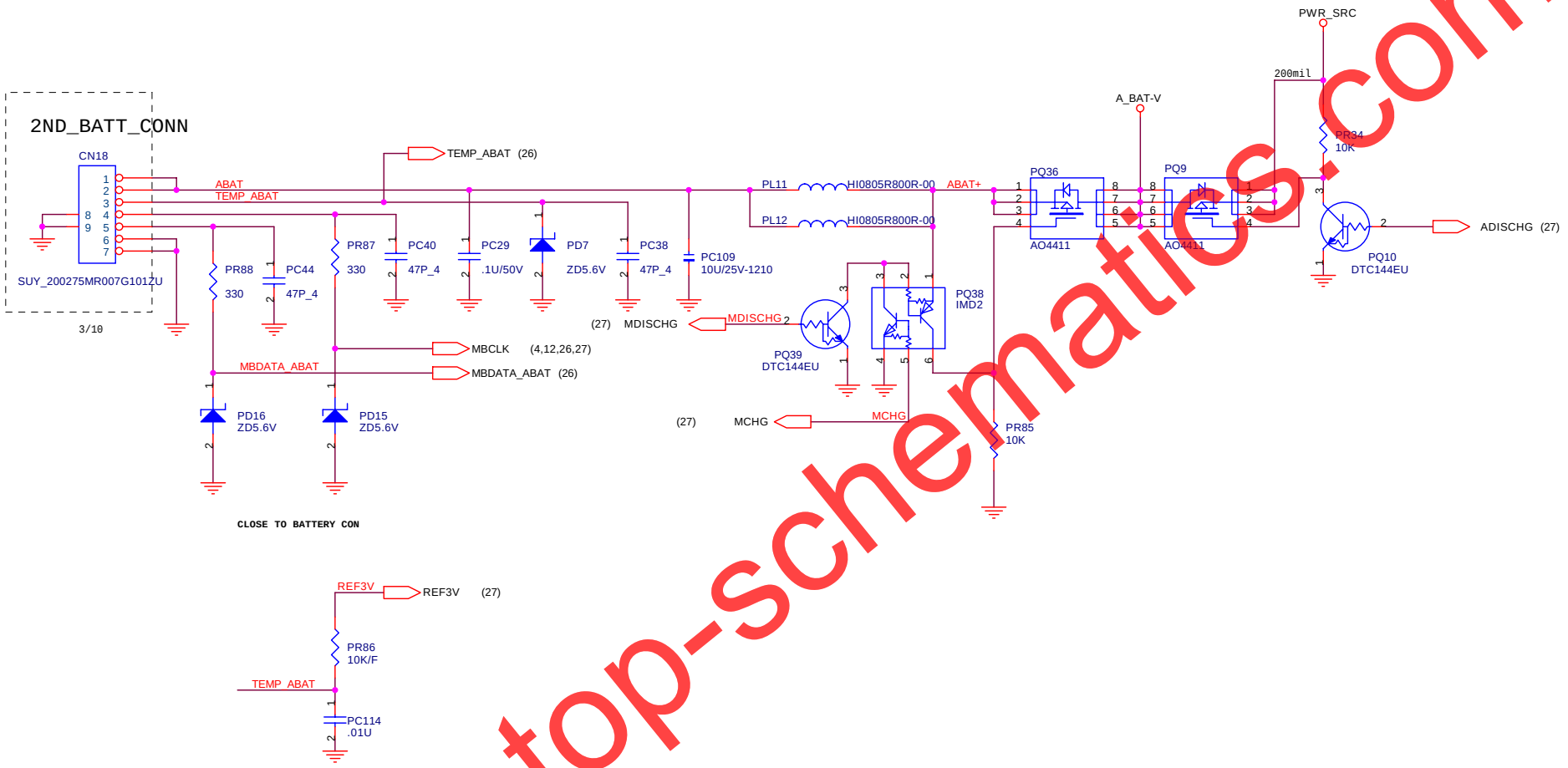
3R3 DCR = 0.0043
16 * 0.0043 *10 = 0.688V (ILIM1)
1R5 DCR = 0.0081
6 * 0.0130 *10 = 0.78V (ILIM2)

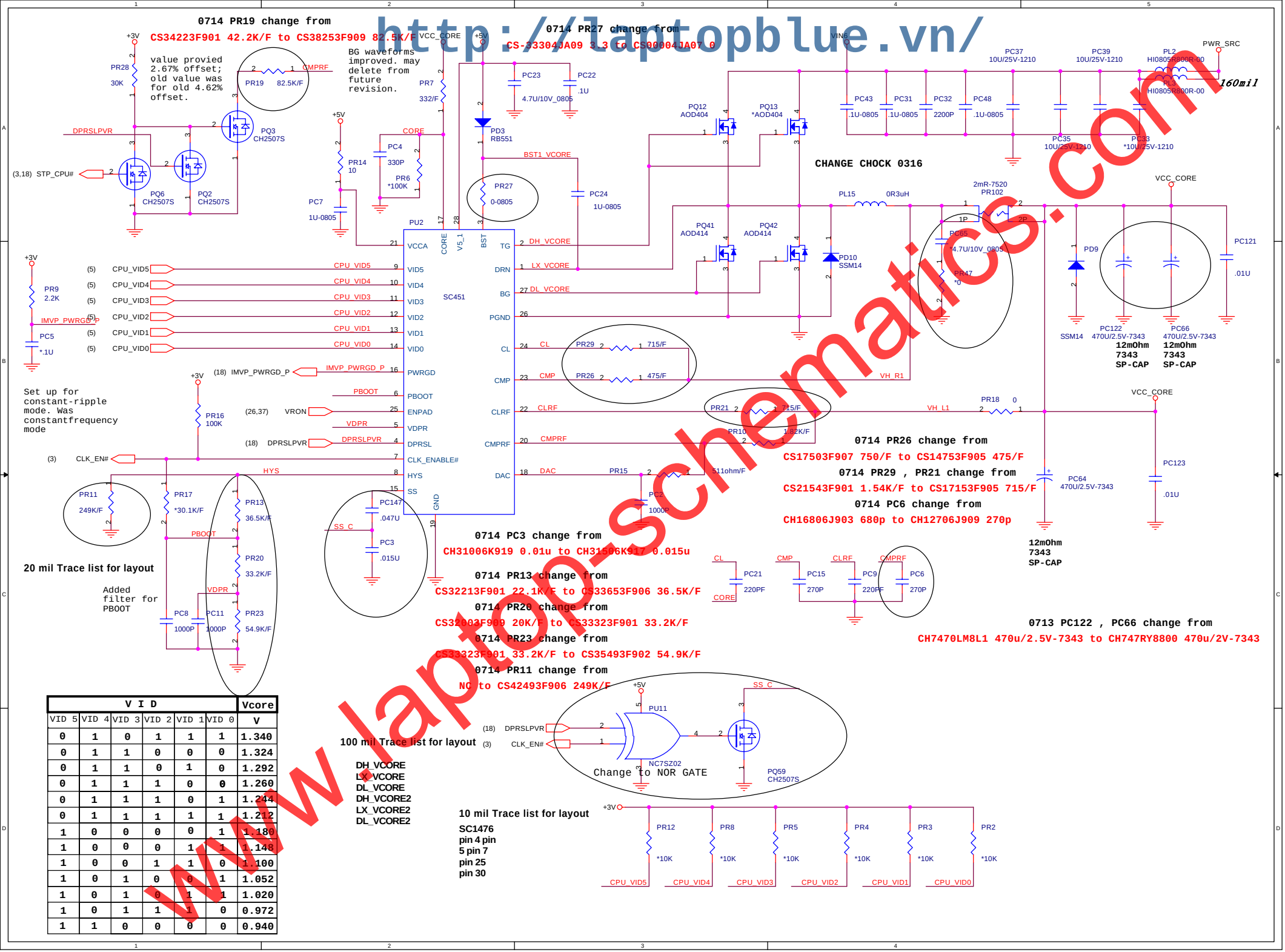
Low: 1.2V
High: 1.0V

0714 PR137 ADD
CS01003F902 10
0714 PC146 ADD
CH41006K911 0.1U

0714 PR93 change from
CS34993F908 49.9K/F to CS35233F908 52.3K/F







V I D						Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V
0	1	0	1	1	1	1.340
0	1	1	0	0	0	1.324
0	1	1	0	1	0	1.292
0	1	1	1	0	0	1.260
0	1	1	1	0	1	1.244
0	1	1	1	1	1	1.212
1	0	0	0	0	1	1.180
1	0	0	0	1	1	1.148
1	0	0	1	1	0	1.100
1	0	1	0	0	1	1.052
1	0	1	0	1	1	1.020
1	0	1	1	1	0	0.972
1	1	0	0	0	0	0.940