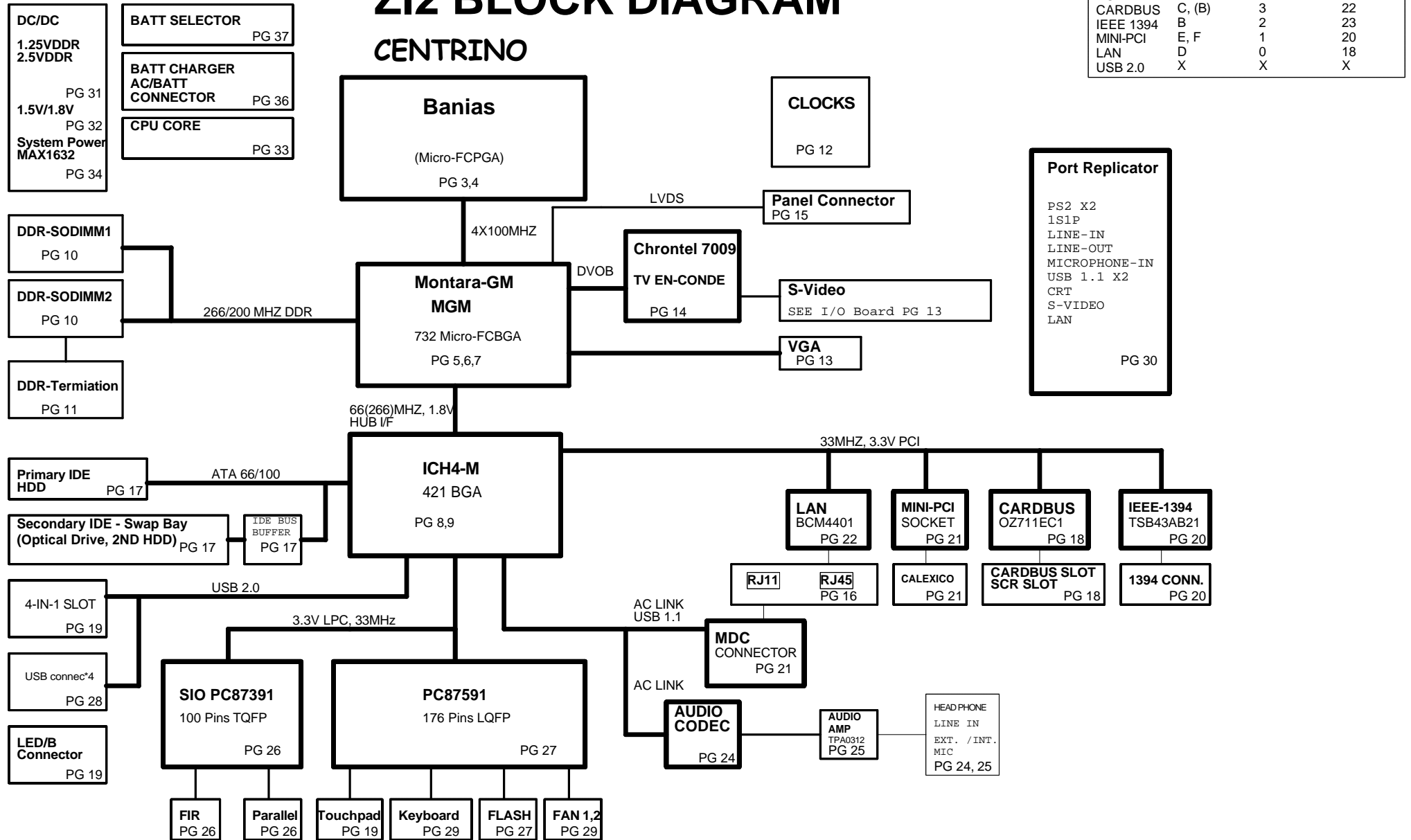


<http://laptopblue.vn/> Z12 BLOCK DIAGRAM

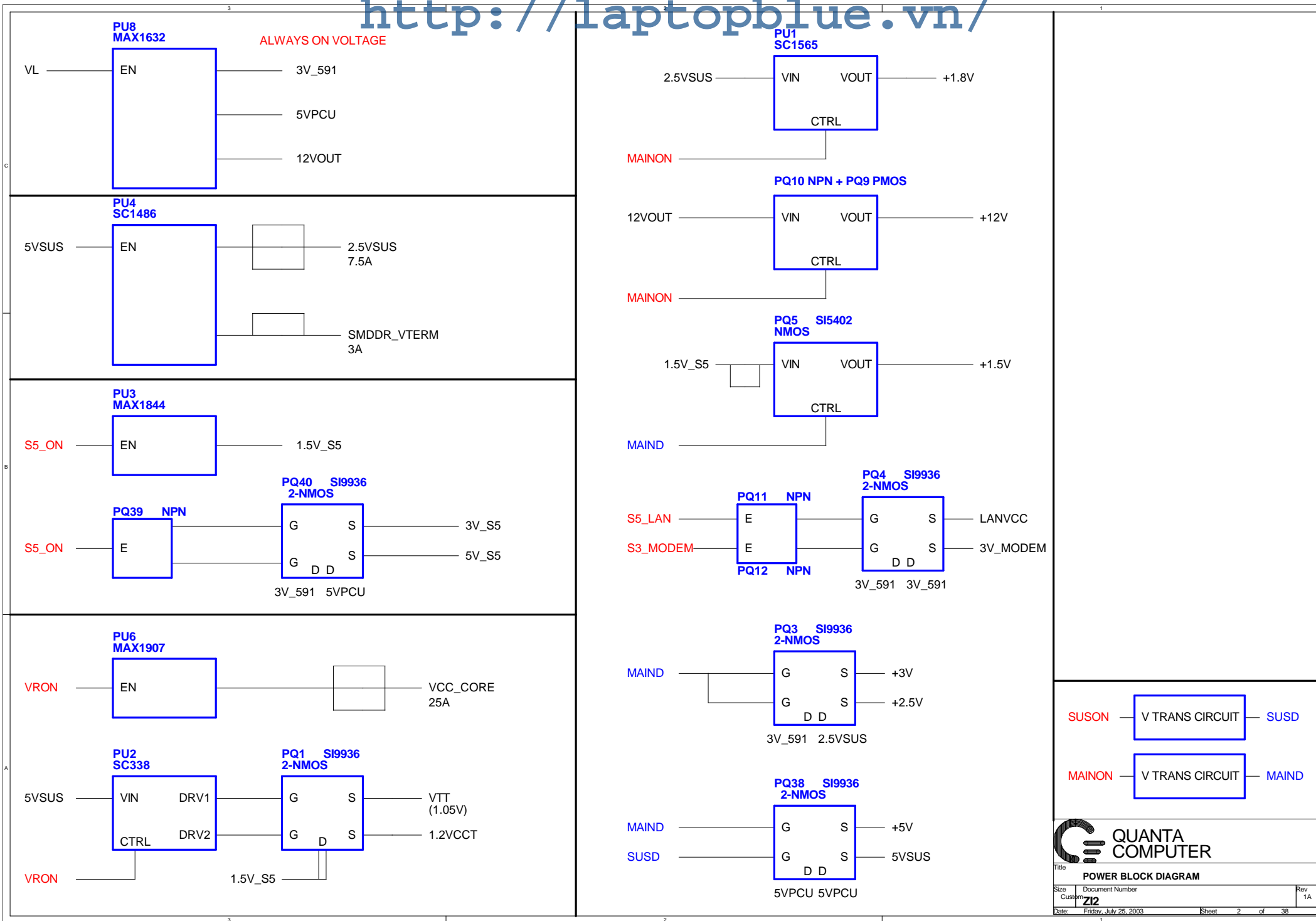
CENTRINO

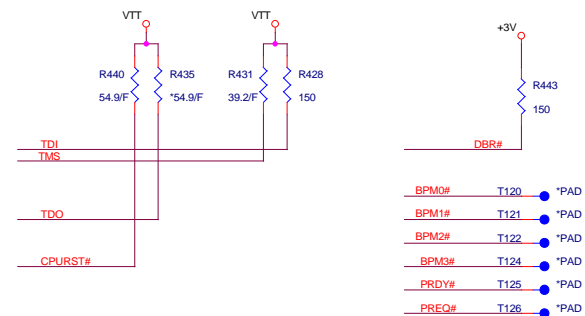
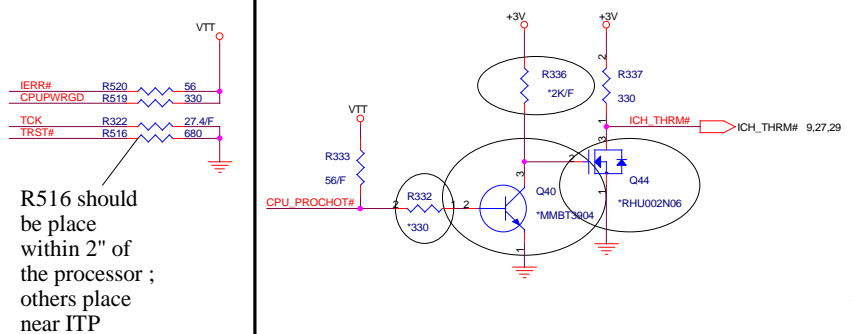
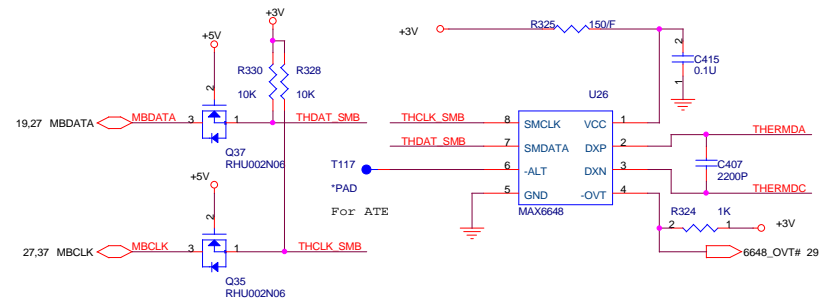
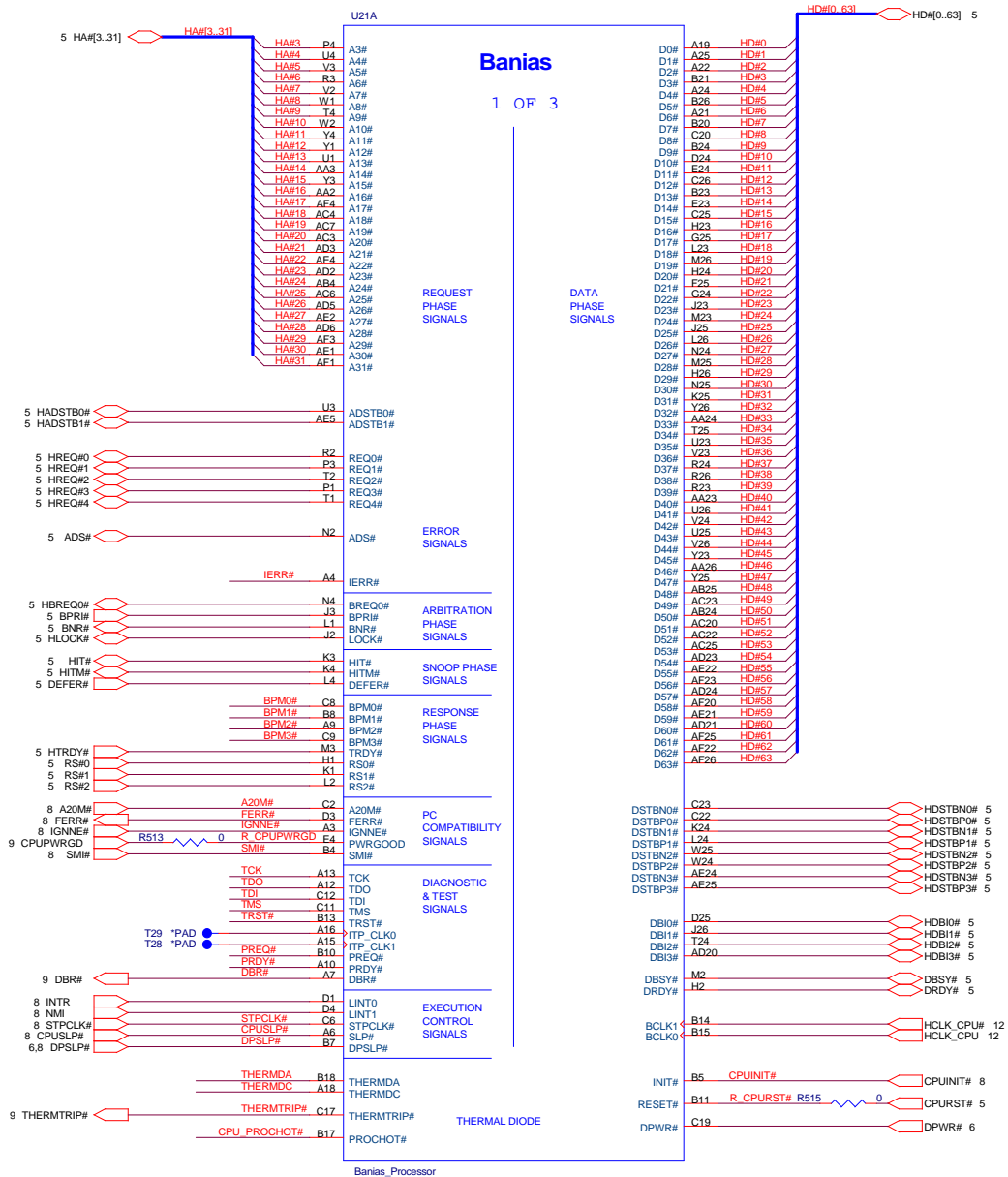


	PIRQ#	REQ#/GNT#	IDSEL
VGA	X	X	X
CARDBUS	C, (B)	3	22
IEEE 1394	B	2	23
MINI-PCI	E, F	1	20
LAN	D	0	18
USB 2.0	X	X	X



Title Schematic Block Diagram1		
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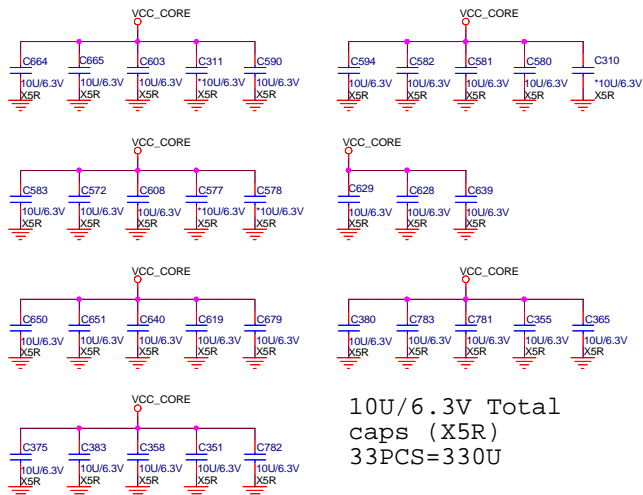
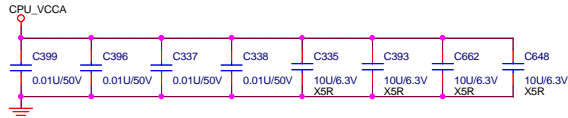
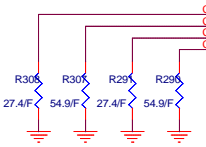




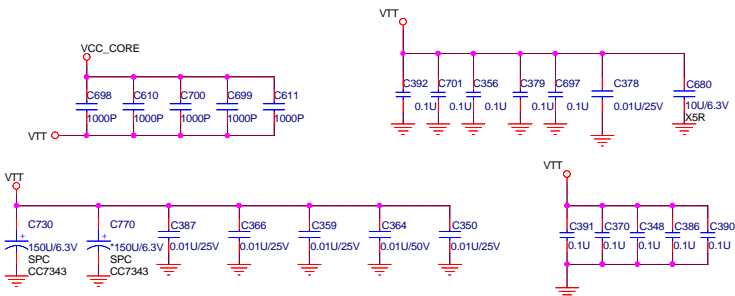
Del ITP700

Del R434,R439,C542,R442

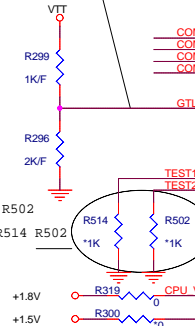
These 4 Resistors need to place with 0.5" of CPU.
Comp0,2 trace need to be $z0=27.4\text{ ohm}$,
comp1,3 traces need to be $z0=55\text{ ohm}$.



10U/6.3V Total
caps (X5R)
33PCS=330U



GTREF0: 2/3 VCCP-2%
T<0.5" 25 mils space
T<0.5" 25 mils space
T<0.5" 25 mils space



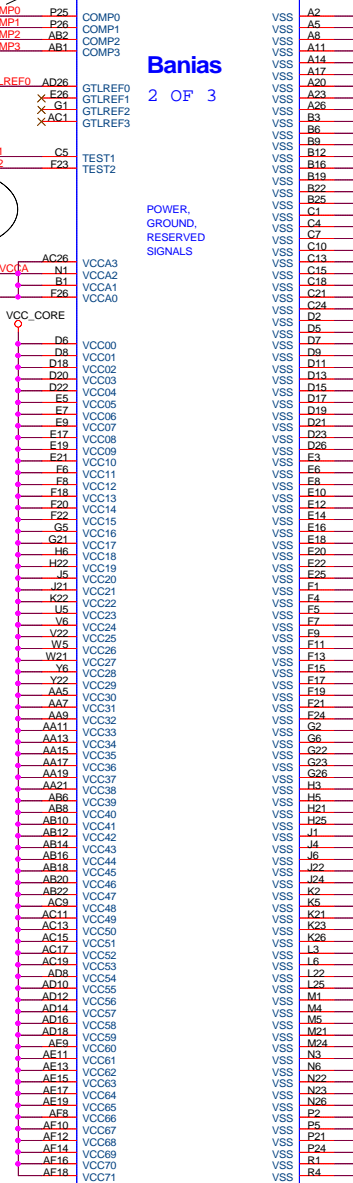
A0 CPU install R514 R502

A1 CPU not install R514 R502

1.5V option is not used.
It is ok to leave in there.

Banias
2 OF 3

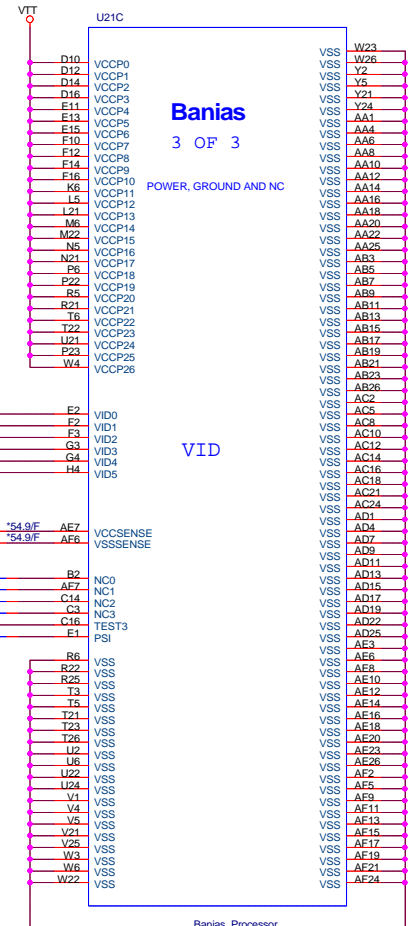
POWER, GROUND, RESERVED SIGNALS



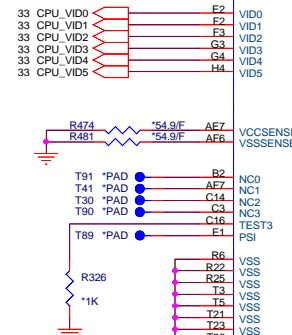
Banias_Processor

Banias
3 OF 3

POWER, GROUND AND NC

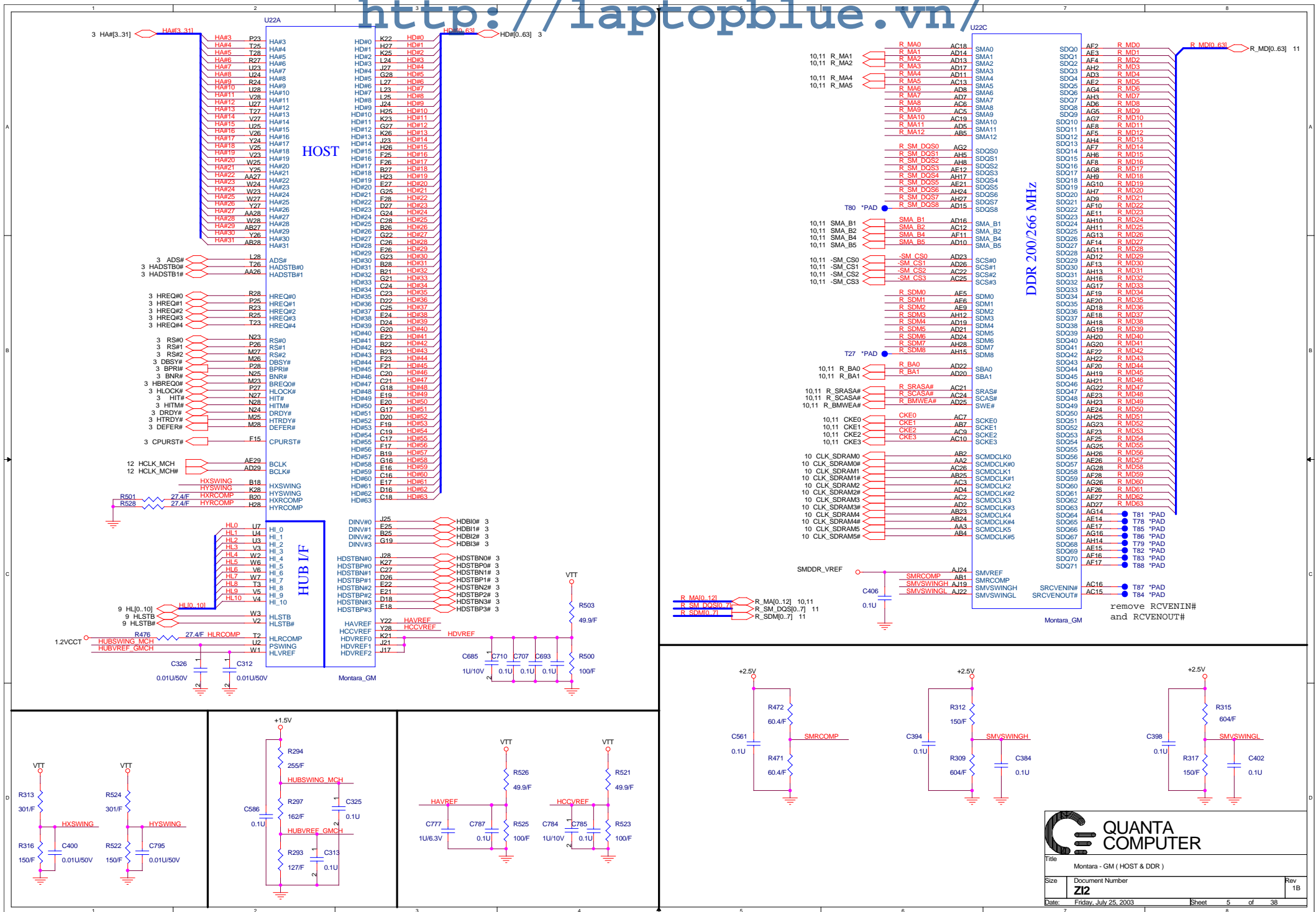


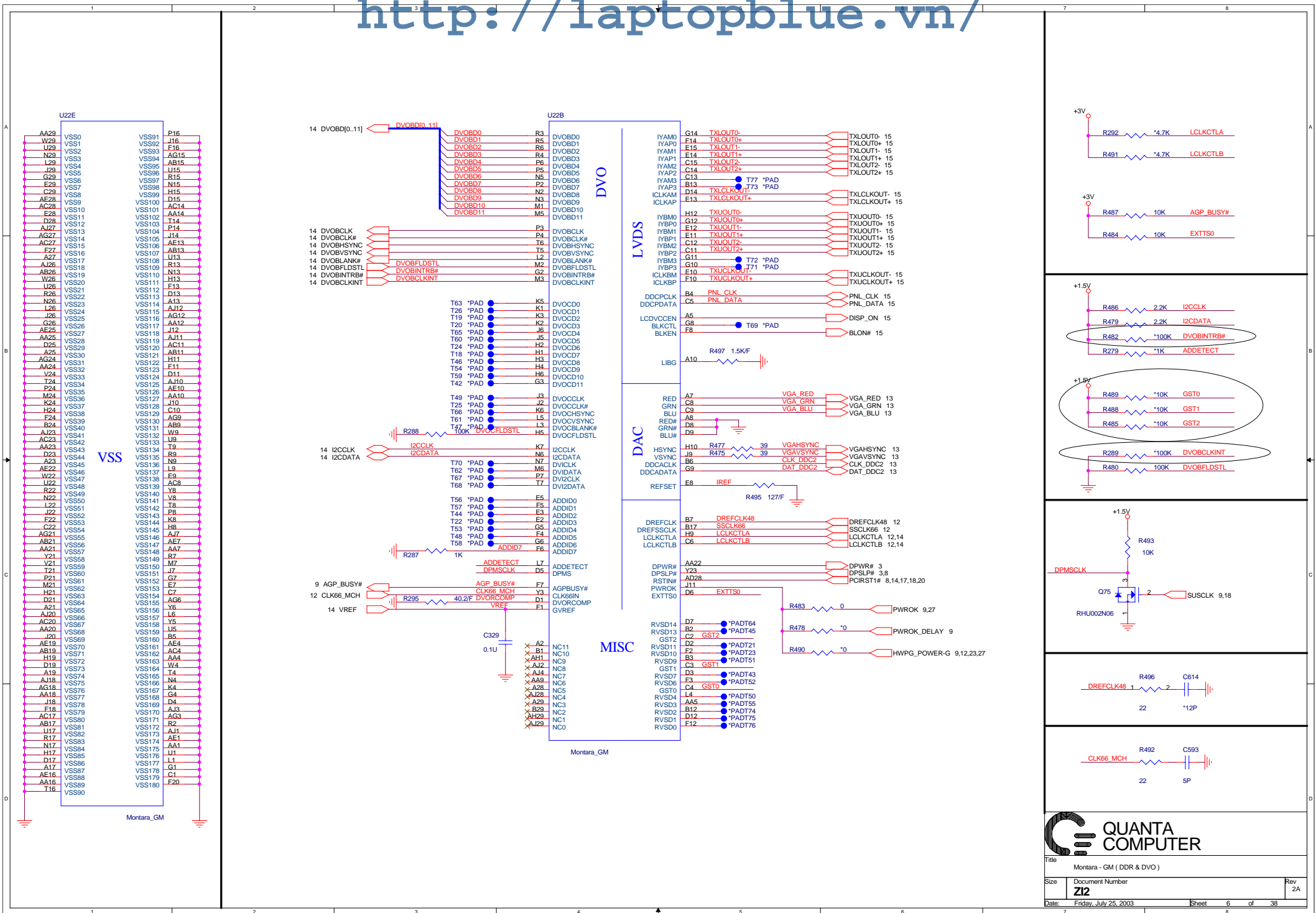
VID

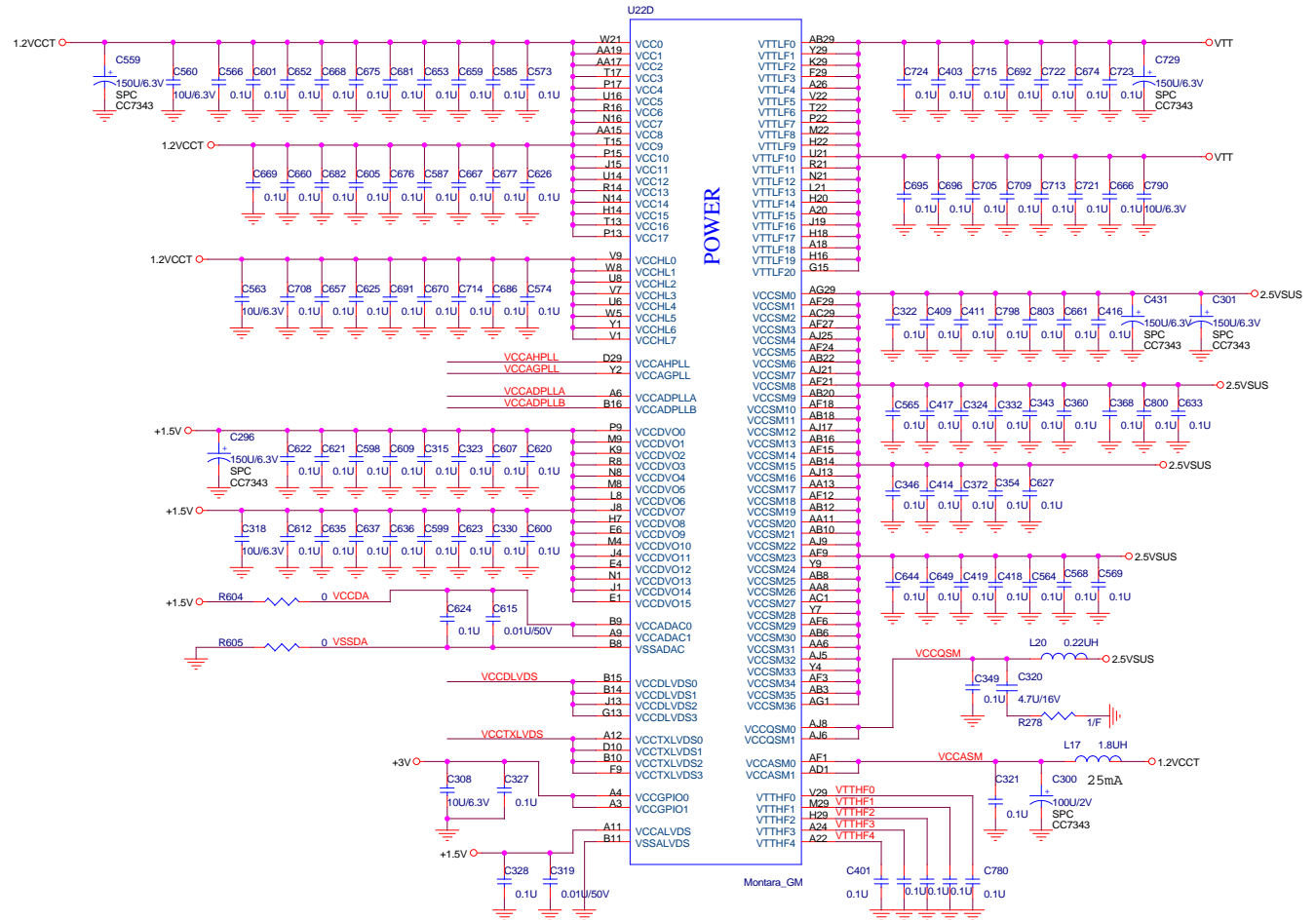
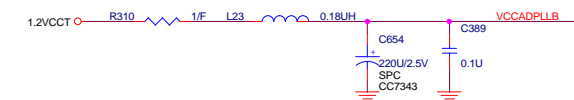
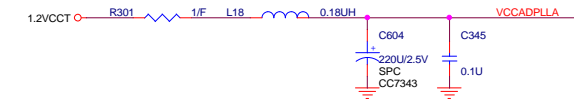
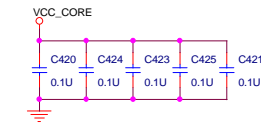
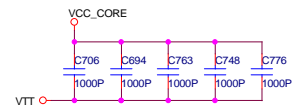
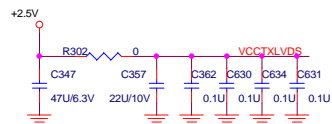
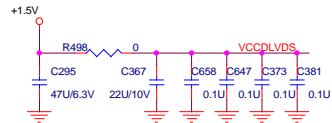
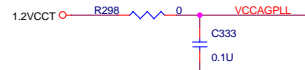
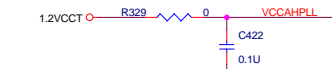


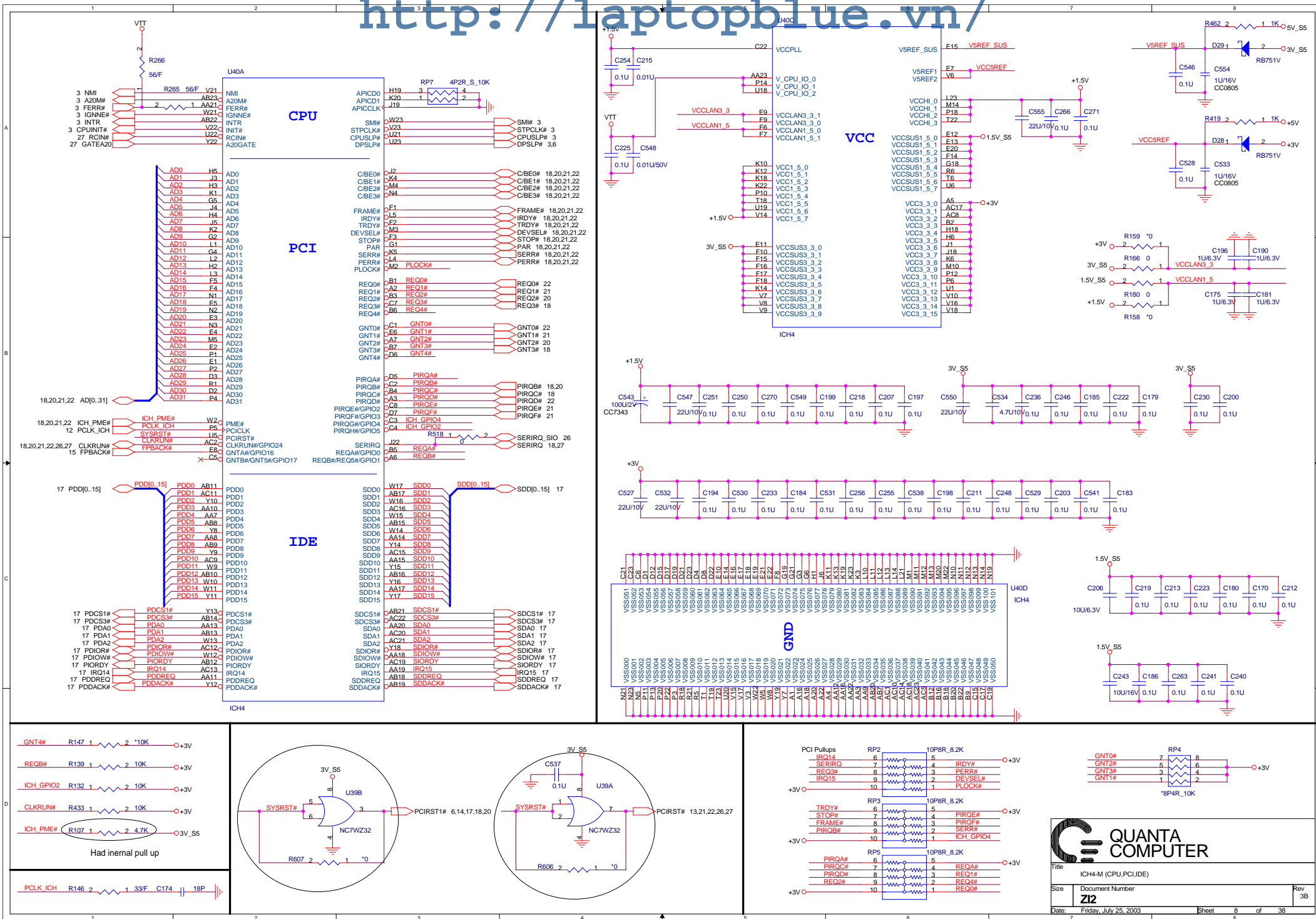
* VCC_BOOT 1.2V
VCC_CORE 1.468V
(DEEPER SLEEP) 0.956V
VTT 1.05V
VCCA 1.8V





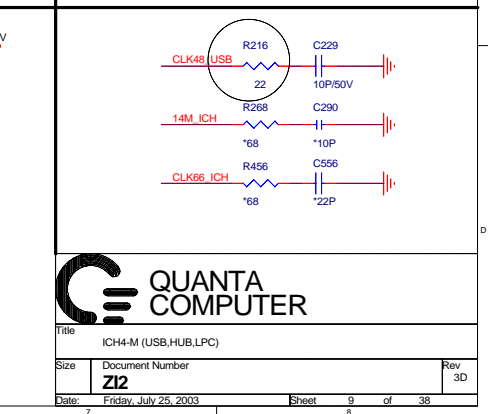
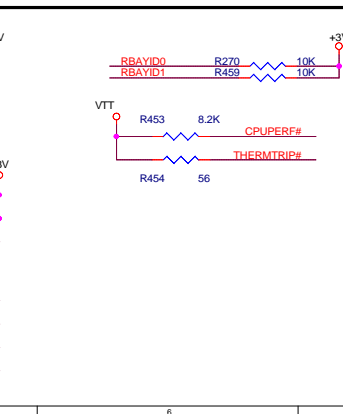
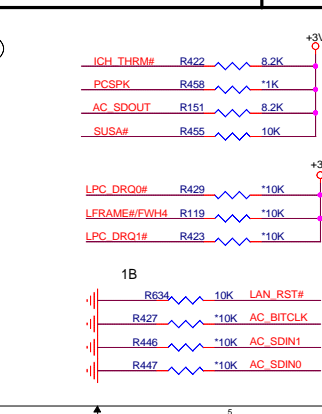
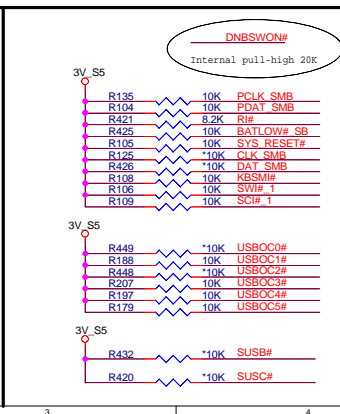
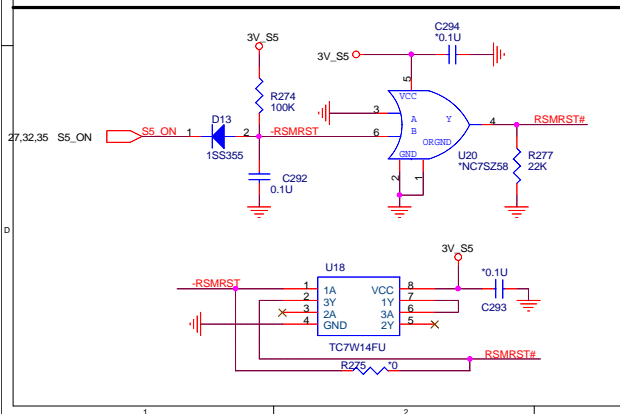
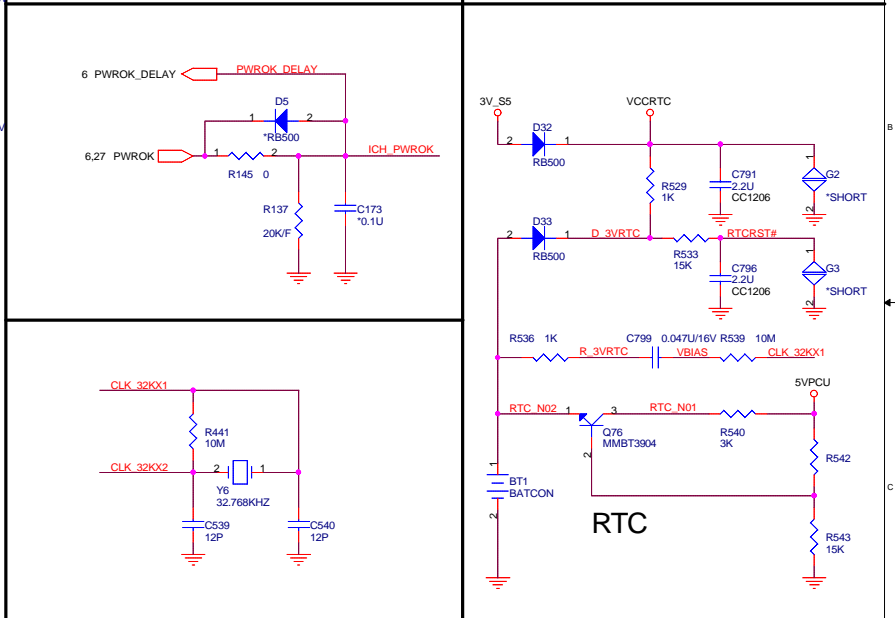
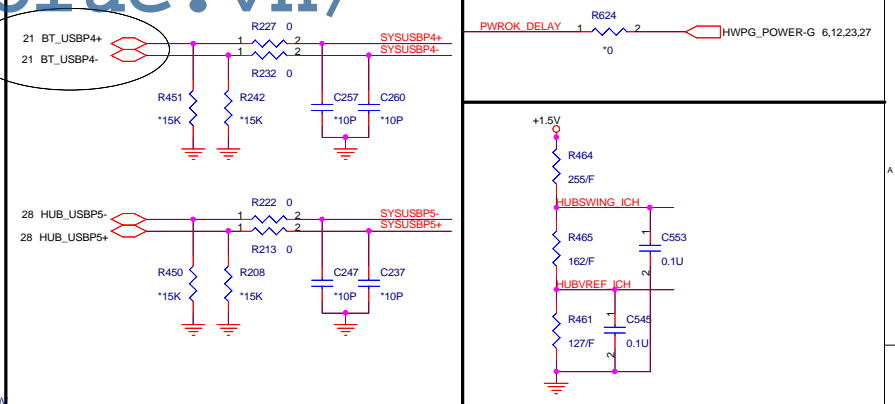
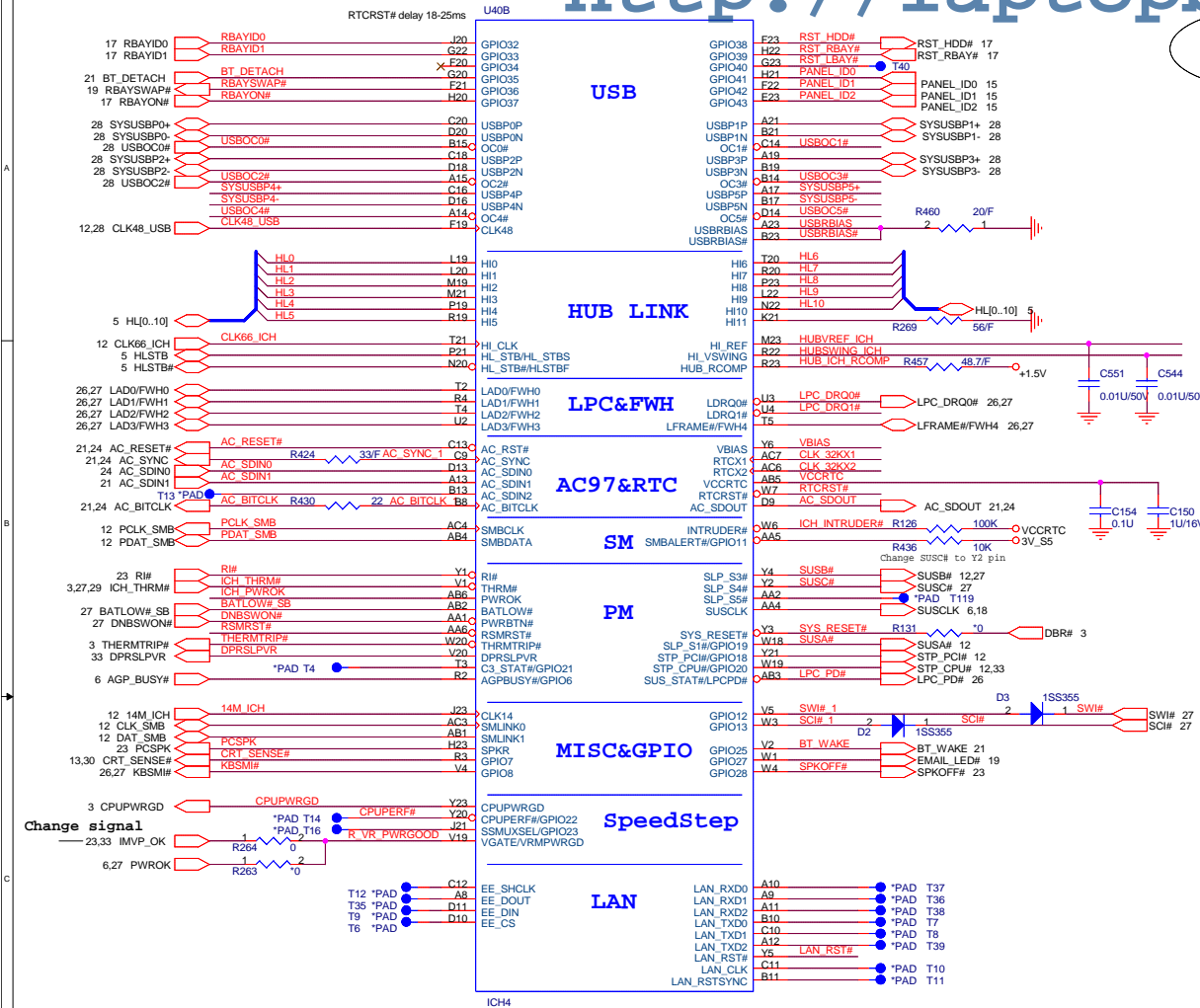


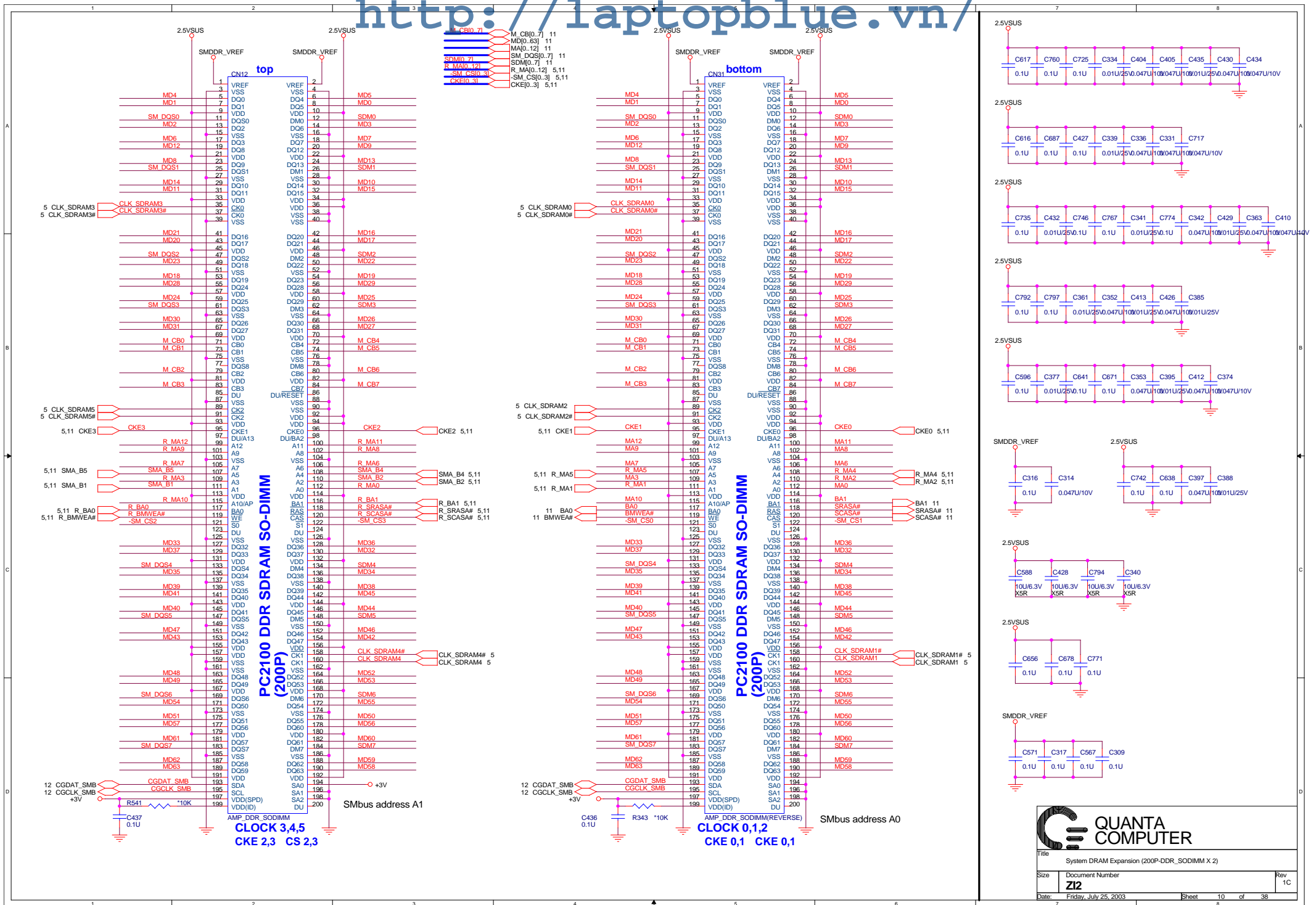




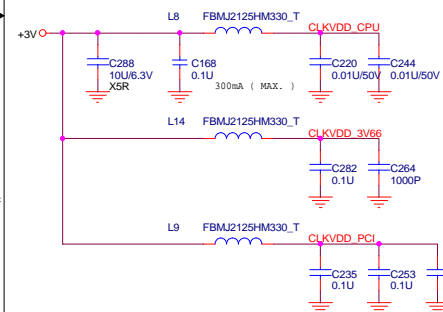
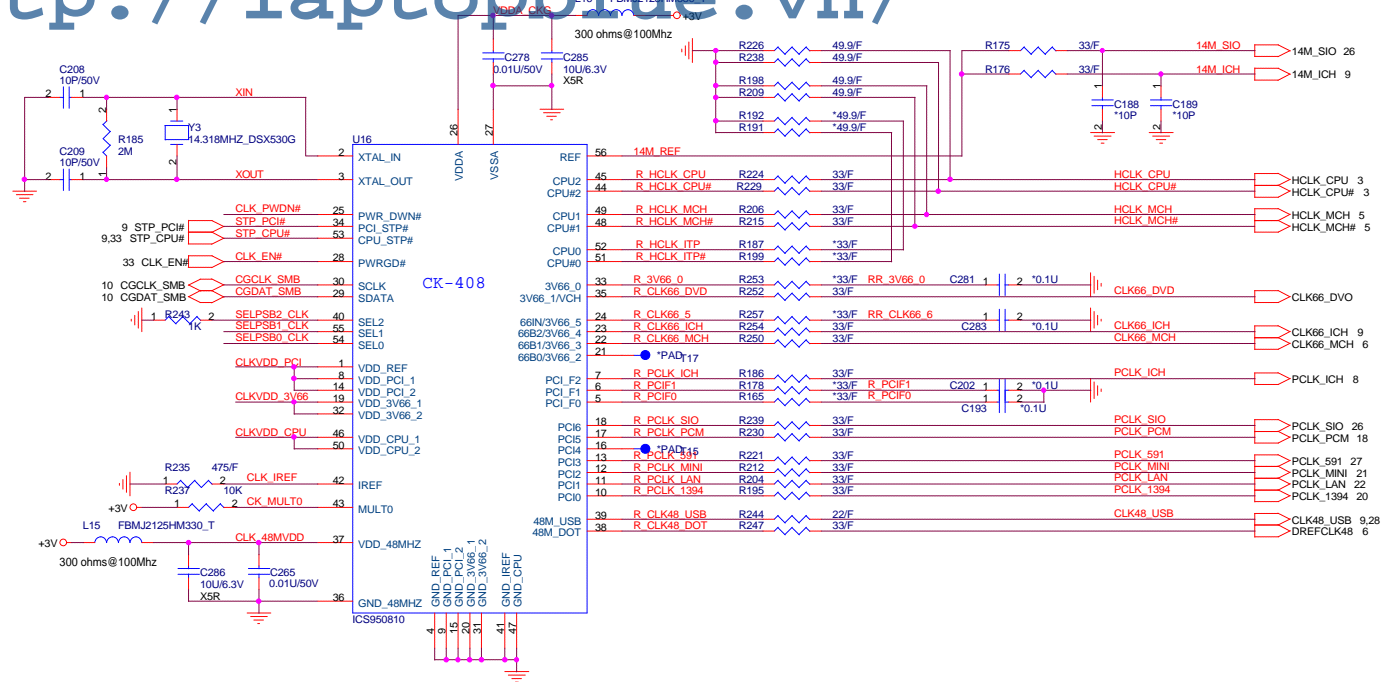
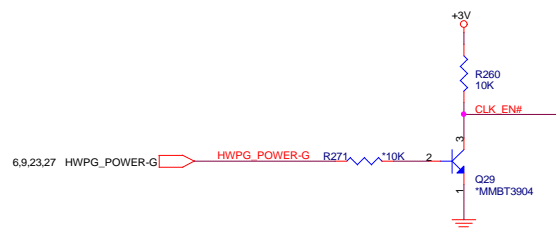
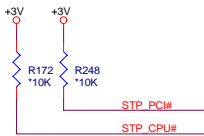
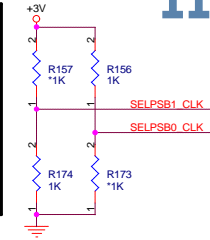
QUANTA COMPUTER

Title		ICH4-M (CPU,PCI,IDE)	
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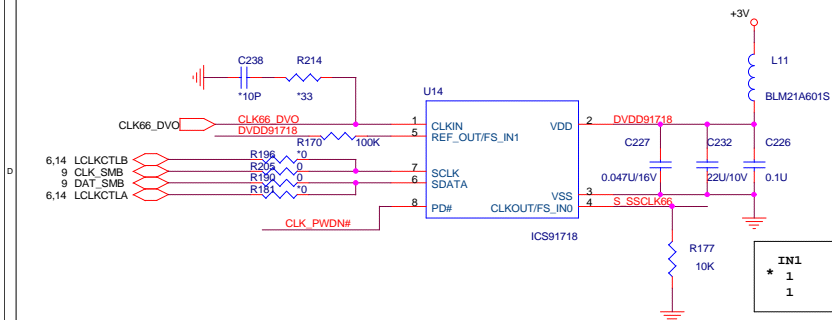
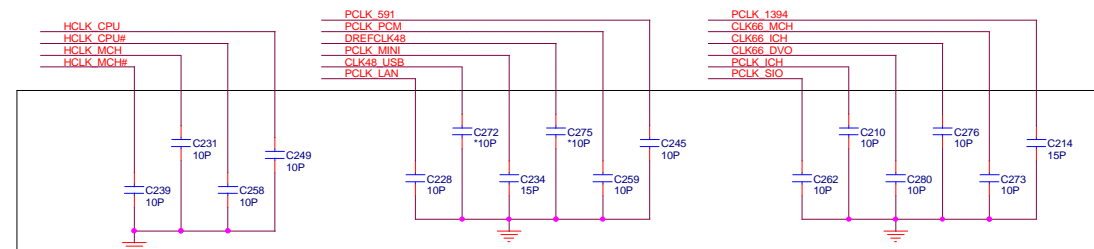
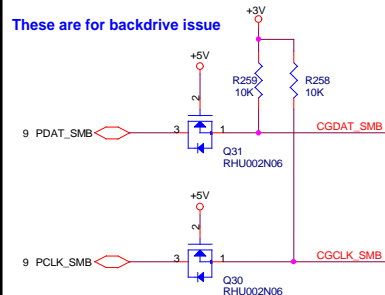




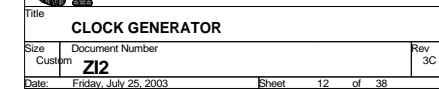
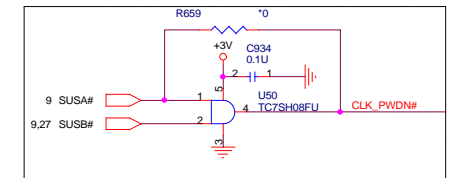
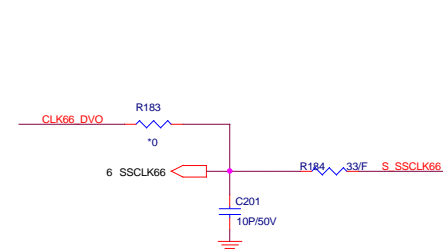


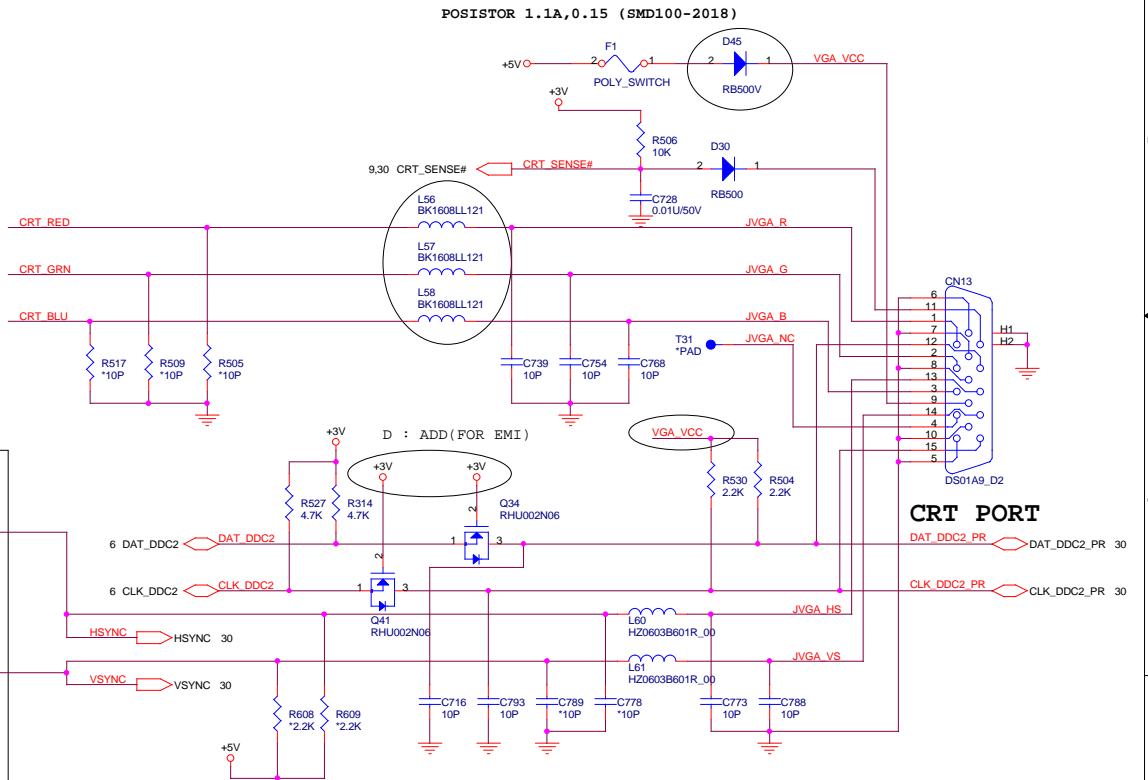
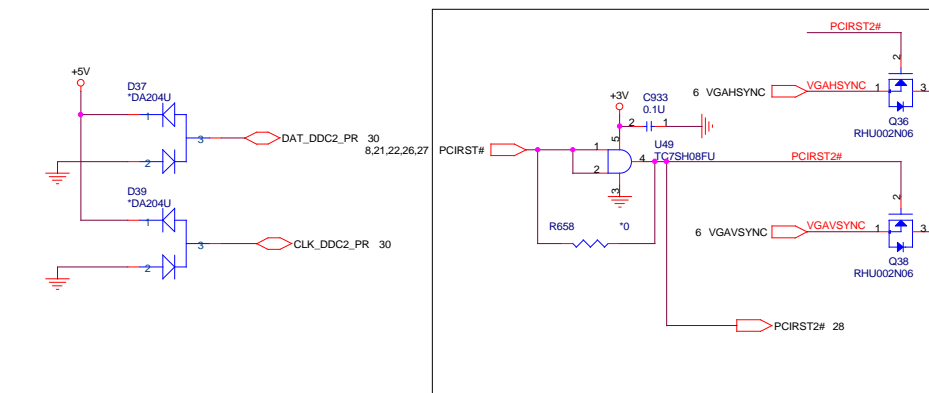
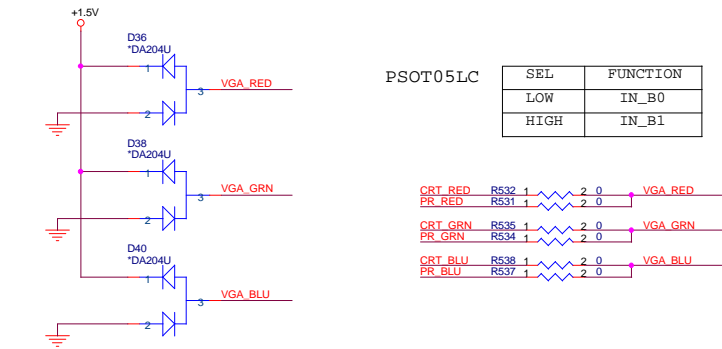
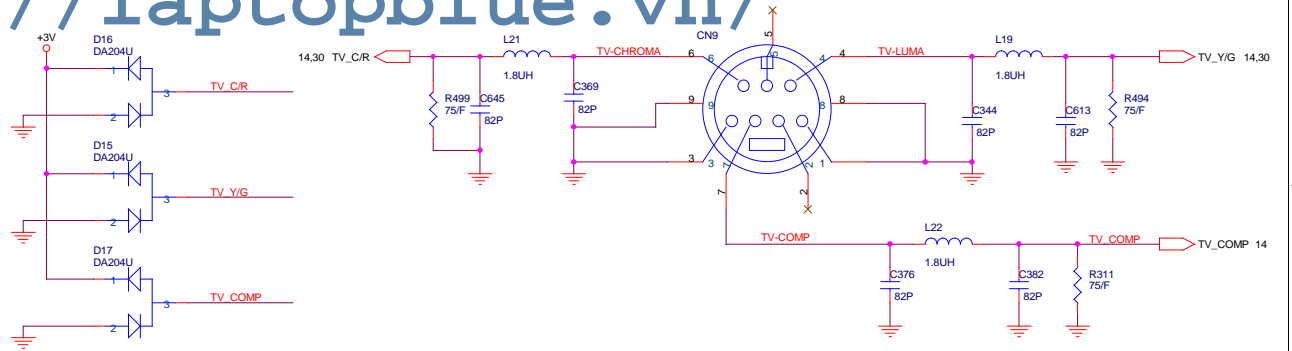
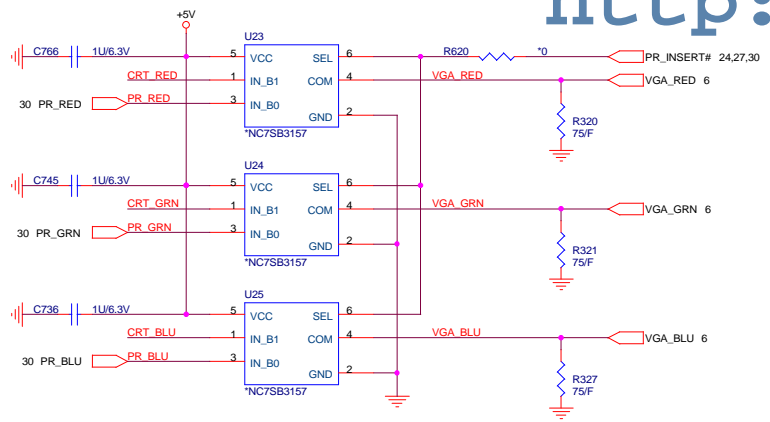


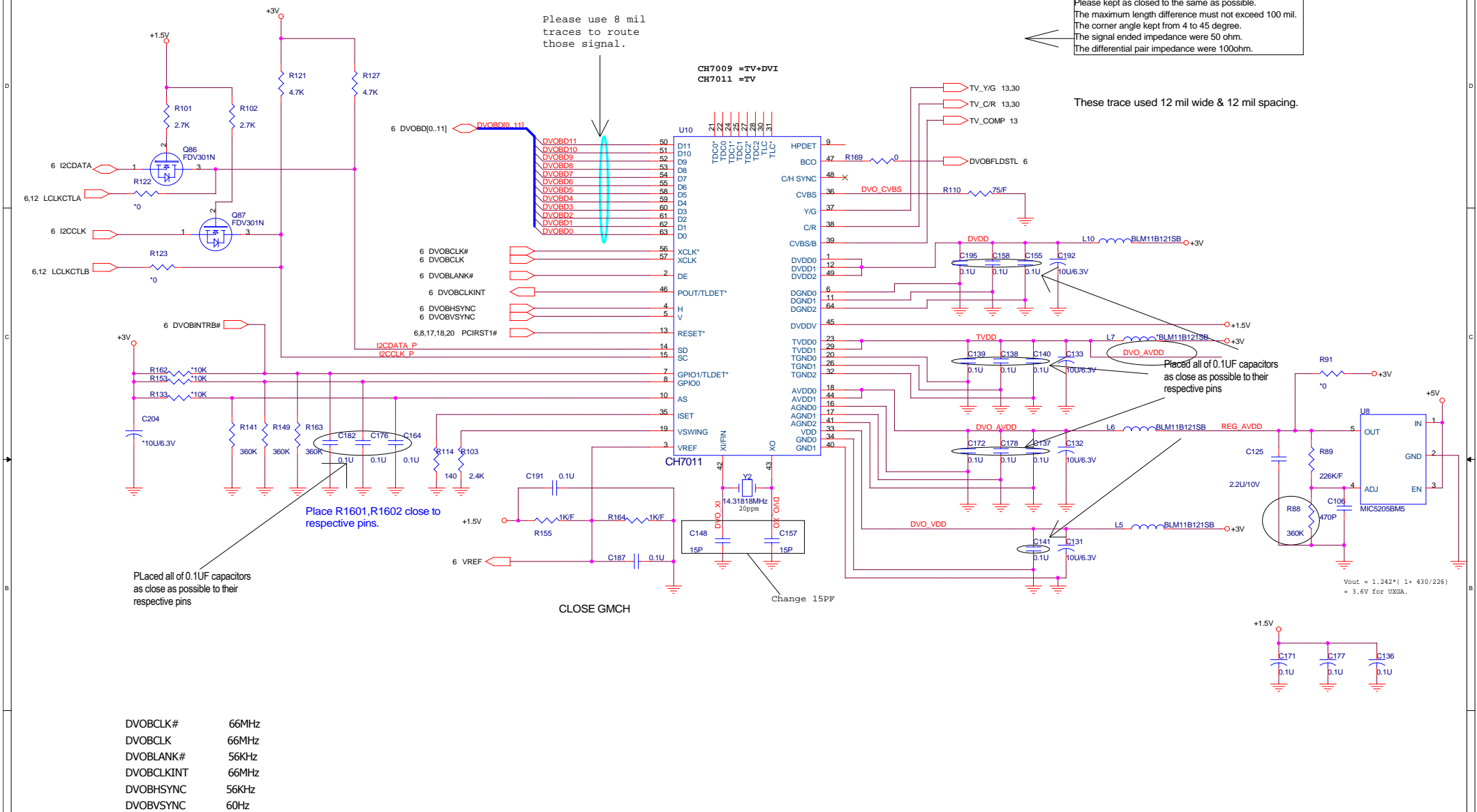
These are for backdrive issue

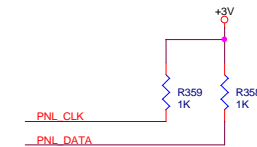


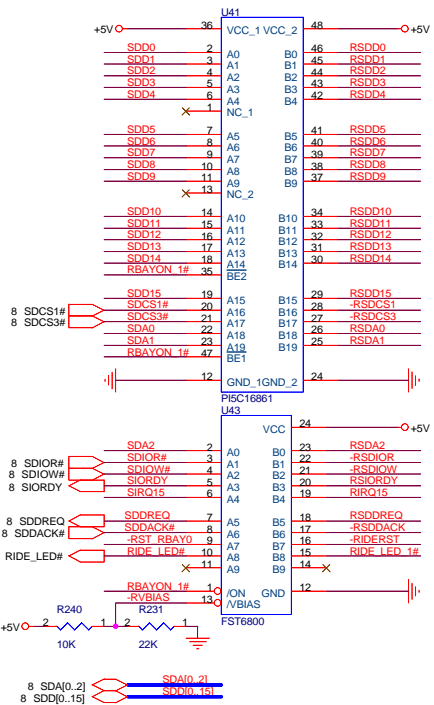
	IN1	IN0			
*	1	0	66Mhz	-1%	DS
	1	1	66Mhz	+/- 1%	CS



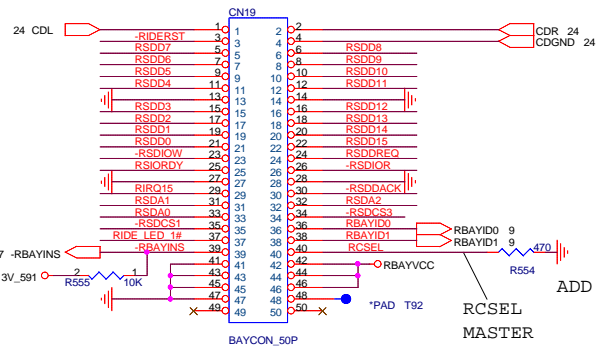






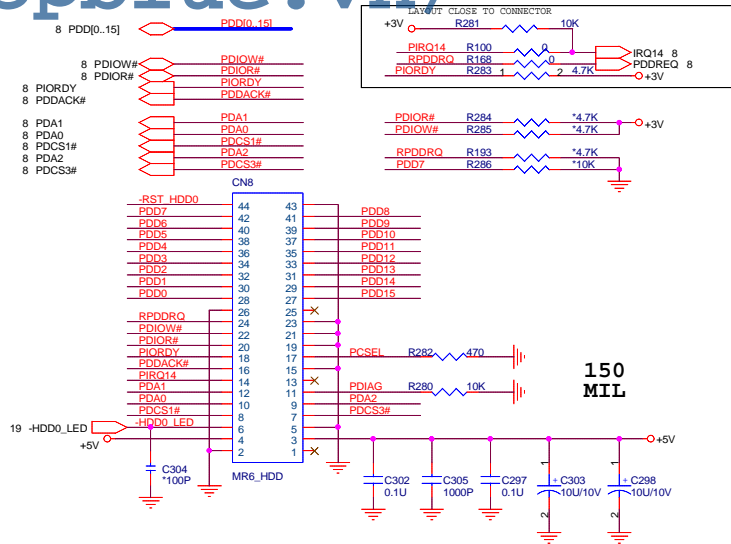


NEED TO CHECK BAY CONNECTOR



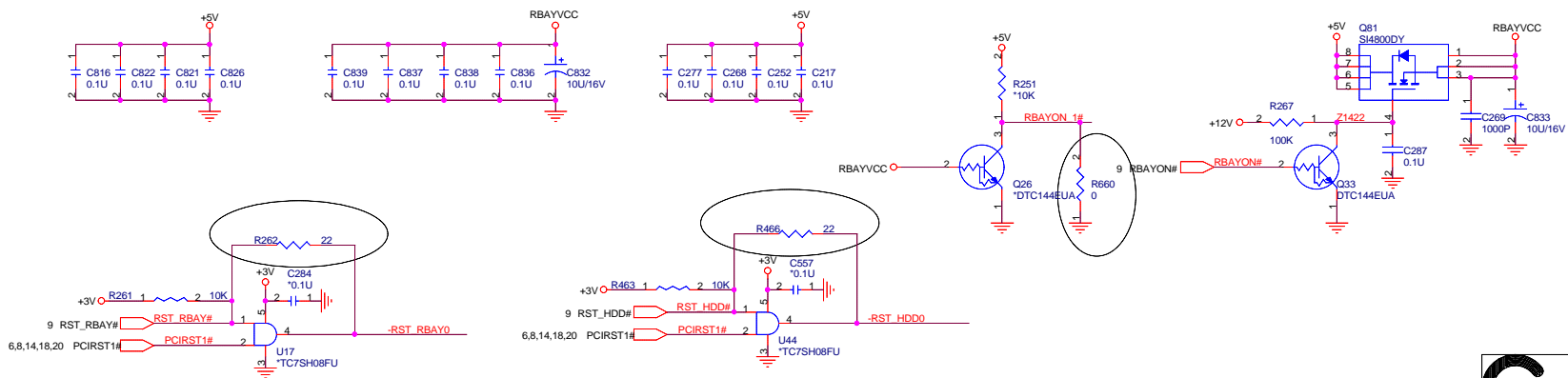
BAY	ID	STATUS
-----	----	--------

RBAYID0/ LBAYID0	RBAYID1/ LBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD



SWAP BAY: OPTICAL DRIVE, 2ND BATTERY, 2ND HDD

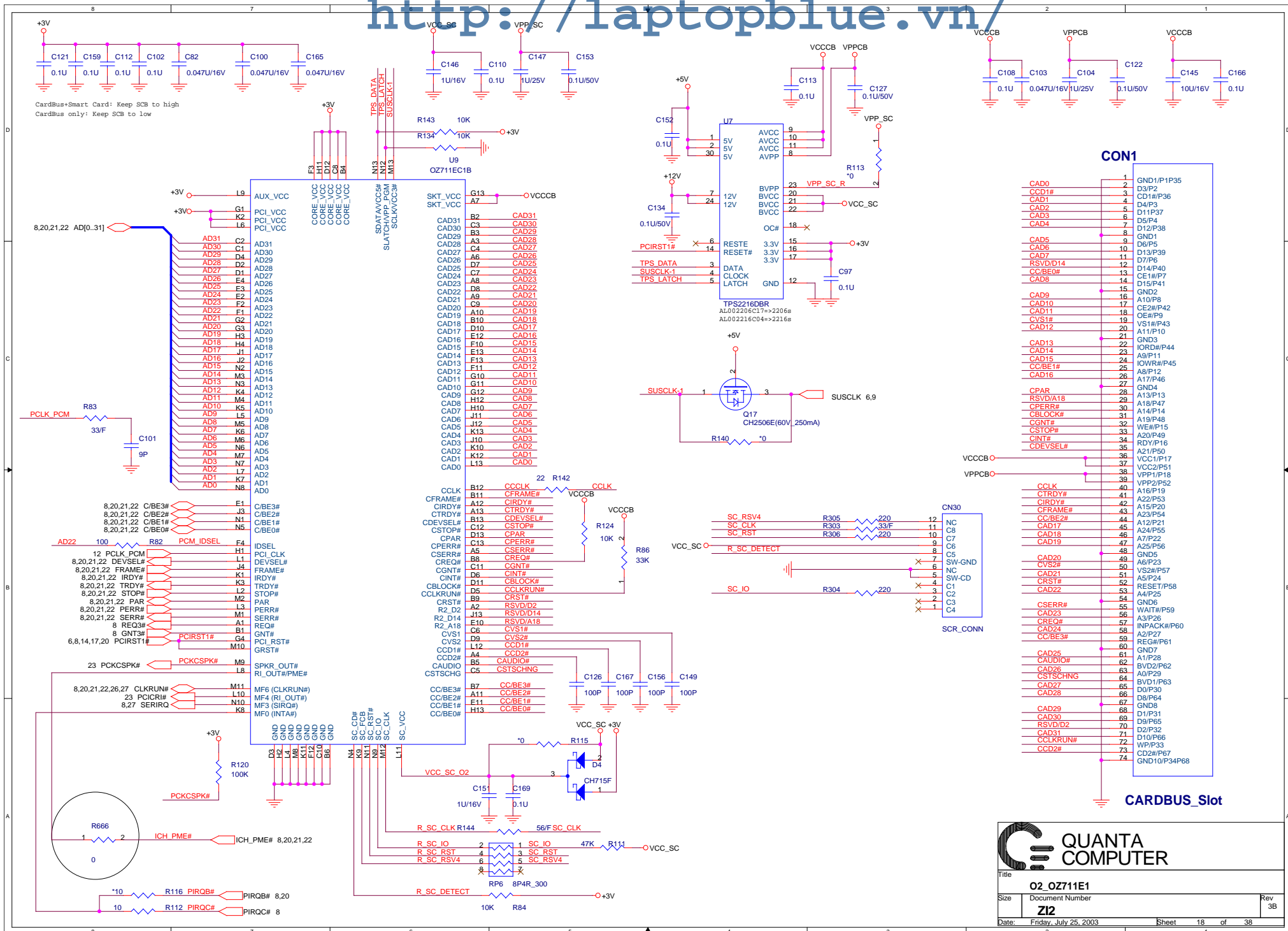
HDD CONNECTOR

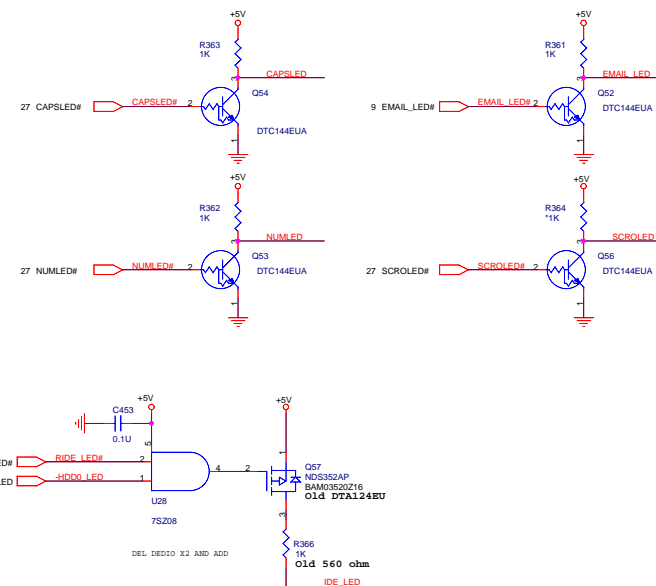
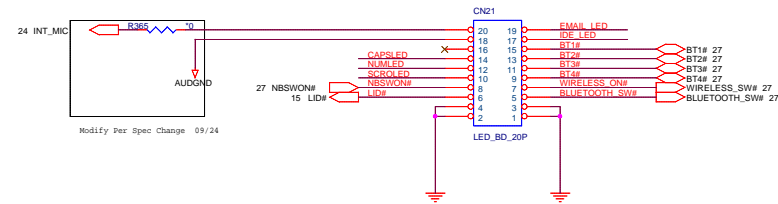


SWAP BAY POWER CONTROL& RESET

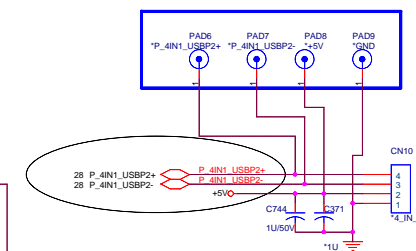
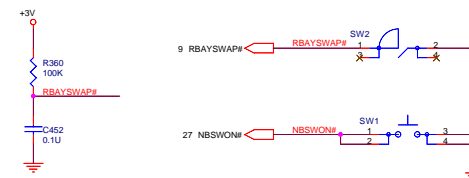
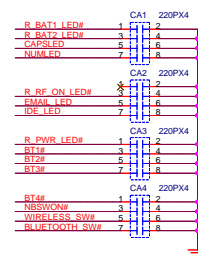


Title			
DEVICE BAY			
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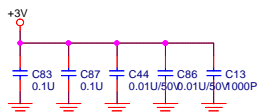




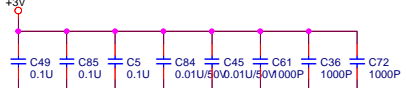
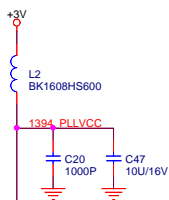
Don't separate this
four PAD for the
function as a CONN



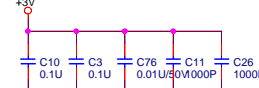
IEEE-1394



Used for vccp
(Pin20, 35, 48, 62, 78)

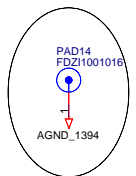
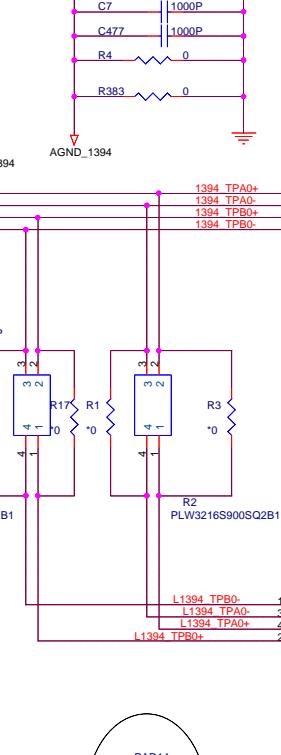
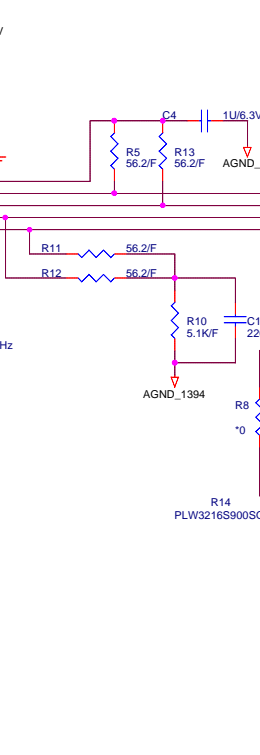
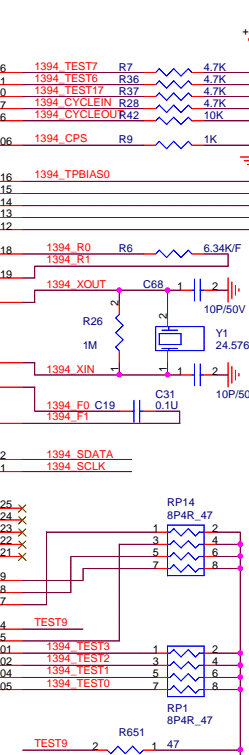
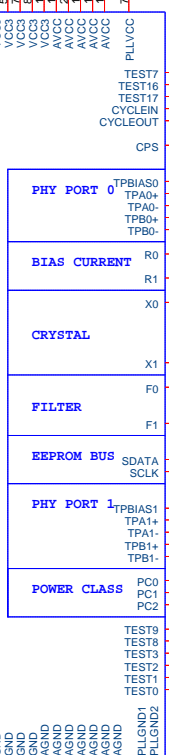
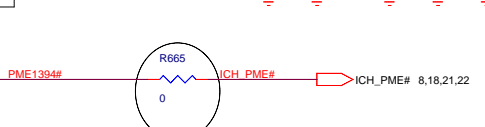
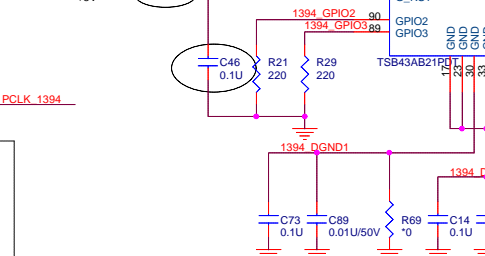
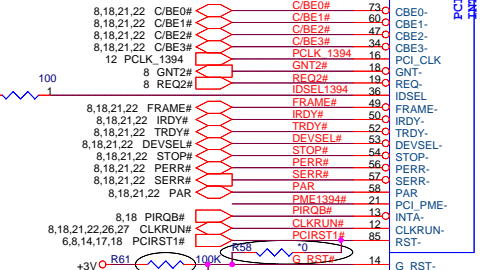
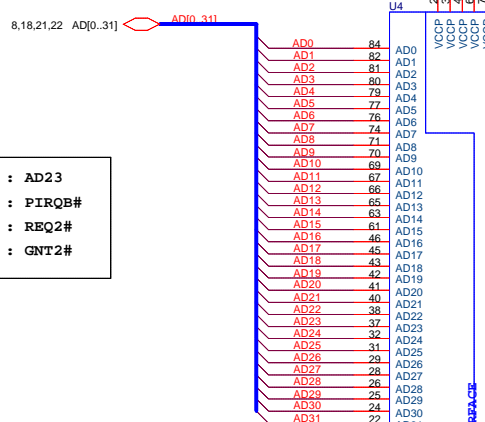


Used for vcc3
(Pin15, 27, 39, 51, 59, 72, 88, 100)

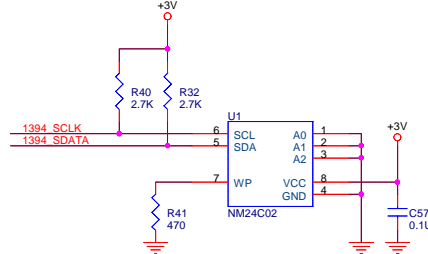


Used for AVCC
(Pin1, 2, 107, 108, 120)

ID Select : AD23
Interrupt Pin : PIRQB#
Request indicates : REQ2#
Grant indicates : GNT2#

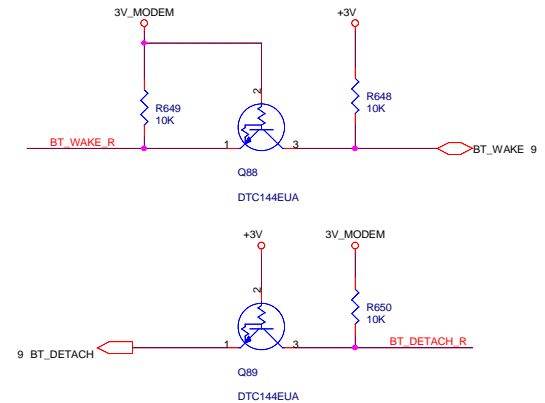
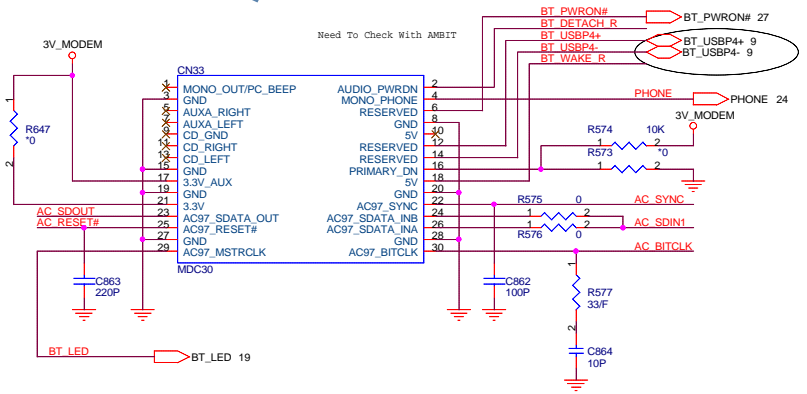
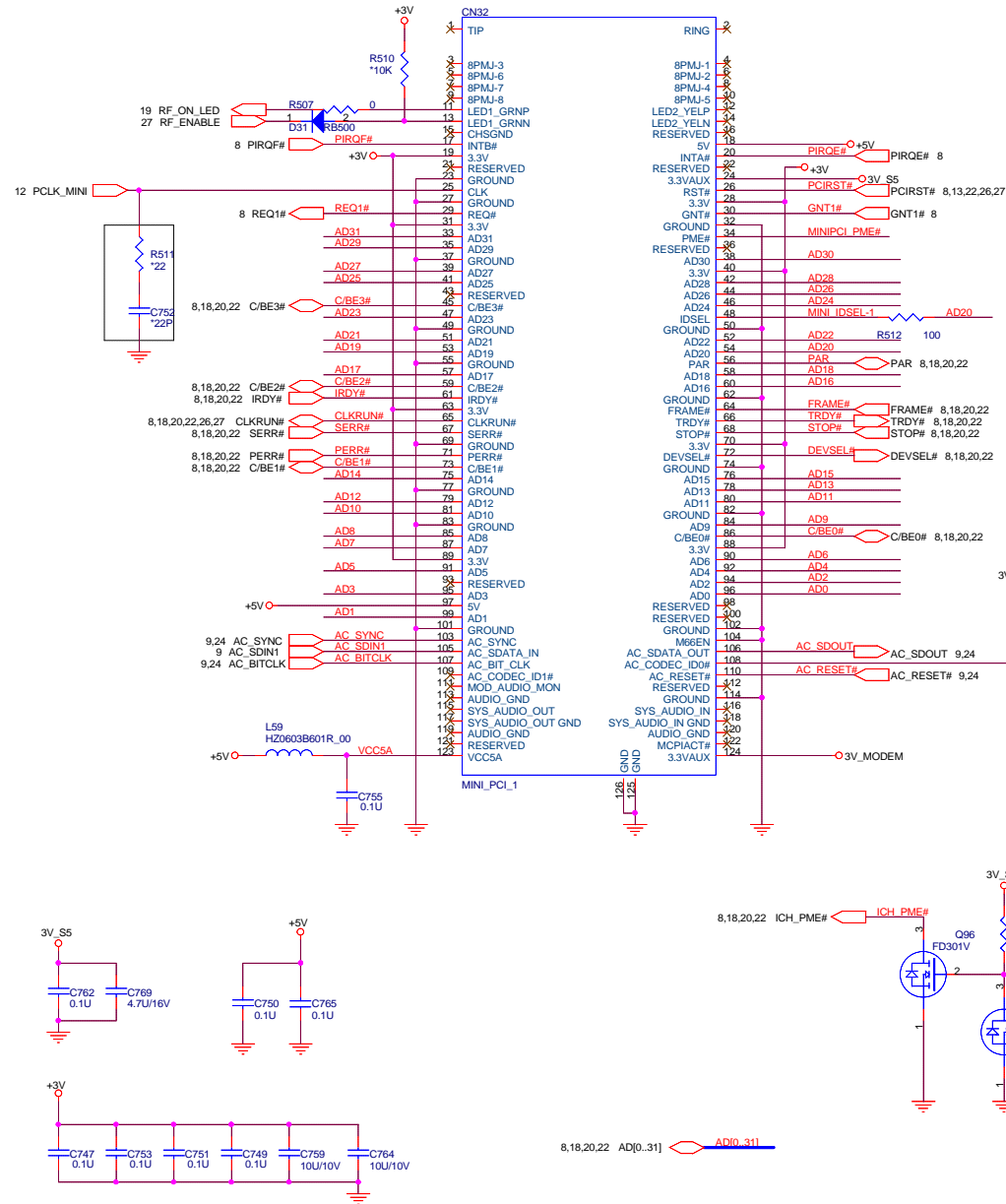


For EMI Request; PAD size is not determined yet.

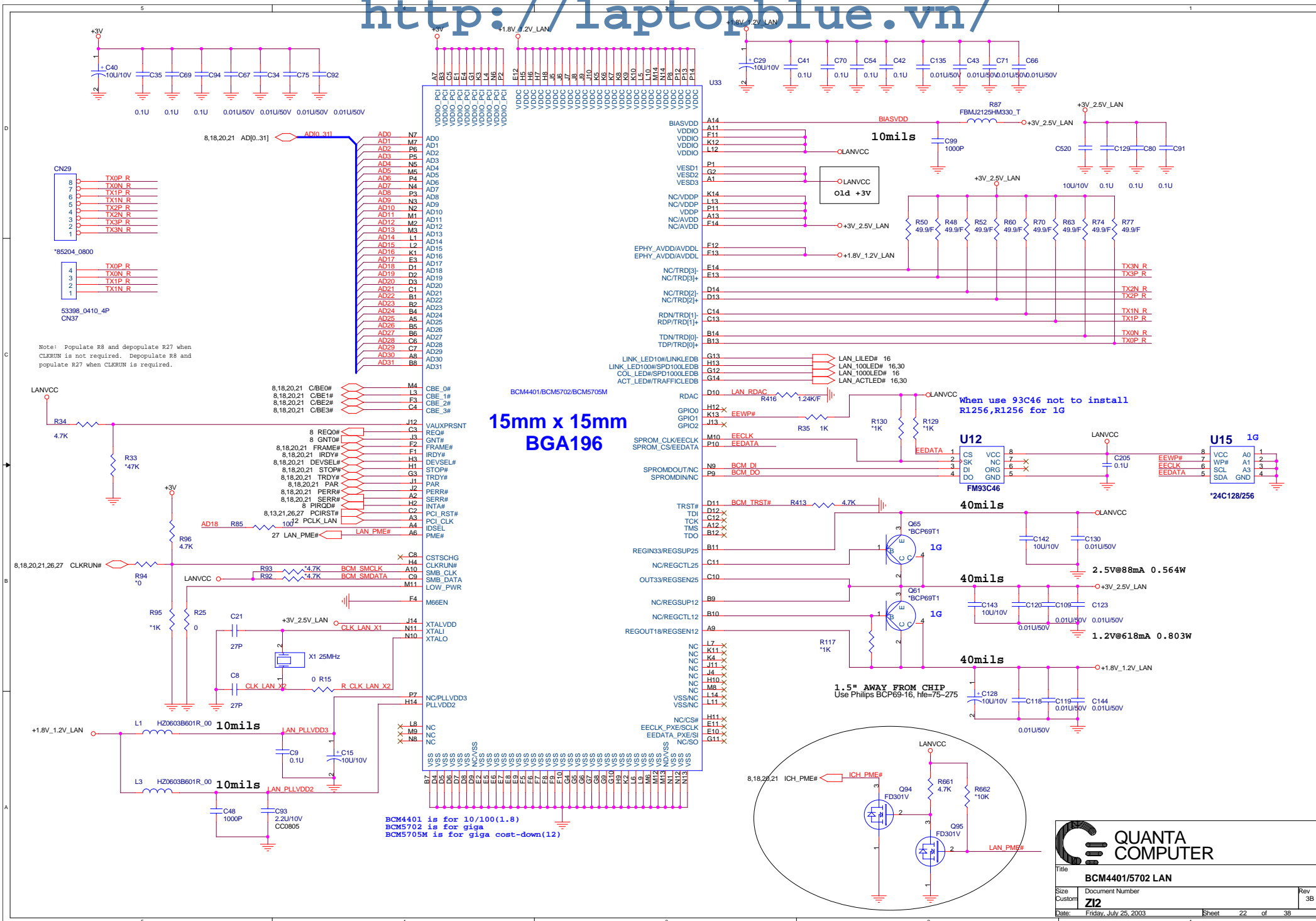


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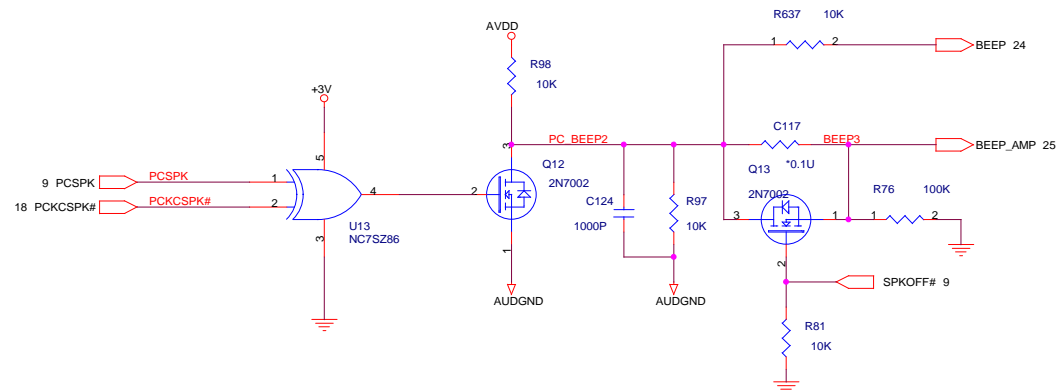
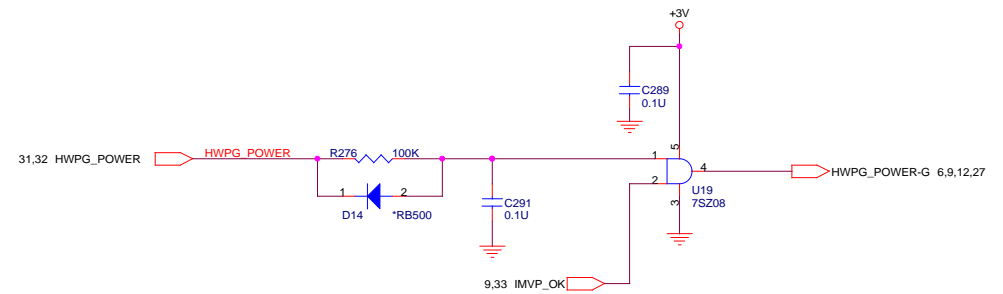
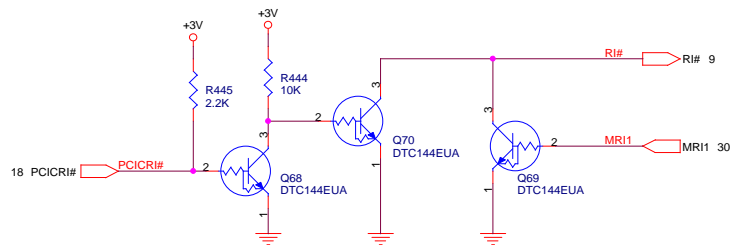
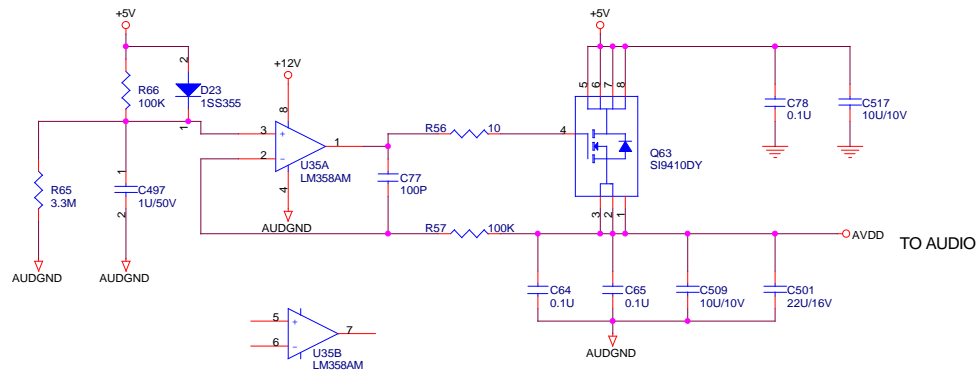
ID Select : AD20
Interrupt Pin : PIRQ# , PIRQF#
Request indicates : REQ1#
Grant indicates : GNT1#

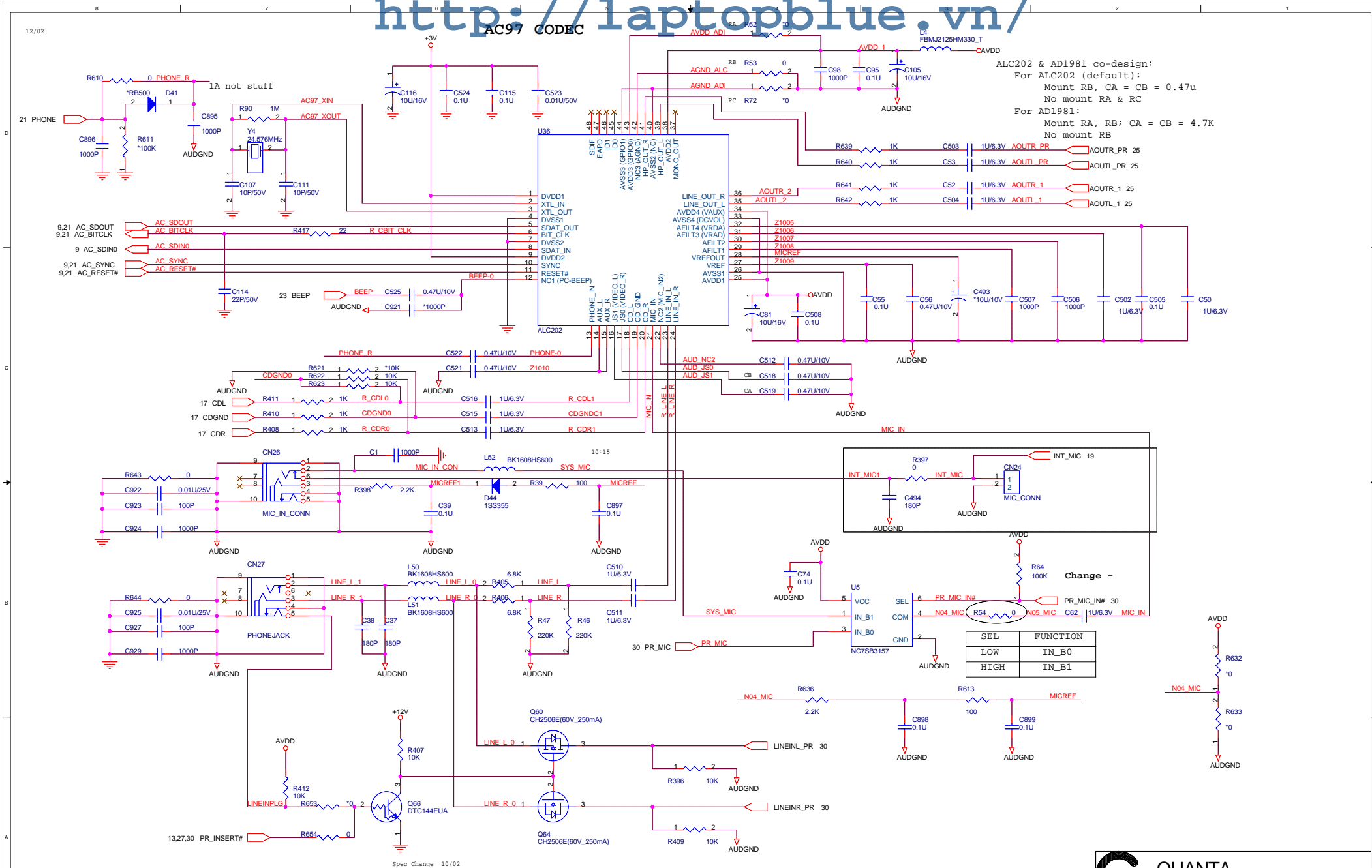


Title			
MINI-PCI & MDC			
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AUDIO POWER





ALC202 & AD1981 co-design:

For ALC202 (default):

Mount RB, CA = CB = 0.47u

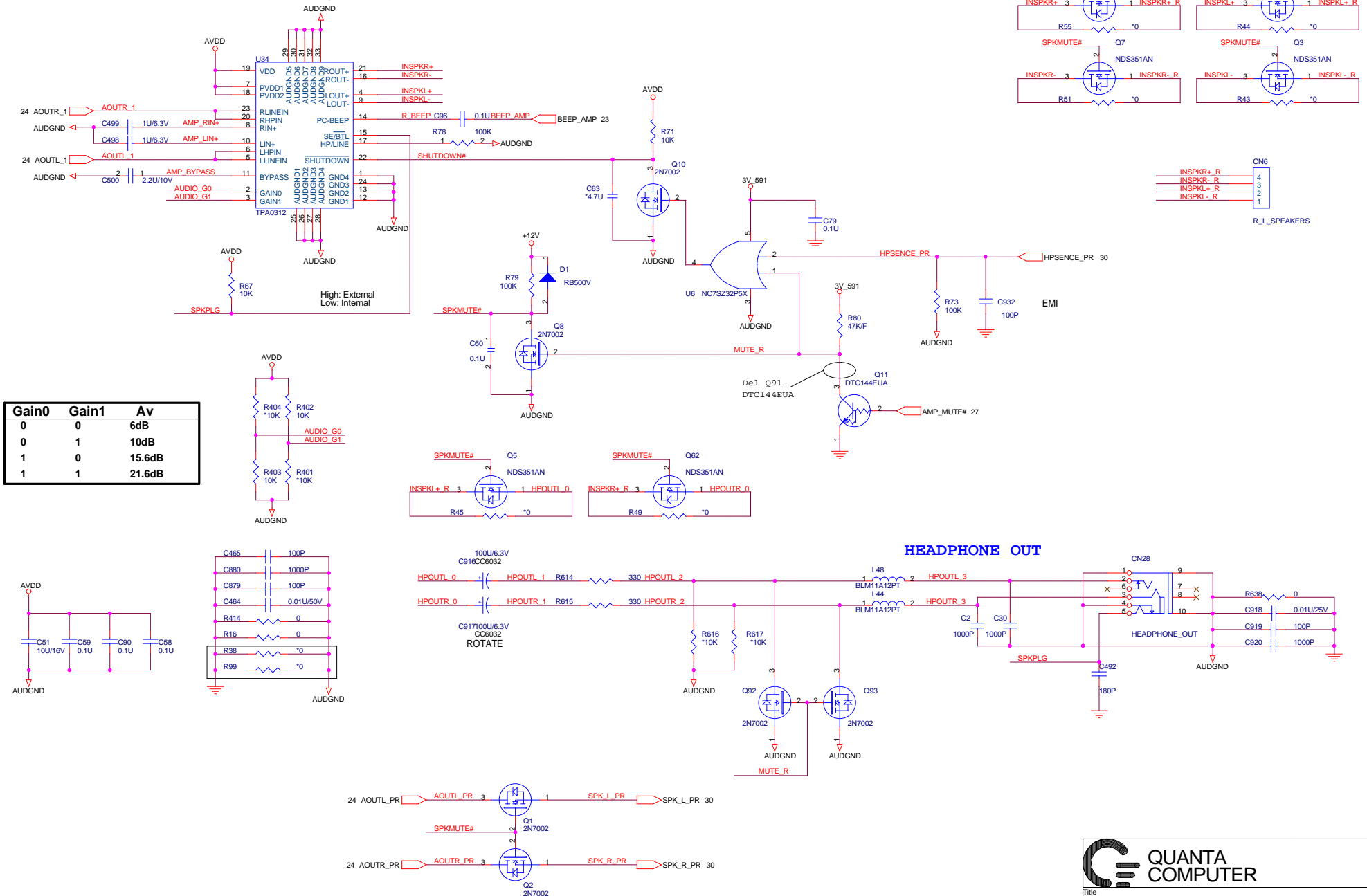
No mount RA & RC

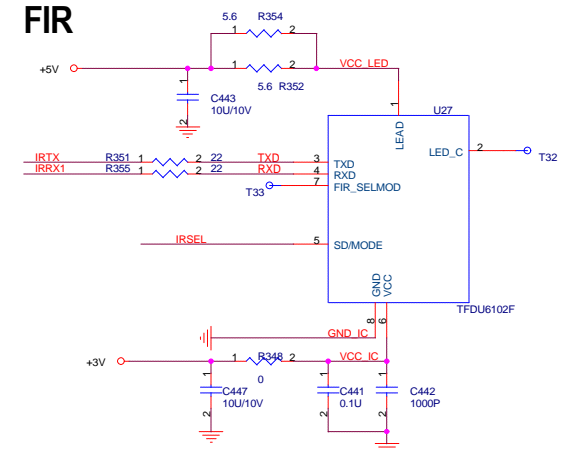
For AD1981:

Mount RA, RB; CA = CB = 4.7K

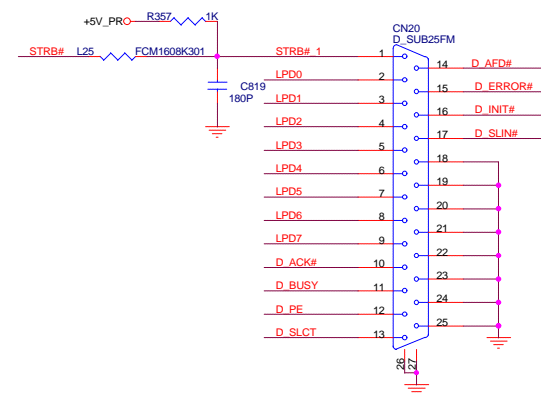
No mount RB

Audio Amplifier





```
40mil: Power/ GND
35mil: VCC_LED, VCC_IC, GND_IC
10mil: TXD, RXD, IRSEL, IRTX,
IRRX1
```



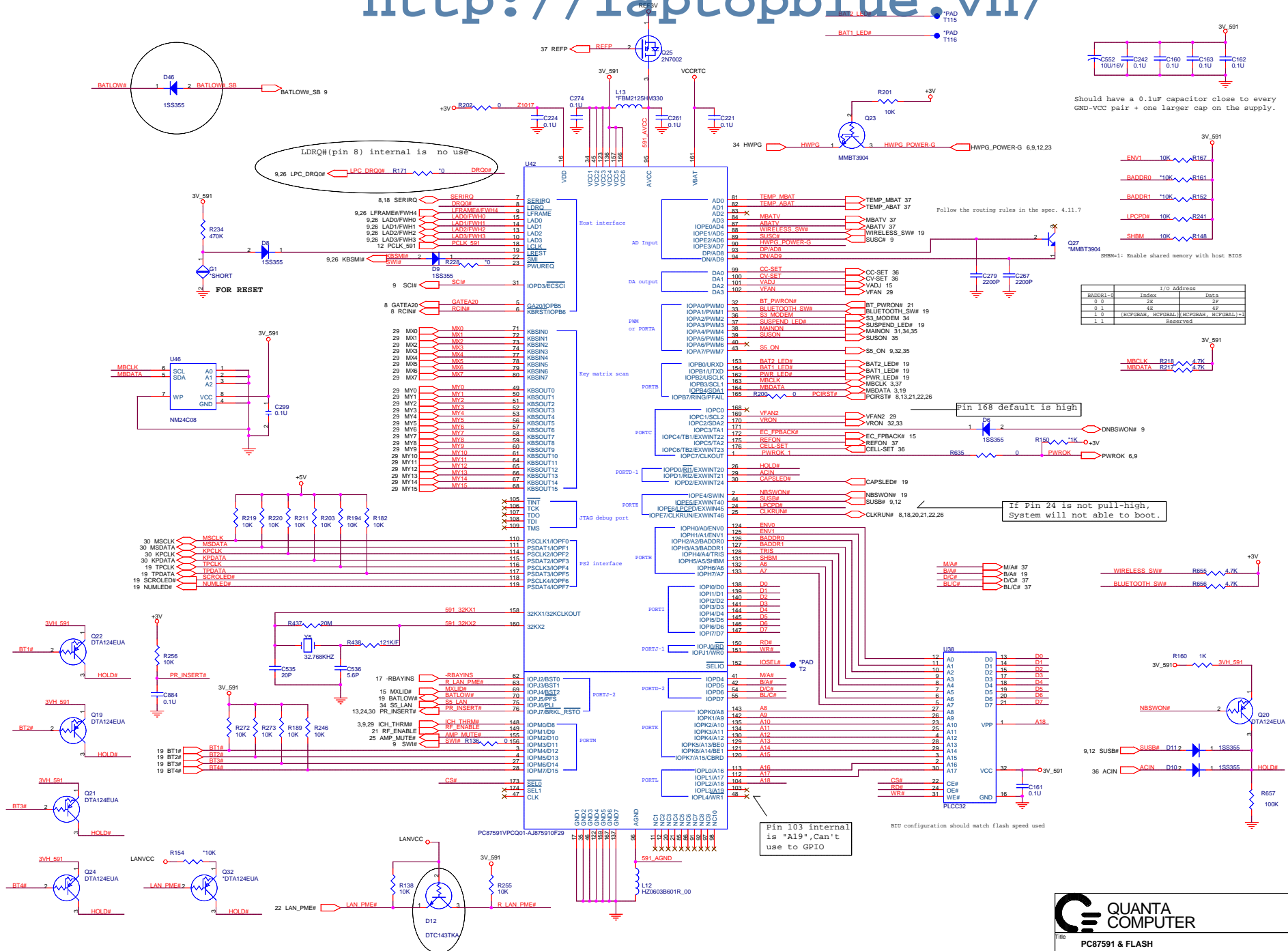
Title **SUPER I/O NS37391**

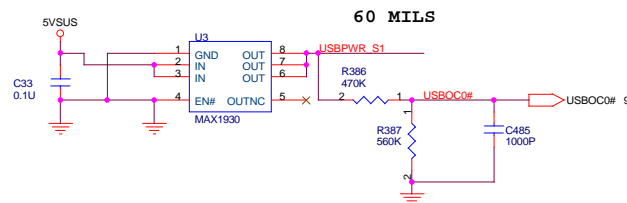
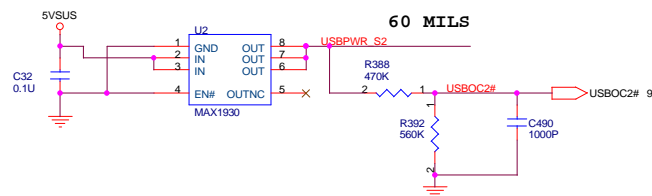
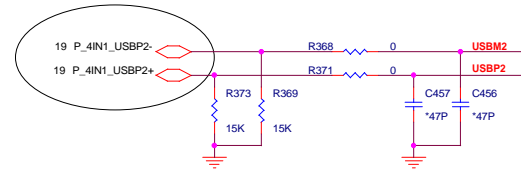
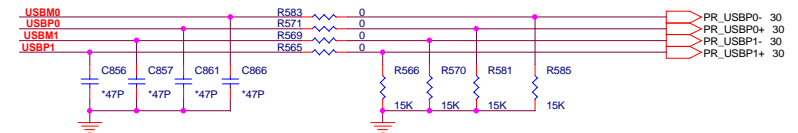
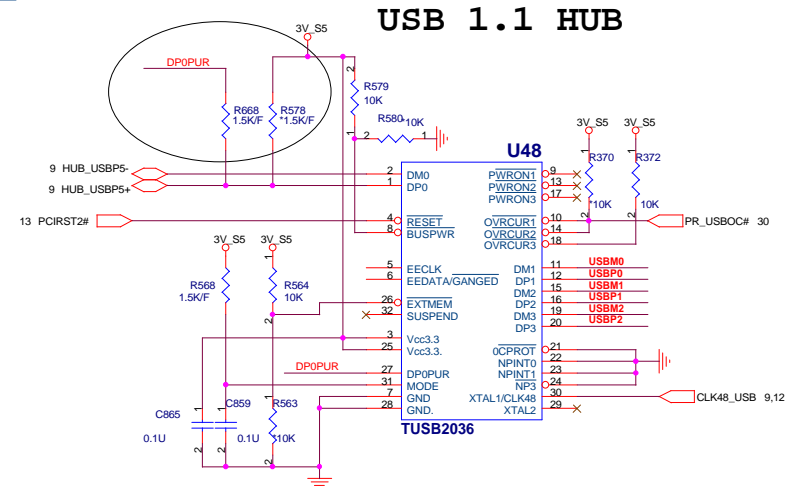
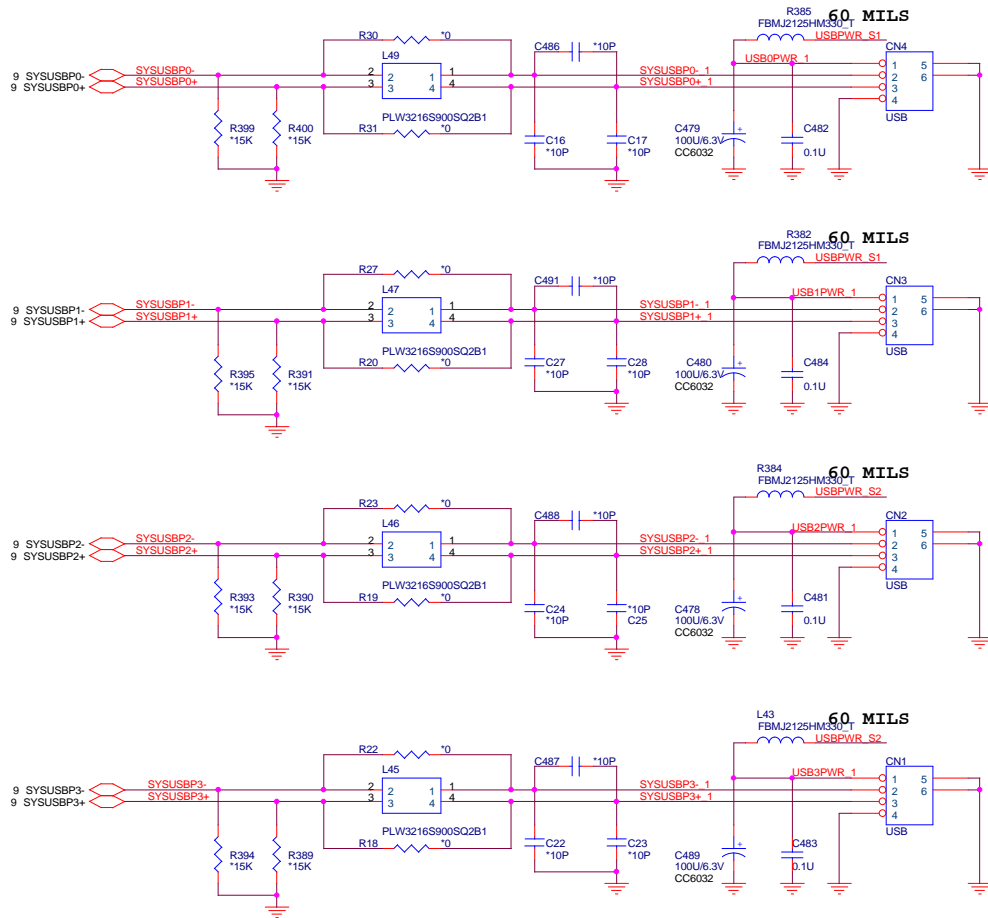
Size	Document Number
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Size	Docu
Custom	71

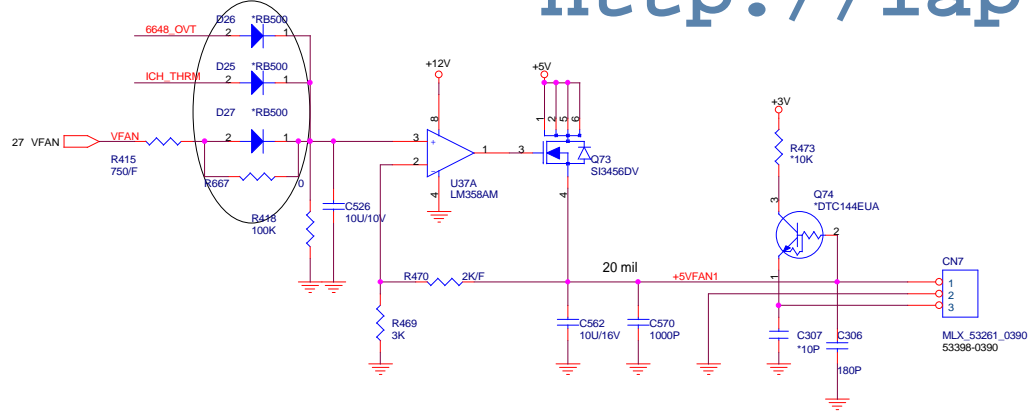
Date: Friday, July 25, 2003

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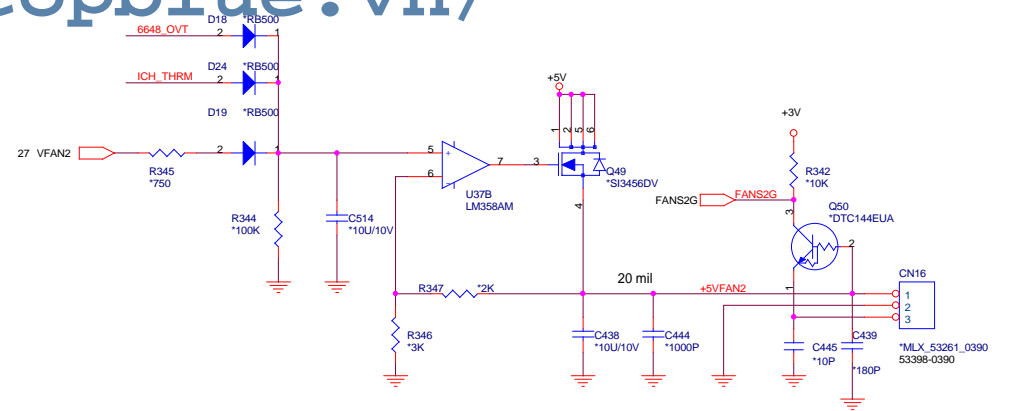




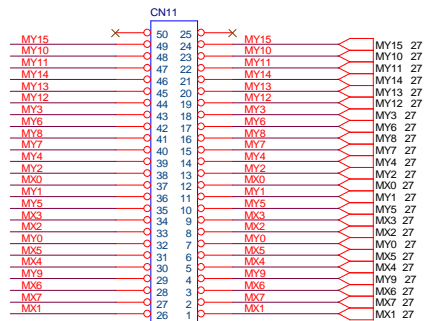
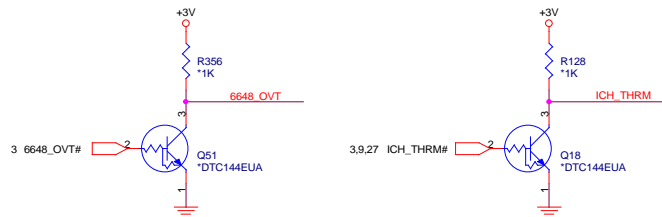
Title			
USB CONN.			
Size	Document Number		Rev
Custom	Z12		3E
Date:	Friday, July 25, 2003	Sheet	28 of 38



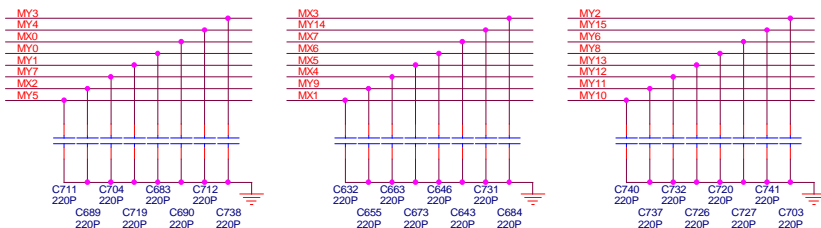
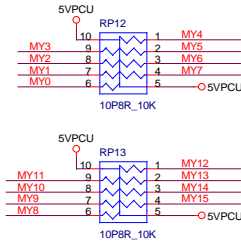
1st FAN OUT CONNECTOR



2nd FAN OUT CONNECTOR



KEYBOARD



Title
K/B , T/P , FAN CON

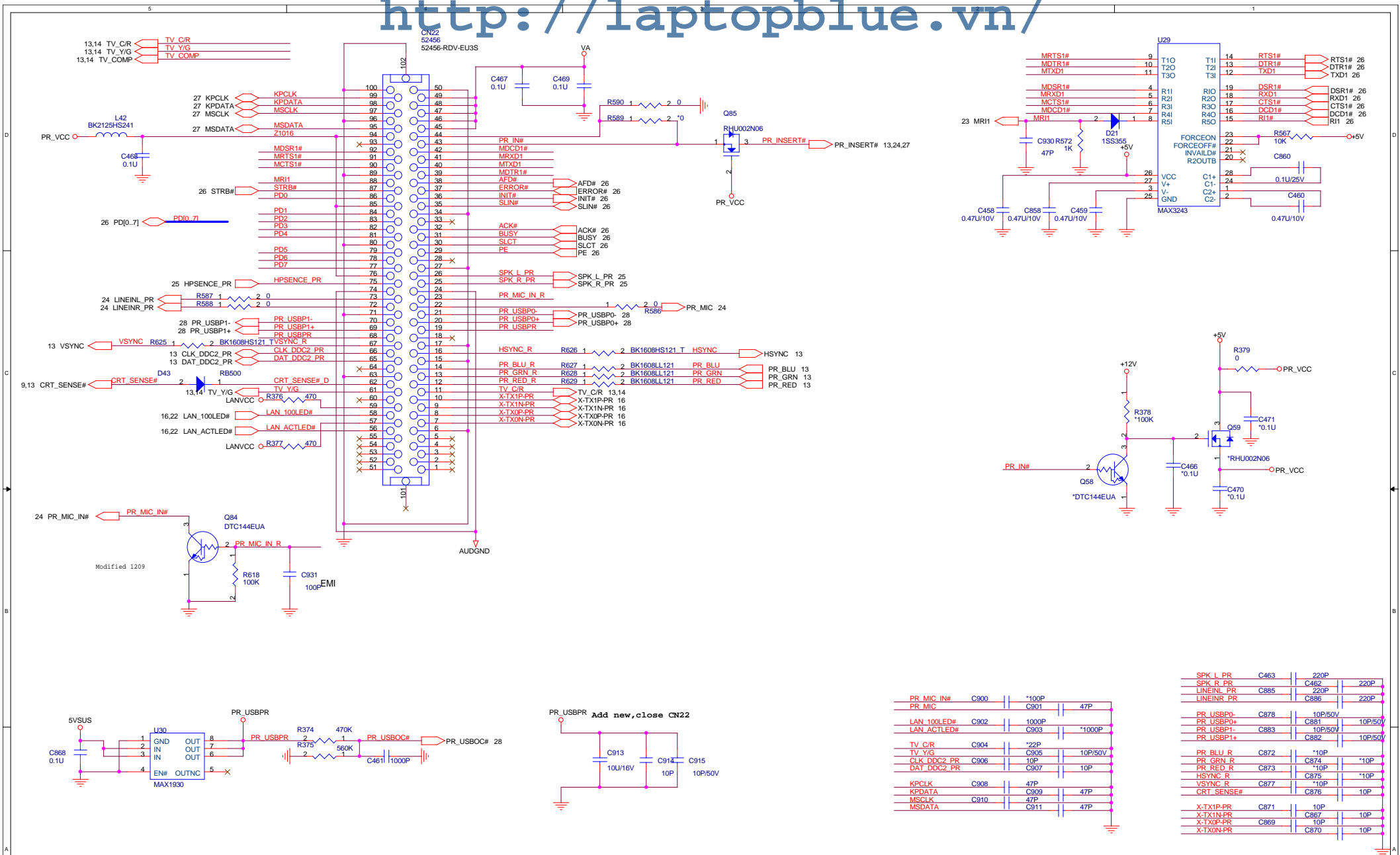
Size
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Document Number
Z12

Date
Friday, July 25, 2003

Rev
3B

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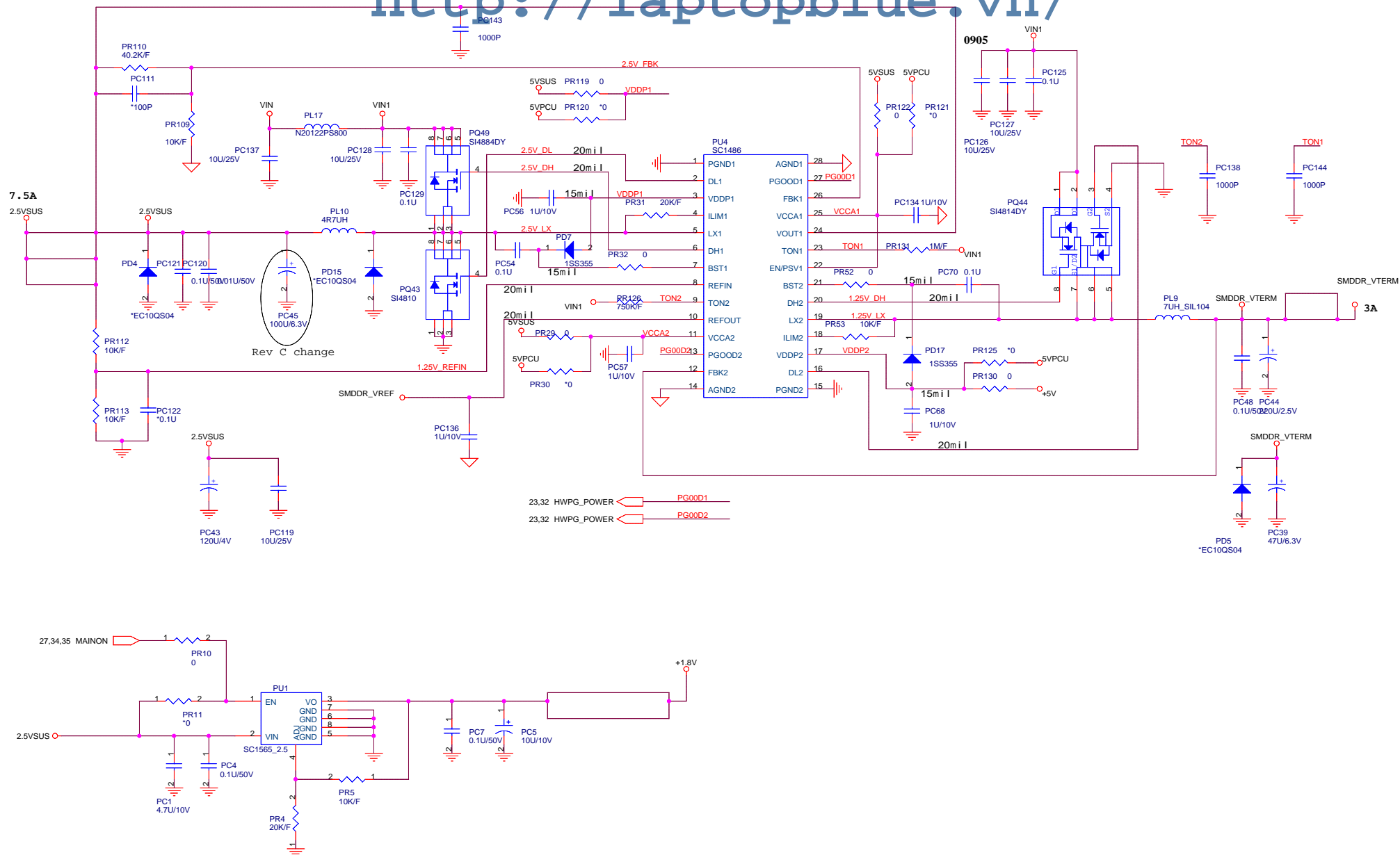


Title	EZ PORT REPLICARTOR
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Size	Document Number
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Custom **712**

Date: Friday, July 25, 2003



Title	2.5VSUS / SMDDR VREF
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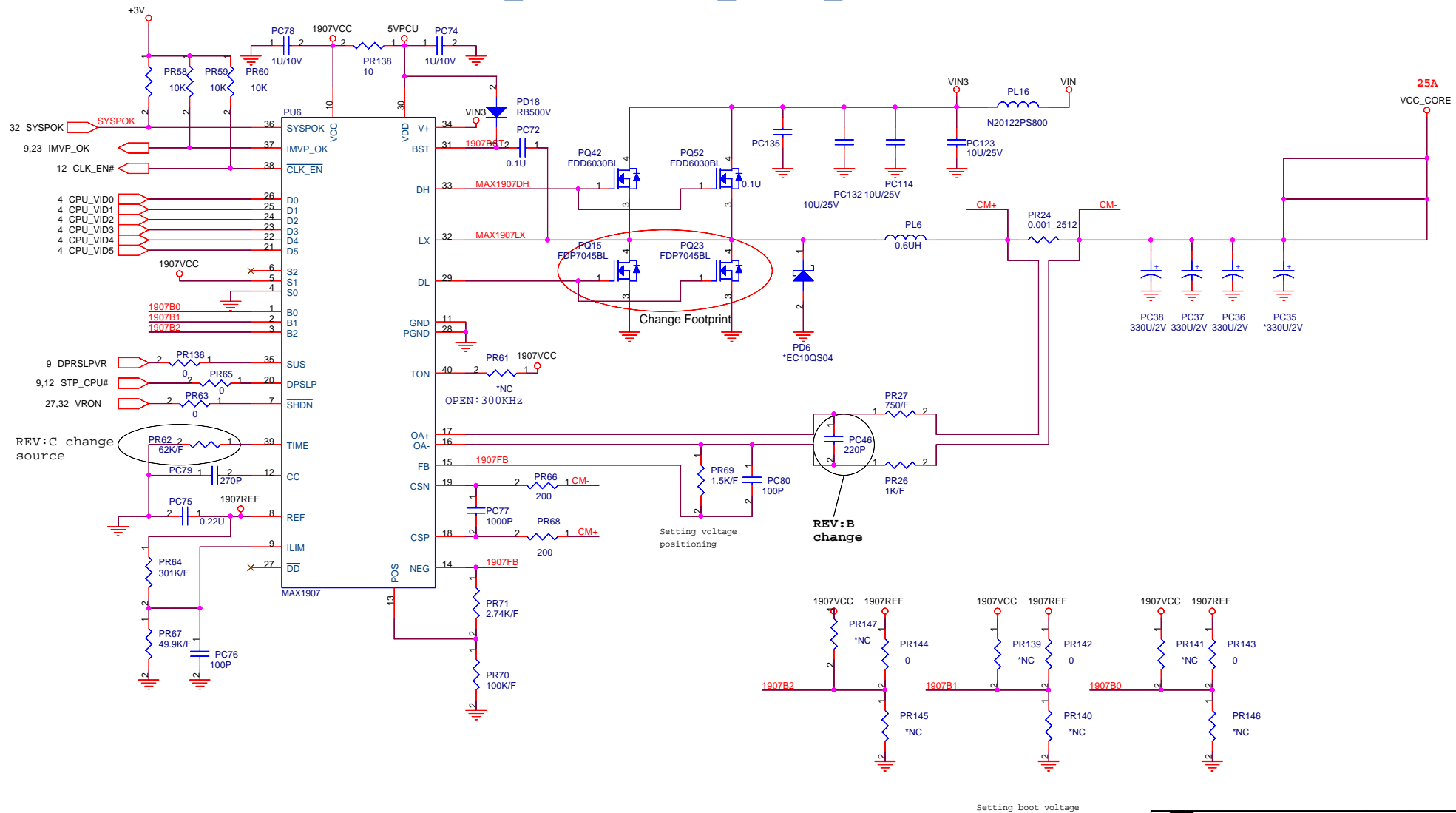
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Size: A3
Document Number: ZI2 (ZG1)


Date: Friday, July 25, 2003

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Rev
3A

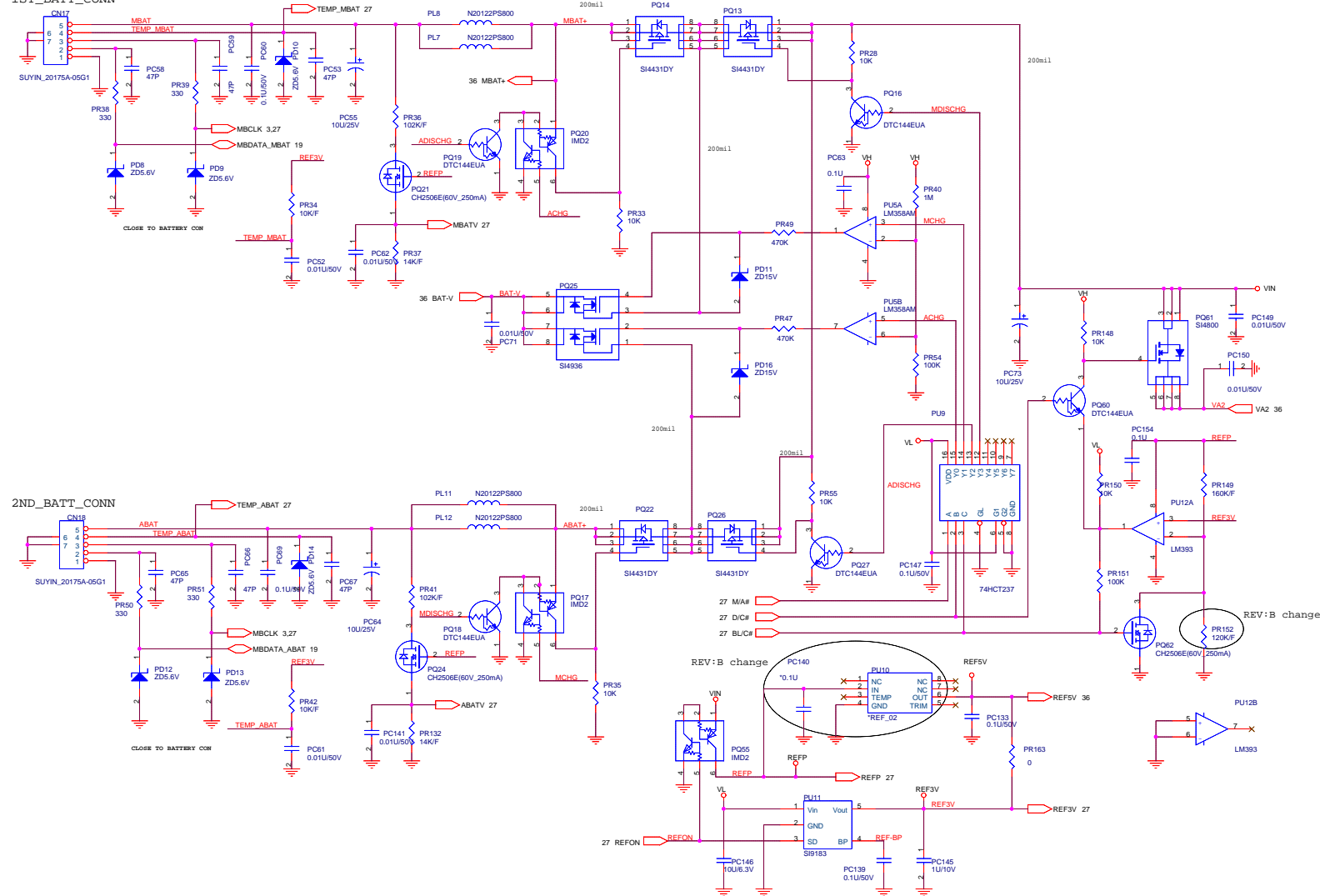


Setting boot voltage

 QUANTA COMPUTER		
Title		
Cpu Power MAX1709		
Size	Document Number	Rev
B	Z11 (ZG1)	1B
Date:	Friday, July 25, 2003	Sheet 33 of 38

[illegible]

1ST_BATT_CONN



2ND_BATT_CONN

