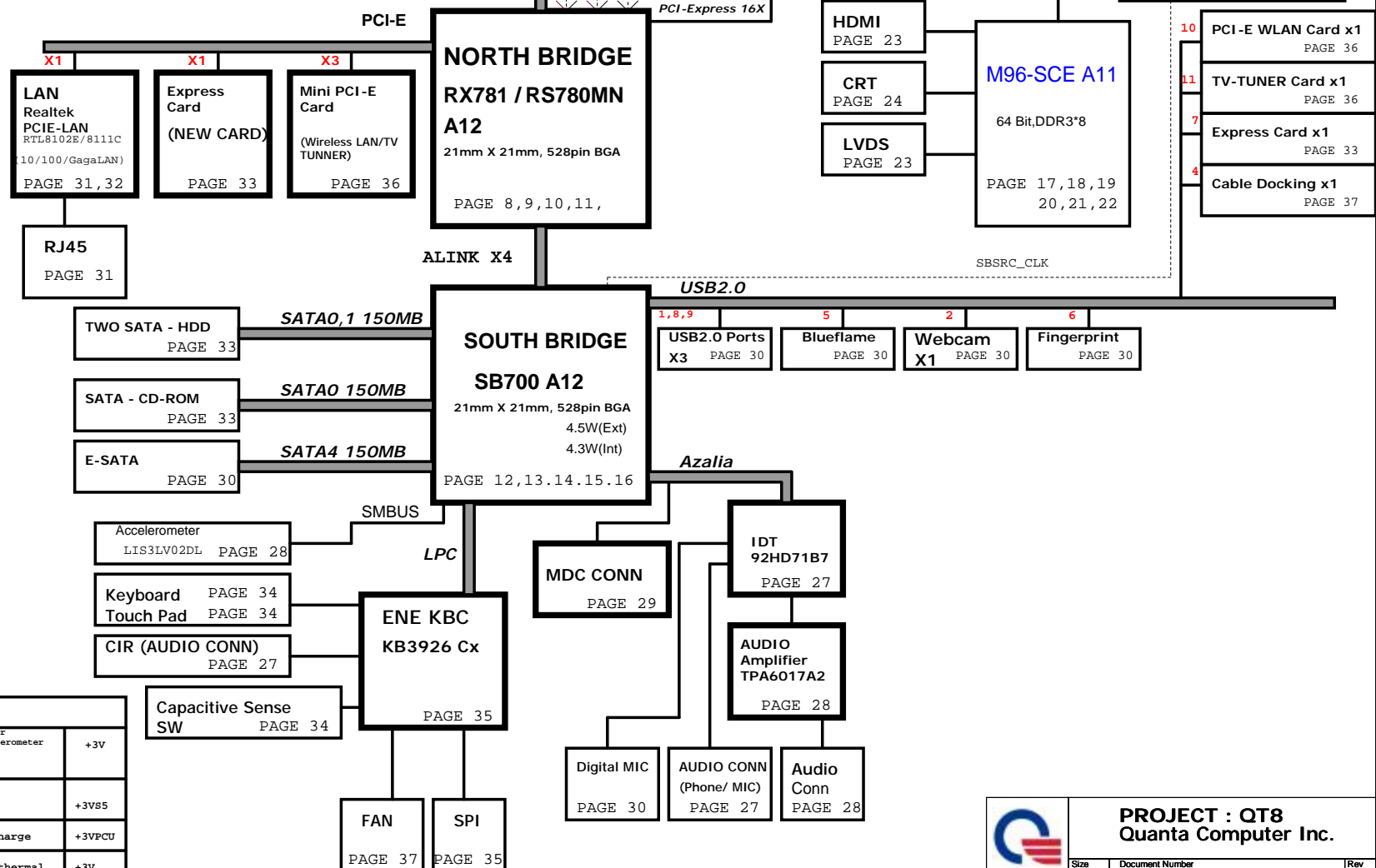
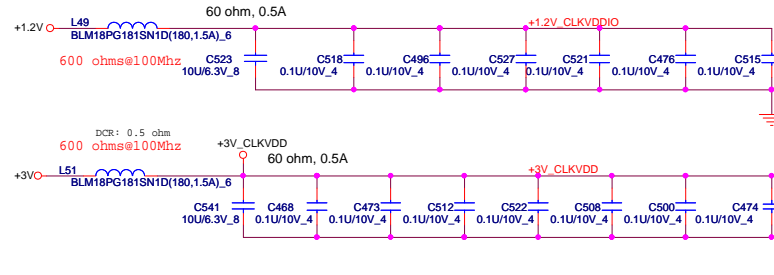


SMBUS TABLE		
SB--SCL0/SD0	Clock gen/Sobson/TV tuner /DDR2/DDR2 thermal/Accelerometer	+3V
	express card	
	Wlan Card	+3VS5
EC --SCL/SD	Battery charge/discharge	+3VPCU
EC--SCL2/SD2	VGA thermal/system thermal	+3V

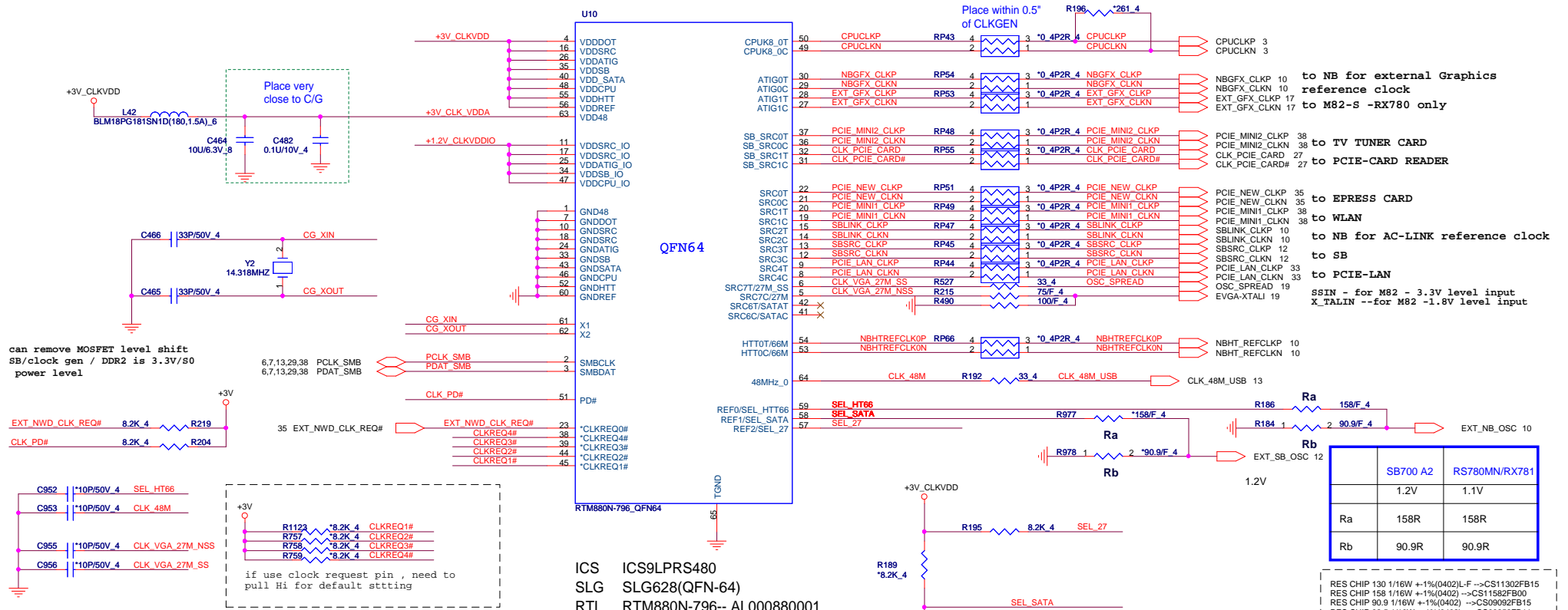


PROJECT : QT8
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Size Custom	Document Number Block Diagram	Rev 1A
Date: Thursday, October 16, 2008	Sheet 1 of 48	



Clock chip has internal serial terminations for differential pairs, external resistors are reserved for debug purpose.



can remove MOSFET level shift SB/clock gen / DDR2 is 3.3V/S0 power level

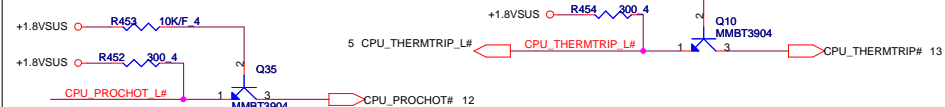
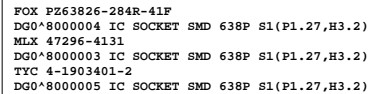
if use clock request pin, need to pull Hi for default setting

ICS ICS9LPRS480
SLG SLG628(QFN-64)
RTL RTM880N-796-- AL000880001

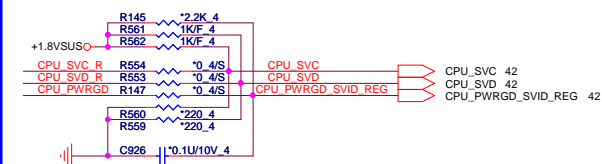
* default	
SEL_HTT66	1 66 MHz 3.3V single ended HTT clock
	0* 100 MHz differential HTT clock
SEL_SATA	1 100 MHz non-spreading differential SRC clock
	0* 100 MHz spreading differential SRC clock
SEL_27	1* 27MHz non-spreading singled clock
	0 100 MHz spreading differential SRC clock

	SB700 A2	RS780MN/RX781
	1.2V	1.1V
Ra	158R	158R
Rb	90.9R	90.9R

RES CHIP 130 1/16W +-1%(0402)-L-F -->CS11302FB15
RES CHIP 158 1/16W +-1%(0402) -->CS11582FB00
RES CHIP 90.9 1/16W +-1%(0402) -->CS09092FB15
RES CHIP 82.5 1/16W +-1%(0402) -->CS08252FB11



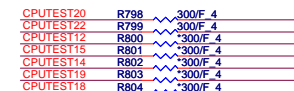
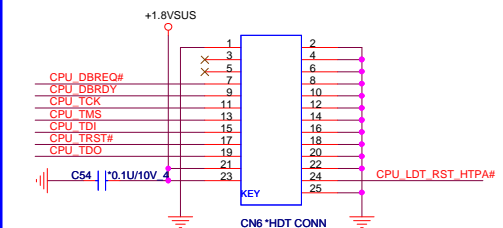
Serial VID



VFIX MODE VID Override Circuit

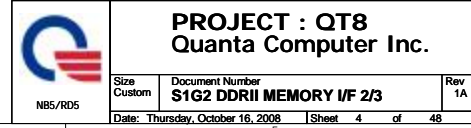
SVC	SVD	Voltage Output
0	0	1.4V
0	1	1.2V
1	0	1.0V
1	1	0.8V

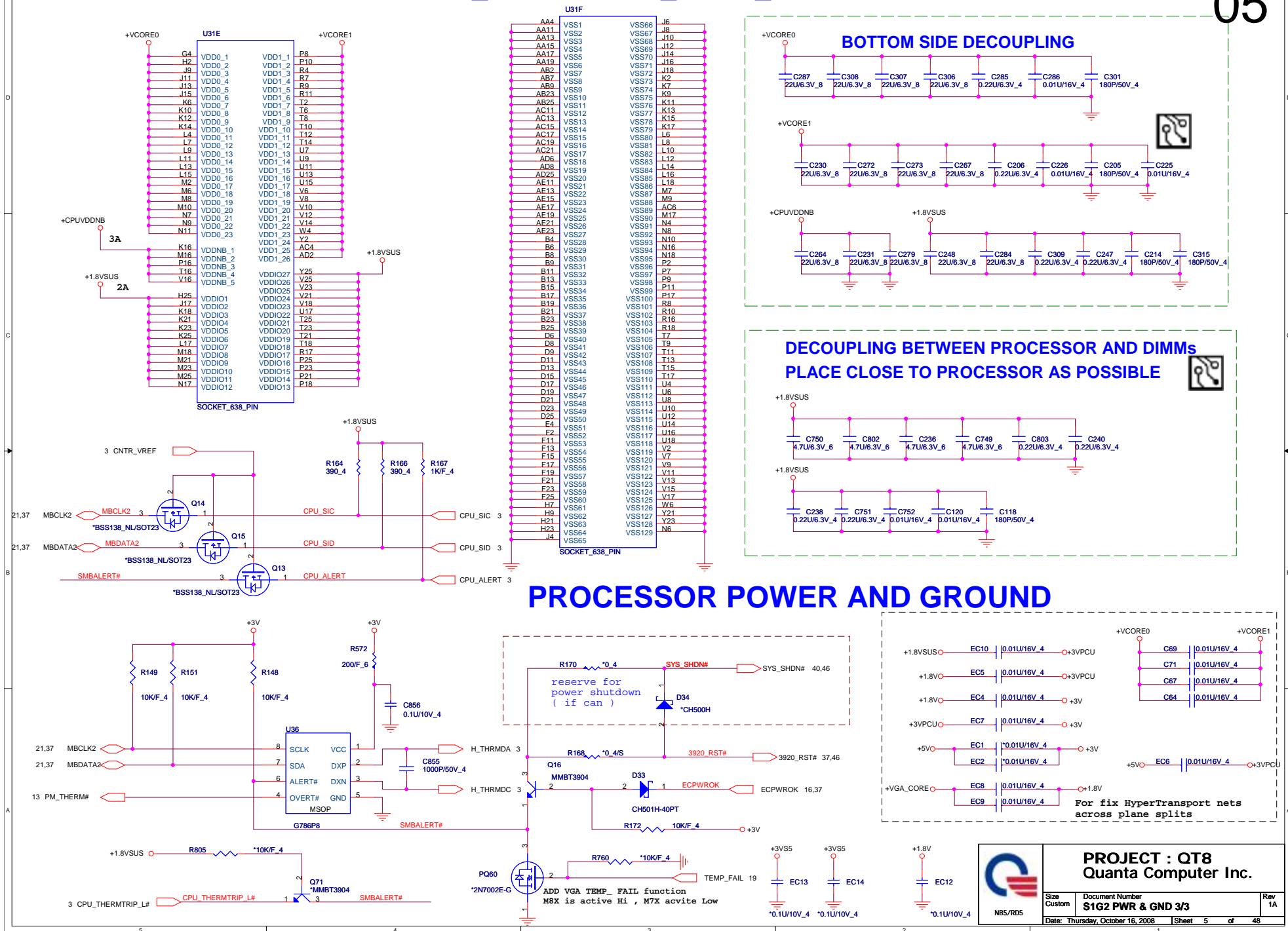
HDT Connector

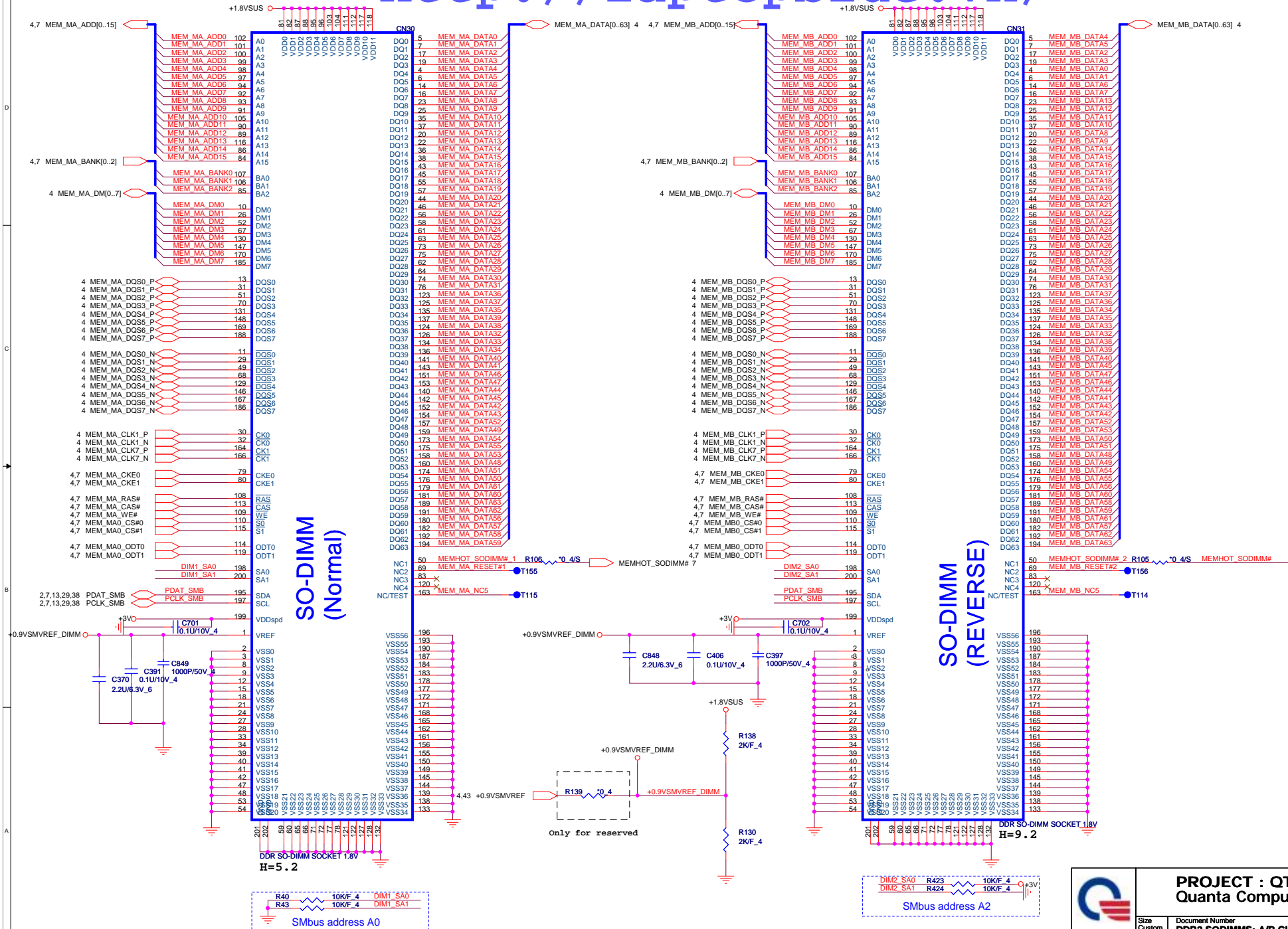


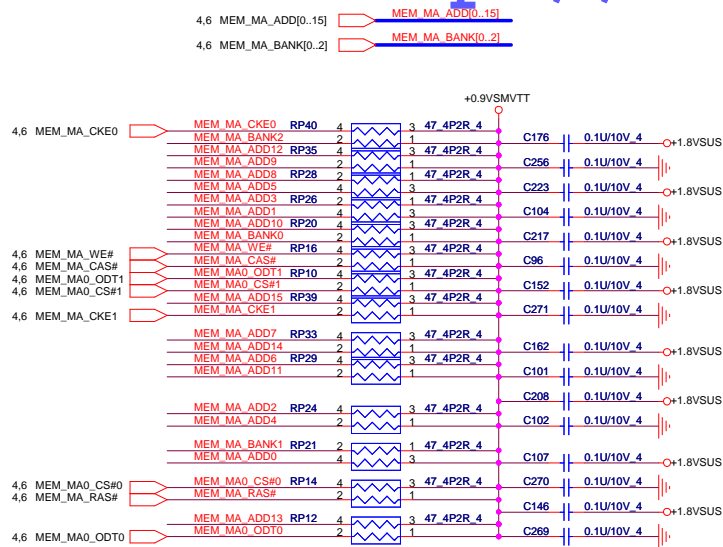
PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number S1G2 HT,CTL WF 1/3	Rev 1A
Date: Thursday, October 16, 2008		Sheet 3 of 48

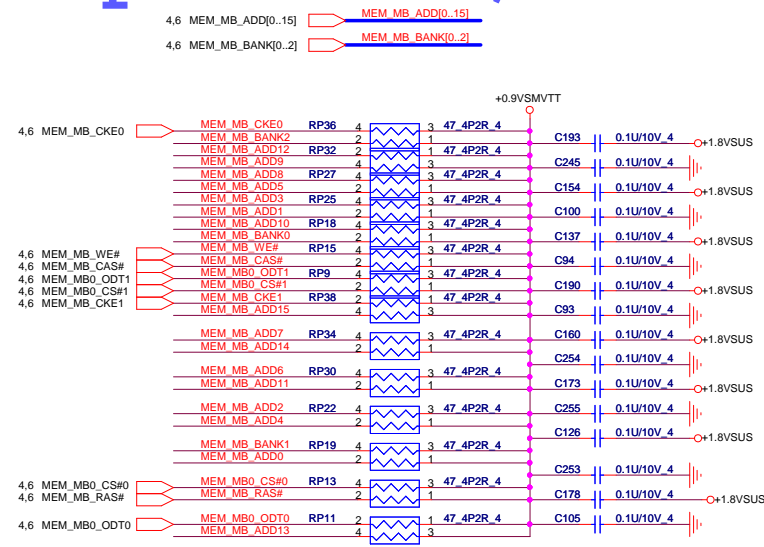
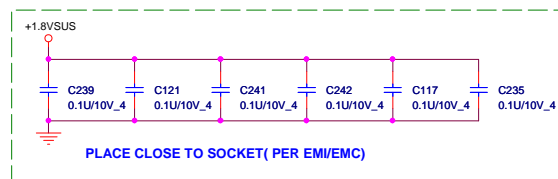




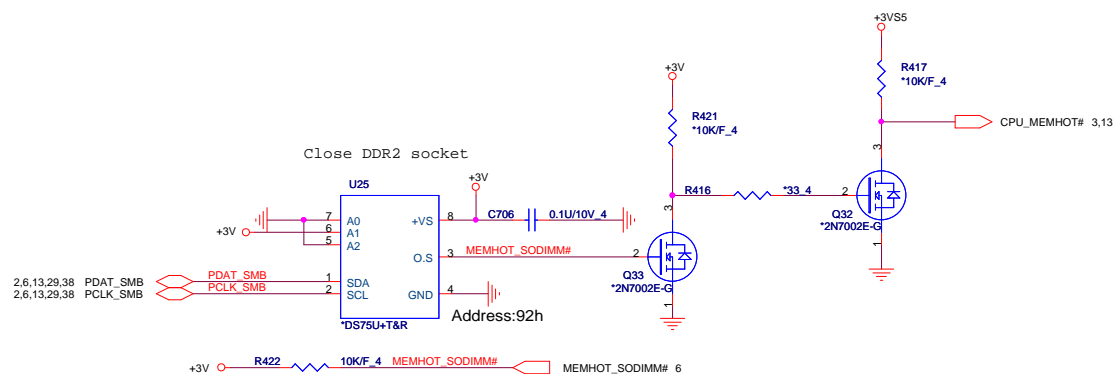
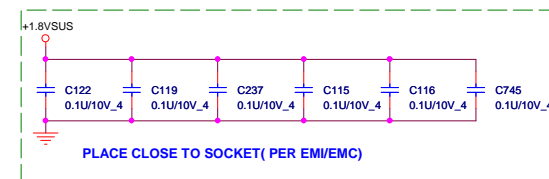


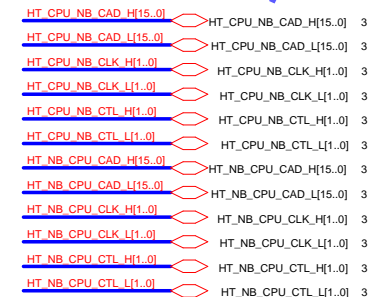


PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH



PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH





signals	RS780	RX780
HT_TXCALP	R641 301 ohm 1%	R641 1.21k ohm 1%
HT_TXCALN		
HT_RXCALP	R655 301 ohm 1%	R655 1.21k ohm 1%
HT_RXCALN		

RES CHIP 301 1/16W +-1%(0402)
P/N : CS13012FB14

U32D		PAR 4 OF 6		
AB12	MEM_A0(NC)	MEM_D0Q/DV0_VSYNC(NC)	AA18	
AE16	MEM_A1(NC)	MEM_DC1/DV0_HSYNC(NC)	AA20	
V11	MEM_A2(NC)	MEM_D0Q/DV0_DE(NC)	AA19	
AE15	MEM_A3(NC)	MEM_D03/DV0_D0(NC)	Y19	
AA12	MEM_A4(NC)	MEM_D04(NC)	V17	
AB16	MEM_A5(NC)	MEM_D05/DV0_D1(NC)	AA17	
AB14	MEM_A6(NC)	MEM_D06/DV0_D2(NC)	AA15	
AD14	MEM_A7(NC)	MEM_D07/DV0_D4(NC)	Y15	
AD13	MEM_A8(NC)	MEM_D08/DV0_D3(NC)	AC20	
AD15	MEM_A9(NC)	MEM_D09/DV0_D5(NC)	AD19	
AC16	MEM_A10(NC)	MEM_DC10/DV0_D6(NC)	AE22	
AE13	MEM_A11(NC)	MEM_D11/DV0_D7(NC)	AC18	
AC14	MEM_A12(NC)	MEM_D12(NC)	AB20	
Y14	MEM_A13(NC)	MEM_D013/DV0_D9(NC)	AD22	
		MEM_DC14/DV0_D10(NC)	AC22	
AD16	MEM_BA0(NC)	MEM_DQ15/DV0_D11(NC)	AD21	
AE17	MEM_BA1(NC)		Y17	
AD17	MEM_BA2(NC)	MEM_DQ0SP/DV0_IDCKP(NC)	W18	
		MEM_DQ0SN/DV0_IDCKN(NC)	AD20	
		MEM_DQ51P(NC)	AE21	
		MEM_DQ51N(NC)		
W12	MEM_RASB(NC)		W17	
Y12	MEM_CASB(NC)		AE19	
AD18	MEM_WEB(NC)			
AB13	MEM_CSB(NC)	MEM_DM0(NC)		
AB18	MEM_CKE(NC)	MEM_DM1/DV0_D8(NC)		
V14	MEM_ODT(NC)			
V15	MEM_CKP(NC)	IOPLLVD18(NC)	AE23	+1.8V
W14	MEM_CKN(NC)	IOPLLVD1(NC)	AE24	+1.1V
		IOPLLVS(NC)	AD23	
AE12	MEM_COMP(NC)		AE18	
AD12	MEM_COMP(NC)	MEM_VREF(NC)		

RS780(RX780)

IOPLLVD- memory PLL
not applicable to RX780



Size Custom	Document Number RS740/RS780-HT LINK I/F 1/5
Date: Thursday, October 16, 2008	Sheet 8 of 48



RS780 Display Port Support (muxed on GFX)

DP0	GFX_TX0, TX1, TX2 and TX3 AUX0 and HPD0
DP1	GFX_TX4, TX5, TX6 and TX7 AUX1 and HPD1

RX780/RS780 POWER DIFFERENCE TABLE

PIN NAME	RX780	RS780	PIN NAME	RX780	RS780
VDDHT	+1.1V	+1.1V	IOPLLVD	NC	+1.1V
VDDHTRX	+1.1V	+1.1V	AVDD	NC	+3.3V
VDDHTTX	+1.2V	+1.2V	AVDDDI	NC	+1.8V
VDDA18PCIE	+1.8V	+1.8V	AVDDQ	NC	+1.8V
VDDG18	+1.8V	+1.8V	PLLVD	NC	+1.1V
VDD18_MEM	NC	+1.8V	PLLVD18	NC	+1.8V
VDDPCIE	+1.1V	+1.1V	VDDA18PCIEPLL	+1.8V	+1.8V
VDDC	+1.1V	+1.1V	VDDA18HTPLL	+1.8V	+1.8V
VDD_MEM	NC	+1.8V/1.5V	VDDLTP18	NC	+1.8V
VDDG33	NC	+3.3V	VDDL18	NC	+1.8V
IOPLLVD18	NC	+1.8V	VDDL33	NC	NC

GROUND

PV2
Change L72, L81, L12 to 0 ohm

+1.1V 2A for RS780M

+1.2V 2A for RS780M+SB700

+1.8V 1A for RS780M+SB700

+1.8V VDDA18PCIE

+1.8V VDDG18 NB

+1.8V VDDG33

+1.8V VDDG33

+1.8V VDDG33

+1.8V VDDG33

+1.8V VDDG33

+1.8V VDDG33

+1.8V VDDG33

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+1.8V VDDG33

PART 5/6

POWER

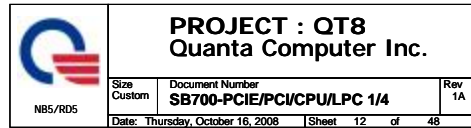
VDD_MEM For UMA RS780 only
Not applicable to RX780
memory I/O transform

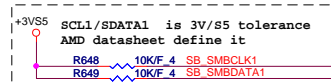
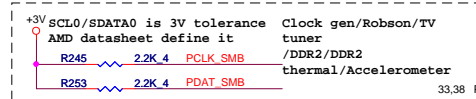
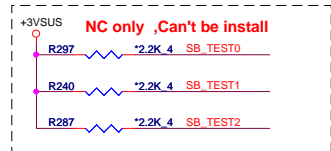


PROJECT : QT8
Quanta Computer Inc.

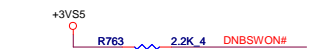
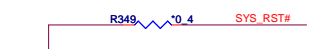
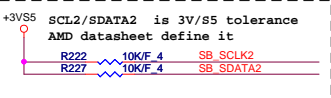
Size Custom Document Number RS740/RS780-POWER5/5 Rev 1A

Date: Thursday, October 16, 2008 1 Sheet 11 of 48

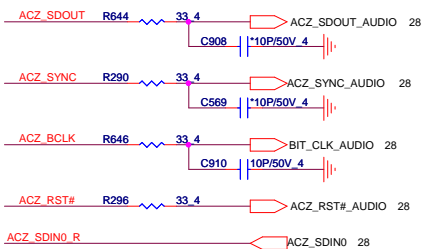




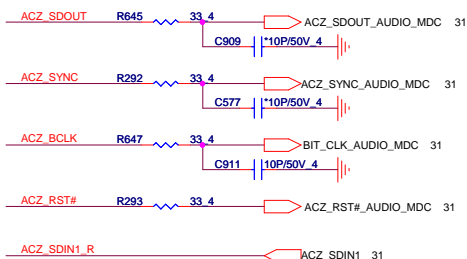
remove pull hi
(chip internal
have pull hi)



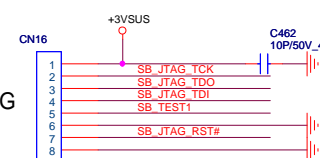
To Azalia



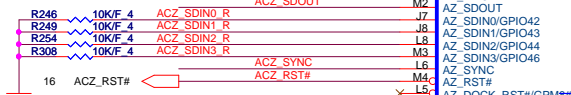
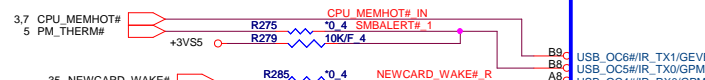
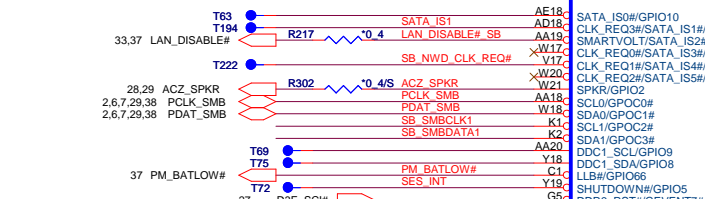
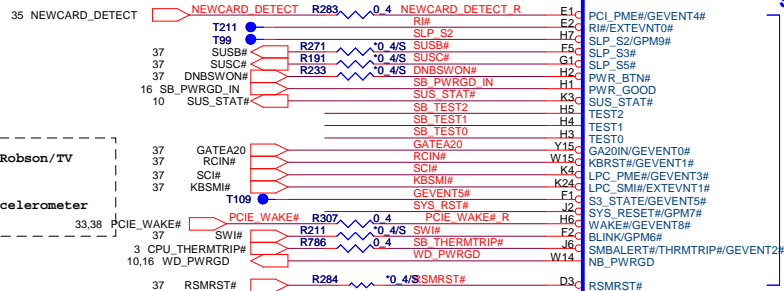
To Modem Board



SB JTAG



*SW JTAG DEBUG



SB700

SB700

Part 4 of 5

USBCLK/14M_25M_48M_OSC

USB_RCOMP

USB_FSD13P

USB_FSD13N

USB_FSD12P

USB_FSD12N

USB_HSD11P

USB_HSD11N

USB_HSD10P

USB_HSD10N

USB_HSD9P

USB_HSD9N

USB_HSD8P

USB_HSD8N

USB_HSD7P

USB_HSD7N

USB_HSD6P

USB_HSD6N

USB_HSD5P

USB_HSD5N

USB_HSD4P

USB_HSD4N

USB_HSD3P

USB_HSD3N

USB_HSD2P

USB_HSD2N

USB_HSD1P

USB_HSD1N

USB_HSD0P

USB_HSD0N

IMC_GPIO8

IMC_GPIO9

IMC_GPIO10

IMC_GPIO11

IMC_GPIO12

IMC_GPIO13

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IMC_GPIO15

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IMC_GPIO217

IMC_GPIO218

IMC_GPIO219

IMC_GPIO220

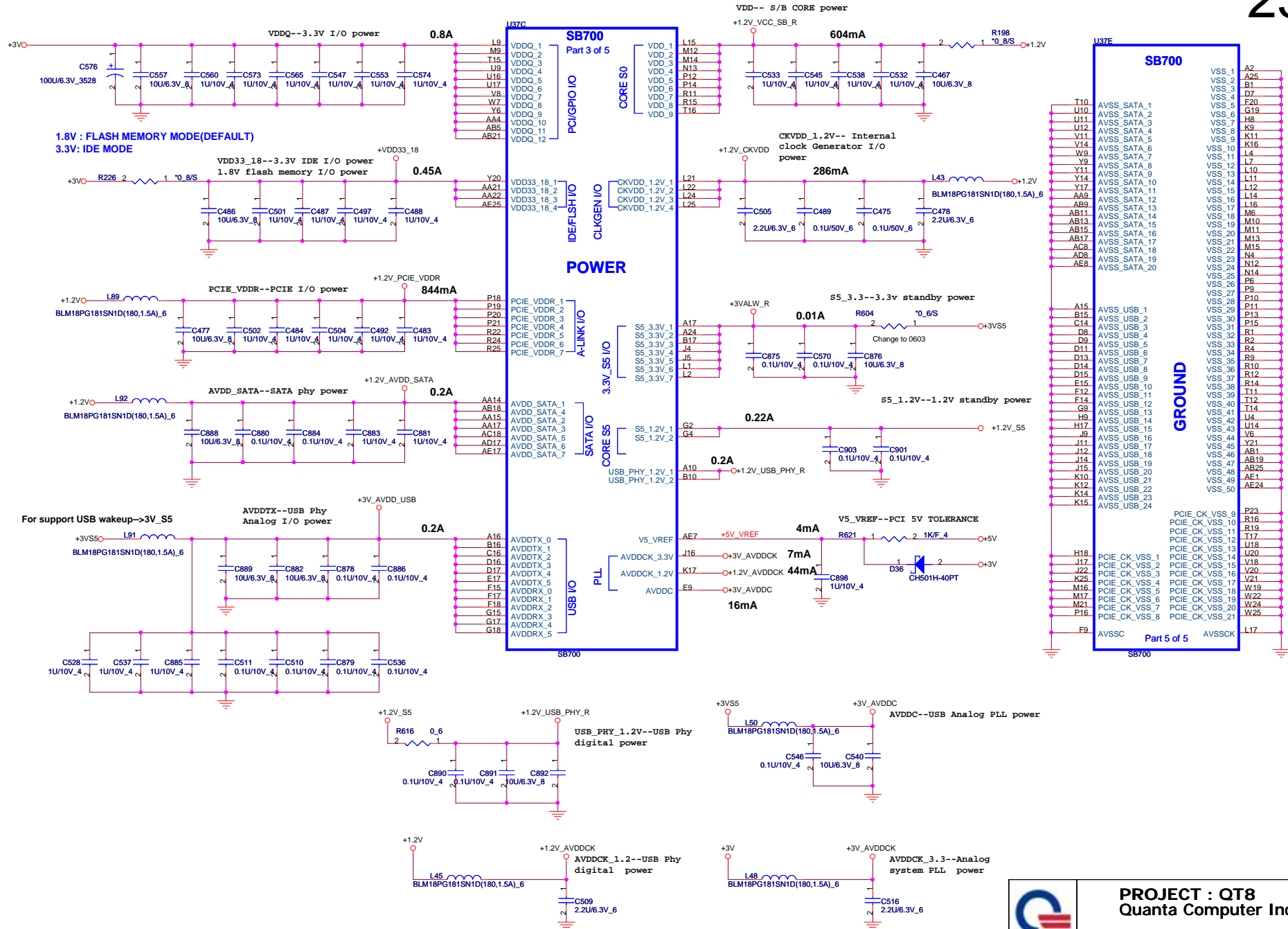
IMC_GPIO221

IMC_GPIO222

IMC_GPIO223

IMC_GPIO224

IMC_GPIO225



PROJECT : QT8
Quanta Computer Inc.

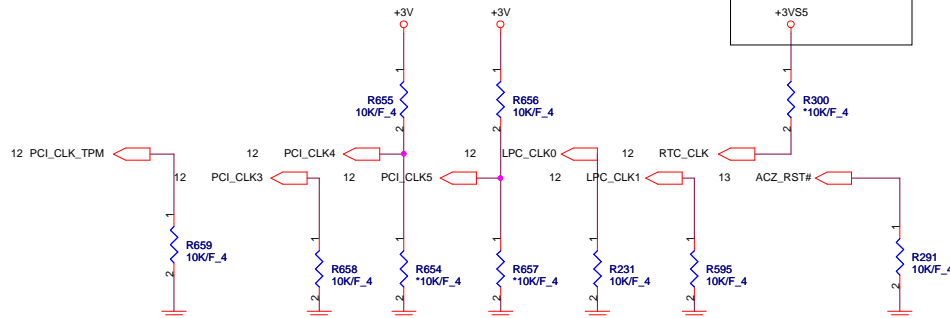
Size Custom	Document Number SB700-PWR/DECOUPLING 4/4	Rev 1A
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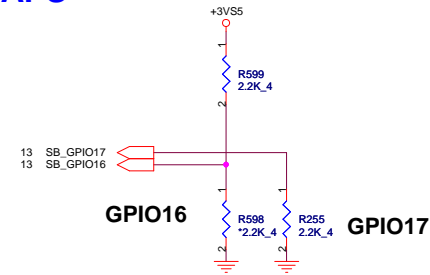
OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

It must ready
before RSMRST#

REQUIRED STRAPS



	PCI_CLK_TPM	PCI_CLK3	PCI_CLK4	PCI_CLK5	LPC_CLK0	LPC_CLK1	RTC_CLK	AZ_RST#
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	IMC ENABLED	CLKGEN ENABLED	INTERNAL RTC DEFAULT	ENABLE PCI ROM BOOT
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			IMC DISABLED DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	DISABLE PCI ROM BOOT DEFAULT

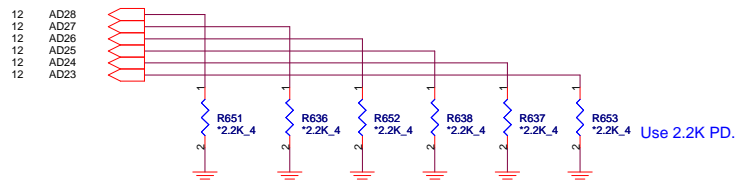


GPIO16 GPIO17

TYPE	GPIO16	GPIO17
FWH	L : 2.2K pull down	L : 2.2K pull down
LPC	NC	L : 2.2K pull down
SPI	L : 2.2K pull down	NC
RSVD	NC	NC

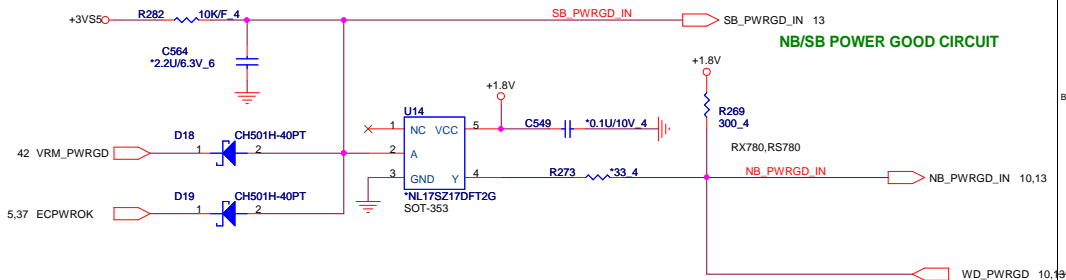
DEBUG STRAPS

SB700 HAS 15K INTERNAL PU FOR PCI_AD[28:23]



	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	RESERVED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	

NB_PWRGD_IN:
RS780/RX780 = 1.8V; RS740 = 3.3V
Do NOT share it with SB_PWRGD when use Internal Clk Gen
(Need SB PLL initialize firstly)



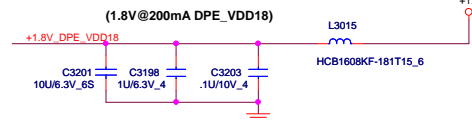
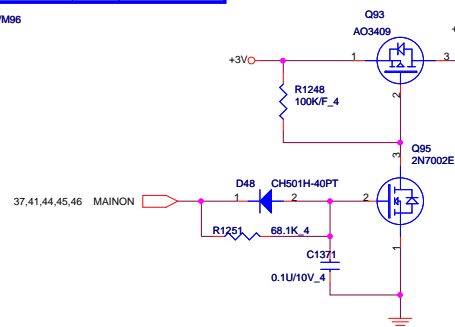
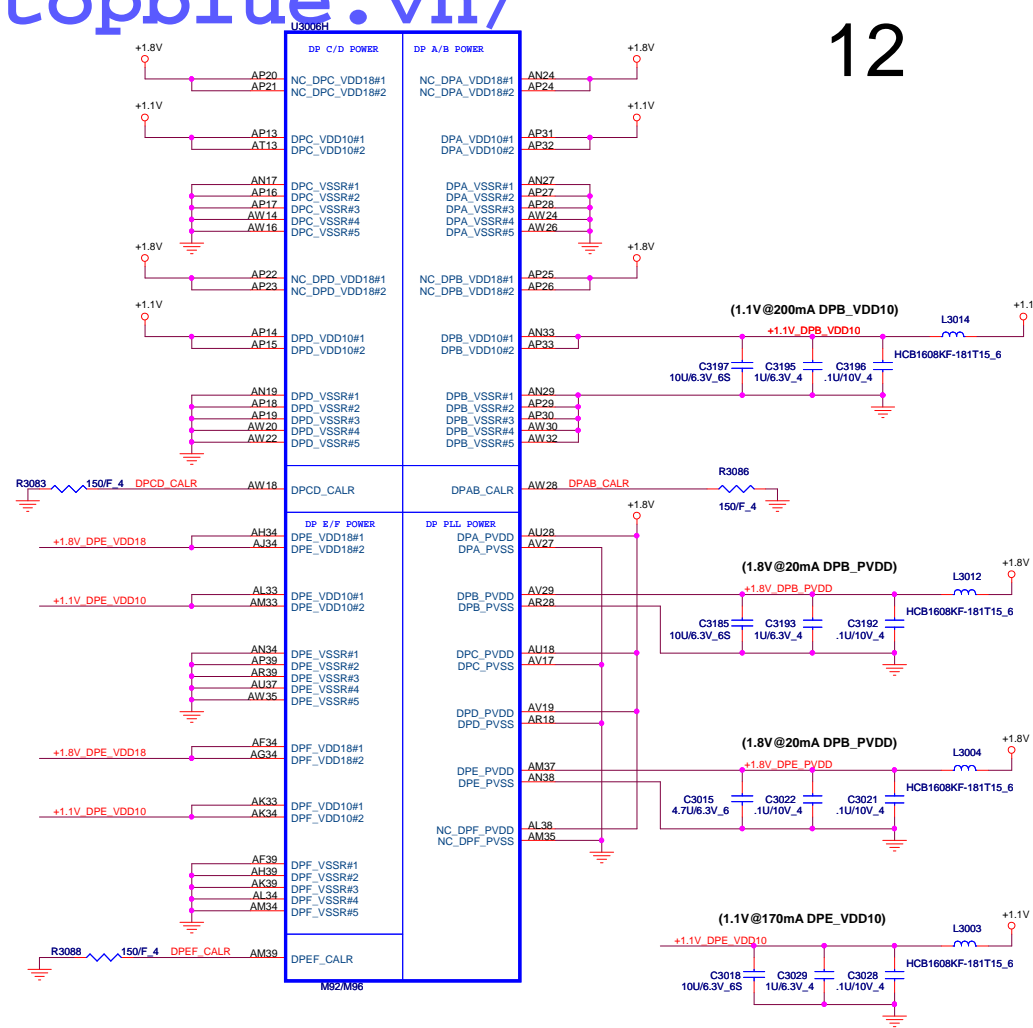
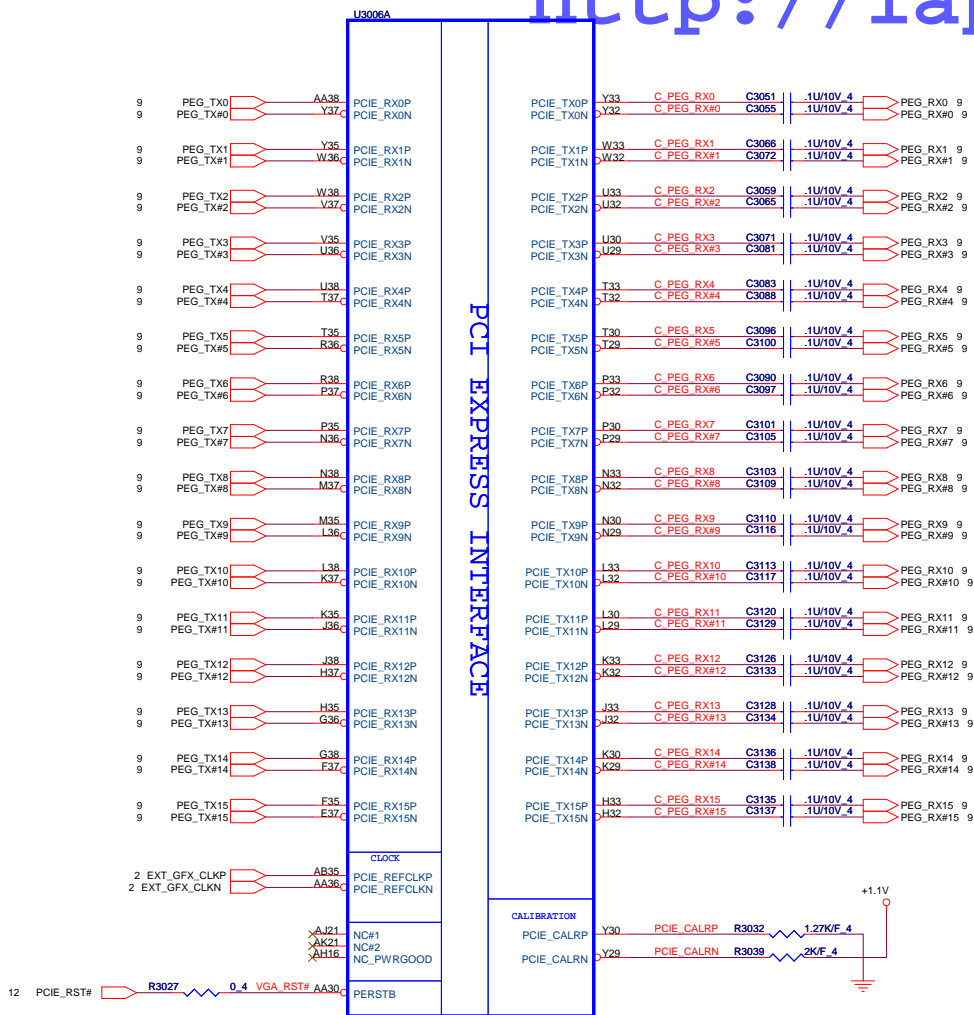
AL17SZ17000 IC(5P) NL17SZ17DFT2G(SOT-353) SOT-353
ALUC1G17000 IC OTHER(5P) SN74AUC1G17DBVR(SOT23-5) SOT23-5

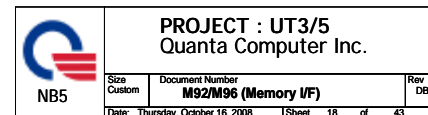


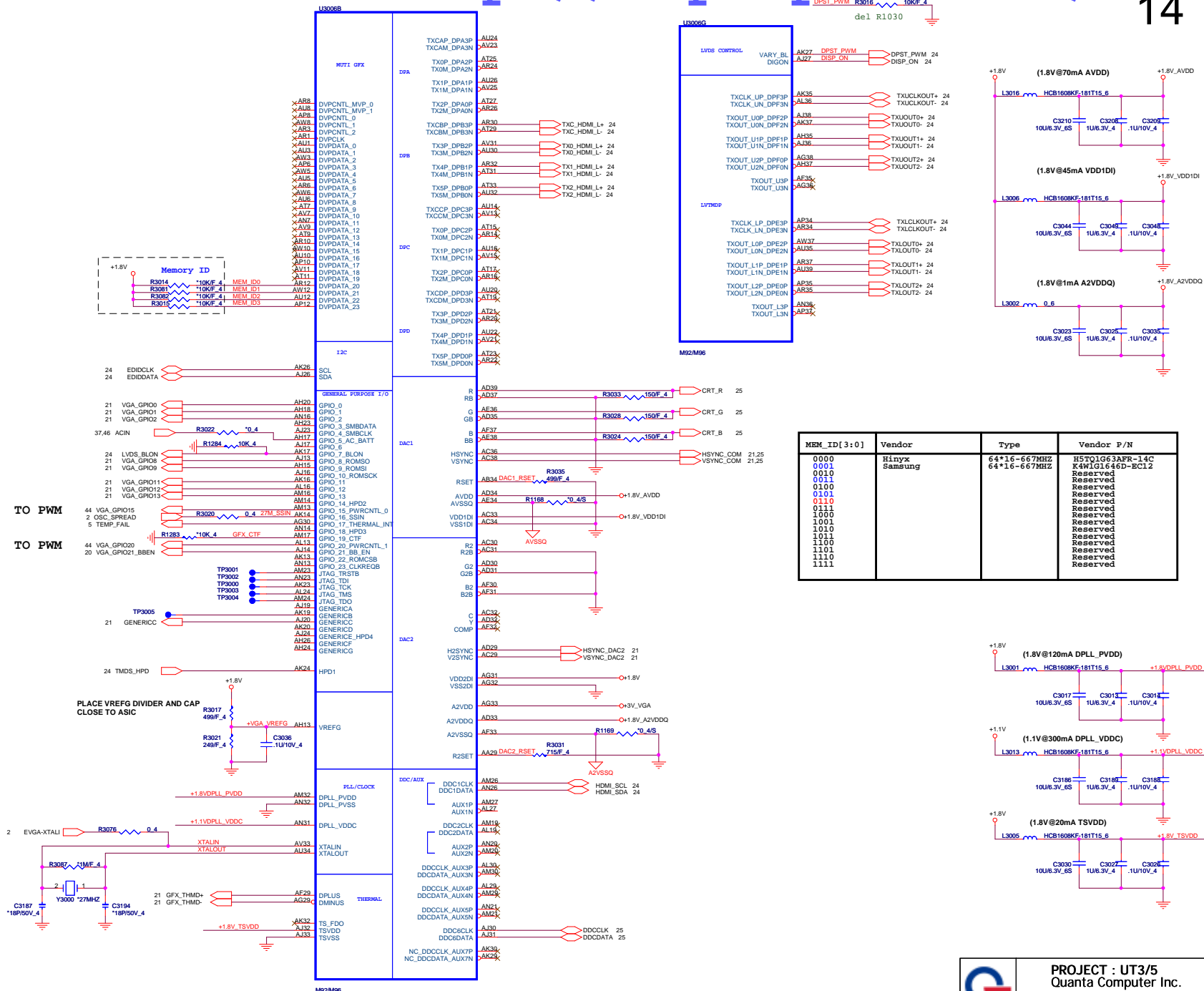
PROJECT : QT8
Quanta Computer Inc.

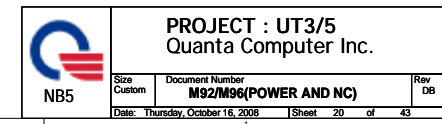
Size	Document Number	Rev
Custom	SB700-STRAPS	1A
Date: Thursday, October 16, 2008 Sheet 16 of 48		

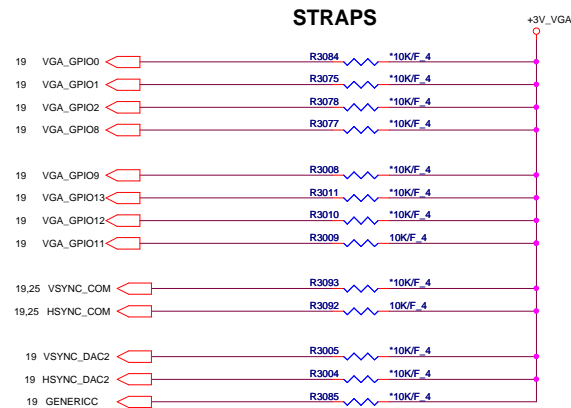
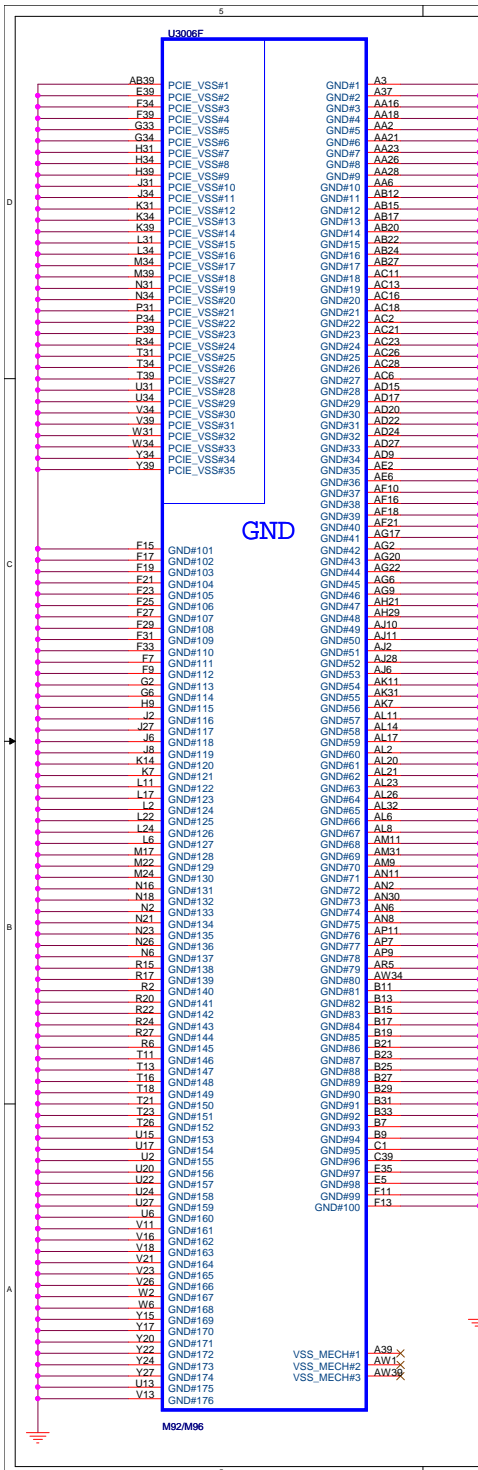
PCI EXPRESS INTERFACE



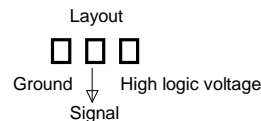






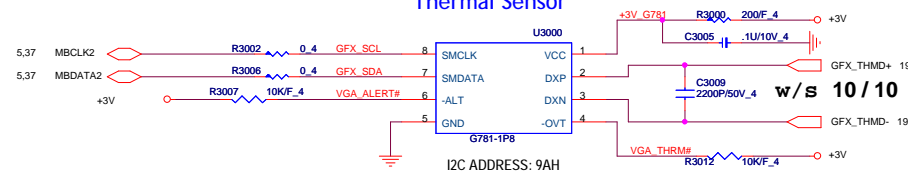


Overlap pads to save space
and to prevent assembly of
both resistors.



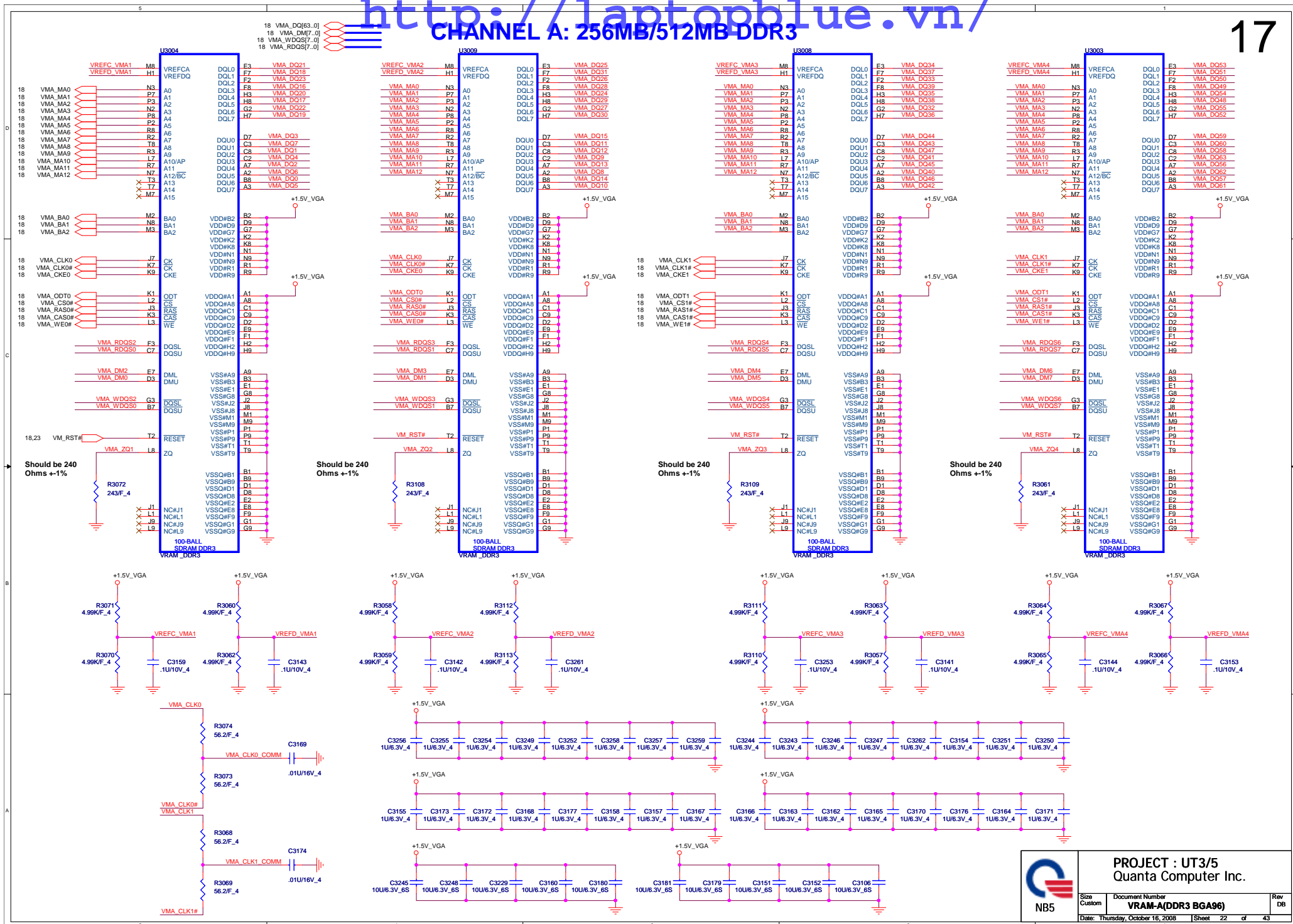
Strap Name	Pin Straps description	Default Value
TX_PWRS_ENB	GPIO0 Transmitter Power Savings Enable 0: 50% Tx output swing for mobile mode 1: full Tx output swing (Default setting for Desktop)	0
TX_DEEMPH_EN	GPIO1 PCI Express Transmitter De-emphasis Enable 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for Desktop)	0
BIF_GEN2_EN	GPIO2 0 = Advertises the PCI-E device as 2.5 GT/s capable at power-on. 1 = Advertises the PCI-E device as 5.0 GT/s capable at power-on. 5.0 GT/s capability will be controlled by software.	0
STRAP_BIF_CLK_PM_EN	GPIO8 Enable CLKREQ# Power Management 0 - CLKREQ# power management capability is disabled 1 - CLKREQ# power management capability is enabled	0
CONFIG[3] CONFIG[2] CONFIG[1] CONFIG[0]	GPIO9 GPIO13 GPIO12 GPIO11 GPIO9,13,12,11 (config 3.2.1.0): a> If BIOS_ROM_EN = 1, then Config(3.0) defines the ROM Type: b> If BIOS_ROM_EN = 0, then Config(3.0) defines the Aperture size:Size of the primary memory apertures claimed in PCI configuration space 000 = 128MB 001 = 256MB 010 = 512MB 011 = 1GB 100 = 2GB 101 = 4GB 110 = 8GB 111 = 16GB	0001
BIOS_ROM_EN	GPIO22 Enable external BIOS ROM device 0 - Disable external BIOS ROM device 1 - Enable external BIOS ROM device	0
AUDIO[0]	VSYNC	
AUD(1)	HSYNC HSYNC - HDMI_EN HDMI connector presence. 0 ?No HDMI connector is present on PCB 1 - HDMI connector is present on the PCB HDMI	1
VIP_DEVICE_STRAP_DIS	V2SYNC If VIP_DEVICE_STRAP_EN is set to ?? then this pin is used to sense whether a VIP slave device is connected to the VIP Host interface. If VIP_DEVICE_STRAP_EN is set to ?? then this pin is not used as a strap at all (i.e. its value during reset is unimportant), and it can be used as a regular GPIO	0
SMS_EN_HARD	H2SYNC	0
CCBYPASS	GENERICC	0

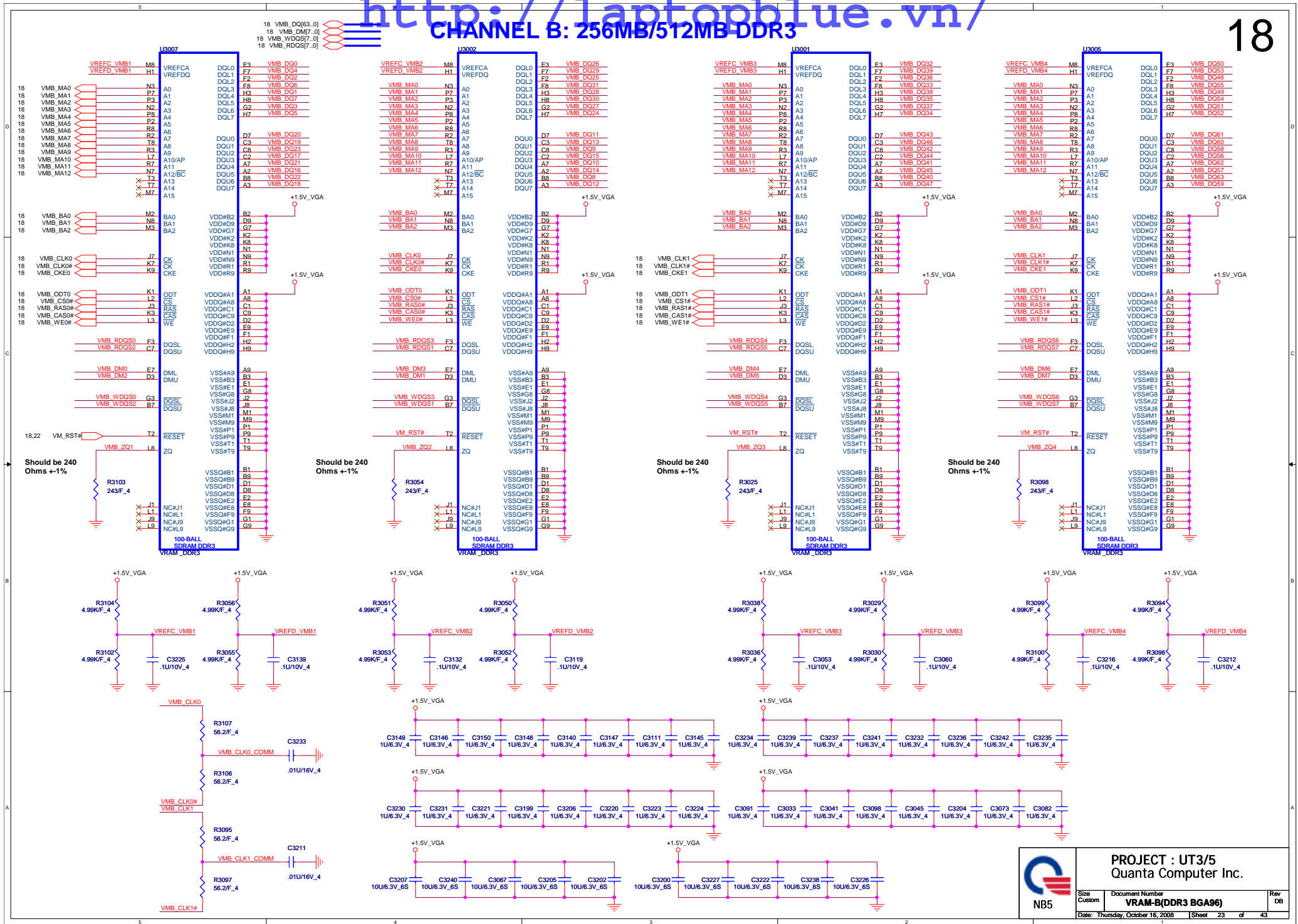
Thermal Sensor



PROJECT : UT3/5
Quanta Computer Inc.

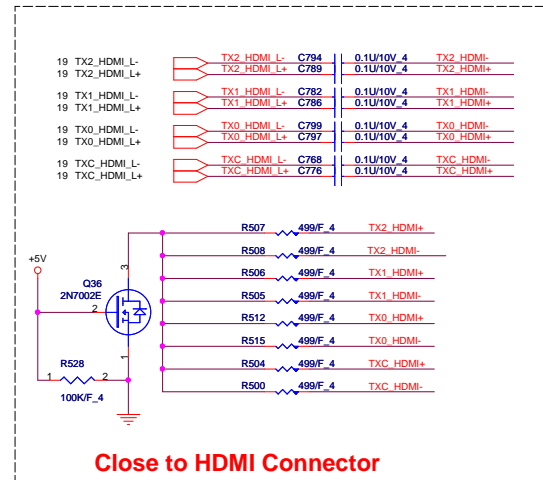
Size	Document Number	Rev
Custom	M92/M96 (GND/Straps/Therm)	DB
Date: Thursday, October 16, 2008	Sheet 21 of 43	



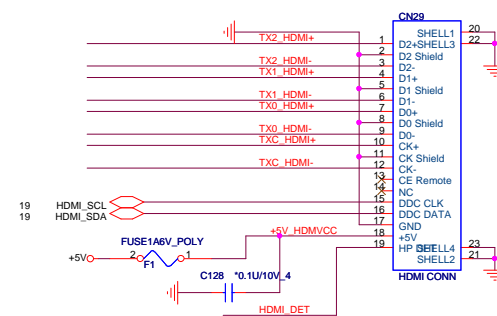


PROJECT : UT3/5
Quanta Computer Inc.

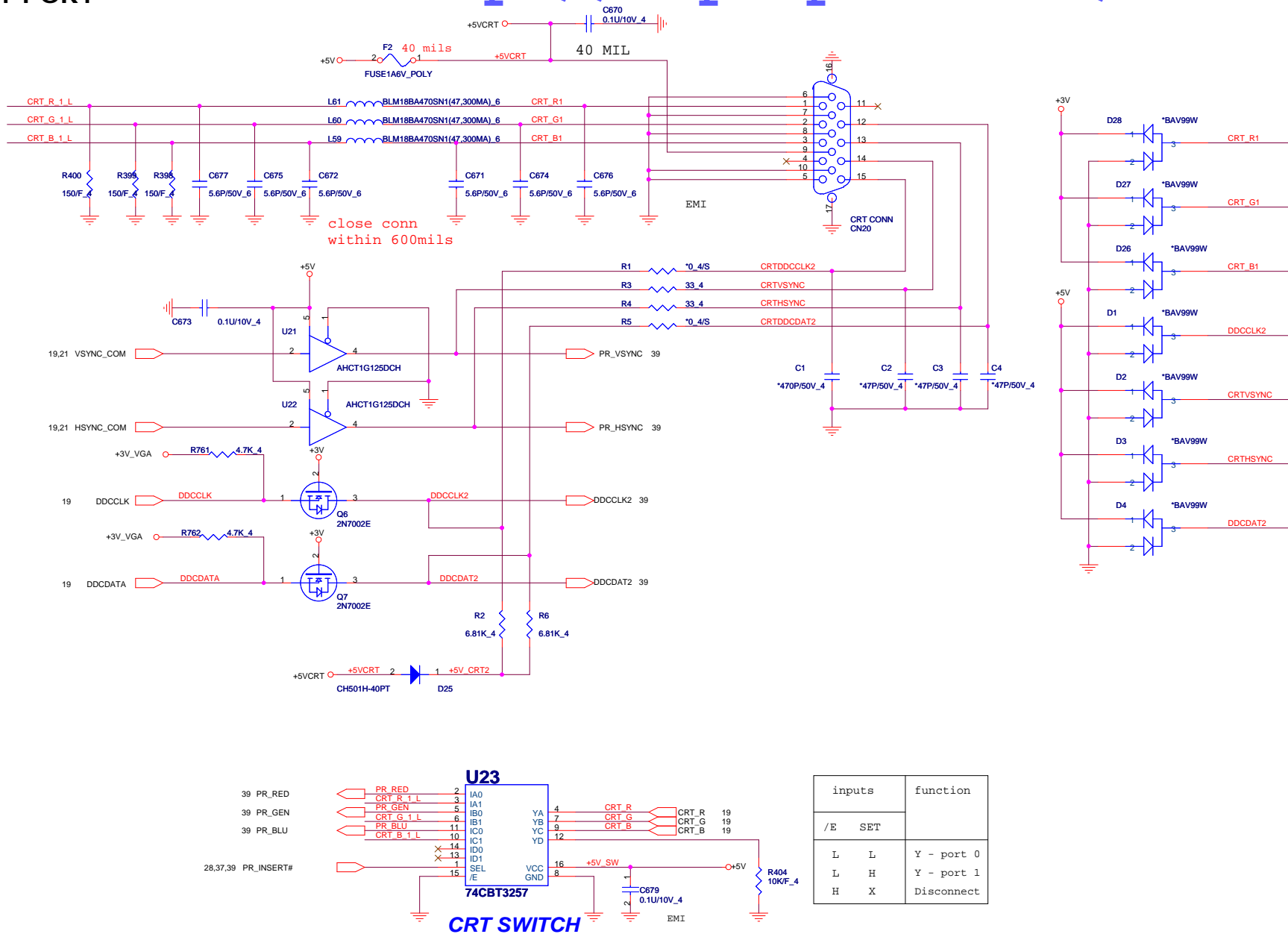
Size Custom	Document Number VRAM-B(DDR3 BGA96)	Rev DB
Date: Thursday, October 16, 2008	Sheet 23 of 43	

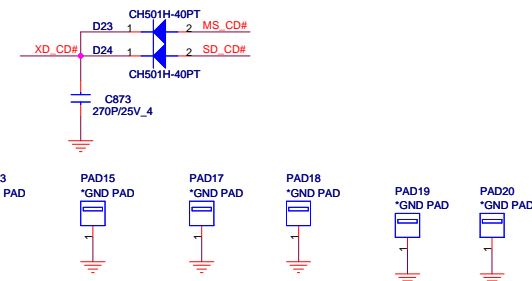
[illegible]

HDMI PORT

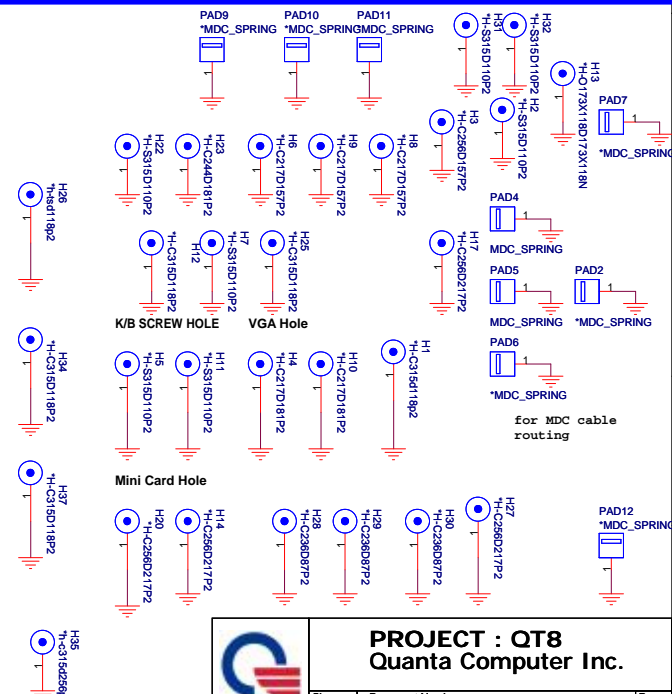
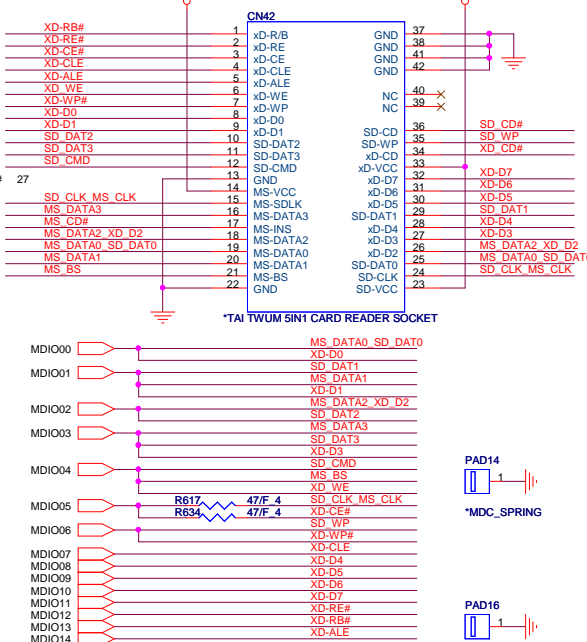
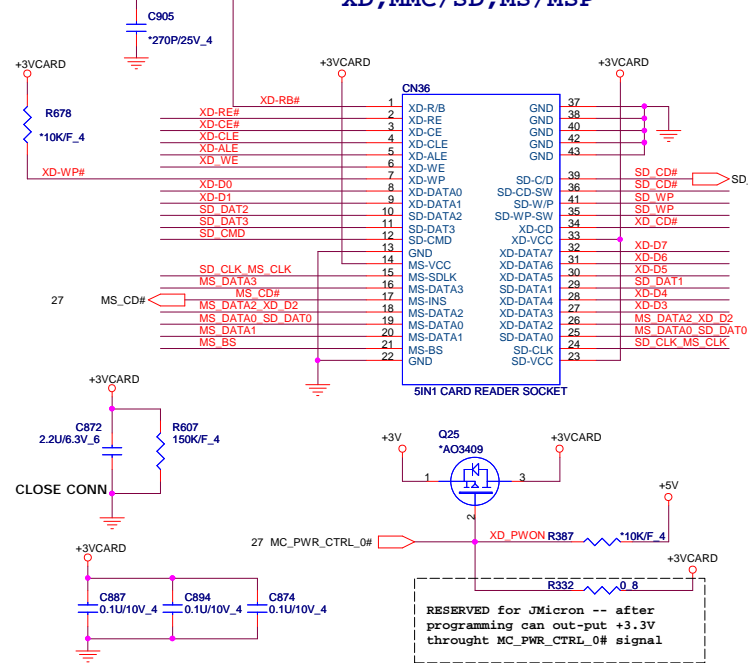


CRT PORT





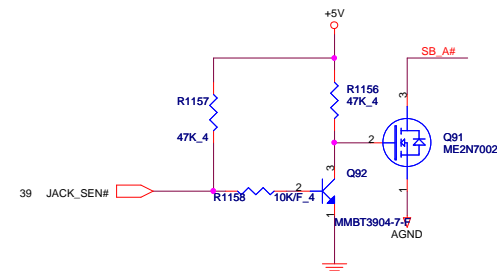
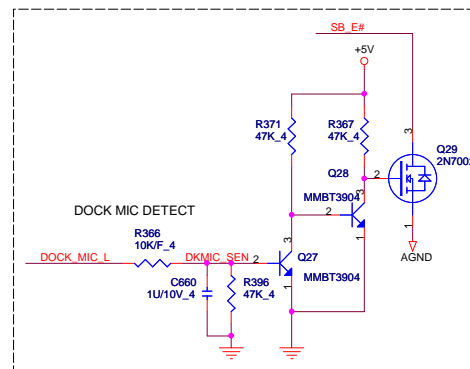
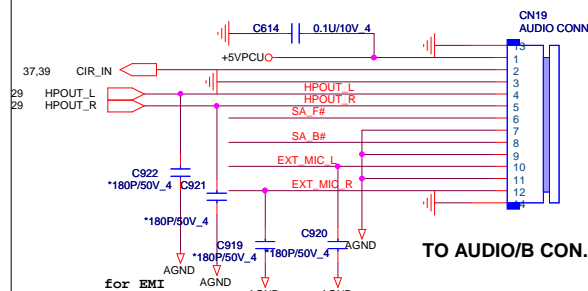
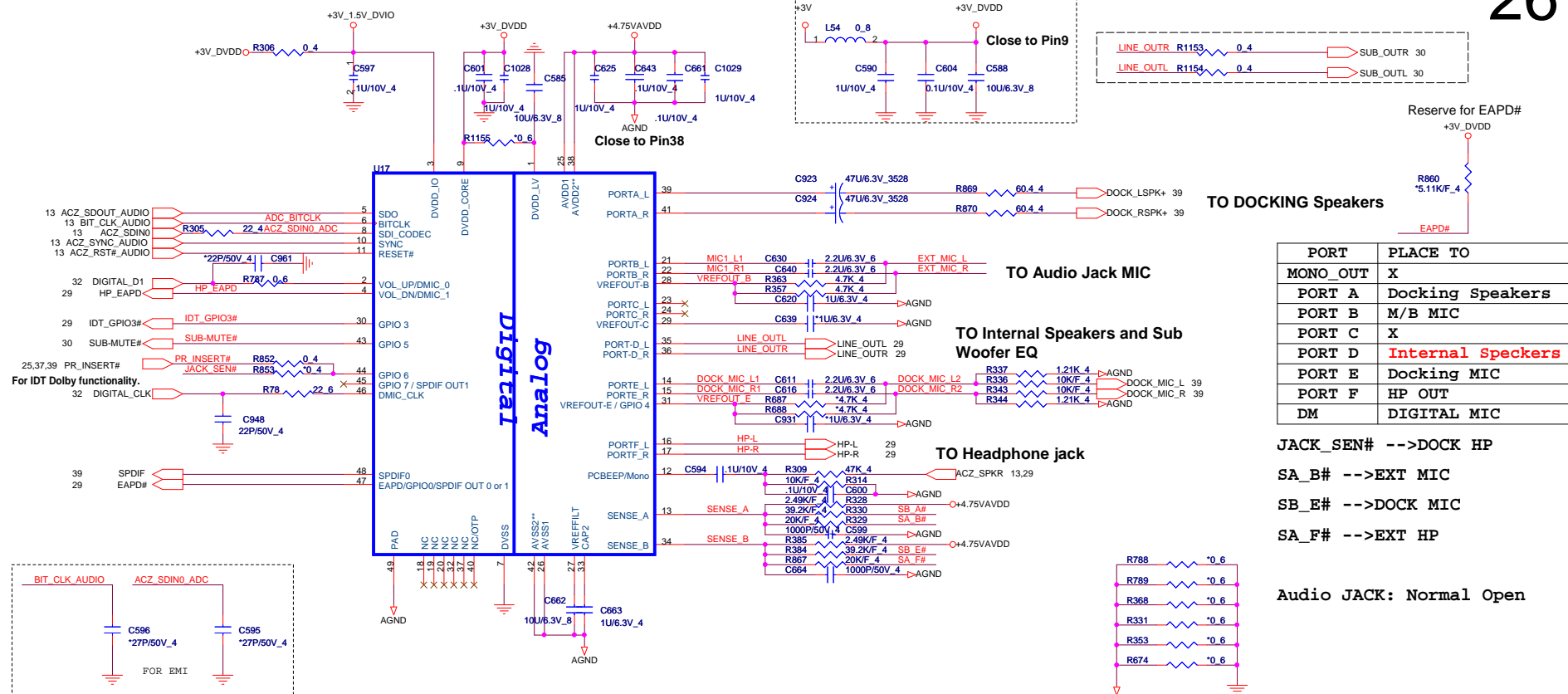
5 IN1 CARD READER XD, MMC/SD, MS/MSP



PROJECT : QT8
Quanta Computer Inc.

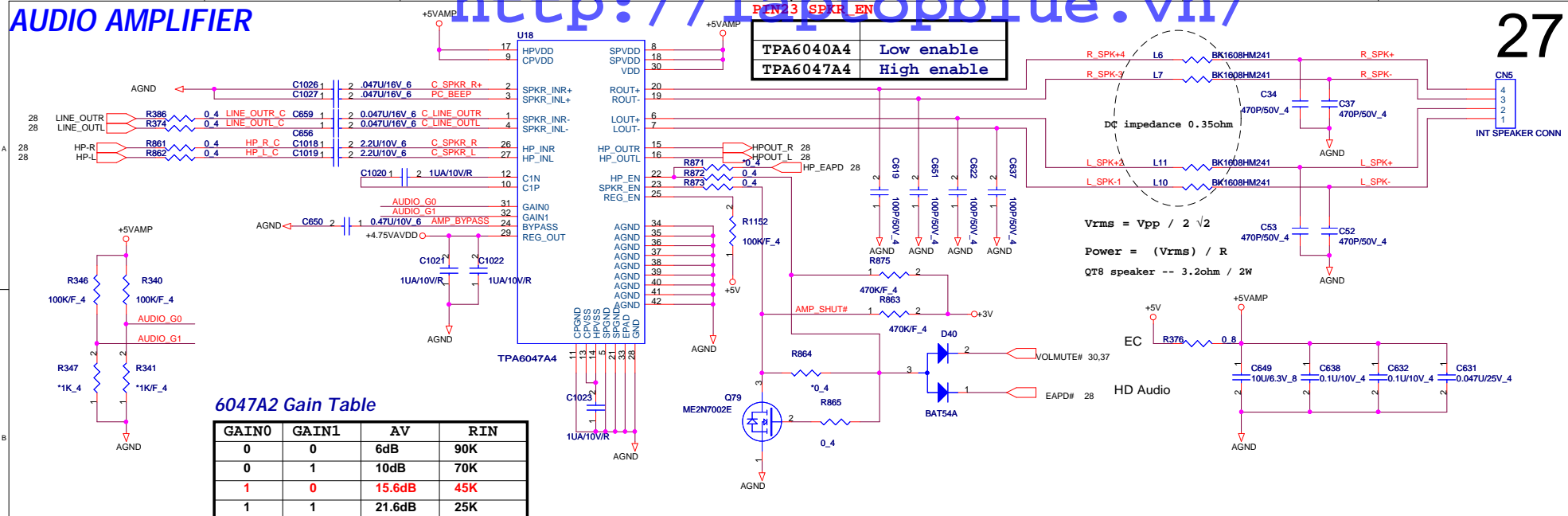
Size Custom	Document Number	Rev 1A
RTS5158 & CR SOCKET & HOLE		
Date: Thursday, October 16, 2008	Sheet 26 of 48	



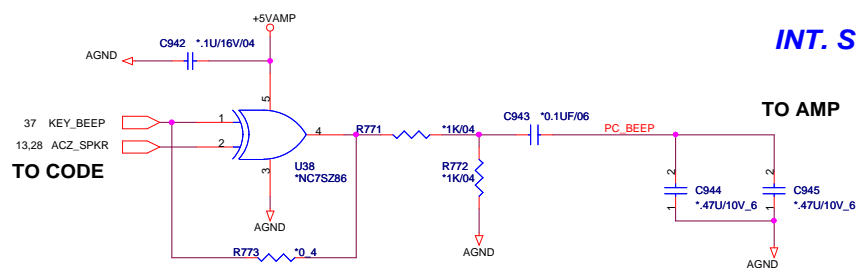


AUDIO AMPLIFIER

27

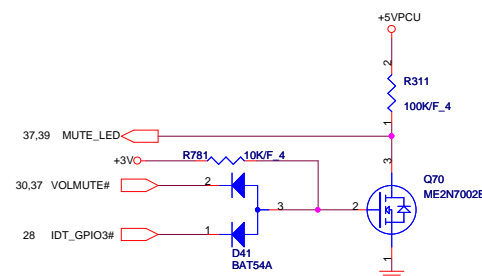


PC-BEEP

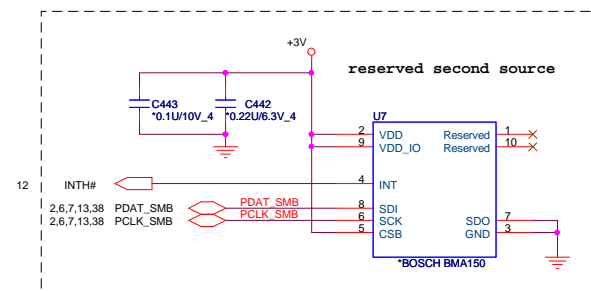
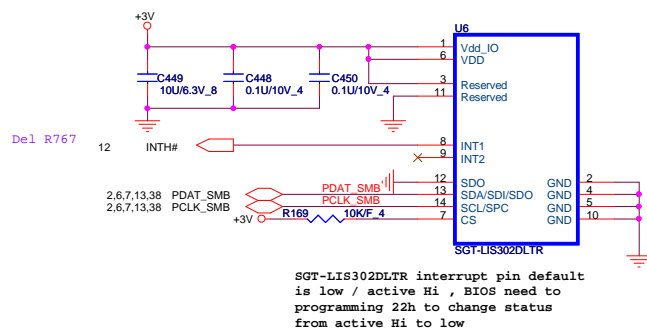


MUTE_LED

Low --> un-MUTE
High --> Mute

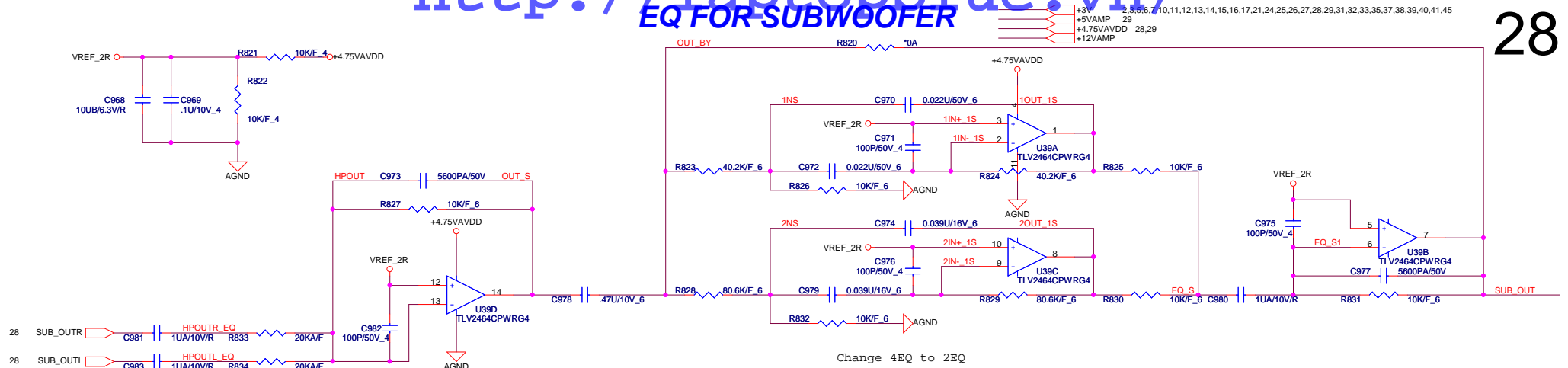


Acceleration sensor

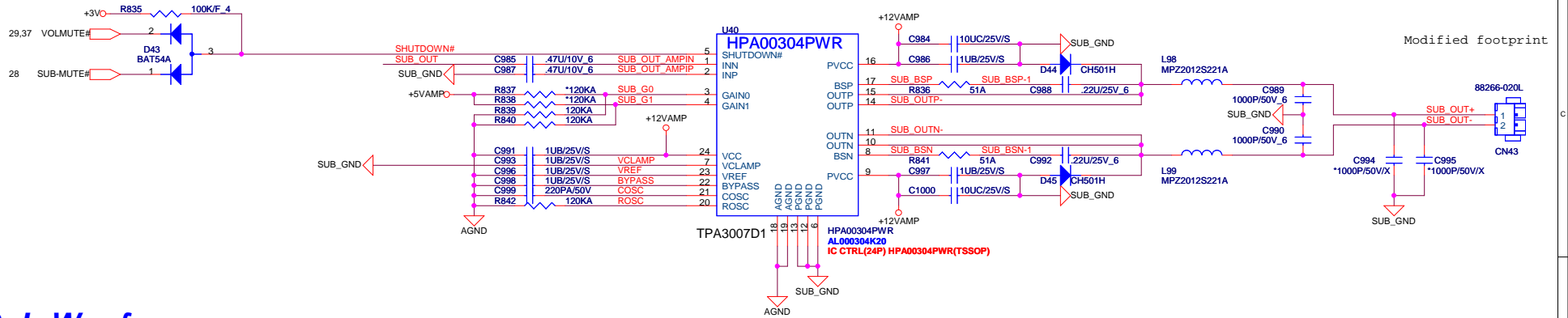


PROJECT : QT8
Quanta Computer Inc.

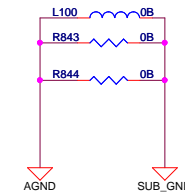
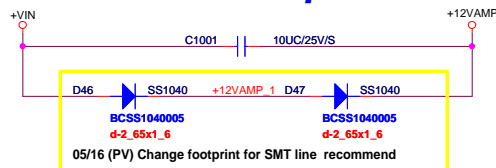
Size Custom Document Number **AMP_TPA6017/INT SPK** Rev 1A
Date: Thursday, October 16, 2008 Sheet 29 of 48



MODEL	UT1	UT2
R823	60.4K/F_6	40.2K/F_6
R824	60.4K/F_6	40.2K/F_6
R828	60.4K/F_6	80.6K/F_6
R829	60.4K/F_6	80.6K/F_6
C970	0.027U/25V_6	0.022U/50V_6
C972	0.027U/25V_6	0.022U/50V_6
C974	0.027U/25V_6	0.039U/16V_6
C979	0.027U/25V_6	0.039U/16V_6

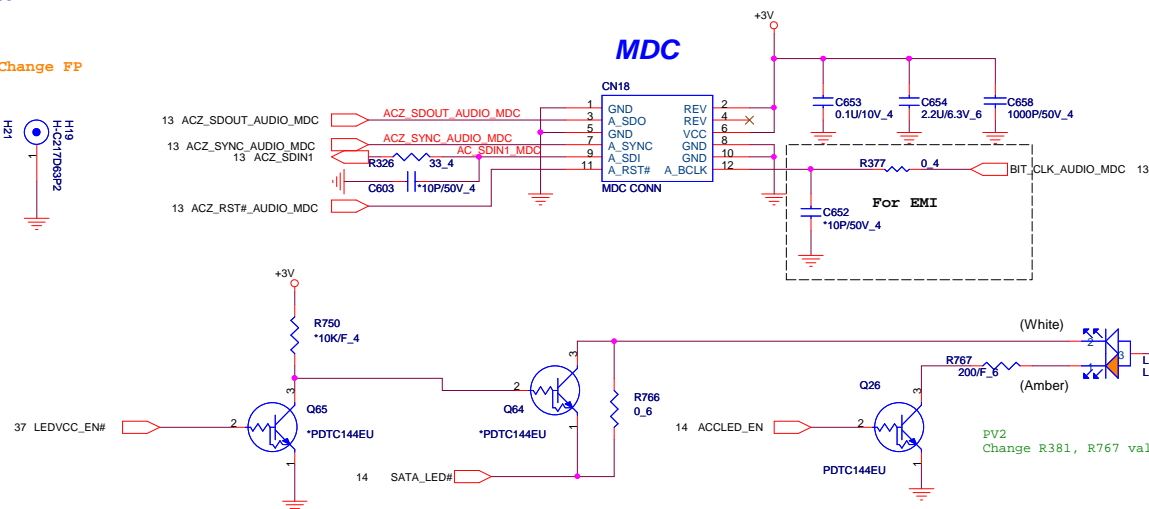
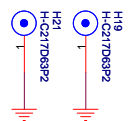


Sub-Woofers power



Modem CONN

DB-1 Change F/P

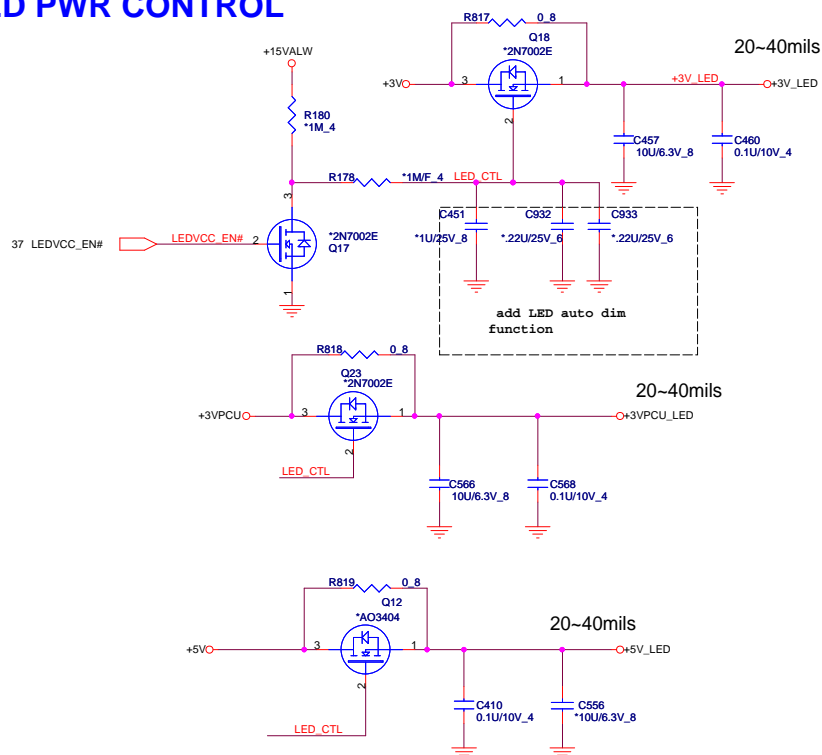


LED



SI-1 modified --
for fix SATA LED
no support LED
light control

LED PWR CONTROL

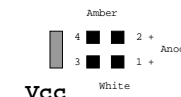
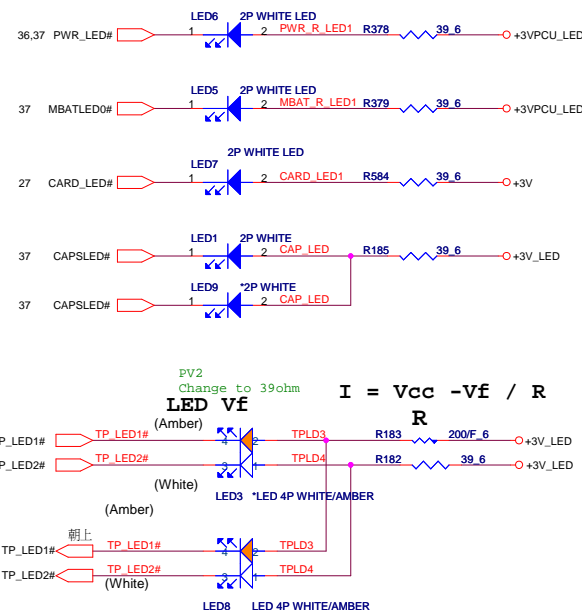


LED1 for 17.3"

LED9 for 16.3"

LED3 for 16.3"

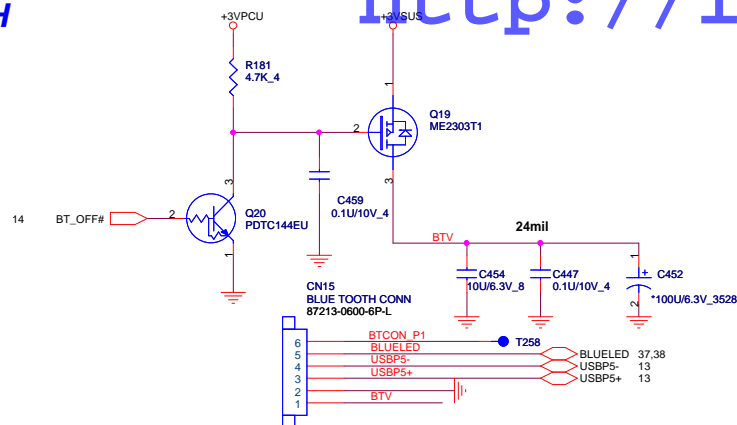
LED8 for 17.3"



PROJECT : QT8
Quanta Computer Inc.

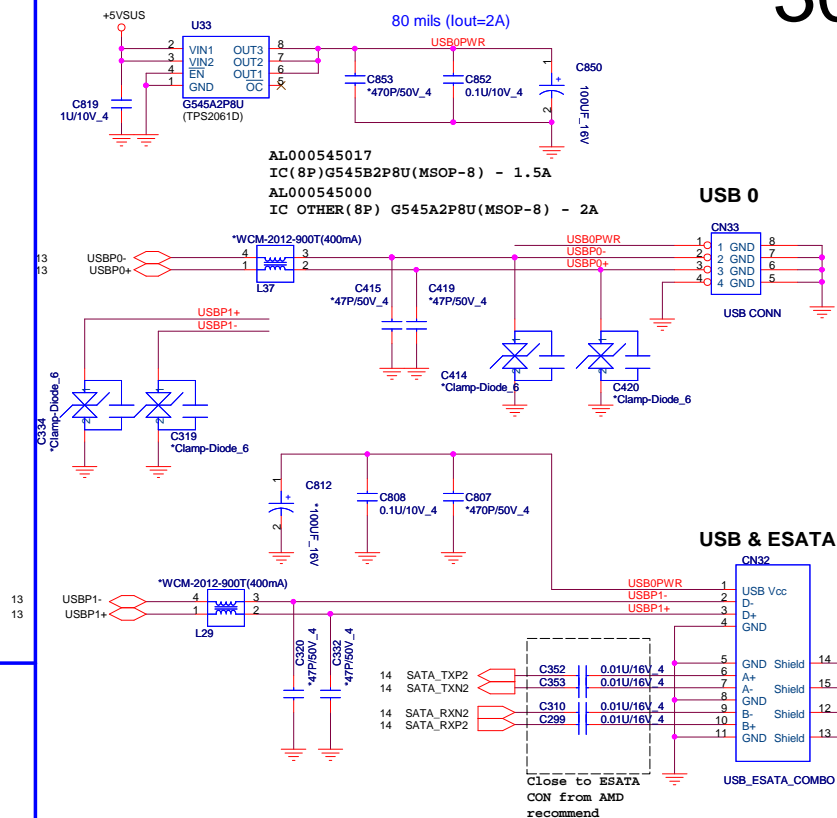
Size Custom	Document Number MDC1.5 Con Accelerometer/LED	Rev 1A
Date: Thursday, October 16, 2008	Sheet 31	of 48

BLUETOOTH



LEFT SIDE USBX1 and E-SATA/USB COMBO

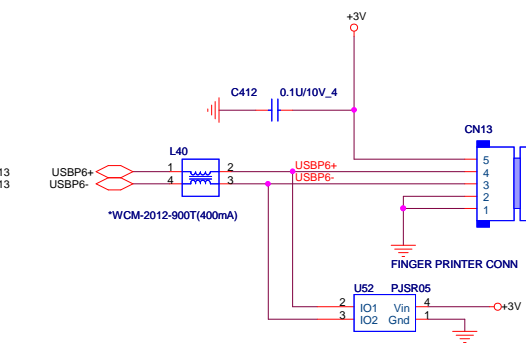
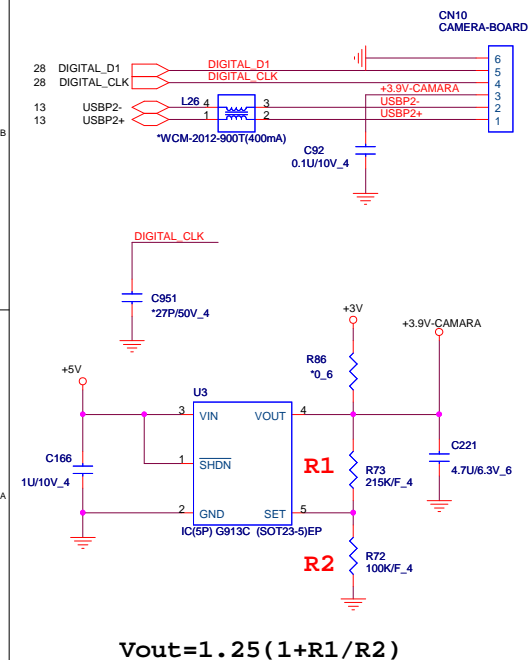
30



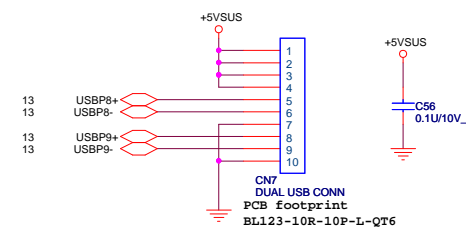
USB CAMERA CONNECT

USB Fingerprint CON

1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)

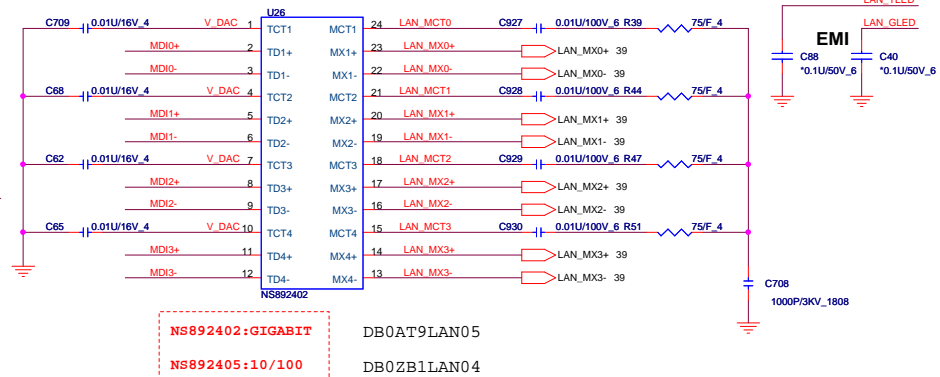


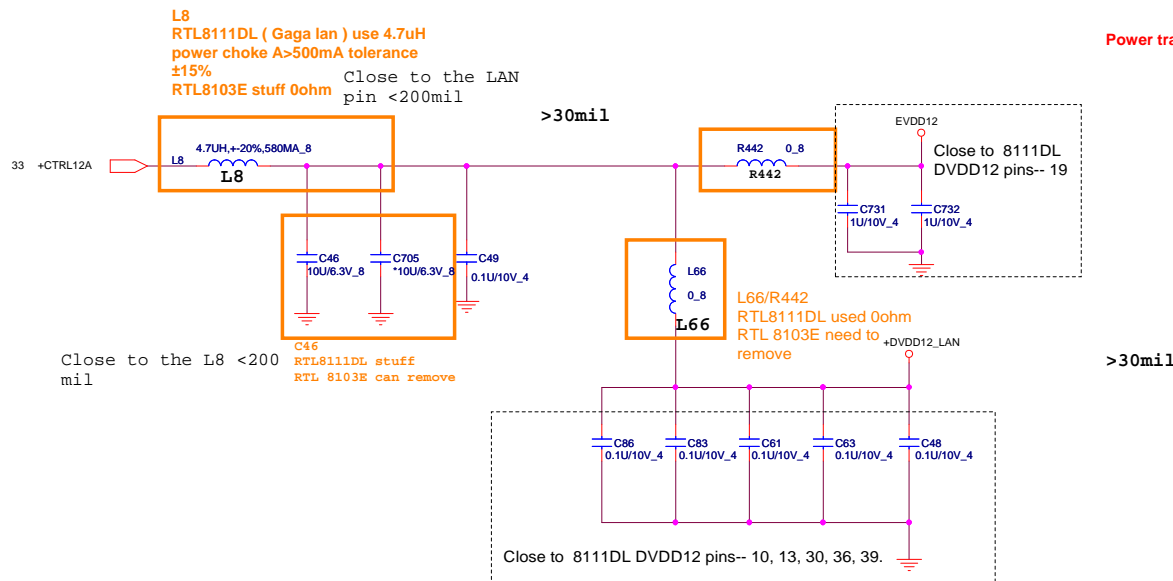
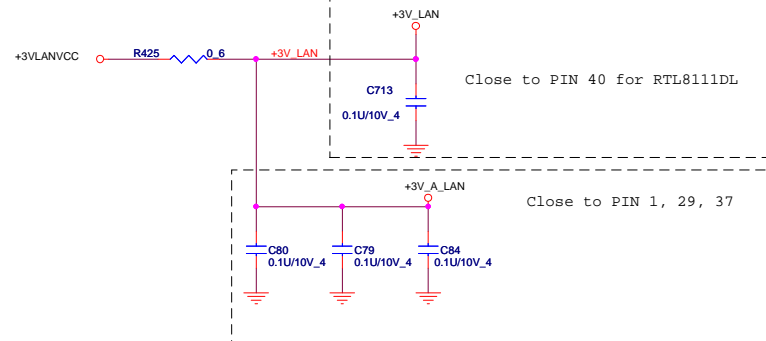
RIGHT SIDE USBX2



PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number BT/WEBCAM/FT/USBX4/ESATA	Rev 1A
Date: Thursday, October 16, 2008	Sheet 32 of 48	

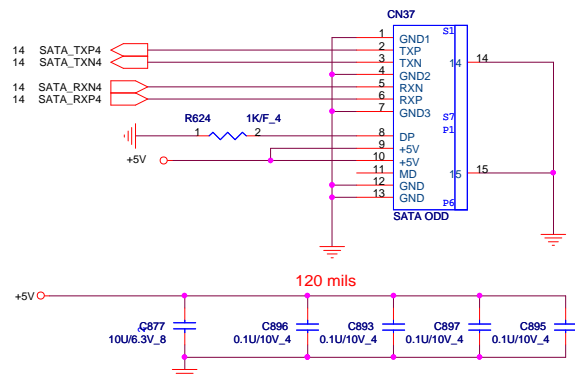




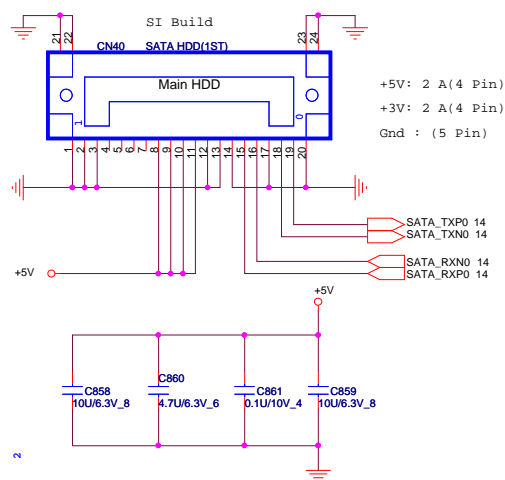
PROJECT : UT12
Quanta Computer Inc.

Size Custom	Document Number LAN Power	Rev 1A
Date: Thursday, October 16, 2008	Sheet 34 of 47	

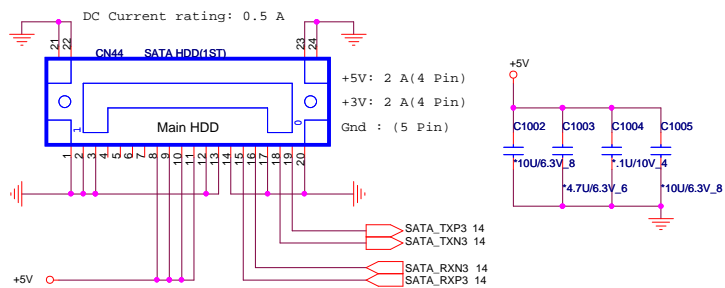
SATA CD-ROM



SATA_1 HDD CONNECTOR

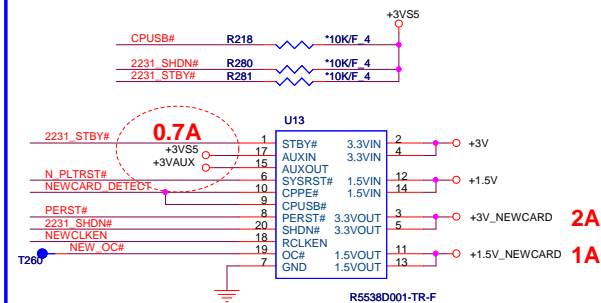
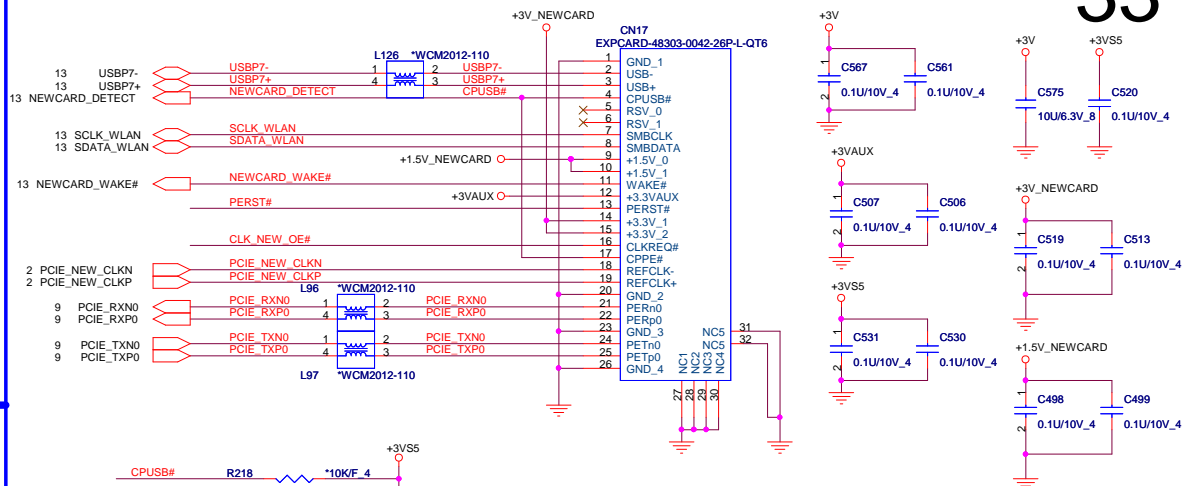


SATA_2 CONNECTOR

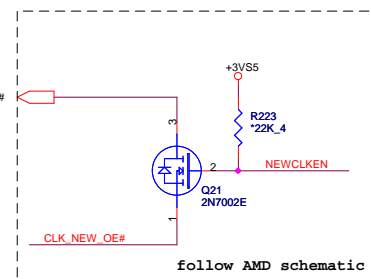
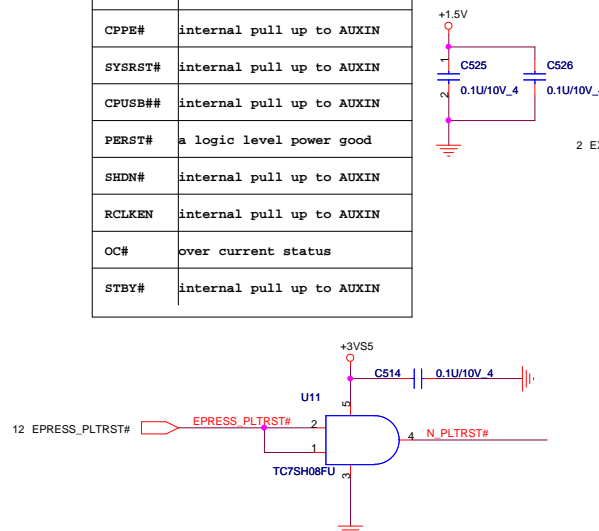


NEWCARD

NEWCARD (PCIEXPRESS*1+ USB*1)



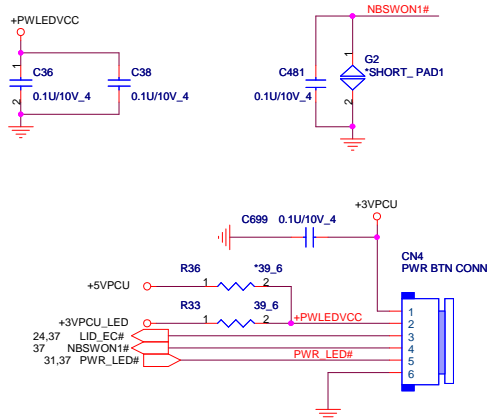
pin name	pull hi/low
CPPE#	internal pull up to AUXIN
SYSRST#	internal pull up to AUXIN
CPUSB##	internal pull up to AUXIN
PERST#	a logic level power good
SHDN#	internal pull up to AUXIN
RCLKEN	internal pull up to AUXIN
OC#	over current status
STBY#	internal pull up to AUXIN



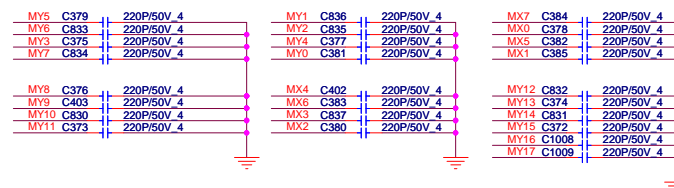
PROJECT : QT8
Quanta Computer Inc.

Size Custom Document Number NEW CARD/SATA ODD/SATA HDD Rev 1A

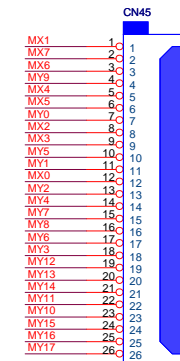
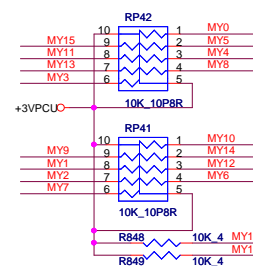
Date: Thursday, October 16, 2008 Sheet 35 of 48



1. +3VPCU(LIDSWITCH PWR)
2. LEDVCC(+3VPCU)
3. LIDSWITCH
4. POWERON#
5. PWRLED#
6. GND

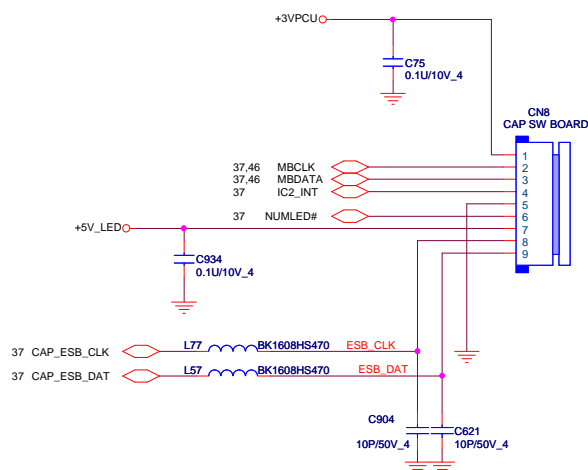


KEYBOARD PULL-UP

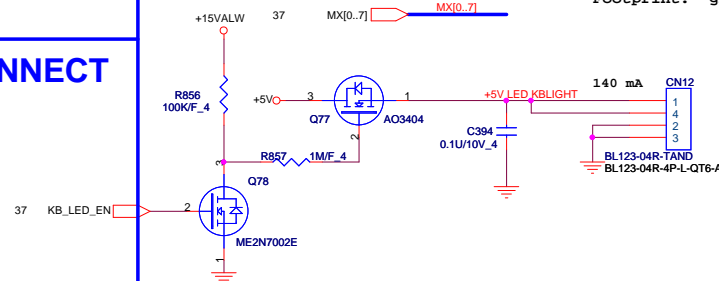


Footprint: "gb1rf260-1253-7f-26p-1"

CAP SW CONNECT

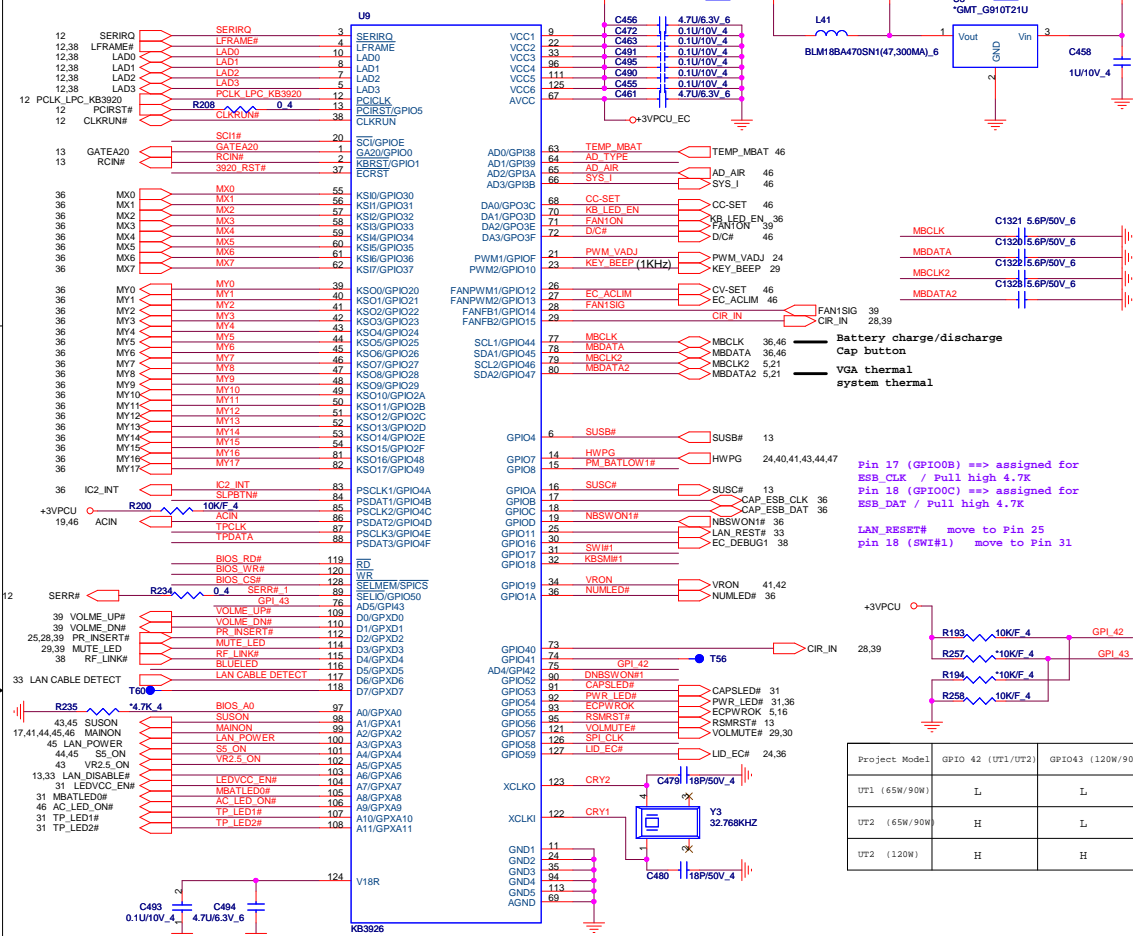


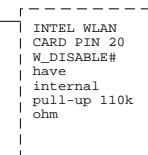
1. +3VPCU
2. MBCLK
3. MBDATA
4. CAP_INT
5. GND
6. NUM LOCK LED
7. +5V
8. ESB_CLK
9. ESB_DAT



PROJECT : QT8
Quanta Computer Inc.

Size Custom	Document Number LED/KEYBOARD/SW	Rev 1A
Date: Thursday, October 16, 2008	Sheet 36 of 48	

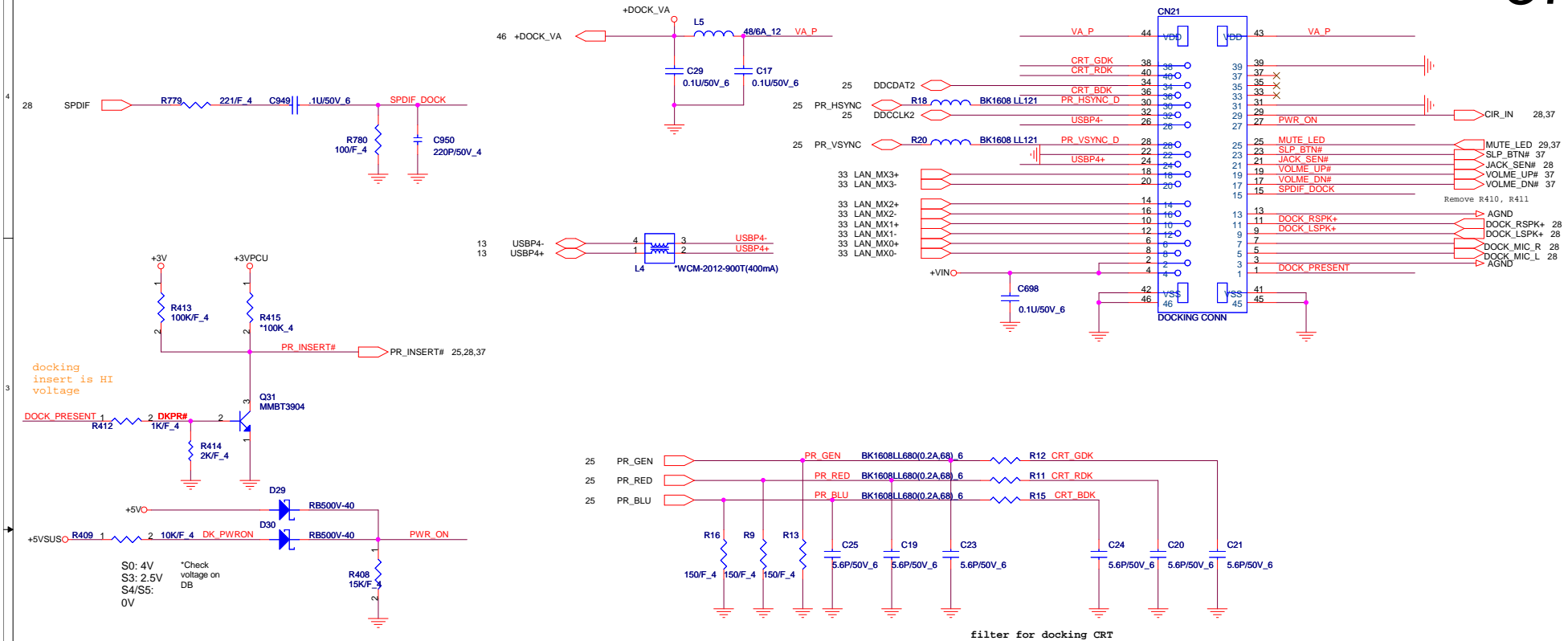




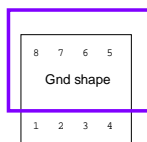
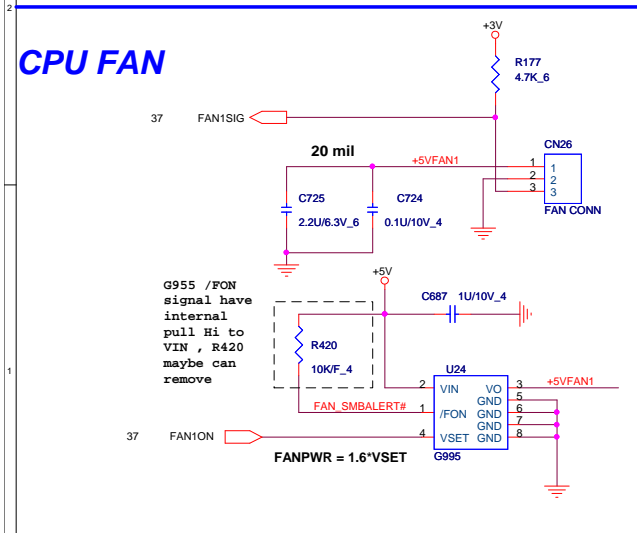
Del EC_debug2 for
CAP board update
on PV



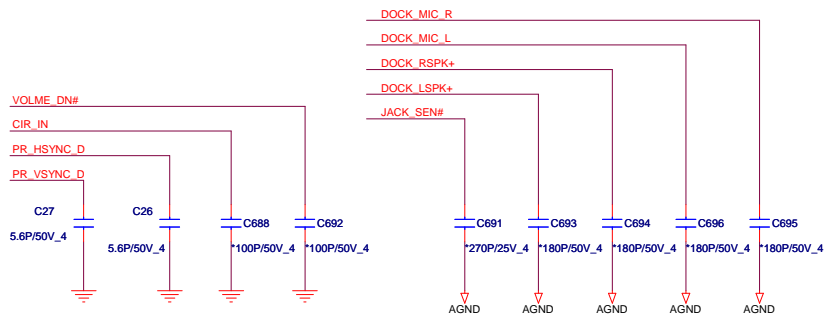
Size Custom	Document Number Mini CARD X 3
Date: Thursday, October 16, 2008	Sheet 38 of 48



CPU FAN



G995 layout notice

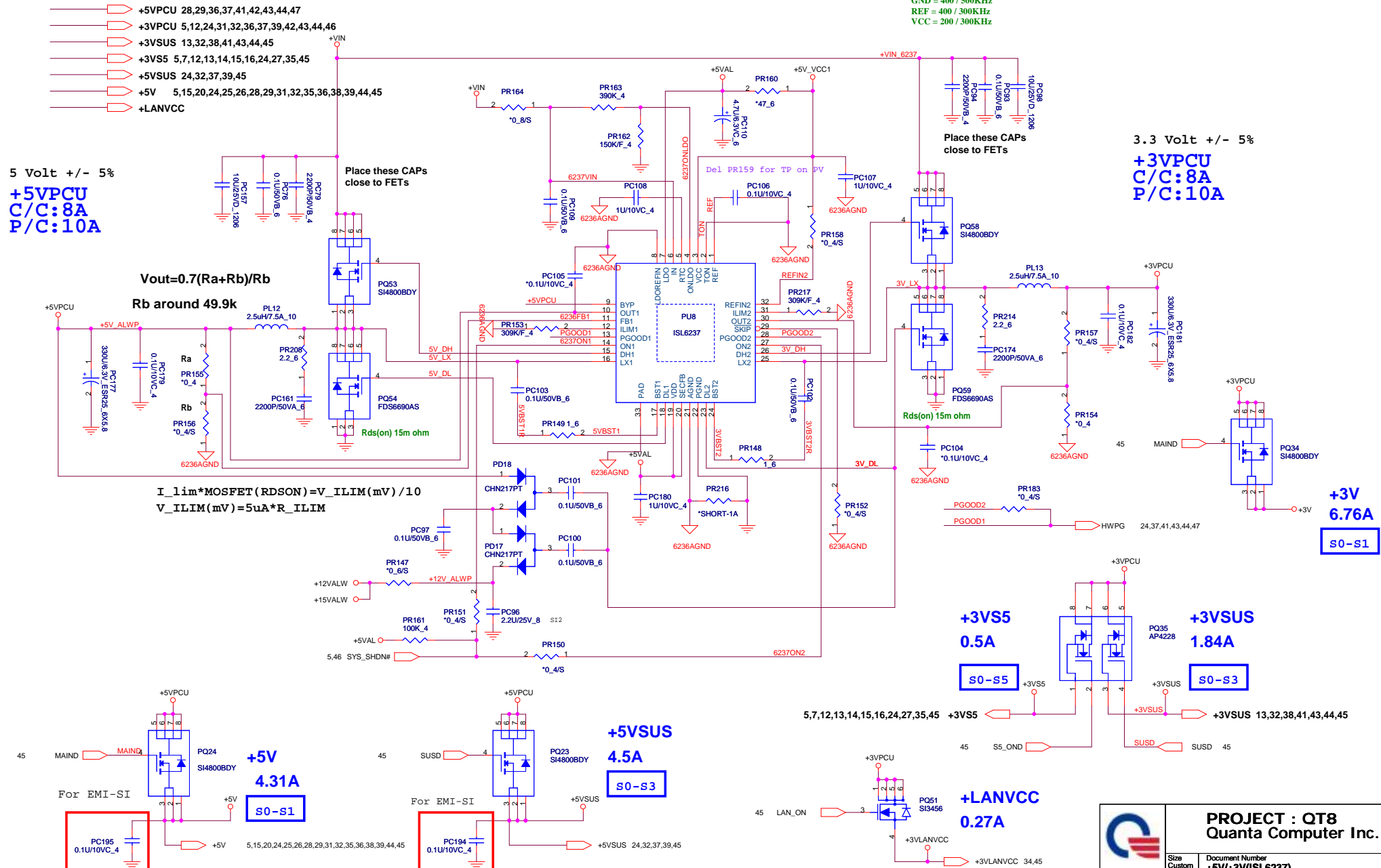

 PROJECT : QT8
 Quanta Computer Inc.

 Size Custom
 Document Number
 CABLE DOCKING/FAN
 Date: Thursday, October 16, 2008 Sheet 39 of 48

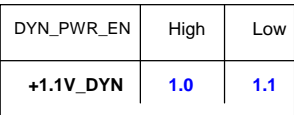
 Rev
 1A

DC/DC +3VPCU/+ 5VPCU/+12VALW

TON: 5V / 3.3V
GND = 400 / 500KHz
REF = 400 / 300KHz
VCC = 200 / 300KHz



+1.2V
12A (4.3A+7.0A)
S0-S1



Vo=0.75(R1+R2)/R2

R_ILIM=I_LIMIT*Rsense/20uA

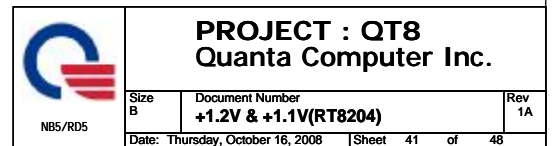
Keep R2 higher than 10Kohm

- > +1.1V 8,9,10,11,17,19,20,45
- > +1.2V 2,3,11,12,14,15

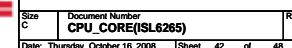
+1.1V **7.0A**

+1.1V

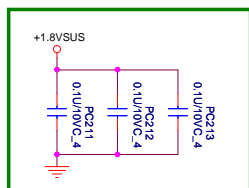
S0-S1

$$V_o = 0.75(R_1 + R_2) / R_2$$


SVC	SVD	Output
0	0	1.4
0	1	1.2
1	0	1.0
1	1	0.8



Add 0.1u CAP PC211, PC212, PC213 for EMI



+2.5V 3
+1.8VSUS 3,4,5,6,7,42,44,47

$I_{lim}(\text{Valley}) = 10\mu A \cdot R_{ILIM} / R_{DS_ON}$
For OCP set.

23.65A
S0-S3

Fix 1.8V Output
51116_VSFILT PR36
0.4

$R_a = (V_{out} - 0.75V) / 0.75 \cdot R_b$
Rb value from 100K to 300K ohm

+2.5V
0.25A
S0-S1

Close to CPU
SI power

$V_{out} = 1.25 \cdot (1 + R1/R2)$

Discrete: SI4856
UMA: SI4800

For EMI

+1.8V
10.4A
S0-S1

For EMI

+0.9VSMVT
2.25A
S0-S3

Mode	Discharge Mode
V5IN	No discharge
VDDQ	Tracking discharge
Gnd	Non-tracking discharge

$V_{TRIP}(mV) = R_{TRIP}(Kohm) \cdot 10(\mu A)$

$I_{OCP} = V_{trip} / R_{ds_on} + I_{Ripple} / 2$

VDDQSET	VDDQ(V)	VTTRREF and Vtt	Note
GND	2.5	$V_{_vddqsns} / 2$	DDR
V5IN	1.8	$V_{_vddqsns} / 2$	DDR2
FB	adjustable	$V_{VDDQSNS} / 2$	$1.5V < VDDQ < 3V$

PROJECT : QT8
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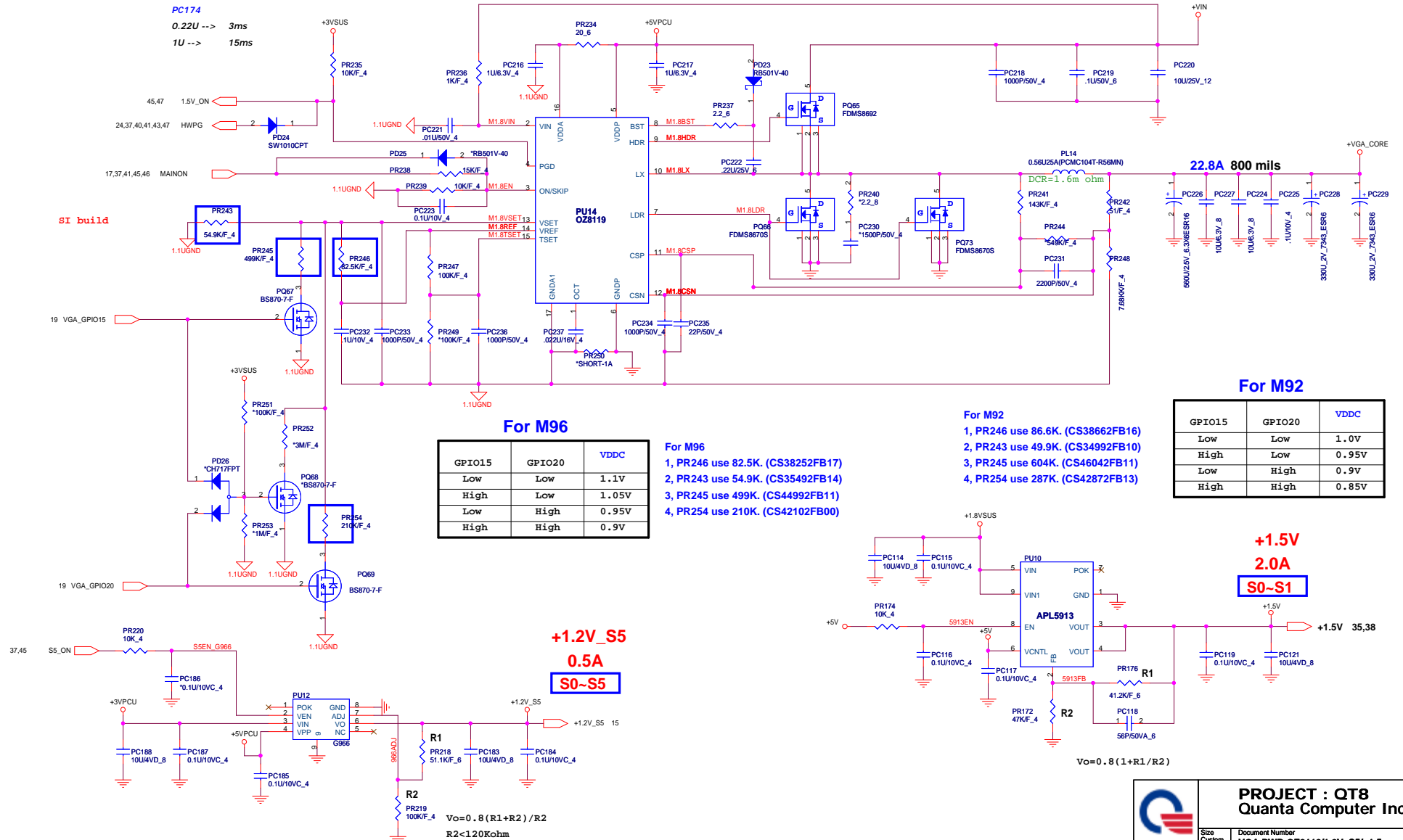
Size Custom	Document Number 1.8VSUS/DDR_VTER/+1.8V/2.5V	Rev 1A
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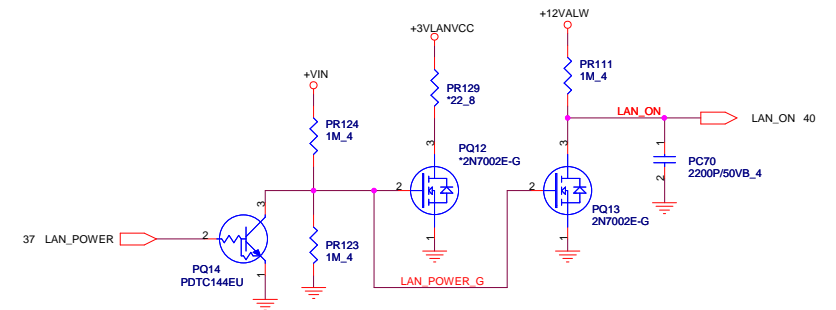
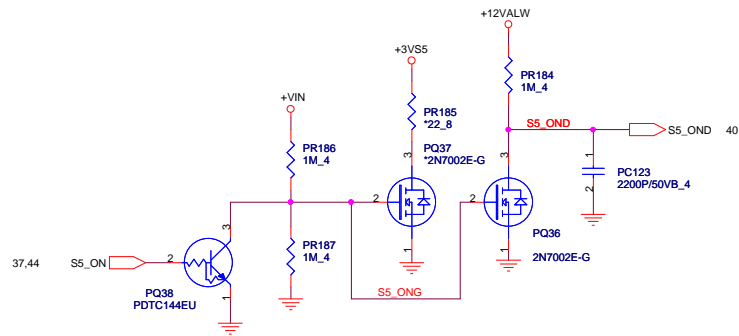
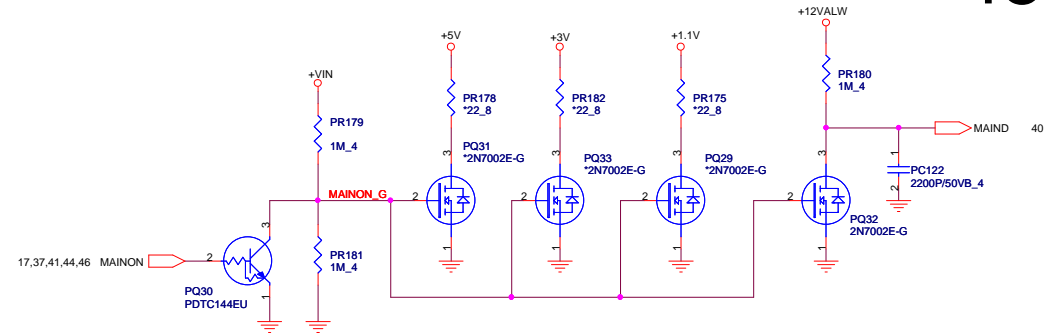
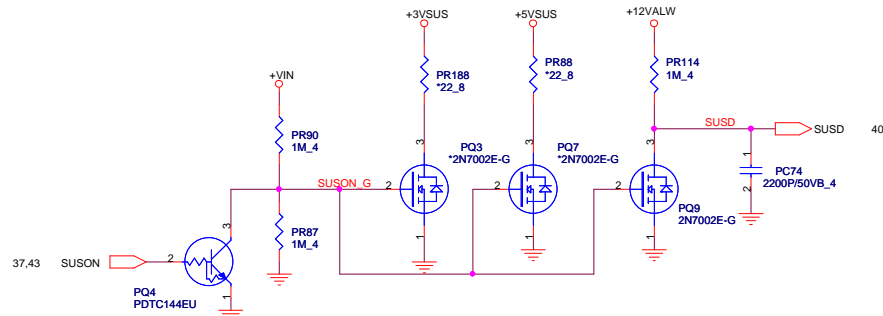
VGA Core & VCC1.1

<http://laptopblue.vn/>

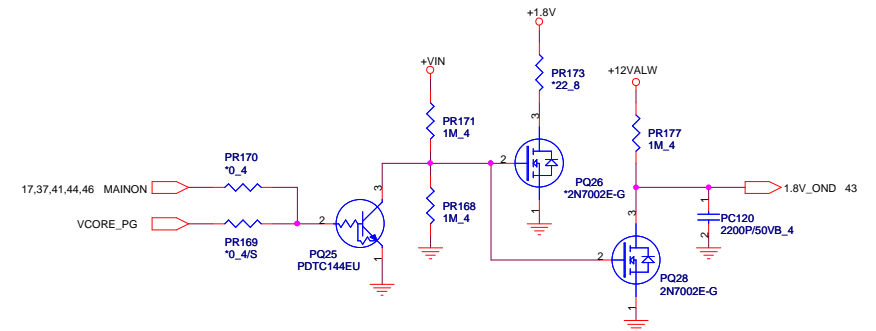
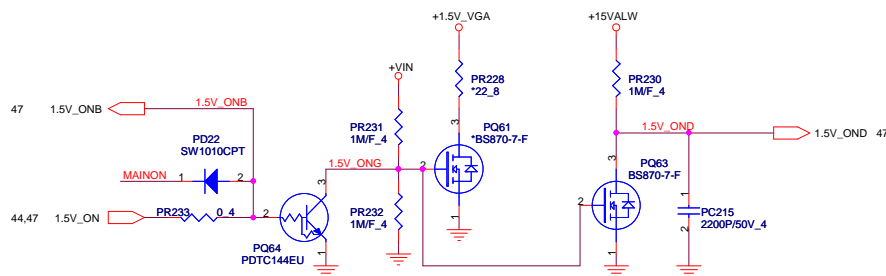
+1.1Volt +/- 5%
Countinue current:17.54A
Peak current:22.8A
OCP minimum 23A


44

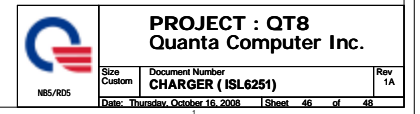


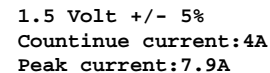



For Discrete Only

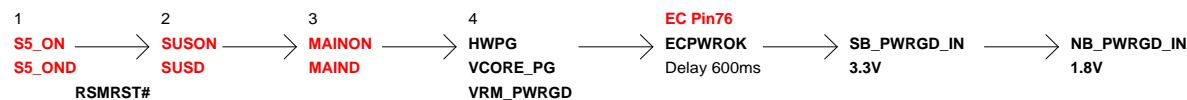
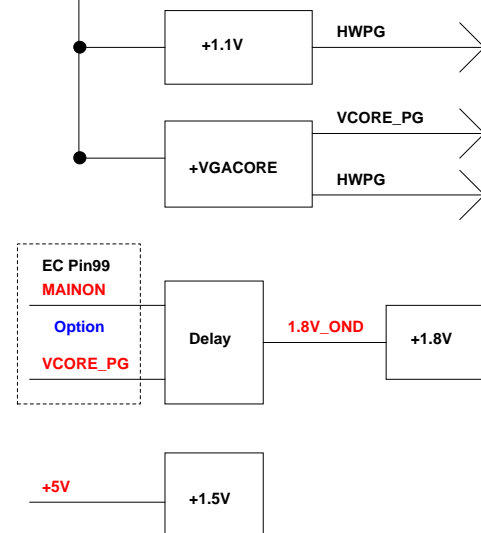
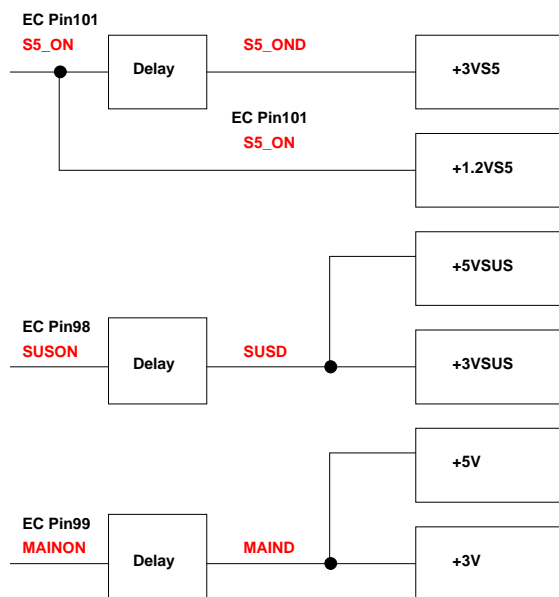
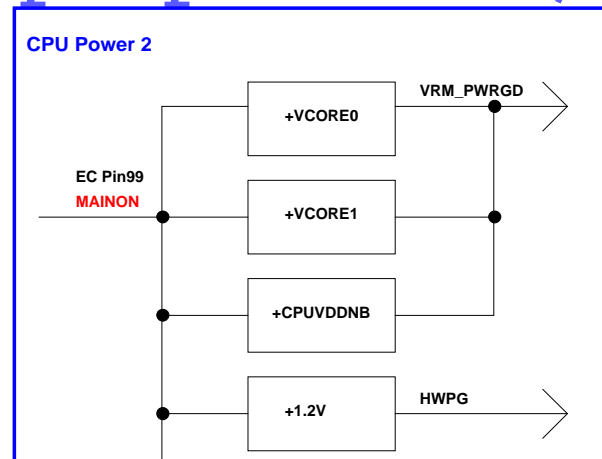
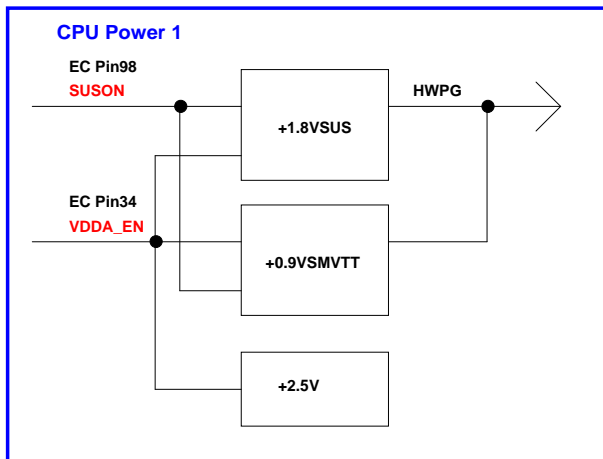


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	Quanta Computer Inc.		
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	Custom	DISCHARGE	1A
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