

http://laptopblue.vn/

EEE PC 1000HD PCB version

GPI29	GPI31	PCB version
0	0	1.0G
0	1	
1	0	
1	1	

USB

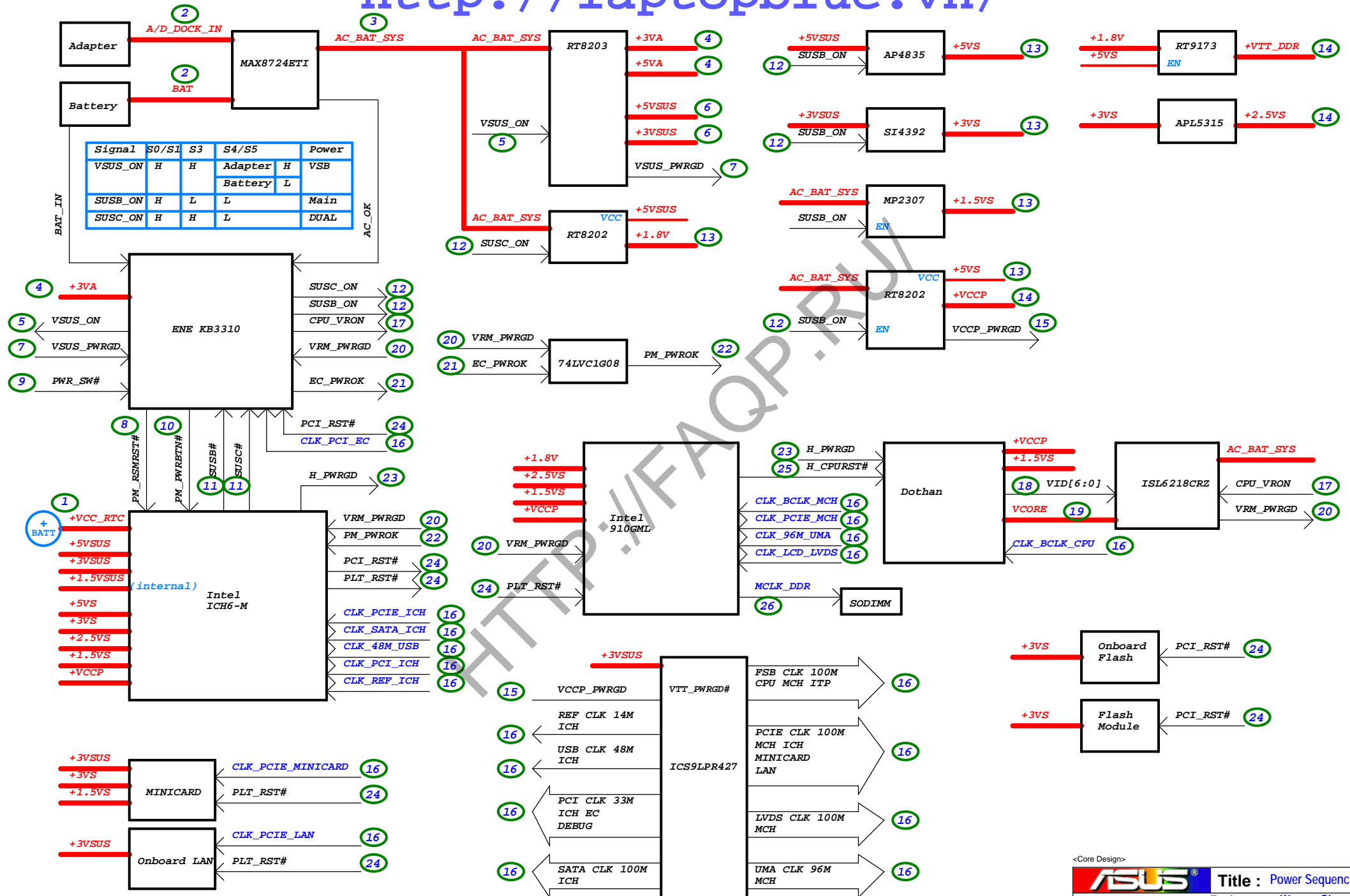
USB 0	Flash Conn
USB 1	USB Conn
USB 2	USB Conn
USB 3	USB Conn
USB 4	Card Reader
USB 5	Minicard
USB 6	BT
USB 7	Camera

PCIE

PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	Minicard

Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC



EC KB3310 GPIO SETTING

Pin	Pin Name	Signal Name	Type	Note
1	GPIO00/GA20	A20GATE	O	
2	GPIO01/KBRST#	RC_IN#	O	
6	GPIO04	HOTKEY_SW0#	I	Internal pull high
13	GPIO05/PCIRST#	PCI_RST#	I	
14	GPIO07	HOTKEY_SW1#	I	Internal pull high
15	GPIO08	EXTSMI#	OD	10K pull high to +3VSB
16	GPIO0A	LID_EC#	I	0-LidOff, 1-LidOn. Internal Pull Up
17	GPIO0B/ESB_CLK	NC	O	
18	GPIO0C/ESB_DAT	NC	O	
19	GPIO0D	HOTKEY_SW2#	I	Internal pull high
20	GPIO0E/SC#	EXT_SC#	O	10K pull high to +3VSB
21	GPIO0F/PWM0	BL_PWM_DA	O	
23	GPIO10/PWM1	BATSEL_4P#	I	battery charging current setting
25	GPIO11/PWM2	PM_PWRBTN#	OD	Internal pull high in ICH
26	GPIO12/FANPWM1	FAN0_PWM	O	CPU Fan
27	GPIO13/FANPWM2	FAN1_PWM	O	VGA Fan
28	GPIO14/FANFB1	FAN0_TACH	I	CPU FanTach
29	GPIO15/FANFB2	FAN1_TACH	I	VGA FanTach
30	GPIO16/E51_TX	E51_TX	O	RS232 debug port
31	GPIO17/E51_RX	E51_RX	I	RS232 debug port
32	GPIO18	PWR_SW#	I	power button, internal pull-up
34	GPIO19/PWM3	MAIL_LED#	O	
36	GPIO1A/NUMLED	NUM_LED#	O	
38	GPIO1D/CLKRUN#	CHG_LED_GREEN#	O	Green LED for charging
39	GPIO20/KSO0/TP_TEST	KSO0	O	
40	GPIO21/KSO1/TP_PLL	KSO1	O	
41	GPIO22/KSO2	KSO2	O	
42	GPIO23/KSO3	KSO3	O	
43	GPIO24/KSO4	KSO4	O	
44	GPIO25/KSO5	KSO5	O	
45	GPIO26/KSO6	KSO6	O	
46	GPIO27/KSO7	KSO7	O	
47	GPIO28/KSO8	KSO8	O	
48	GPIO29/KSO9	KSO9	O	
49	GPIO2A/KSO10	KSO10	O	
50	GPIO2B/KSO11	KSO11	O	
51	GPIO2C/KSO12	KSO12	O	
52	GPIO2D/KSO13	KSO13	O	
53	GPIO2E/KSO14	KSO14	O	
54	GPIO2F/KSO15	KSO15	O	
55	GPIO30/KSI0	KSI0	I	Internal pull high
56	GPIO31/KSI1	KSI1	I	Internal pull high
57	GPIO32/KSI2	KSI2	I	Internal pull high
58	GPIO33/KSI3	KSI3	I	Internal pull high
59	GPIO34/KSI4	KSI4	I	Internal pull high
60	GPIO35/KSI5	KSI5	I	Internal pull high
61	GPIO36/KSI6	KSI6	I	Internal pull high
62	GPIO37/KSI7	KSI7	I	Internal pull high
63	GPI38/AD0	BAT_ICHG	I	
64	GPI39/AD1	BAT_CONFIG	I	Battery configuration
65	GPIO3A/AD2	BAT_SENSE	I	Battery Voltage Sensor
66	GPIO3B/AD3	BAT_TS	I	Battery Thermal Sensor
68	GPO3C/DA0	DOC	O	Trigger Clock Gen

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EC KB3310 Other Pin SETTING

Pin	Pin Name	Signal Name	Type	Note
3	SERIRQ	INT_SERIRQ	I/OD	10K pull high to +3V
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	100K pull high to +3VA_EC
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#/SPIDI	SPI_SO	I	
120	WR#/SPIDO	SPI_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	V18R	P	Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#/SELMEM#	SPI_CE#	O	

Pin	Pin Name	Signal Name	Type	Note
70	GPO3D/DA1	LCD_BACKOFF#	O	
71	GPO3E/DA2	CLK_PWRSERVE#	O	
72	GPO3F/DA3	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	EC_RSMRST#	O	10K pull down to GND
75	GPI42	BAT_IN	I	
76	GPI43	CLRTC_EC	I	
77	GPIO44/SCL1	SMB0_CLK	I/OD	4.7K pull high to +3VA_EC
78	GPIO45/SDA1	SMB0_DAT	I/OD	4.7K pull high to +3VA_EC
79	GPIO46/SCL2	SMB1_CLK	I/OD	10K pull high to +3V
80	GPIO47/SDA2	SMB1_DAT	I/OD	10K pull high to +3V
81	GPIO48/KSO16	NC	I	for KB type detection
82	GPIO49/KSO17	NC	I	for KB type detection
83	GPIO4A/PSCLK1	NC	O	LCD_SCL
84	GPIO4B/PSDAT1	NC	O	LCD_SDA
85	GPIO4C/PSCLK2	NC	O	LCD_CSB
86	GPIO4D/PSDAT2	NC	O	LCD_VSYNC
87	GPIO4E/PSCLK3	TP_CLK	I/OD	10K pull high to +3V
88	GPIO4F/PSDAT3	TP_DAT	I/OD	10K pull high to +3V
89	GPIO50/SELIO#	BATSEL_3S	O	Battery series, H:3S, L:4S
90	GPIO52/E51_CS#	CHG_LED_UP#	O	
91	GPIO53/CAPLED	CAP_LED#	O	
92	GPIO54	PWR_LED_UP	O	
93	GPIO55/SCRLED	SCRLED_LED#	O	
95	GPIO56	HOTKEY_SW3#	I	Internal pull high
97	GPXOA00/SDICS#	SPI_MODE#	O	4.7K pull down to GND
98	GPXOA01/SDICLK	SUSC_ON	O	
99	GPXOA02/SDIDO	VSUS_ON	O	
100	GPXOA03	CPU_VRON	O	
101	GPXOA04	SUSB_ON	O	
102	GPXOA05	EC_PWROK	O	
103	GPXOA06	PM_LEVELDOWN#	O	
104	GPXOA07	CHG_EN#	O	Battery charging enabled
105	GPXOA08	PRECHG	O	
106	GPXOA09	SPI_WP#	O	
107	GPXOA10	OP_SD#	O	Audio OP
108	GPXOA11	BAT_LEARN	O	
109	GPXID0/SDIDI	BATSEL_2P#	O	Battery parallel, H:1P, L:2P-3P
110	GPXID1	CPU_LEVELDOWN#	O	
112	GPXID2	THRO_CPU	O	Active if CPU temperature over spec
114	GPXID3	PM_SUSB#	I	100K pull down to GND
115	GPXID4	PM_SUSC#	I	100K pull down to GND
116	GPXID5	VRM_PWRGD	I	Pull high to +3V
117	GPXID6	VSUS_PWRGD	I	
118	GPXID7	BATSEL_LiFe	O	
121	GPIO57	INTERNET#	I	Internal pull high
126	GPIO57/SPICLK	SPI_CLK	O	
127	GPIO59/TEST_CLK	NC	O	Internal pull high

<Core Design>

		Title : EC Pin Define	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size	Project Name	Rev	
A3	1000HD	1.3G	
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1.0G From 1000H 2008.3.31.2030 circuit

- 1.Change CPU to Dothan
- 2.Change NB to 910GML
- 3.Change SB to ICH6-M
- 4.VCORE control change to ISL6218CRZ

1.1G

- 1.Change Project name to 1000D
- 2.Support LiFe Battery
- 3.Add speaker connect

1.2G

- 1.Change Project name to 1000HD
- 2.Modify page 45.
- 3.Add and reserved CE1 CE5 CE6 for USB port
- 4.Modify schematic of LED
- 5.Remove Capacitor of Microphone from clock and data signal
- 6.Add page33 Hotkey de-bounced related schematic
- 7.Add page23 PERST#pull down 1M ohm

1.3G

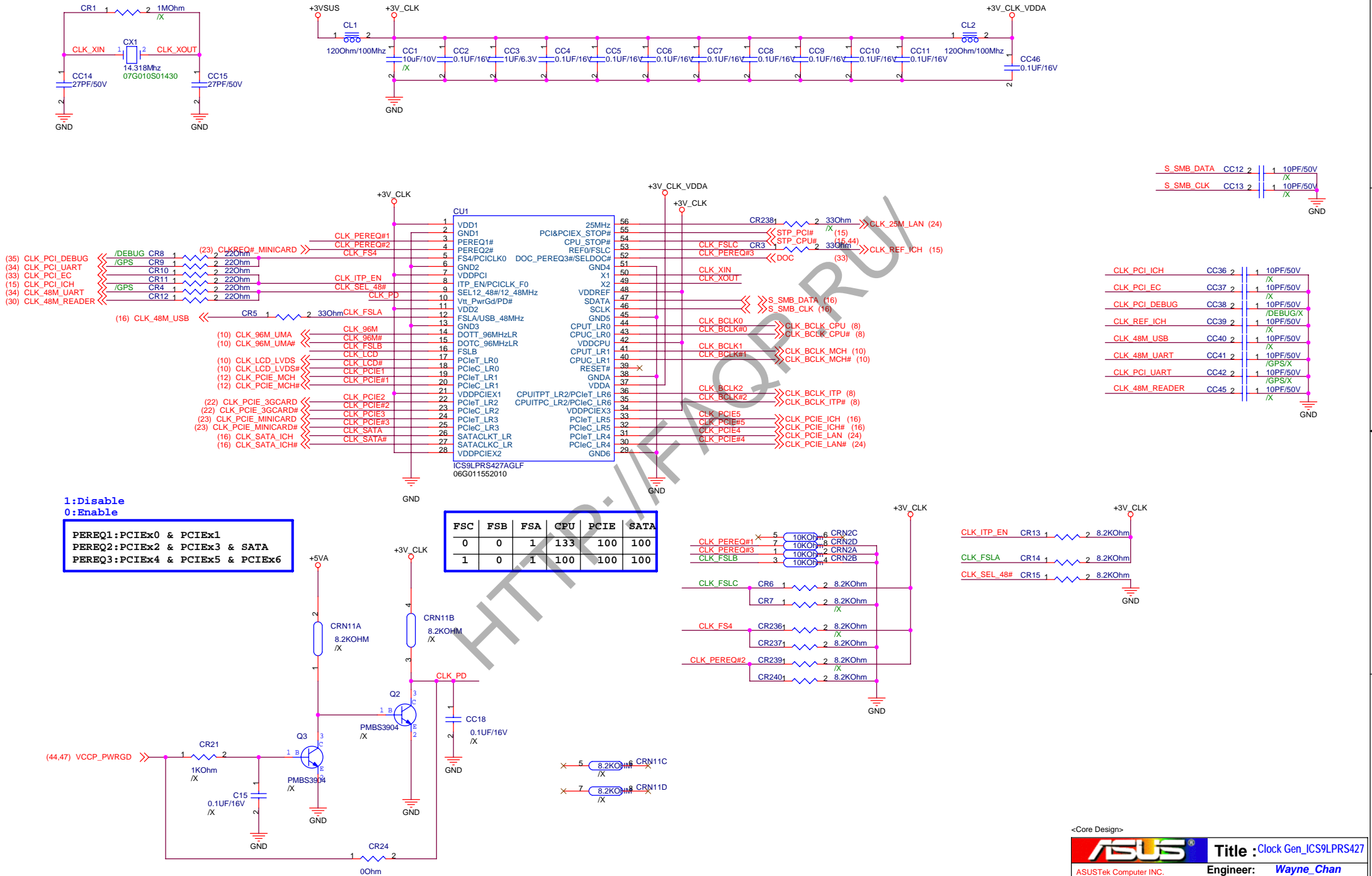
- 1.Change net BAT_TS pull up to +3VA_AEC
- 2.Change SD1.2 pull up to +3VA_AEC
- 3.Add and reserved R287,R288,R289,R290 for USB power
- 4.Add PR670,PR671
- 5.Add PR483,PR42, PR230, PR231,remove PJP400

<Core Design>

		Title : History	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size	Project Name		Rev
A3	1000HD		1.3G
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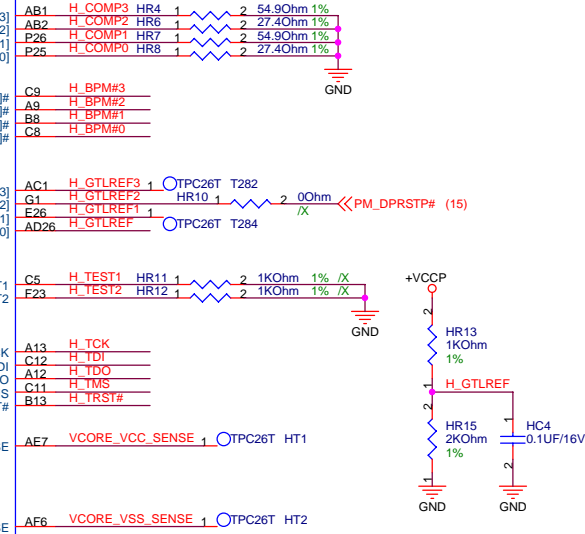
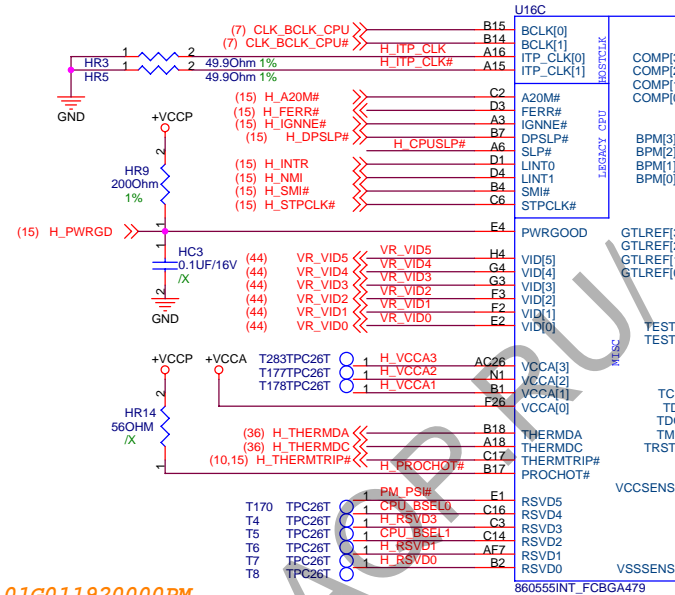
<http://laptopblue.vn/>

[HTTP://FAQP.RU/](http://faqp.ru/)

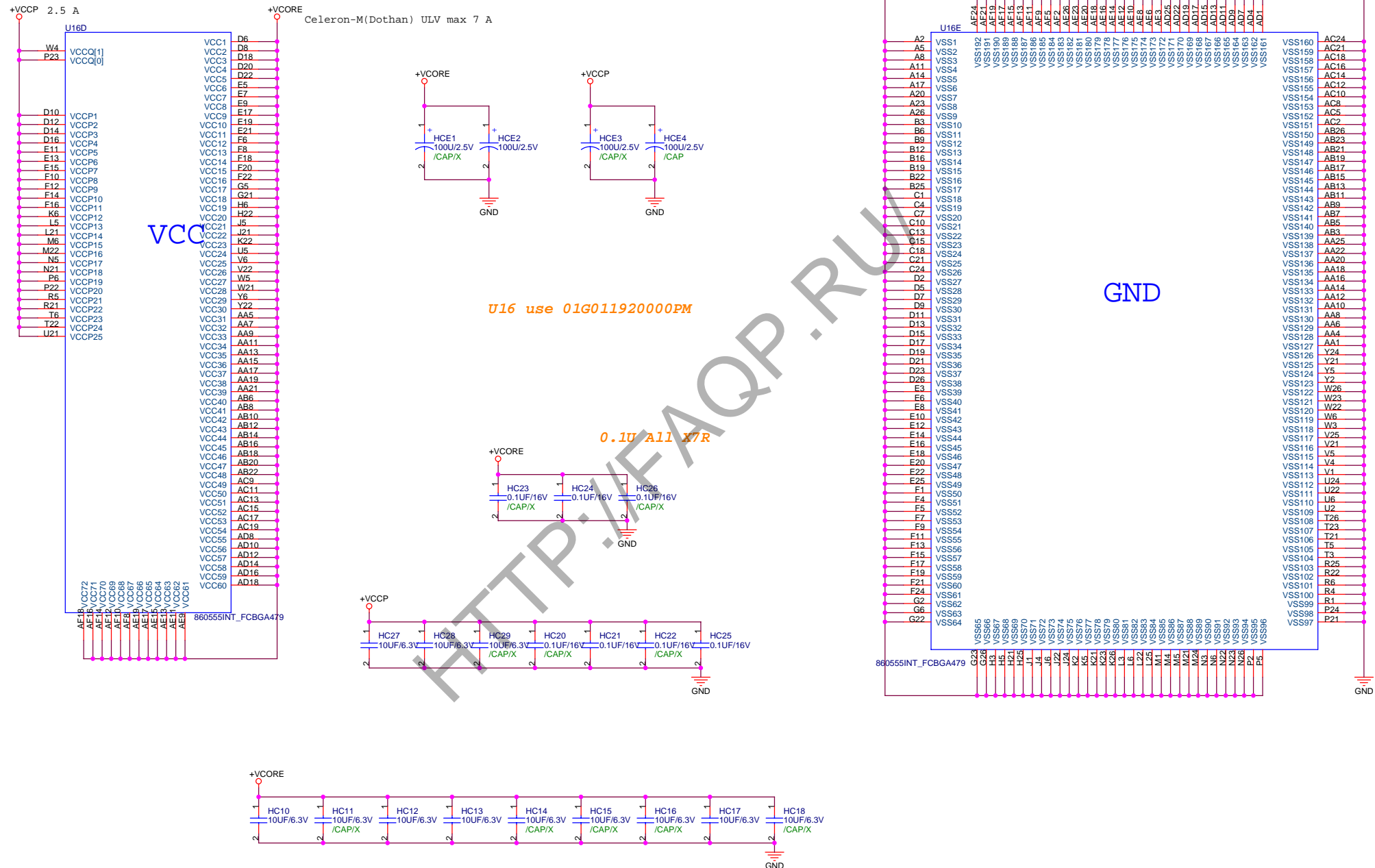




U16 use 01G011920000PM



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<Core Design>



Title : Dothan_PWR_GND

ASUSTek Computer INC.

Engineer: *Wayne_Chan*

Size	Project Name
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A3	1000HD
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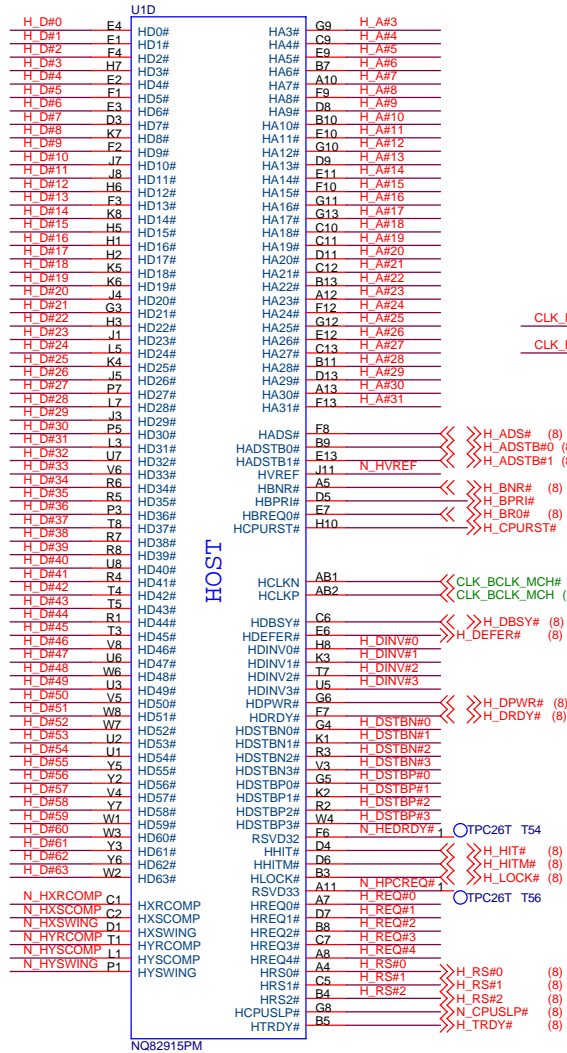
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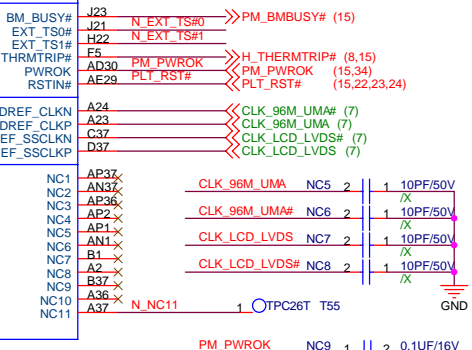
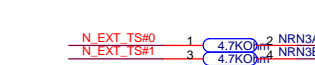
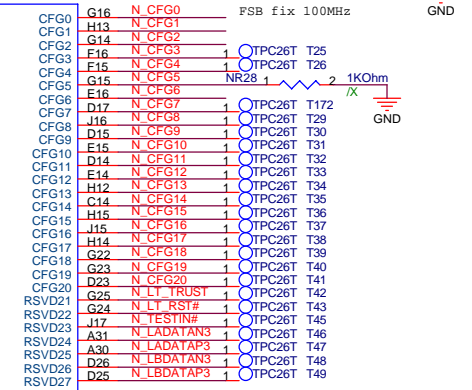
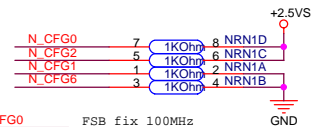
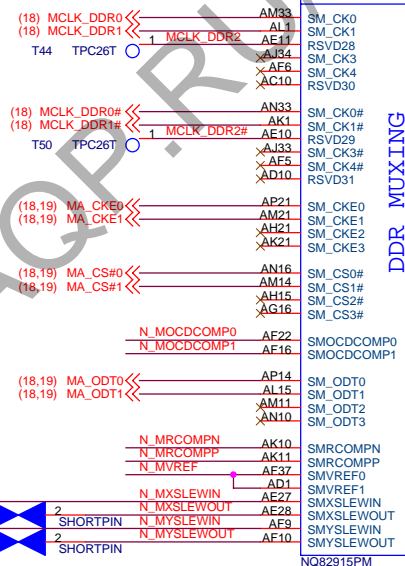
BCLK	FSB	BSEL2	BSEL1	BSEL0
100	400	H	L	H

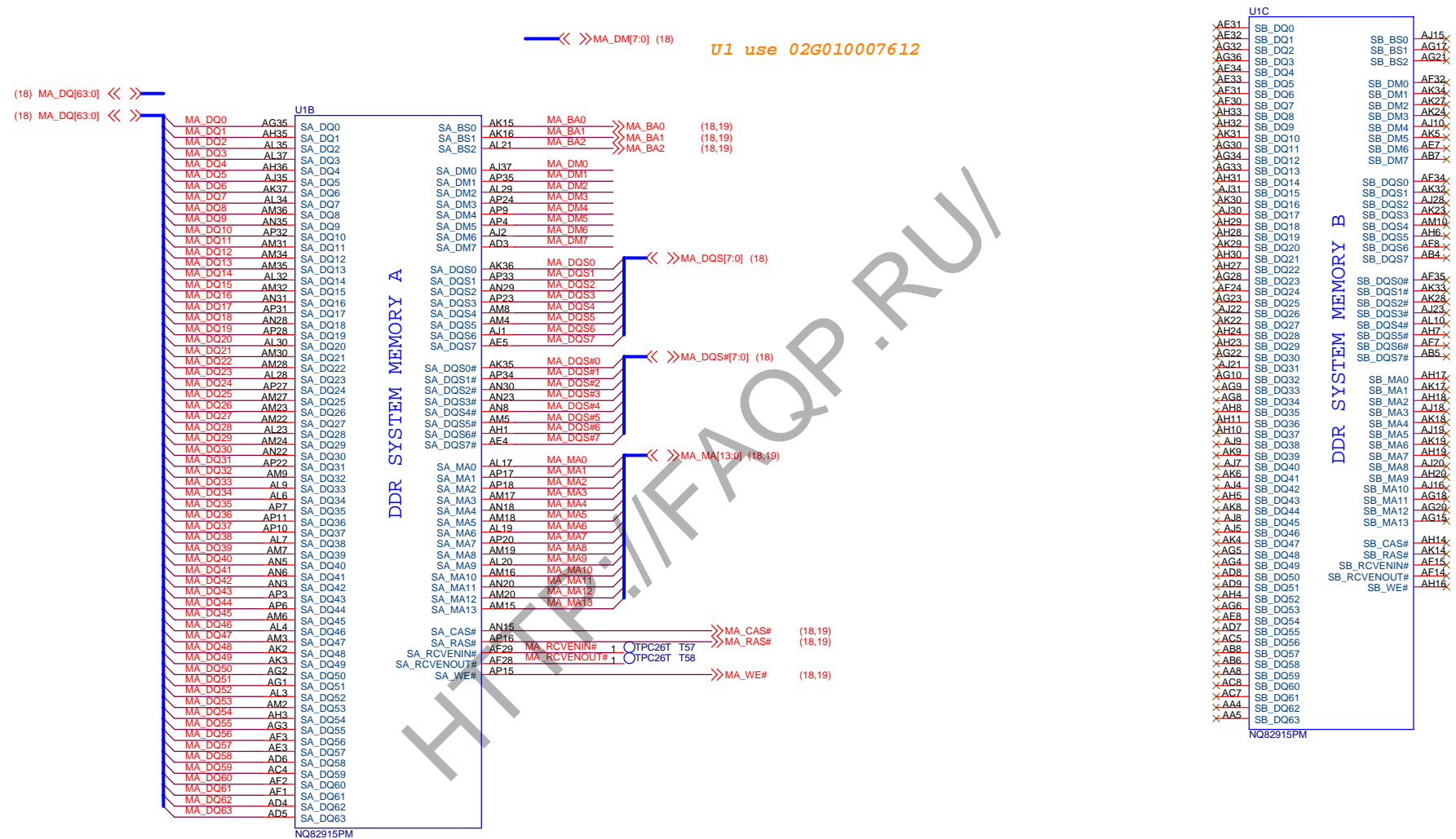
(8) H_D#63:0 << >> H_DSTBN#3:0 (8) << >> H_DINV#3:0 (8) << >> H_A#31:3 (8)
(8) H_D#63:0 << >> H_DSTBP#3:0 (8) << >> H_REQ#4:0 (8) << >> H_A#31:3 (8)

<< >> DML_TXN#3:0 (16)
<< >> DML_TXP#3:0 (16)
<< >> DML_RXN#3:0 (16)
<< >> DML_RXP#3:0 (16)

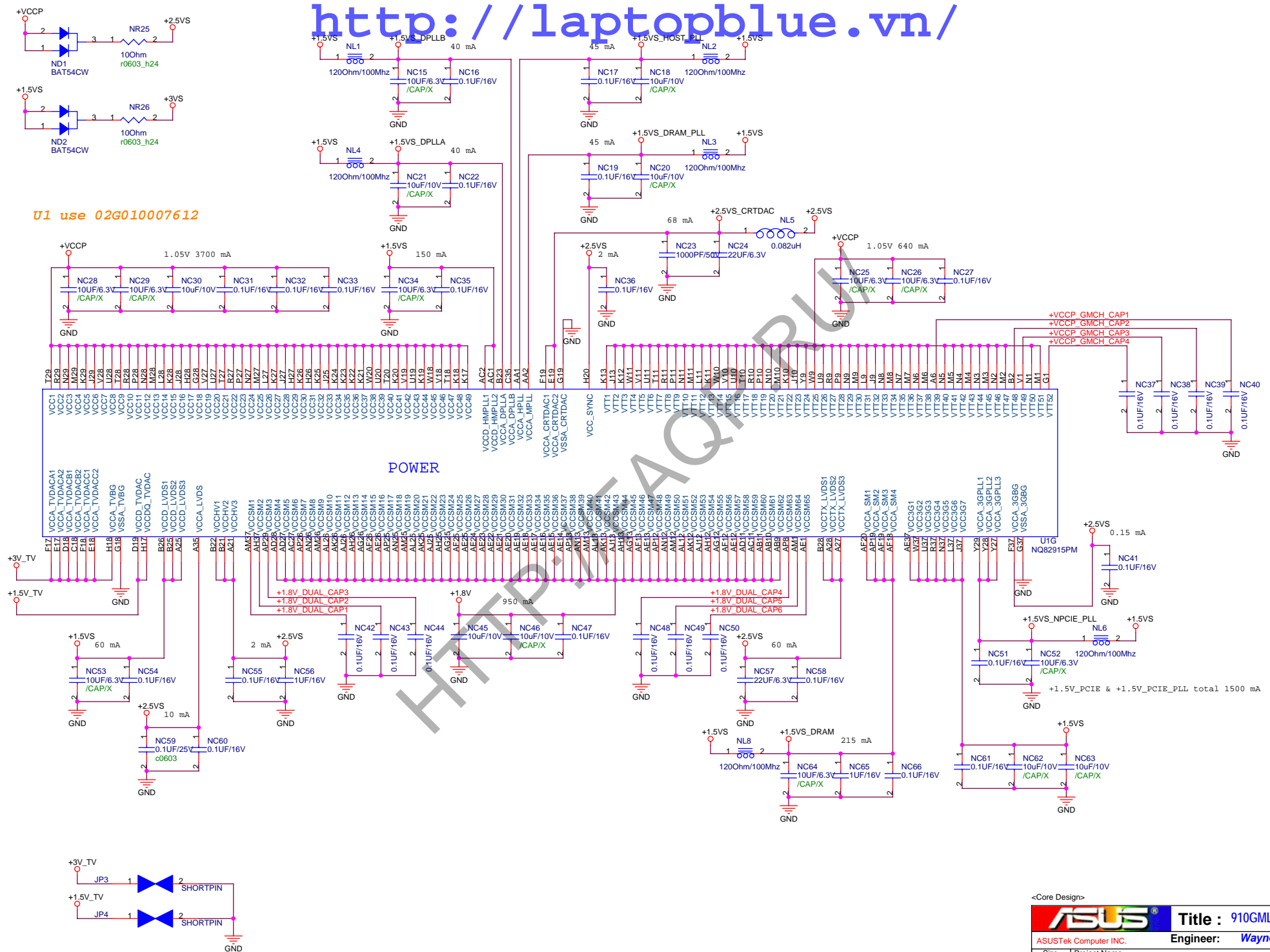


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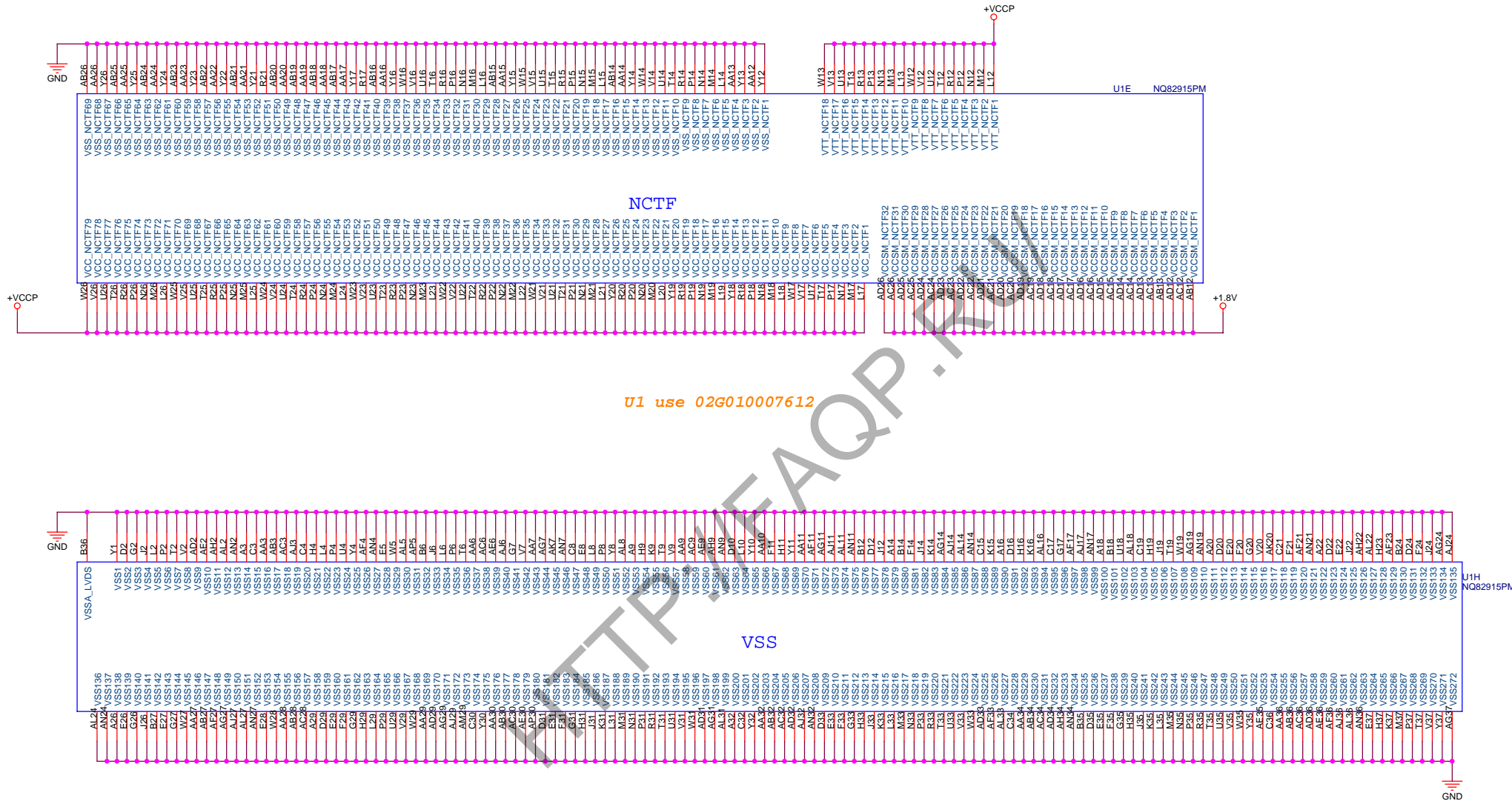




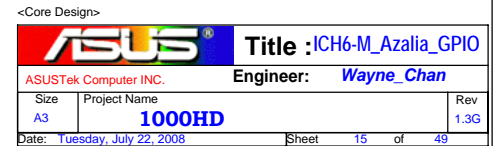
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Size A3	Project Name 1000HD	Rev 1.3G	
Date: Tuesday, July 22, 2008		Sheet 13 of 49	

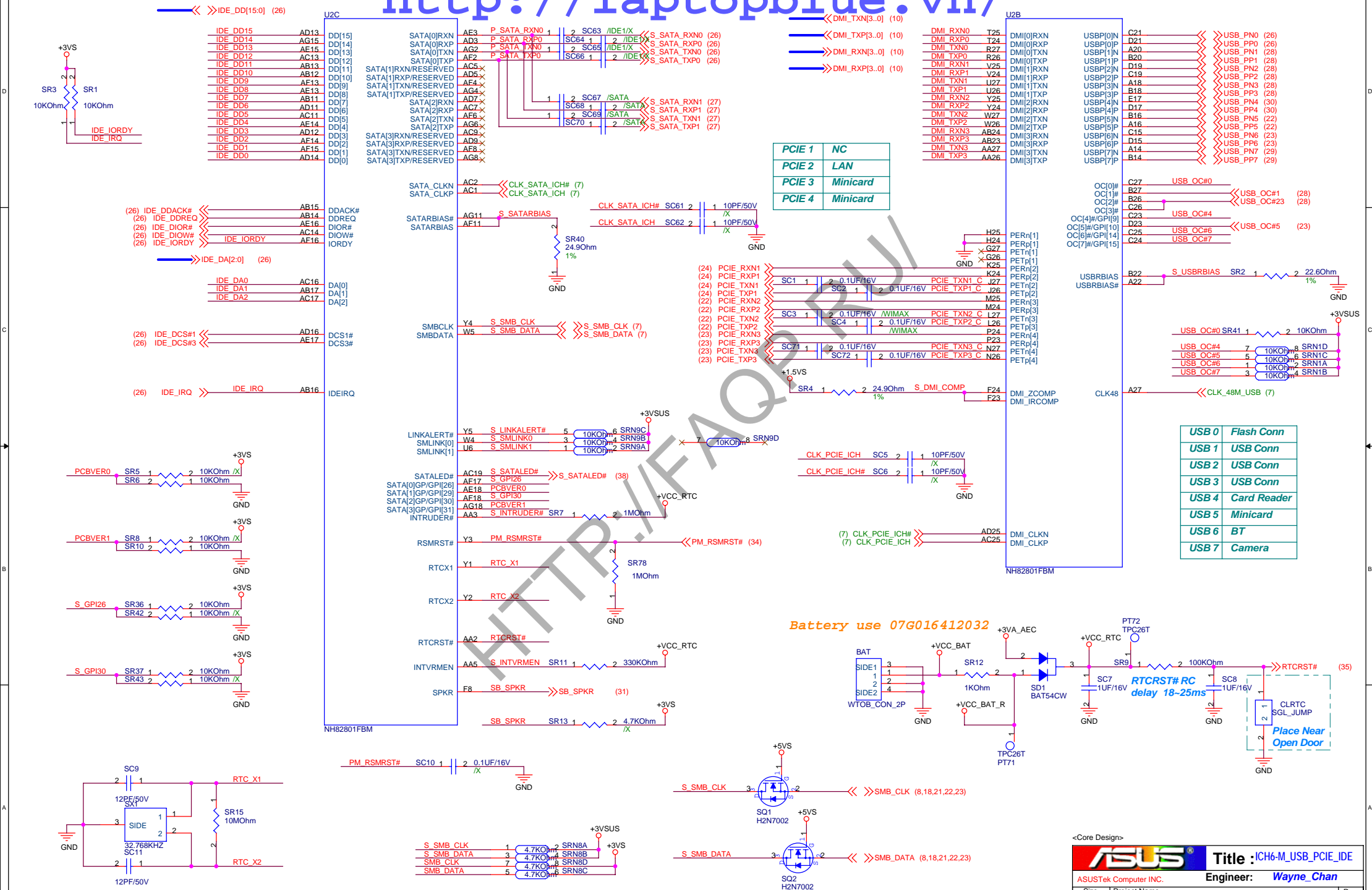


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<Core Design>





<Core Design>



Title : ICH6-M_PWR_GND

ASUSTek Computer INC.

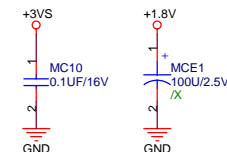
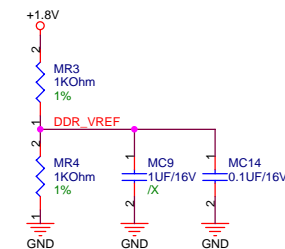
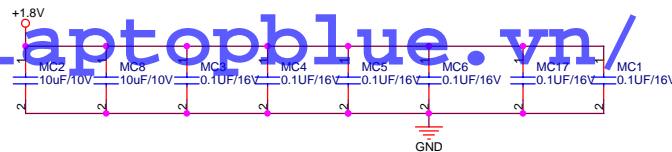
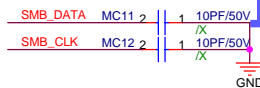
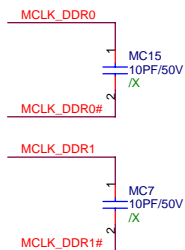
Engineer: *Wayne_Chan*

Size	Project Name
A3	1000HD

Rev
1.3G

Date: Tuesday, July 22, 2008

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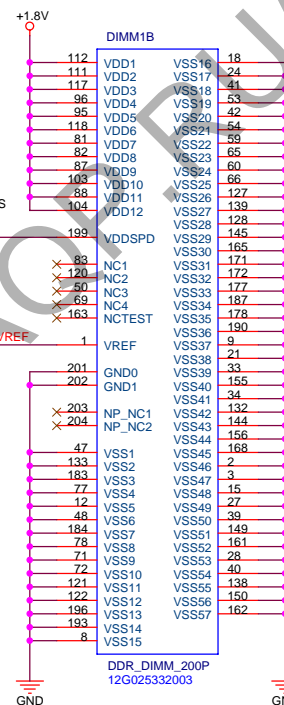


STD Type

DIMM1A			
MA_MA0	102	A0	DQ0
MA_MA1	101	A1	DQ1
MA_MA2	100	A2	DQ2
MA_MA3	99	A3	DQ3
MA_MA4	98	A4	DQ4
MA_MA5	97	A5	DQ5
MA_MA6	94	A6	DQ6
MA_MA7	92	A7	DQ7
MA_MA8	93	A8	MA_DQ8
MA_MA9	91	A9	MA_DQ9
MA_MA10	90	A10/AP	MA_DQ10
MA_MA11	89	A11	MA_DQ11
MA_MA12	88	A12	MA_DQ12
MA_MA13	86	A13	MA_DQ13
	85	A14	MA_DQ14
MA_BA2	84	A15	MA_DQ15
	85	A16_BA2	
MA_BA0	107	BA0	DQ16
MA_BA1	106	BA1	DQ17
	110	S0#	DQ18
	115	S1#	DQ19
(10,19) MA_CS#0	30	CK0	DQ20
(10,19) MA_CS#1	32	CK0#	DQ21
(10) MCLK_DDR0#	164	CK1	DQ22
(10) MCLK_DDR1#	166	CK1#	DQ23
(10,19) MA_CKE0	79	CKE0	MA_DQ24
(10,19) MA_CKE1	80	CKE1	MA_DQ25
(11,19) MA_CAS#	113	CAS#	MA_DQ26
(11,19) MA_RAS#	108	RAS#	MA_DQ27
(11,19) MA_WE#	109	WE#	MA_DQ28
	198	SA0	MA_DQ29
	200	SA1	MA_DQ30
(8,16,21,22,23) SMB_CLK	197	SCL	MA_DQ31
(8,16,21,22,23) SMB_DATA	195	SDA	MA_DQ32
	114	ODT0	MA_DQ33
(10,19) MA_ODT0	119	ODT1	MA_DQ34
(10,19) MA_ODT1			MA_DQ35
MA_DM0	10	DM0	MA_DQ36
MA_DM1	26	DM1	MA_DQ37
MA_DM2	52	DM2	MA_DQ38
MA_DM3	67	DM3	MA_DQ39
MA_DM4	130	DM4	MA_DQ40
MA_DM5	147	DM5	MA_DQ41
MA_DM6	170	DM6	MA_DQ42
MA_DM7	185	DM7	MA_DQ43
MA_DQS0	13	DQS0	MA_DQ44
MA_DQS1	31	DQS1	MA_DQ45
MA_DQS2	51	DQS2	MA_DQ46
MA_DQS3	70	DQS3	MA_DQ47
MA_DQS4	131	DQS4	MA_DQ48
MA_DQS5	148	DQS5	MA_DQ49
MA_DQS6	169	DQS6	MA_DQ50
MA_DQS7	188	DQS7	MA_DQ51
MA_DQS#0	11	DQS#0	MA_DQ52
MA_DQS#1	29	DQS#1	MA_DQ53
MA_DQS#2	49	DQS#2	MA_DQ54
MA_DQS#3	68	DQS#3	MA_DQ55
MA_DQS#4	129	DQS#4	MA_DQ56
MA_DQS#5	146	DQS#5	MA_DQ57
MA_DQS#6	167	DQS#6	MA_DQ58
MA_DQS#7	186	DQS#7	MA_DQ59

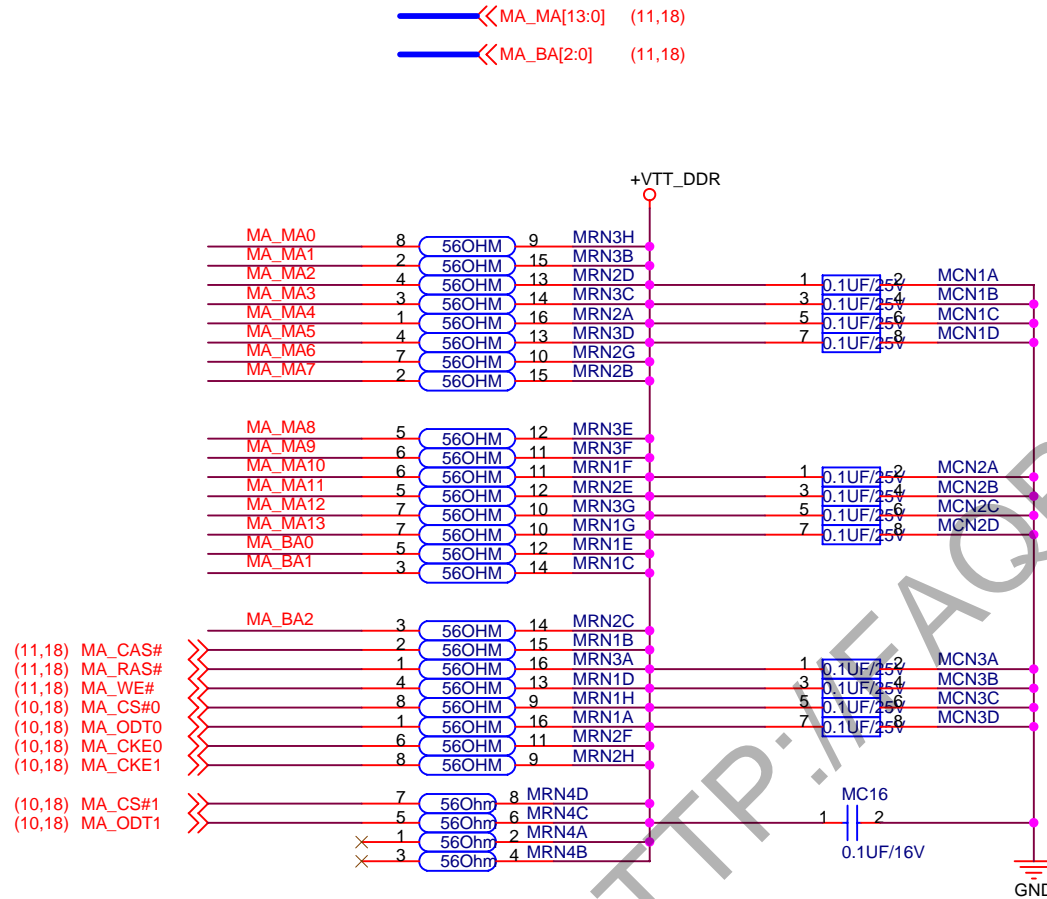
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12G025332003

GROUP1
GROUP2
SWAP



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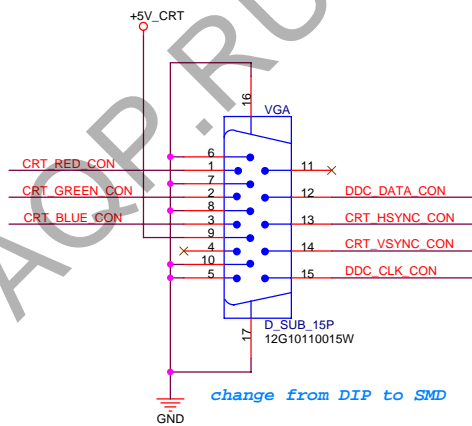
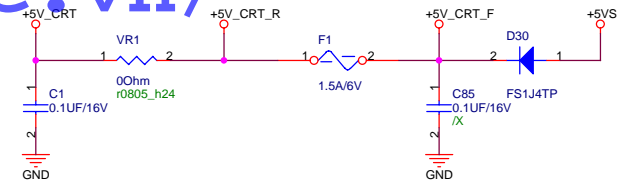
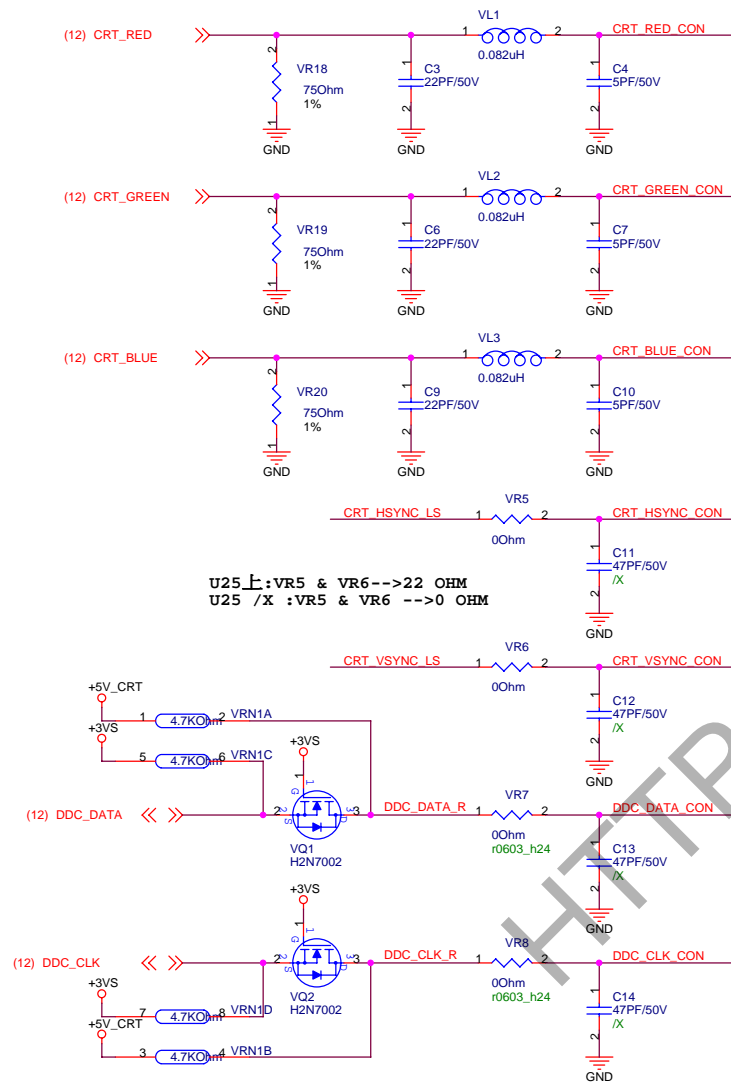
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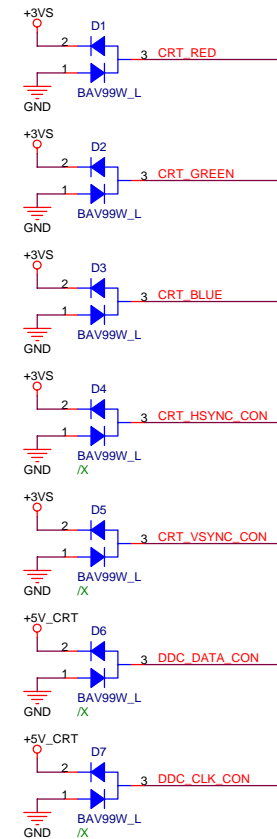
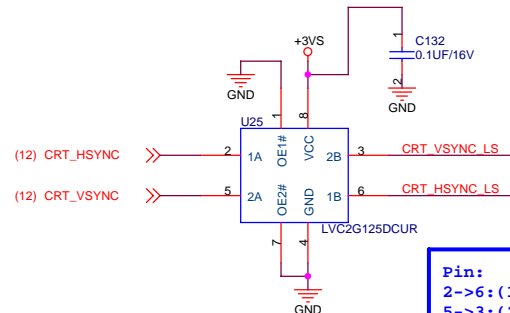
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ASUS®		Title : DDR2_Termination	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A4	Project Name 1000HD		Rev 1.3G
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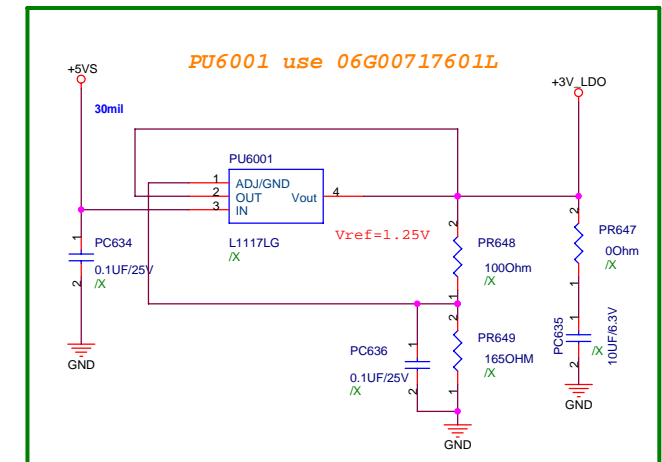
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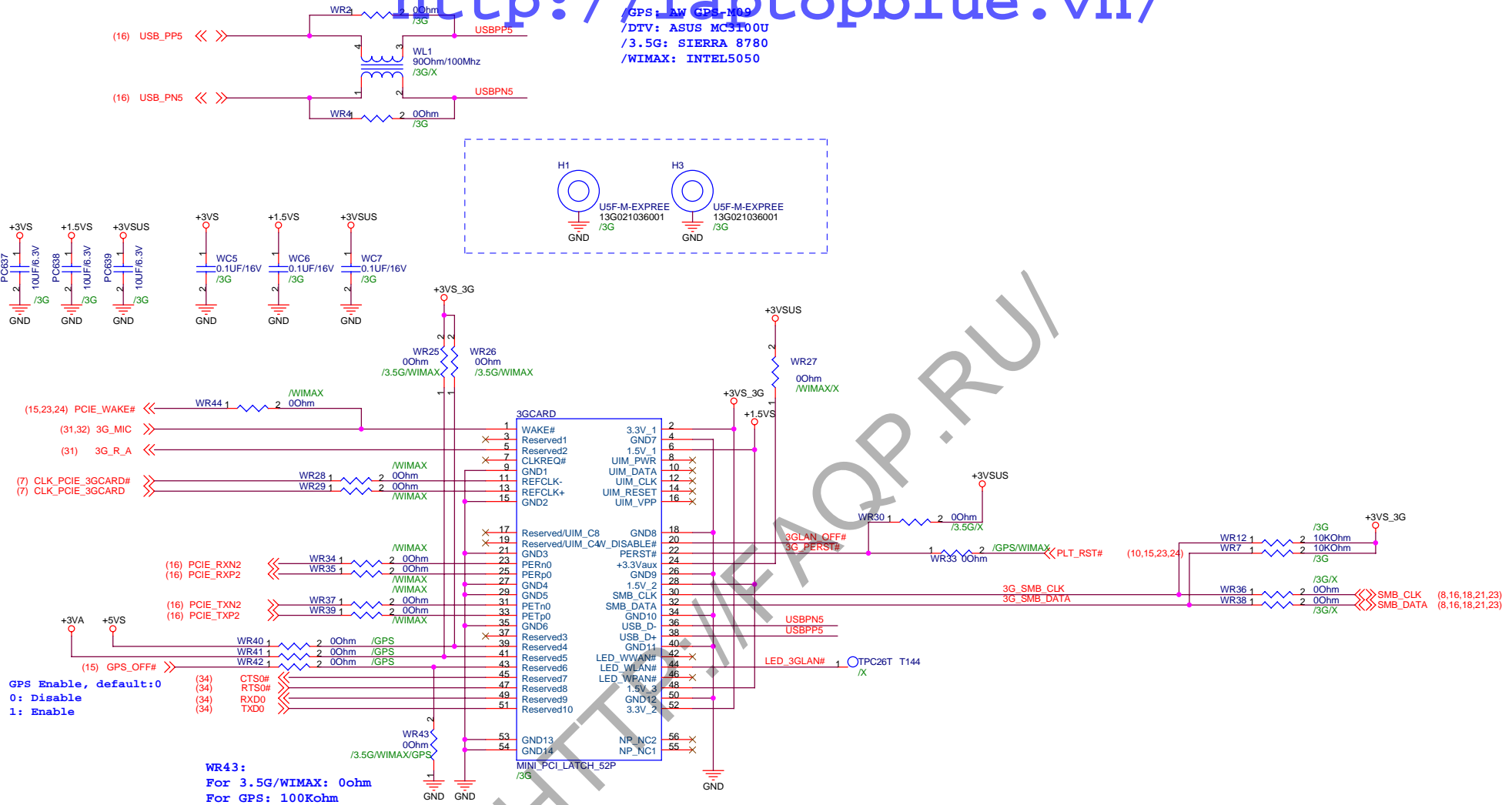
VGA use 12G10110015W & 12G10110015N



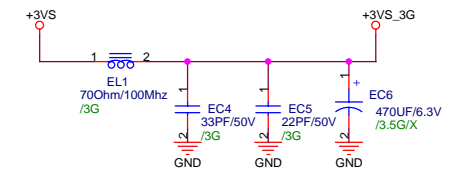
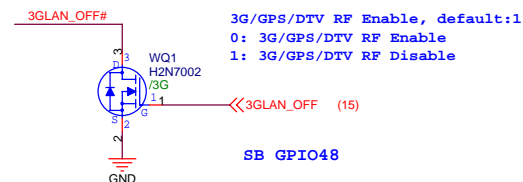
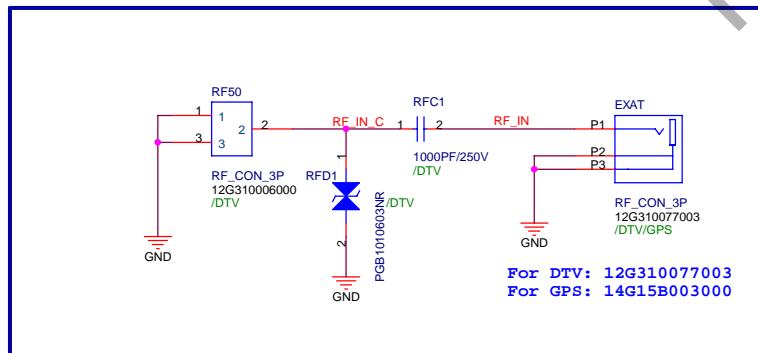
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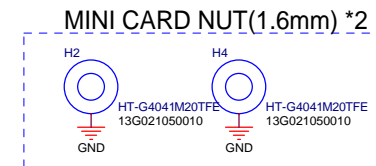
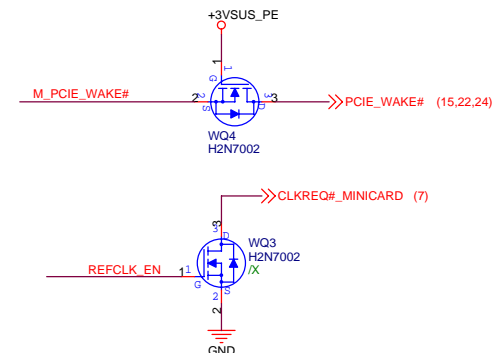
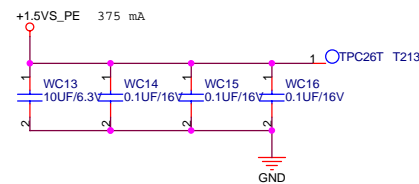
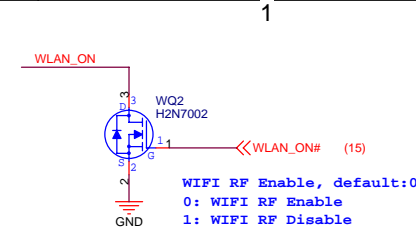
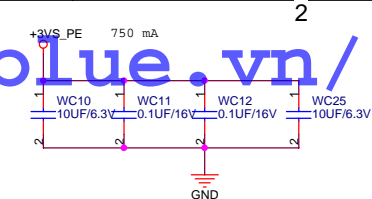
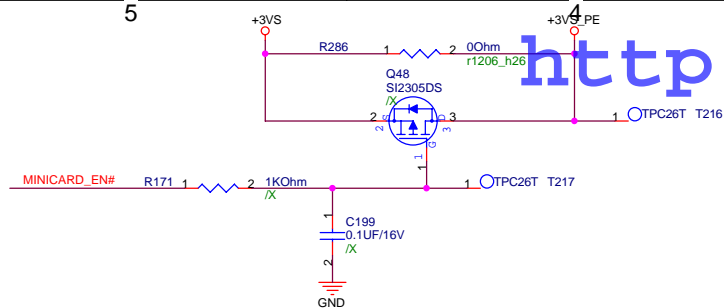


GPS: AW GPS-M09
/DTV: ASUS MC5100U
/3.5G: SIERRA 8780
/WIMAX: INTEL5050

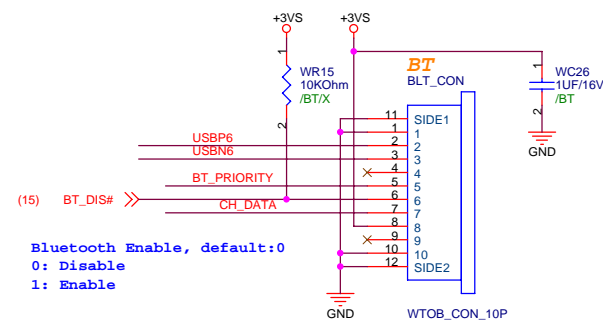


External Antenna





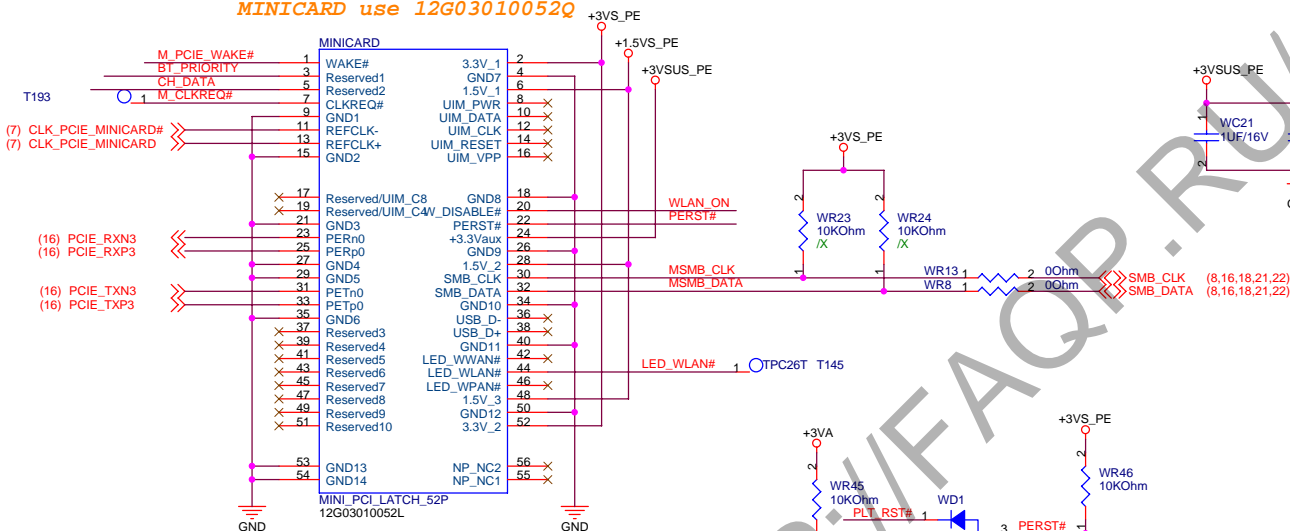
BlueTooth



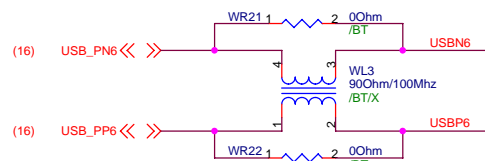
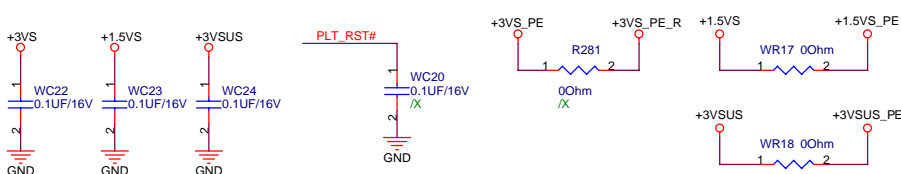
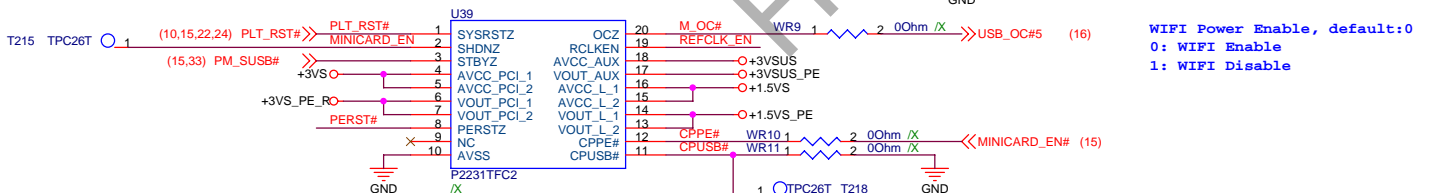
Bluetooth Enable, default:0
0: Disable
1: Enable

WIFI Power Enable, default:0
0: WIFI Enable
1: WIFI Disable

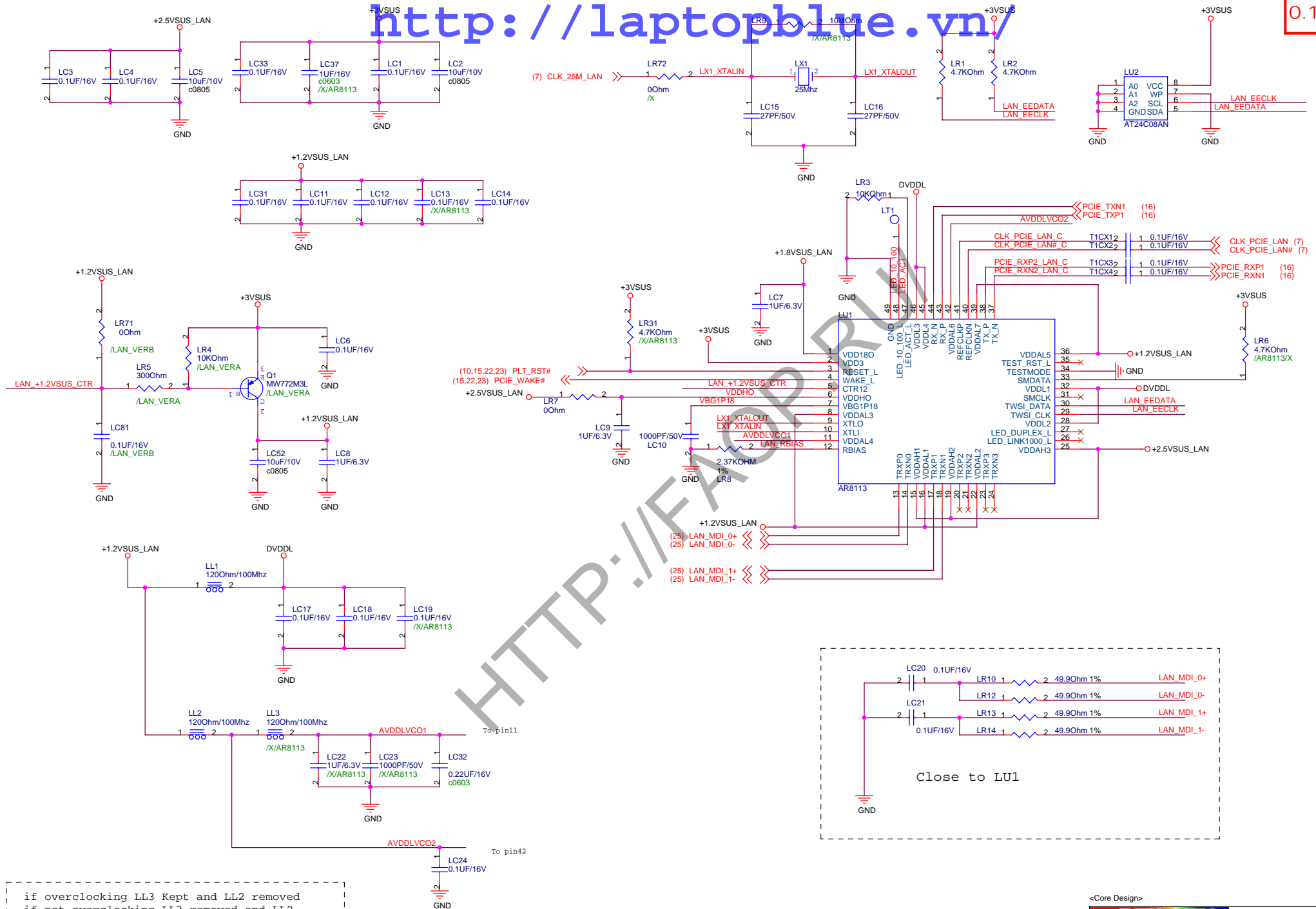
MINICARD use 12G03010052Q



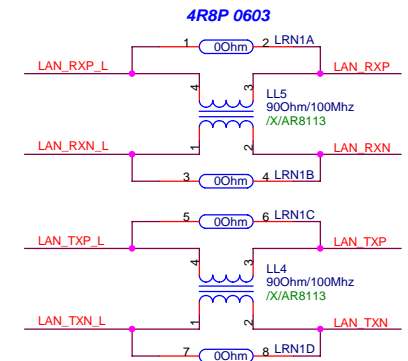
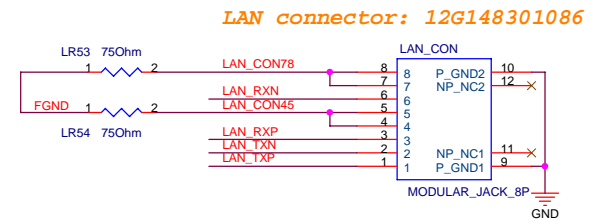
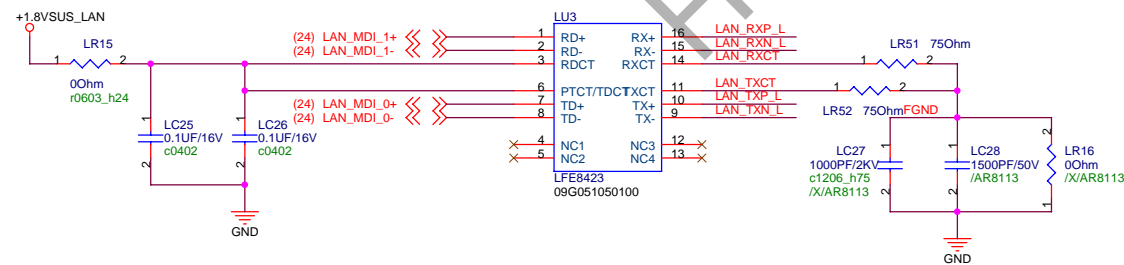
U39 use 06G030057011

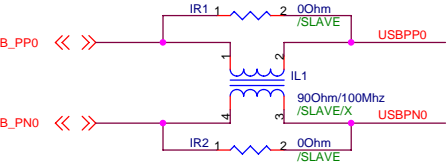
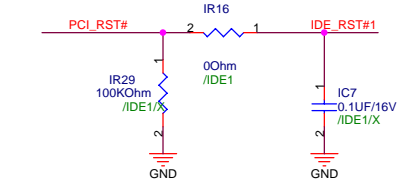
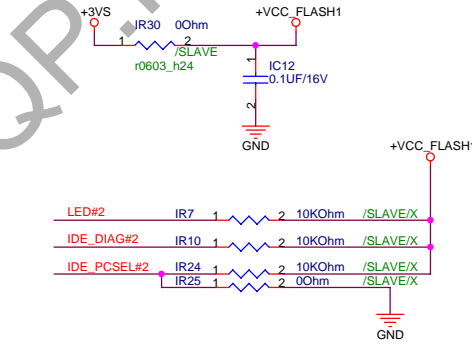
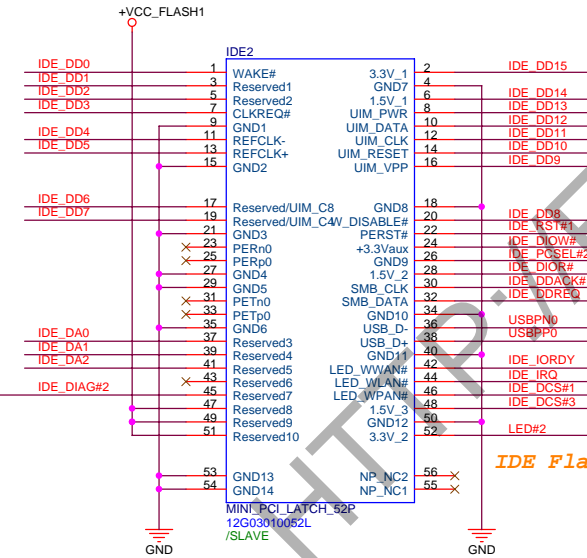
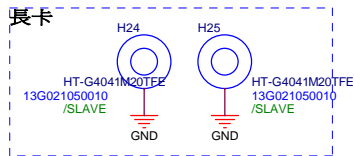
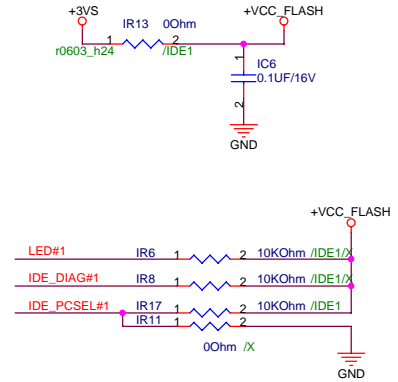
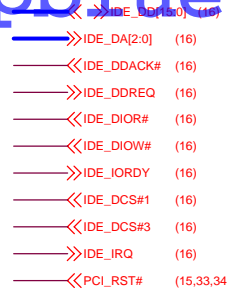
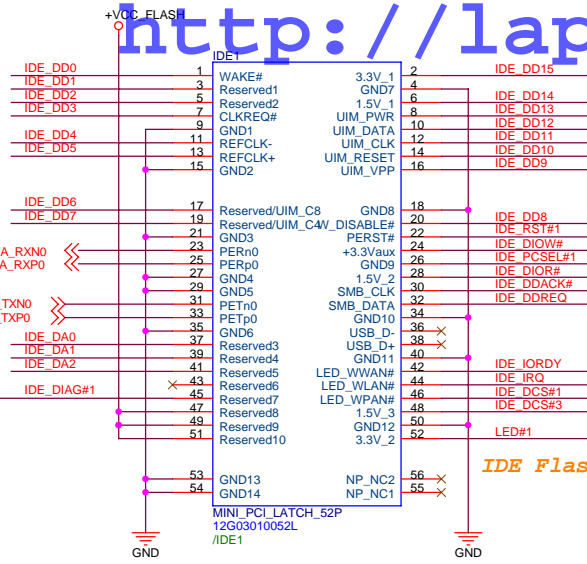
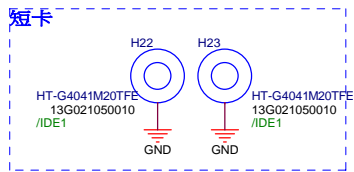


<Core Design>



if overclocking LL3 Kept and LL2 removed
if not overclocking LL3 removed and LL2
Kept

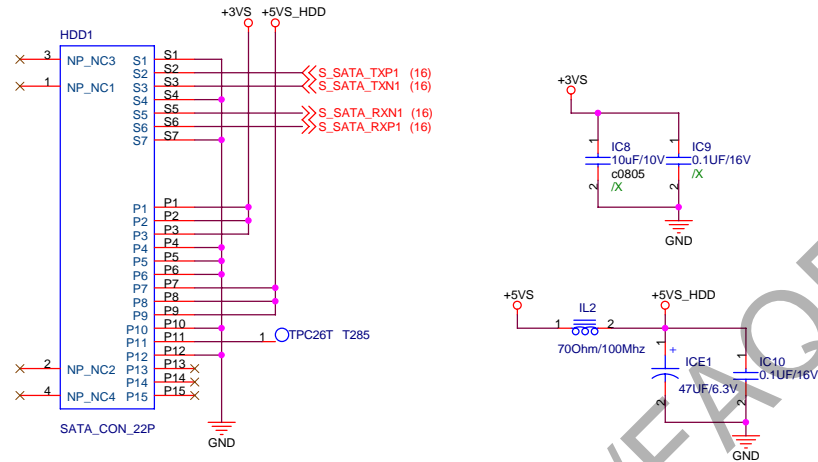




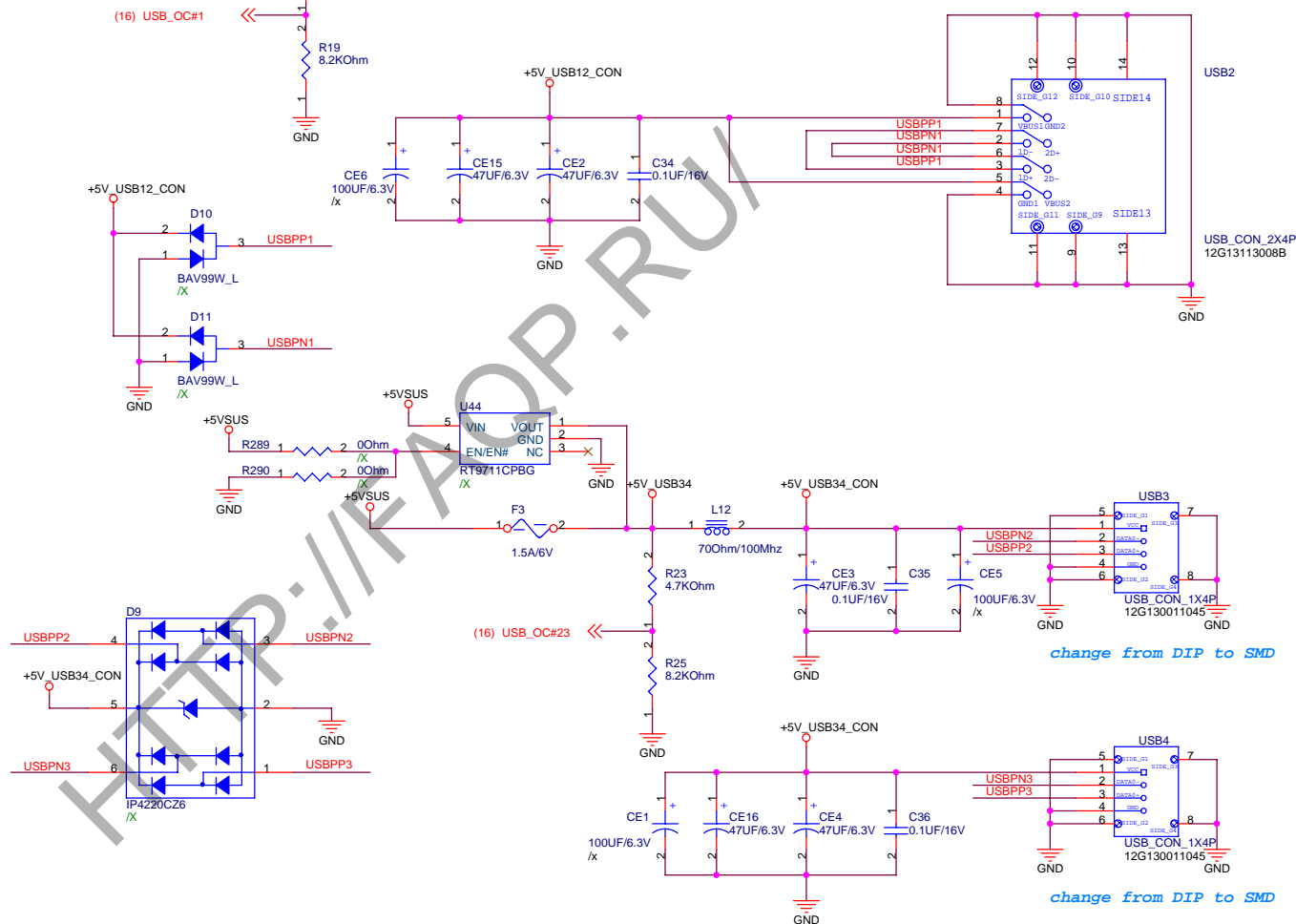
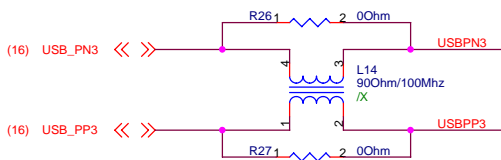
Naming Rule:
IC: IU?
R: IR?
C: IC?
L: IL?

Change ODD to
SATA IF

SATA HDD Connector

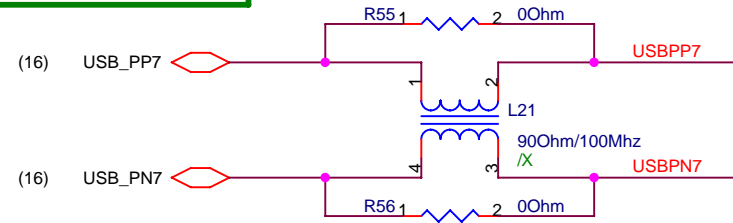
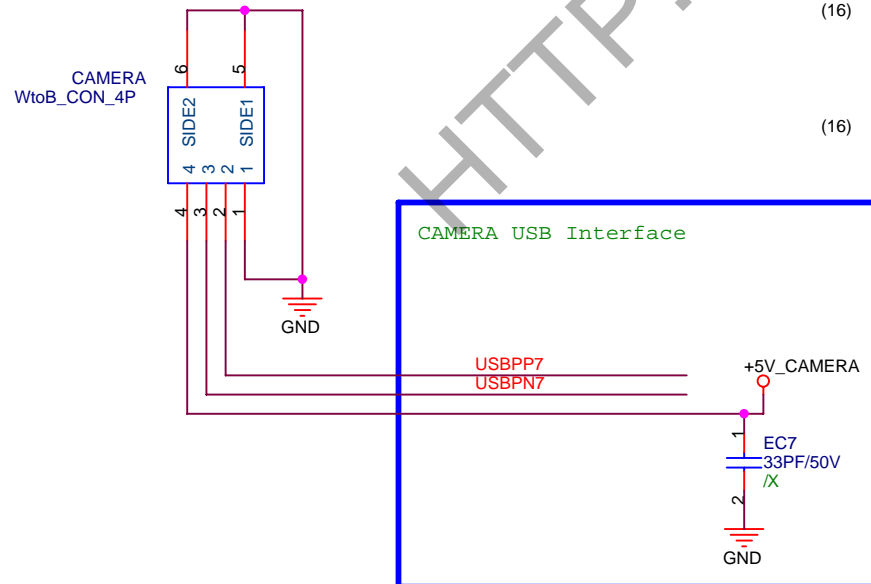
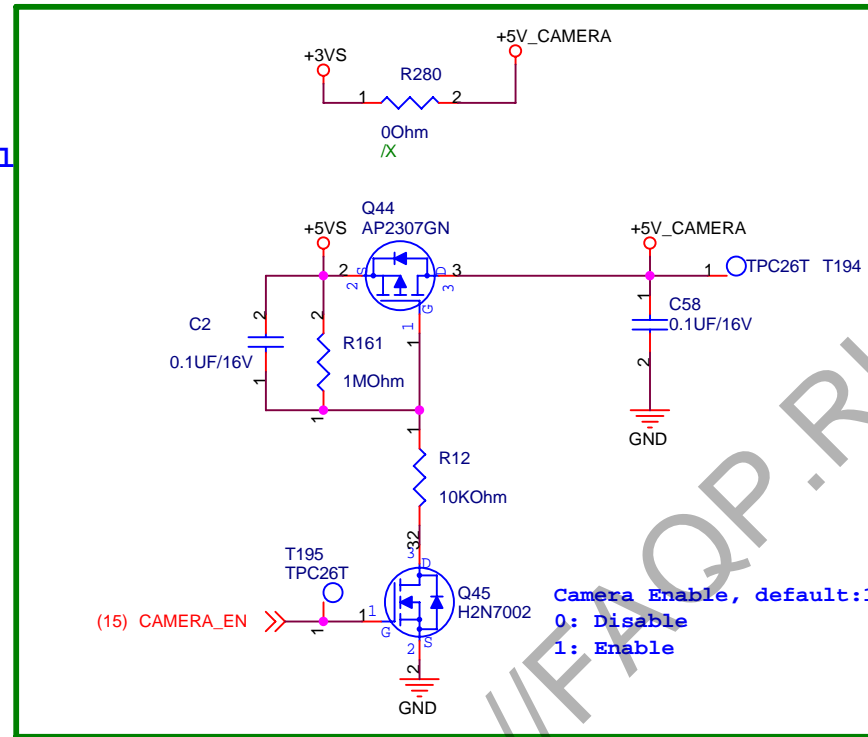


<Core Design>

[illegible]

		Title : USB Port	
ASUSTek Computer INC.		Engineer: <u>Wayne_Chan</u>	
Size A3	Project Name 1000HD	Rev 1.3G	
Date: <u>Tuesday, July 22, 2008</u>		Sheet <u>28</u> of <u>49</u>	

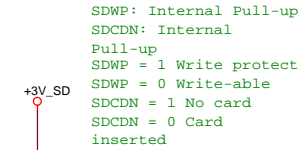
Power Control



<Core Design>

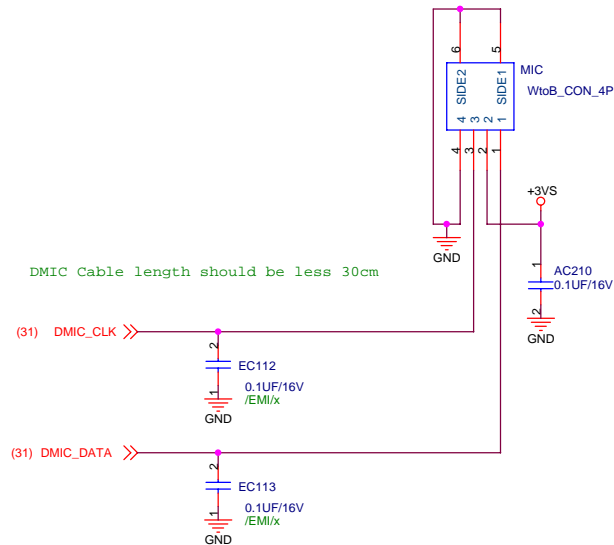
		Title : Camera Power	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A4	Project Name 1000HD		Rev 1.3G
Date: Tuesday, July 22, 2008		Sheet 29 of 49	

```
CLKSEL:
B52: Internal pull-down
C52: Internal pull-up
```

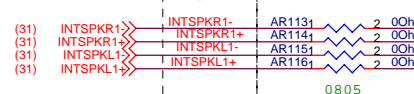


Card Insert: Pin.10 and Pin.12 are Shorted.
Card not Insert: Pin.10 and Pin.12 are Opened.
Write Protect: Pin.11 and Pin.12 are Opened.
Write Enable: Pin.11 and Pin.12 are Shorted.

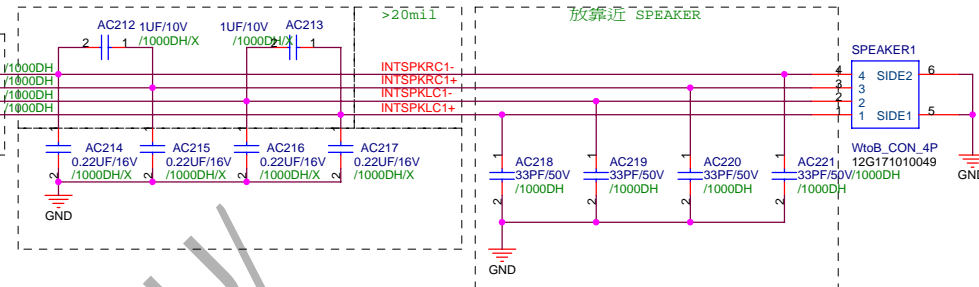
```
Card Reader Enable, default:0
0: Enable
1: Disable
```

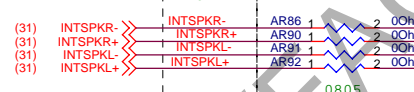
Total length from speakerR+- L+-(pin40 41 44 45) to internal speaker please as short as possible(<20cm is better) >20mil



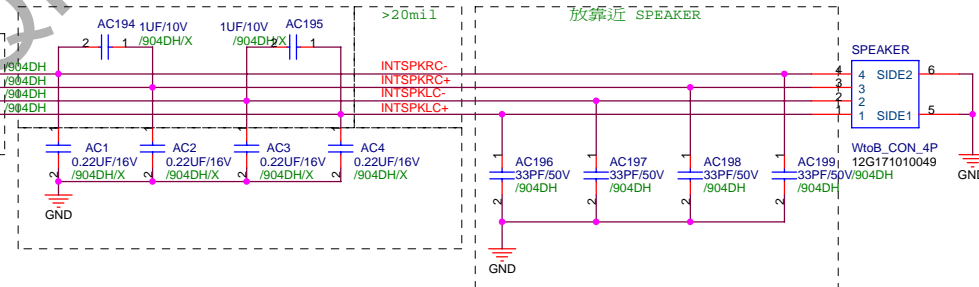
If choke AR113, AR114, AR115, AR116 are mounted, please mount AC212 AC213 to avoid EMI issue.



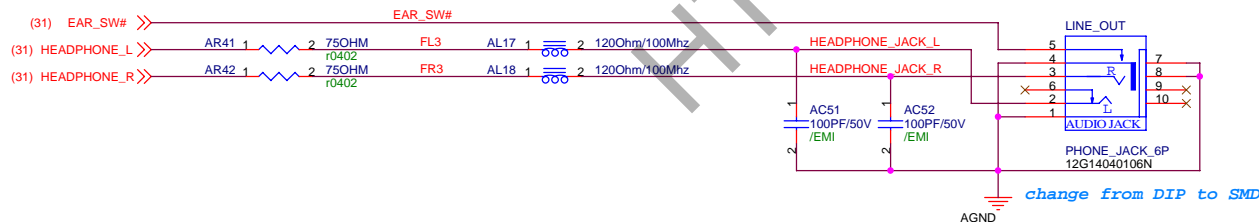
Total length from speakerR+- L+-(pin40 41 44 45) to internal speaker please as short as possible(<20cm is better) >20mil



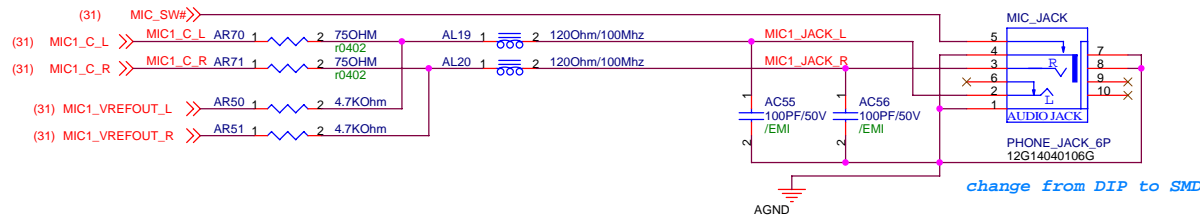
If choke AR86, AR90, AR91, AR92 are mounted, please mount AC194 AC195 to avoid EMI issue.



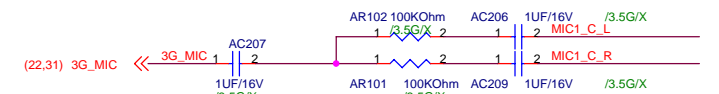
LINE_OUT use 12G140501060



R70 and R71: If don't need retasking function, change to 1K.

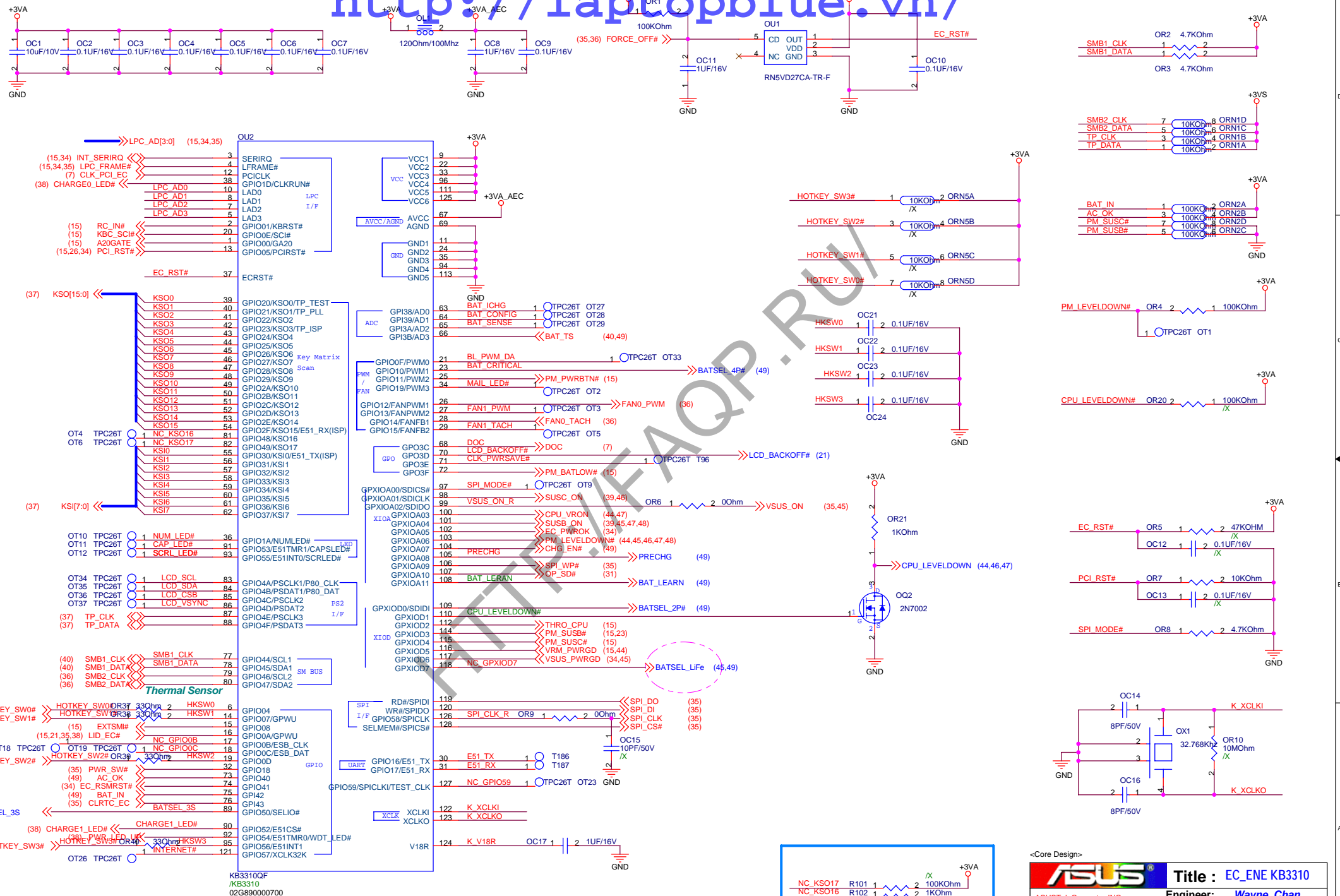


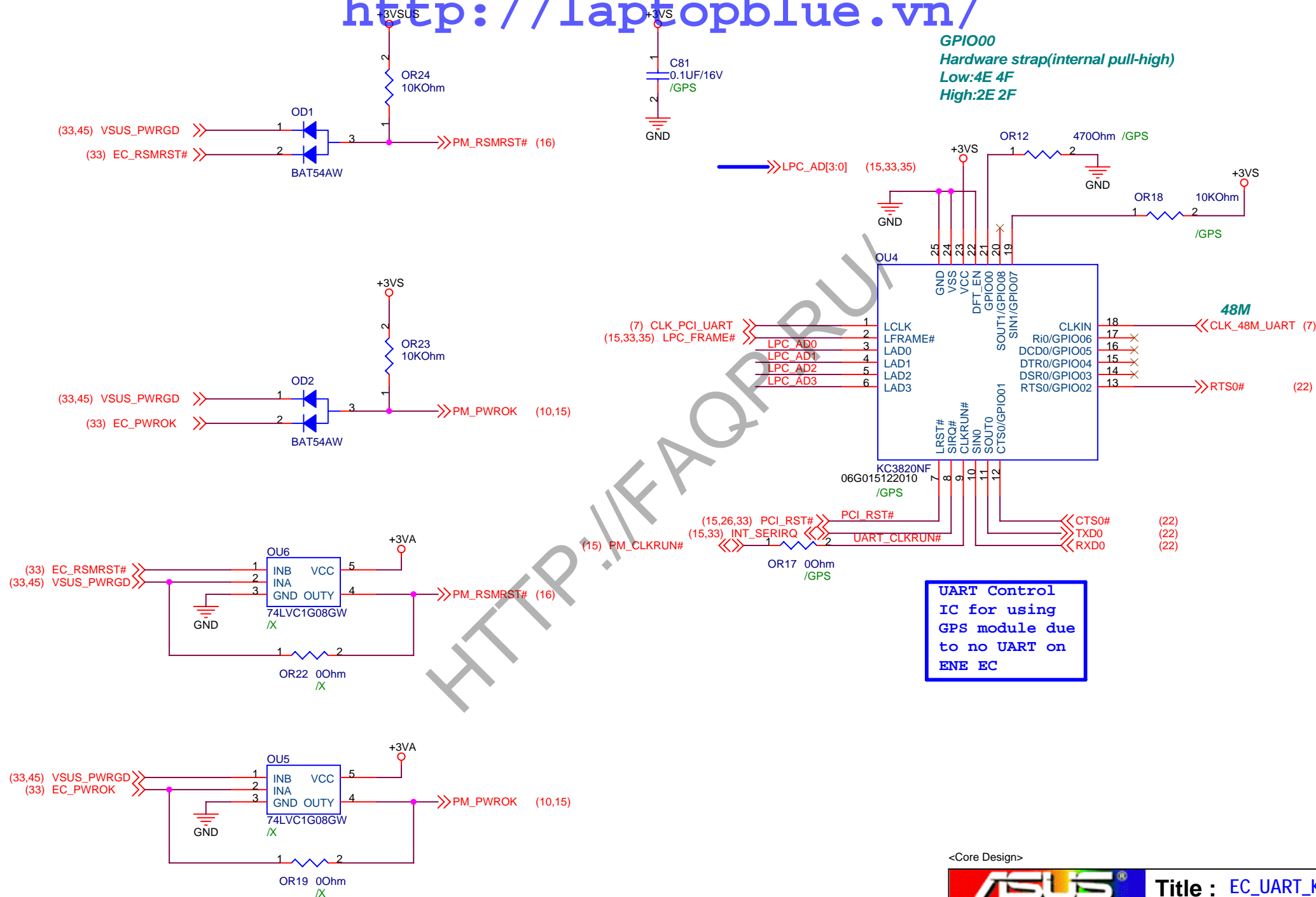
MIC_JACK use 12G14040106Y



<Core Design>

ASUS		Title : ALC269-2	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A3	Project Name 1000HD	Rev 1.3G	
Date: Tuesday, July 22, 2008	Sheet 32 of 49		

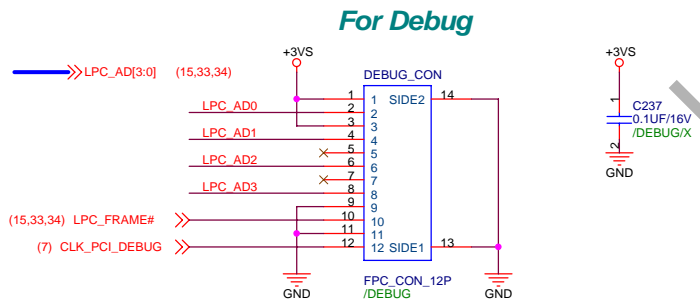
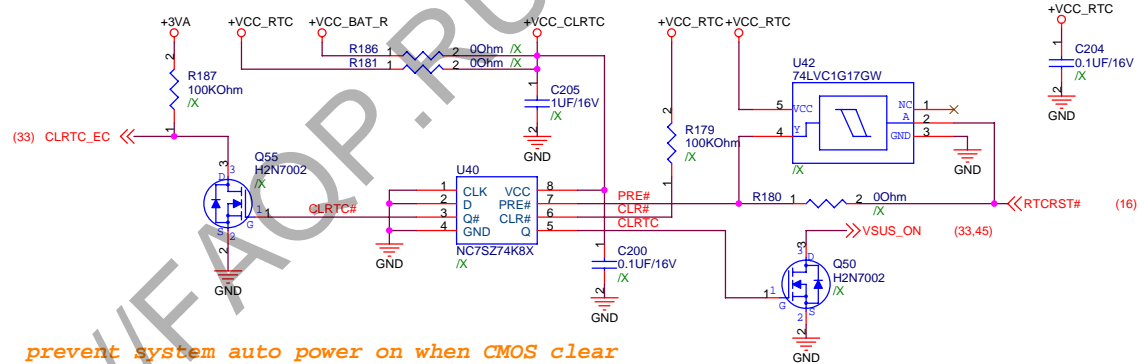
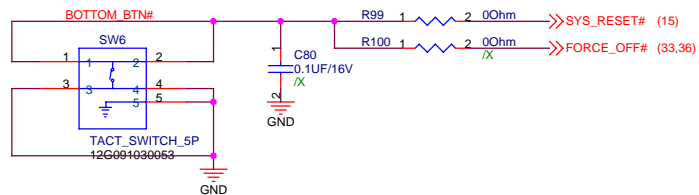
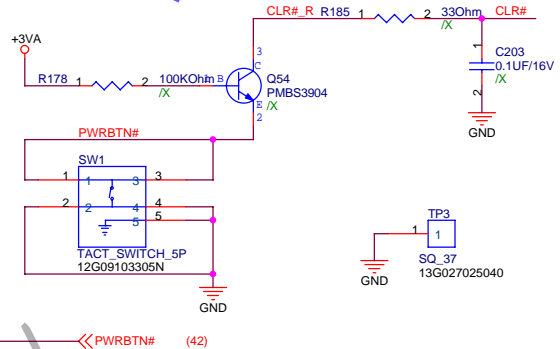
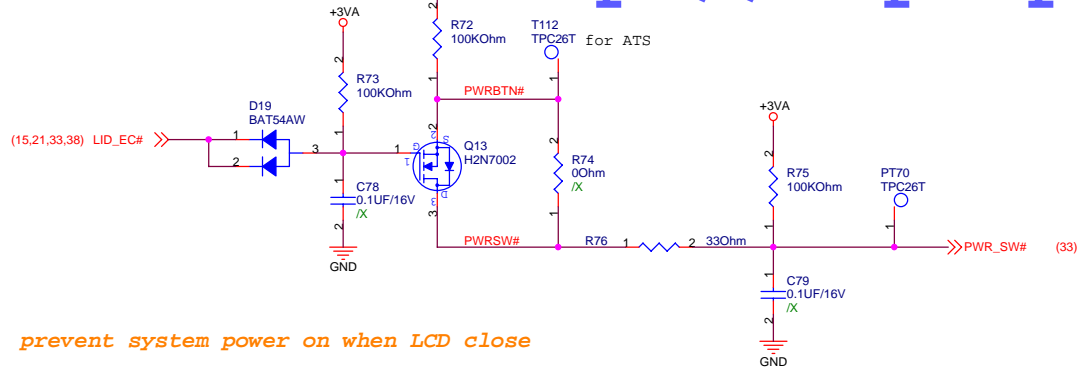




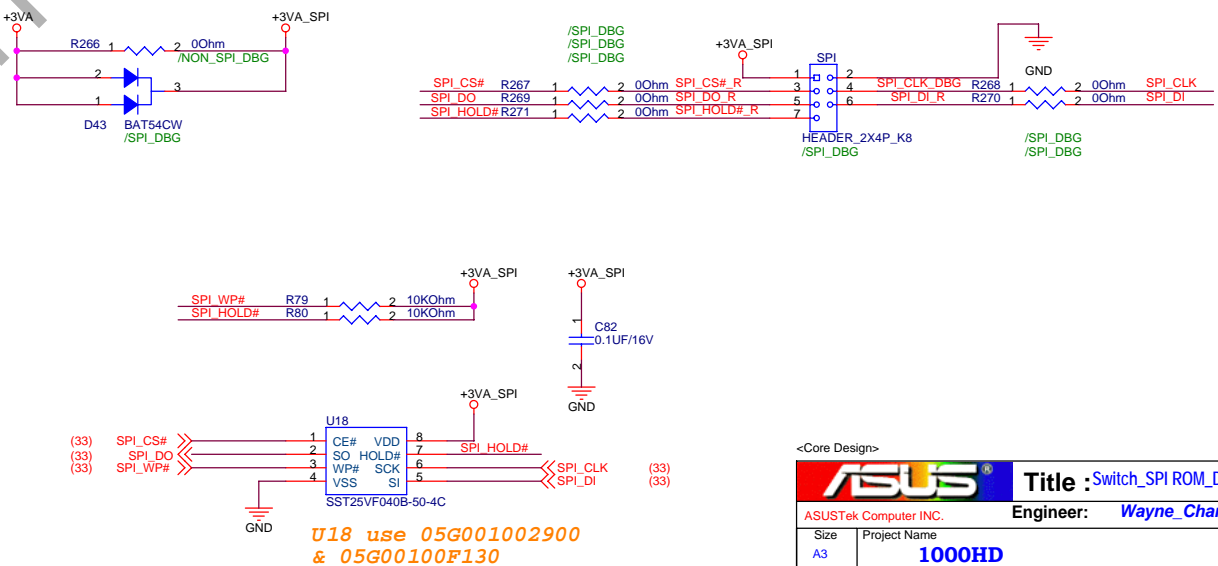
<Core Design>

		Title : EC_UART_KC3820	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A4	Project Name 1000HD		Rev 1.3G
Date: Tuesday, July 22, 2008		Sheet 34	of 49

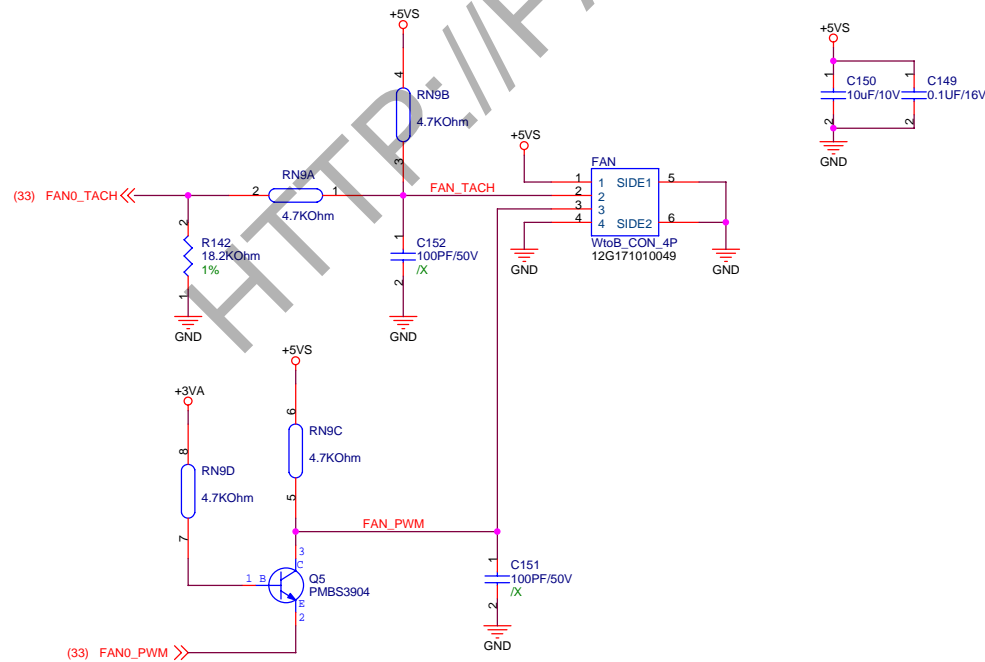
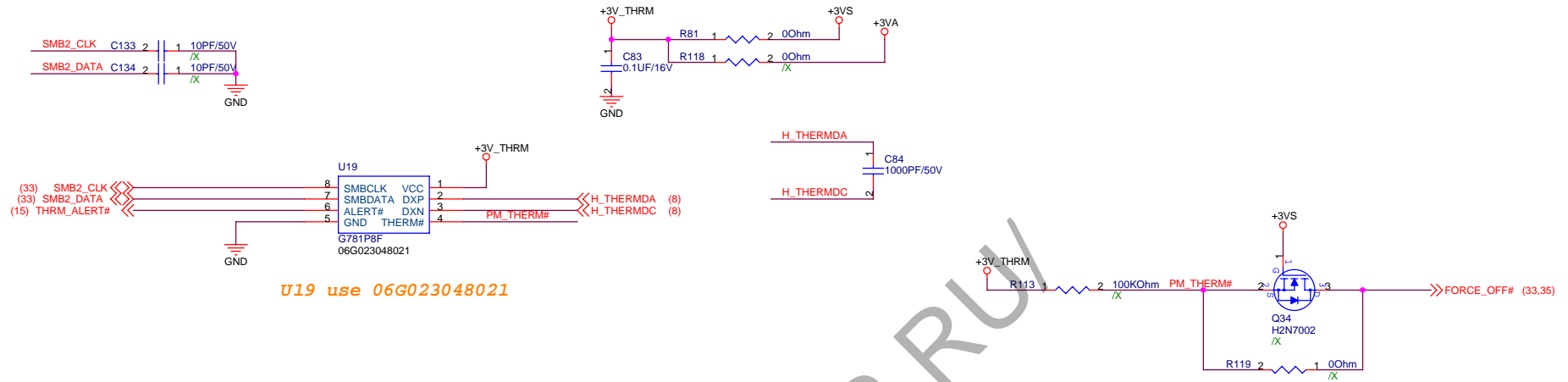
http://laptopblue.vn/



```
Debug Card cable use Z96 Touch Pad cable, P/N:
14G124110126, 14G124110120, 14G124110121
14G124110124, 14G124110125
```



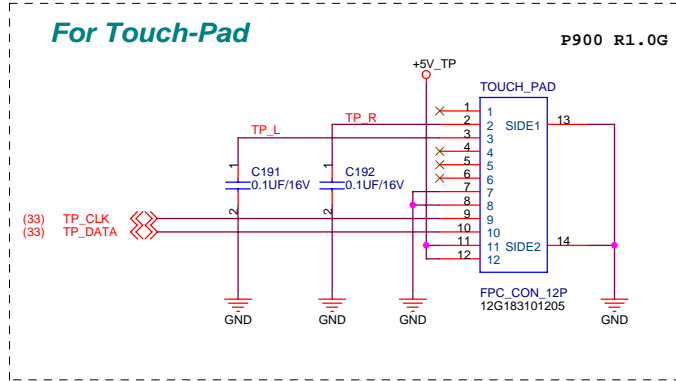
<http://laptopblue.vn/>



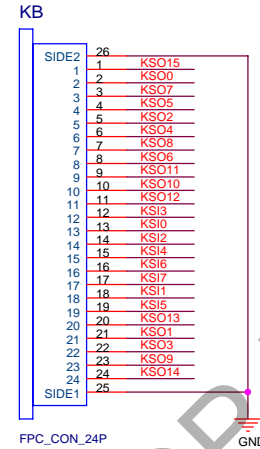
<Core Design>

ASUS		Title : Thermal Sensor_FAN	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size A3	Project Name 1000HD	Rev 1.3G	
Date: Tuesday, July 22, 2008		Sheet 36 of 49	

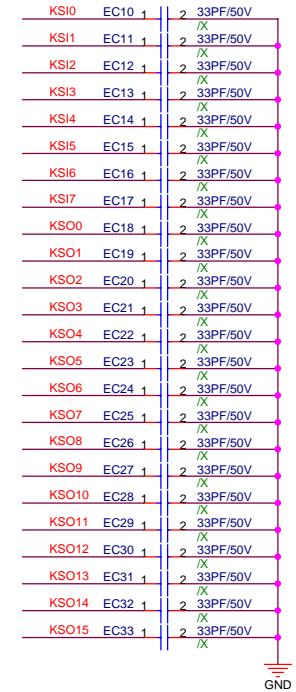
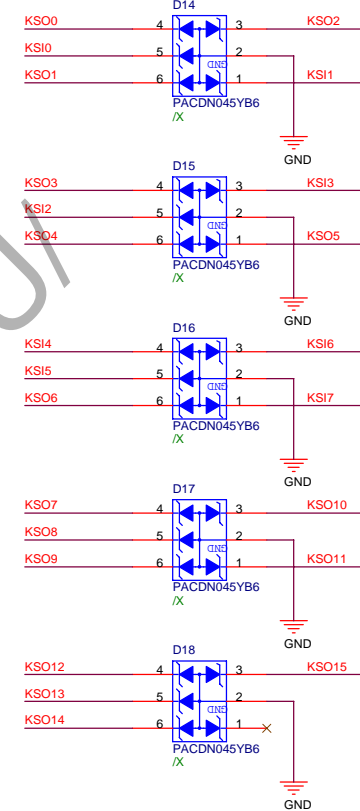
For Touch-Pad



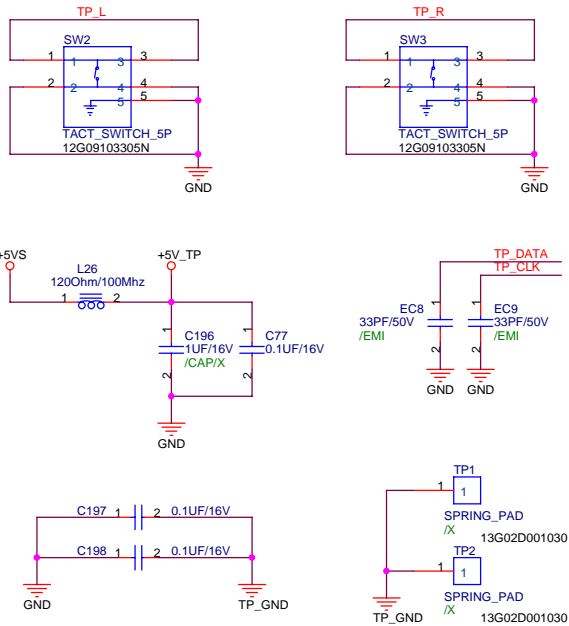
For Keyboard Connector



— KSO[15:0] (33)
— KSI[7:0] (33)

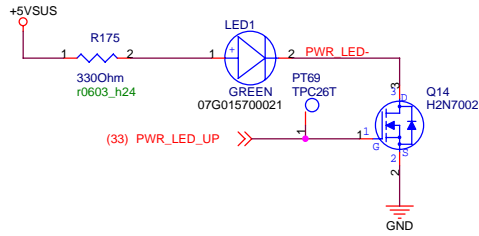


SW2, SW3 use 12G09103305N

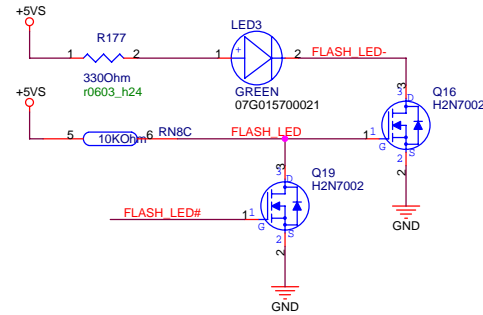


<Core Design>

for POWER LED

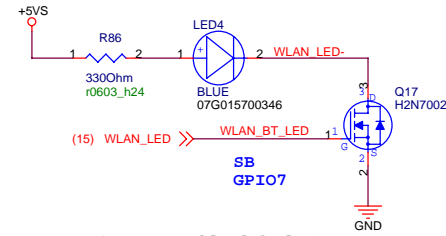


for FLASH LED



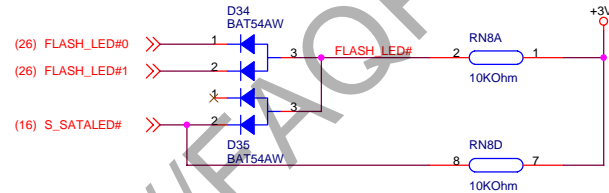
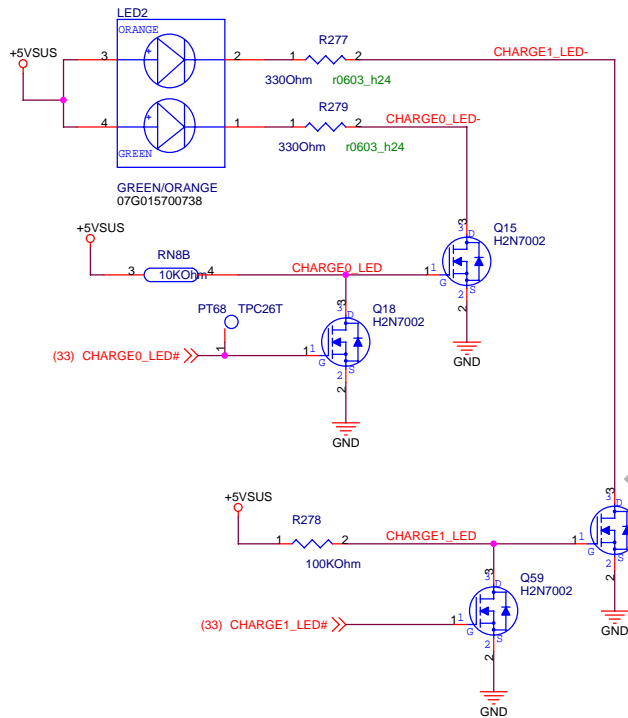
for WLAN/BlueTooth LED

R86 use 4.7K OHm 10G213472003030

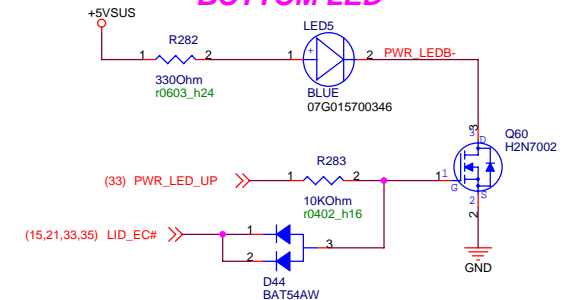


WIFI/BT LED Enable,default:1
0: WIFI and BT are both disabled
1: one of WIFI and BT is Enable or both are Enable

for CHARGE LED



for POWER BOTTOM LED

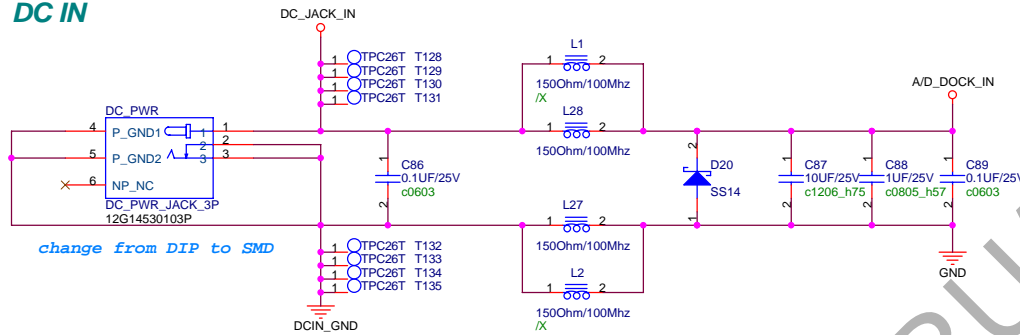


<Core Design>

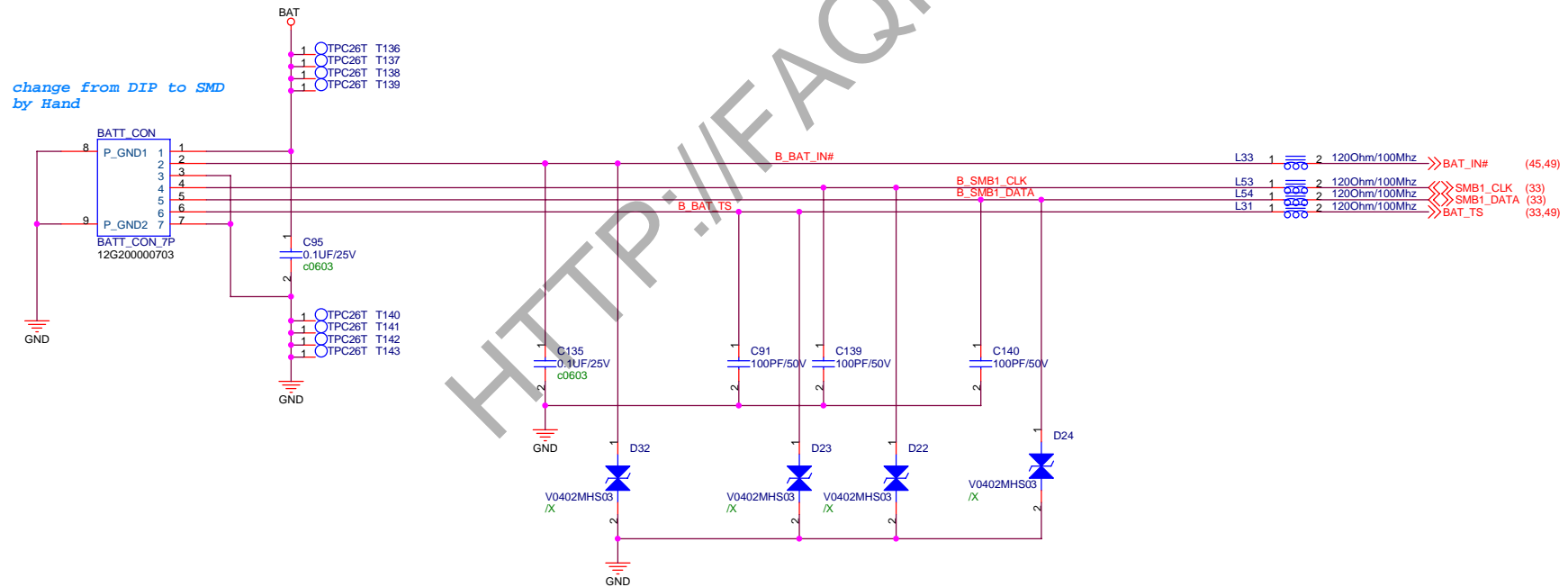
<Core Design>

		Title : <u>Discharge</u>	
ASUSTek Computer INC.		Engineer: <u>Wayne_Chan</u>	
Size	Project Name		Rev
A3	1000HD		1.3G
Date:	Tuesday, July 22, 2008	Sheet	39 of 49

DC IN

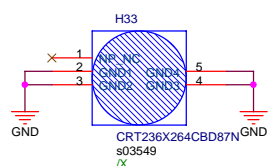
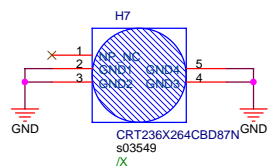
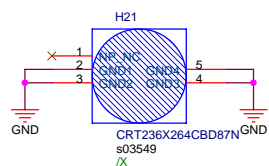
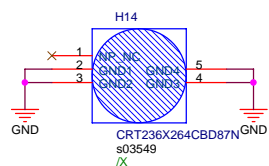
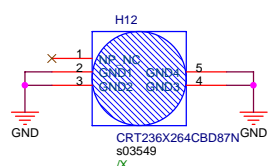
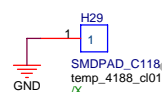
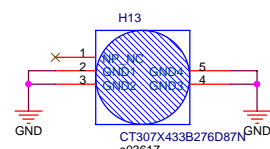
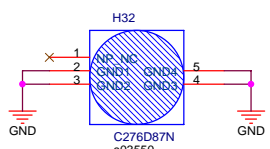
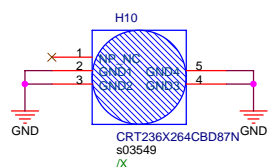
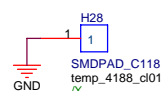
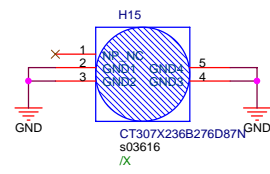
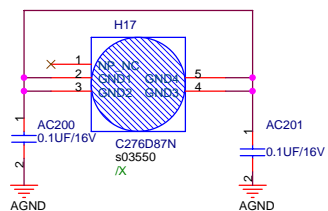
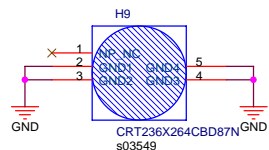
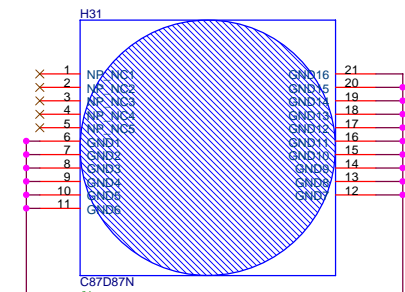
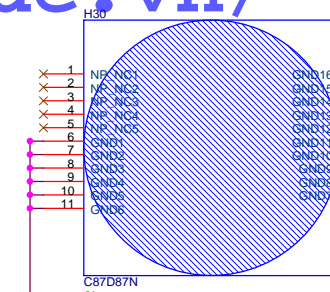
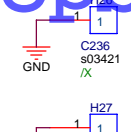
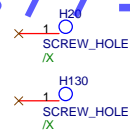
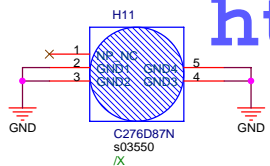
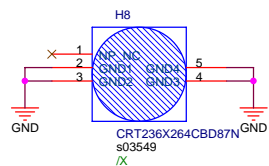


BAT IN



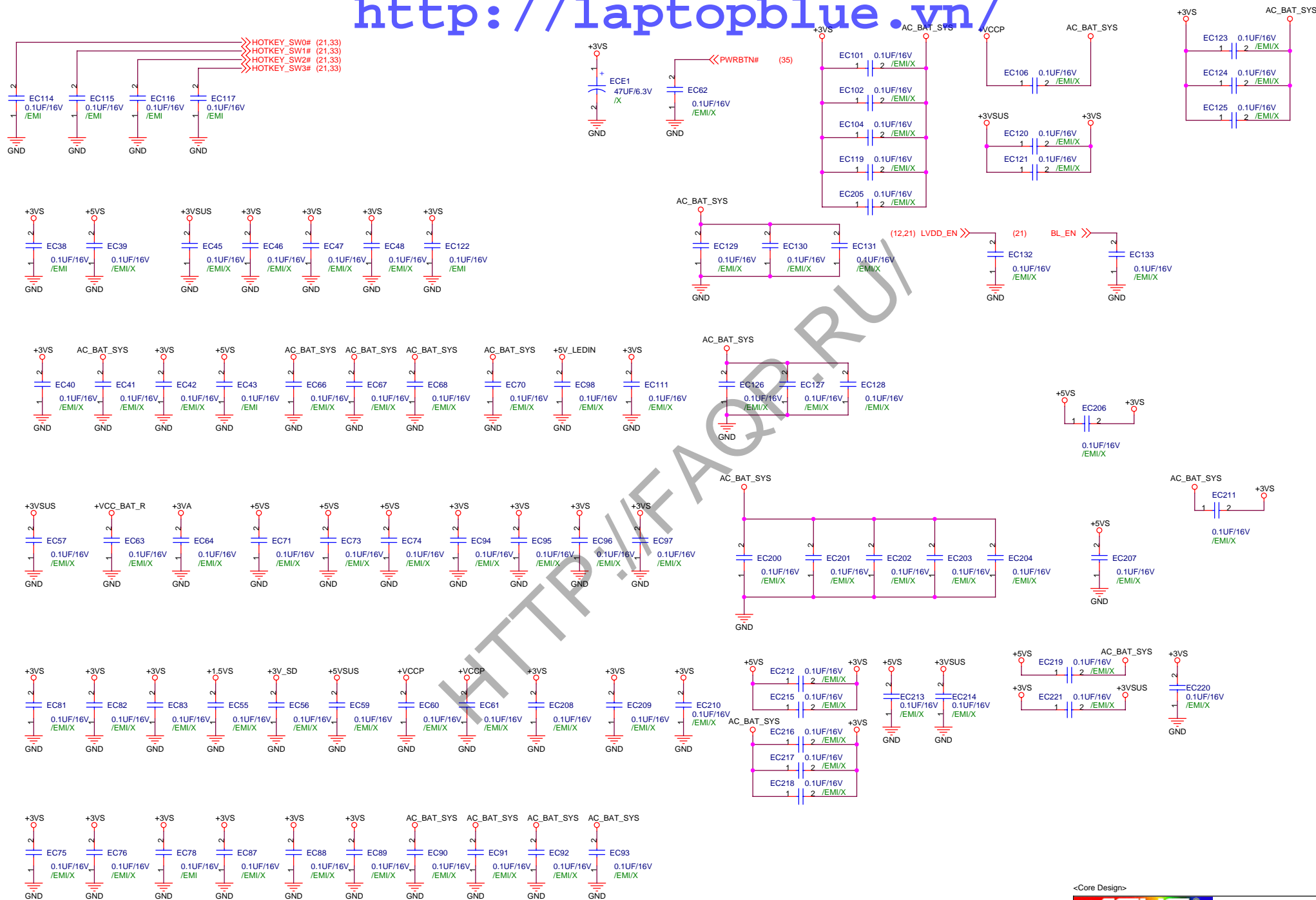
<Core Design>

<http://laptopblue.vn/>



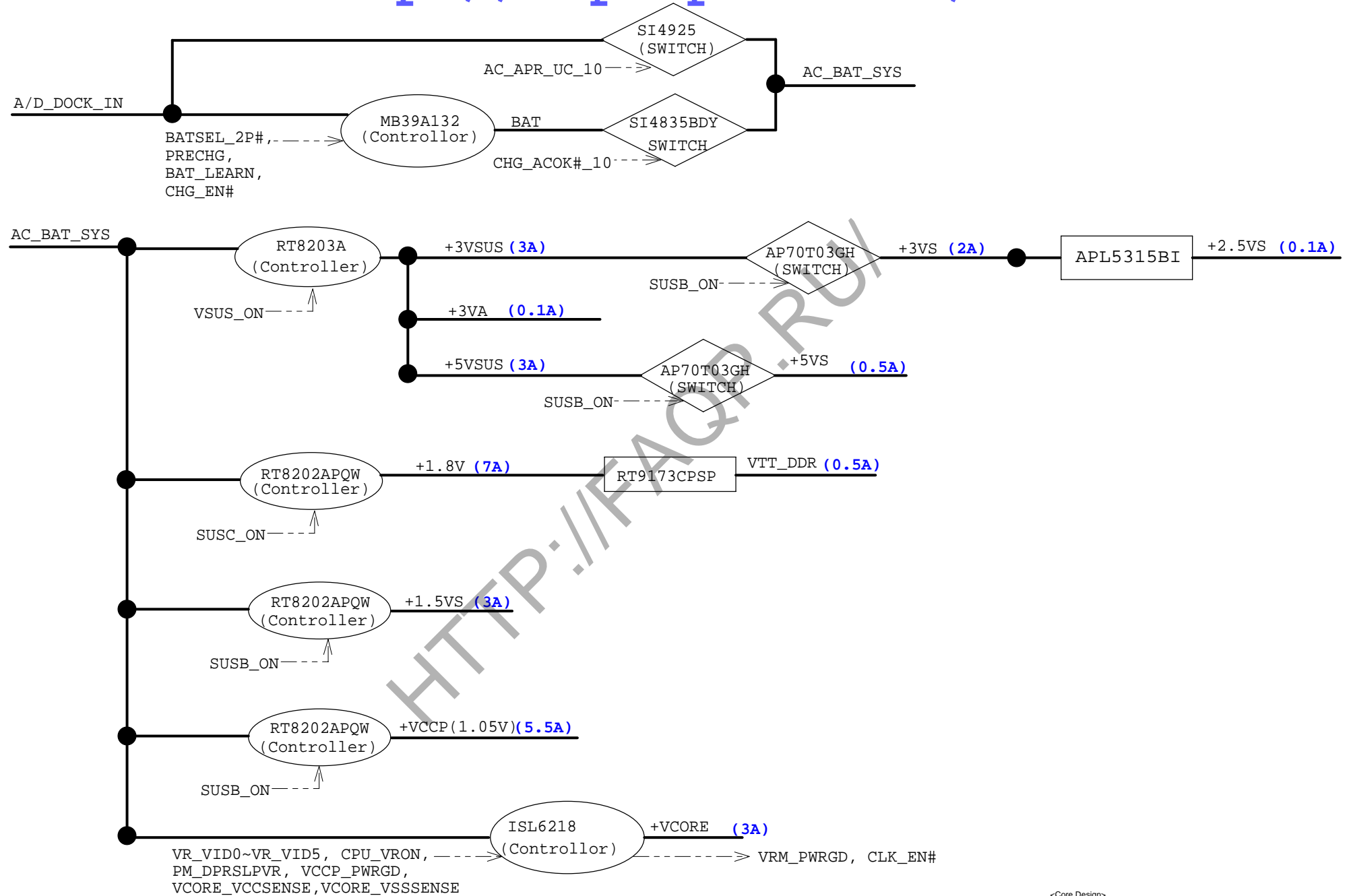
<Core Design>

		Title : Screw Hole	
ASUSTek Computer INC.		Engineer: Wayne_Chan	
Size	Project Name		Rev
A3	1000HD		1.3G
Date: Tuesday, July 22, 2008	Sheet		41 of 49

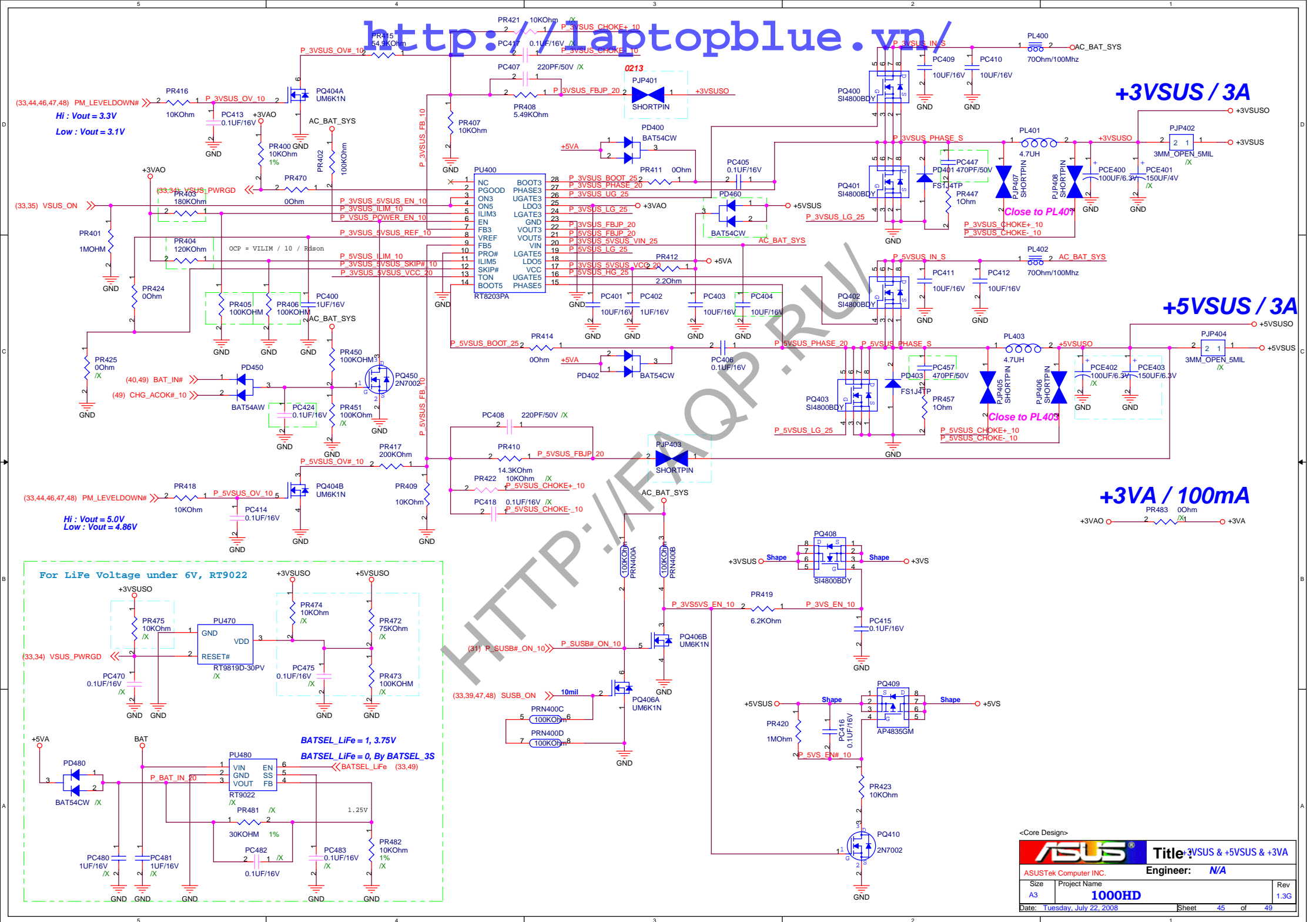


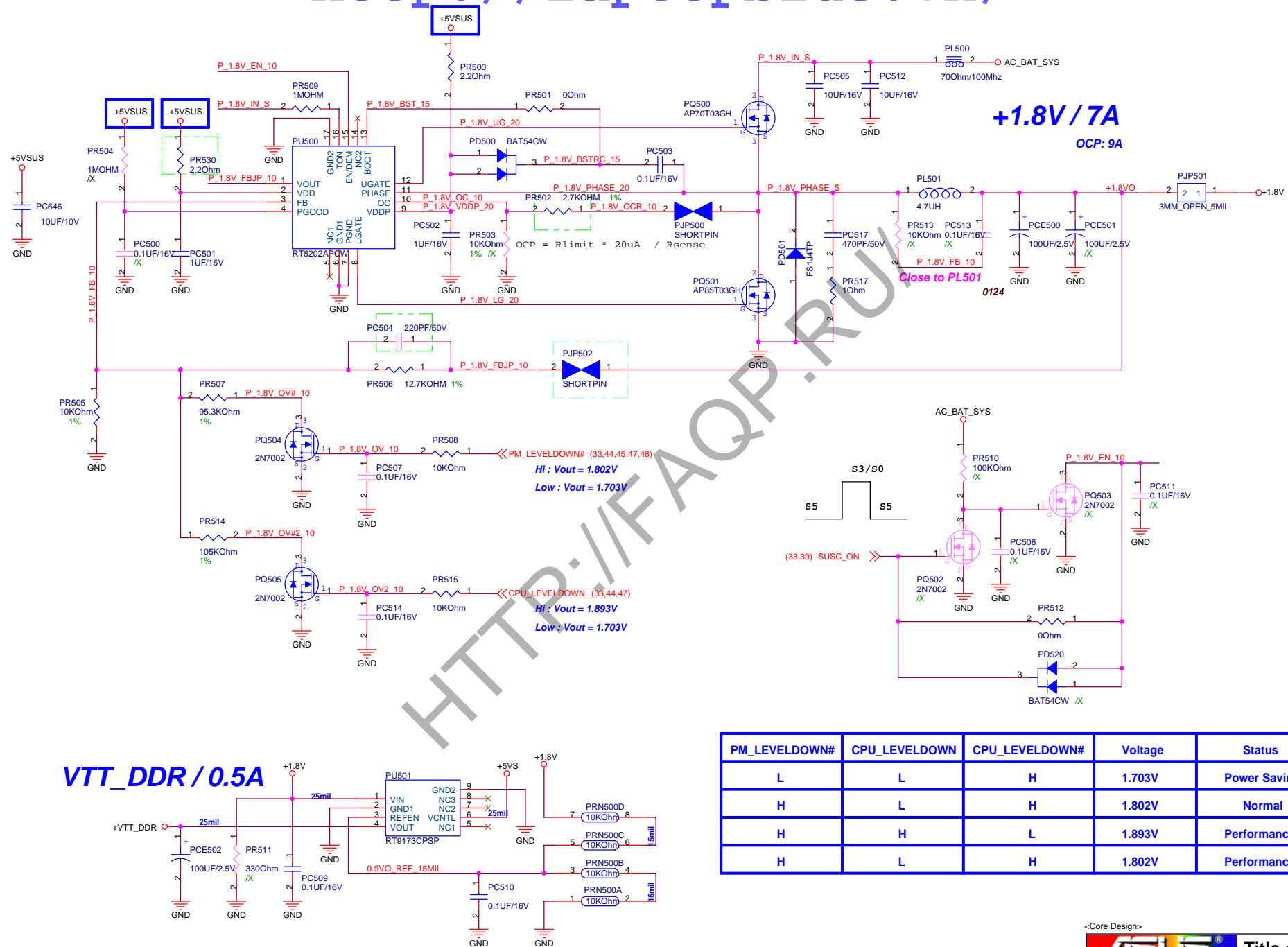
<Core Design>

			Title : EMI	
ASUSTek Computer INC.			Engineer: Wayne_Chan	
Size	Project Name		Rev	
A3	1000HD		1.3G	
Date:	Tuesday, July 22, 2008		Sheet	42 of 49



<Core Design>





PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	1.703V	Power Saving
H	L	H	1.802V	Normal
H	H	L	1.893V	Performance
H	L	H	1.802V	Performance

DEFAULT

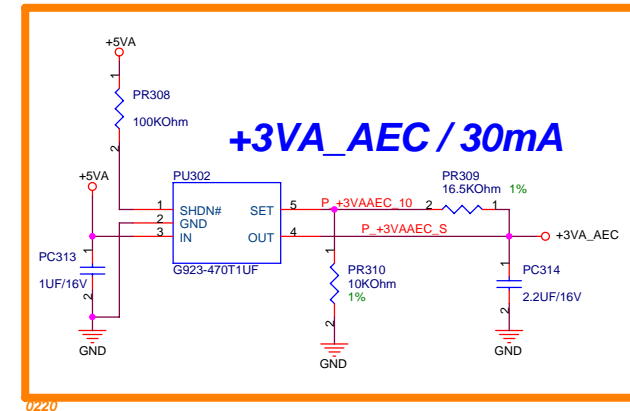
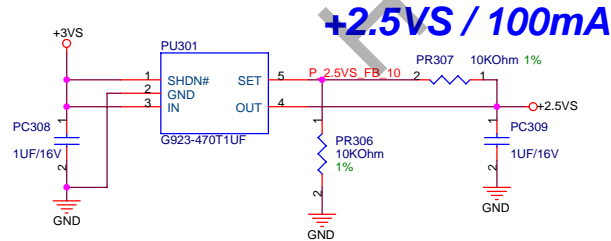
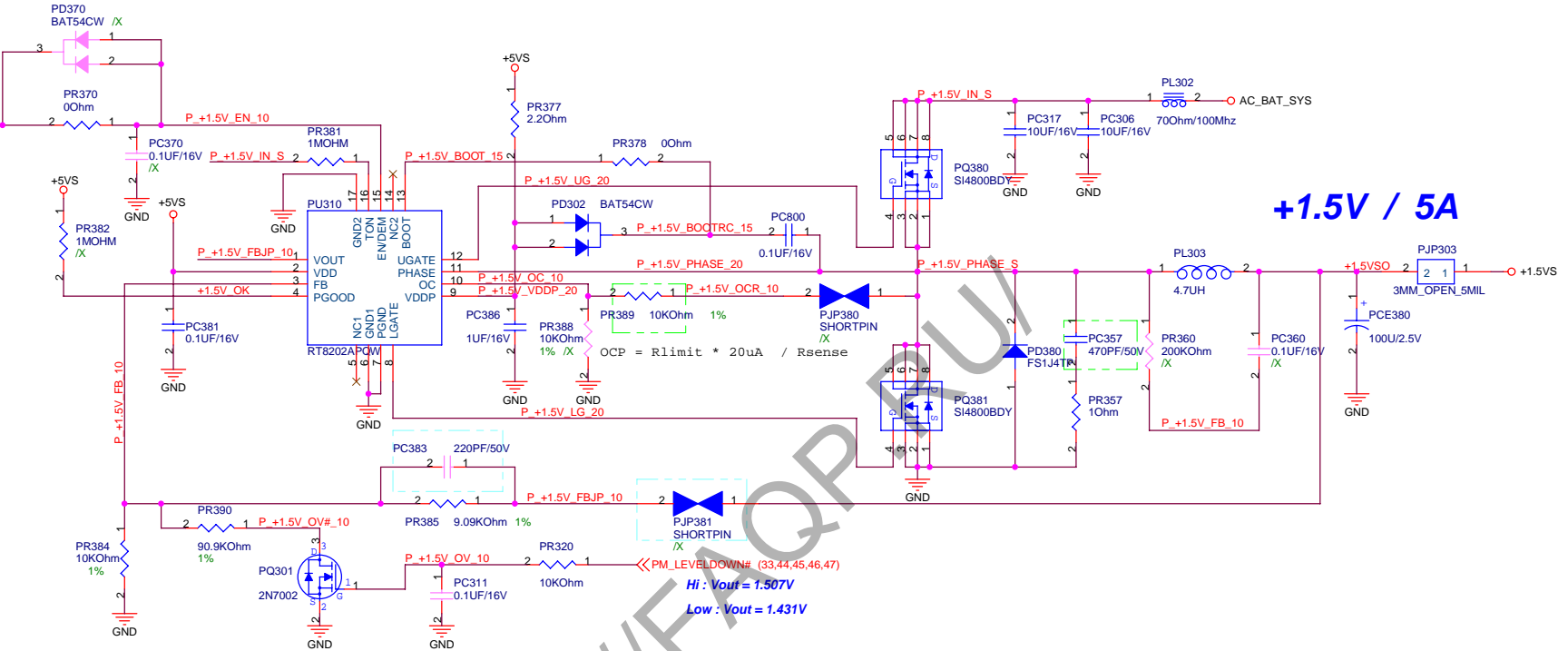
EN/DEM PIN:

Hi : DEM mode

Low : Disable

OD : CCM mode

(33,39,45,47) SUSB_ON



<Core Design>

