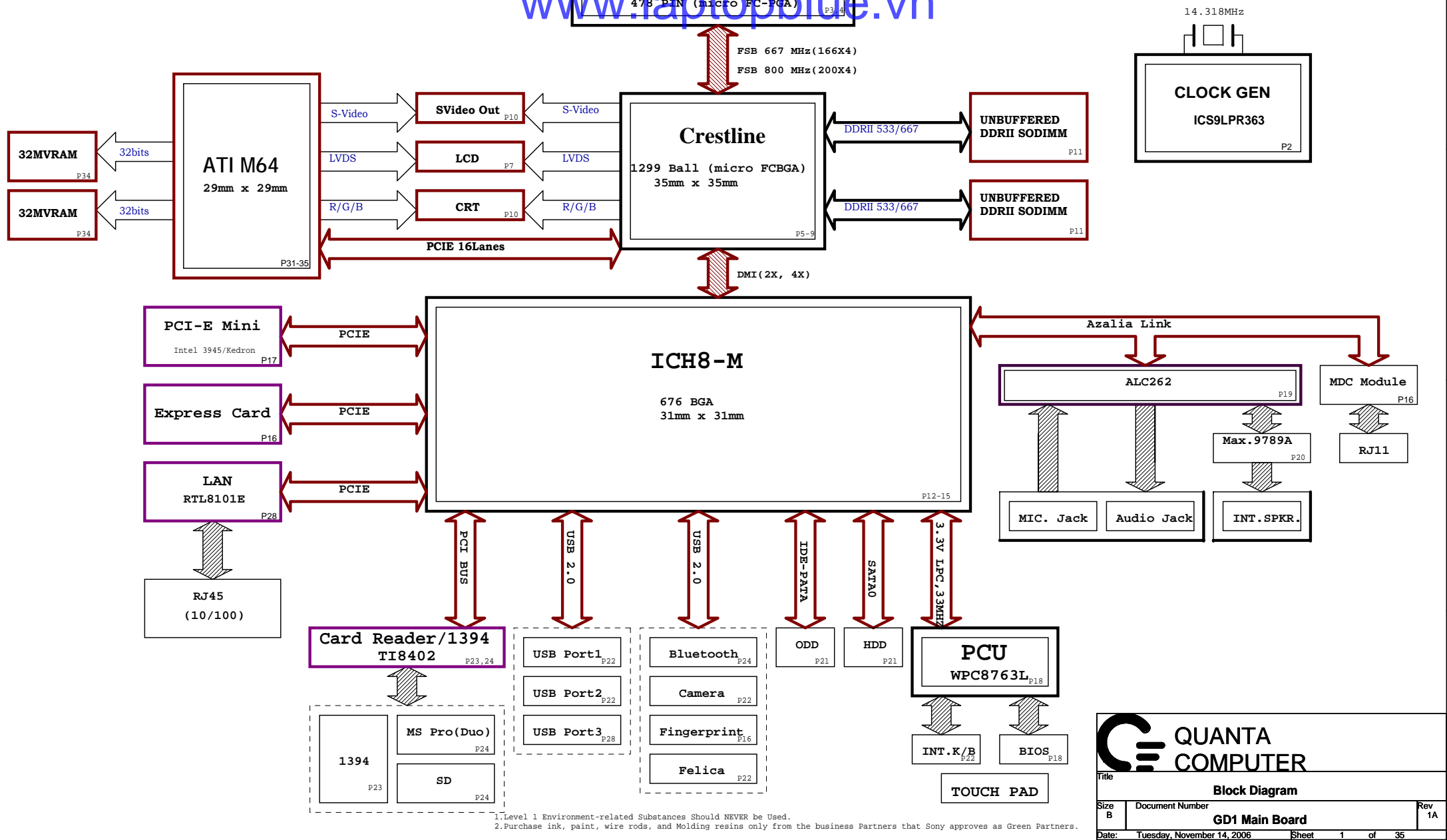
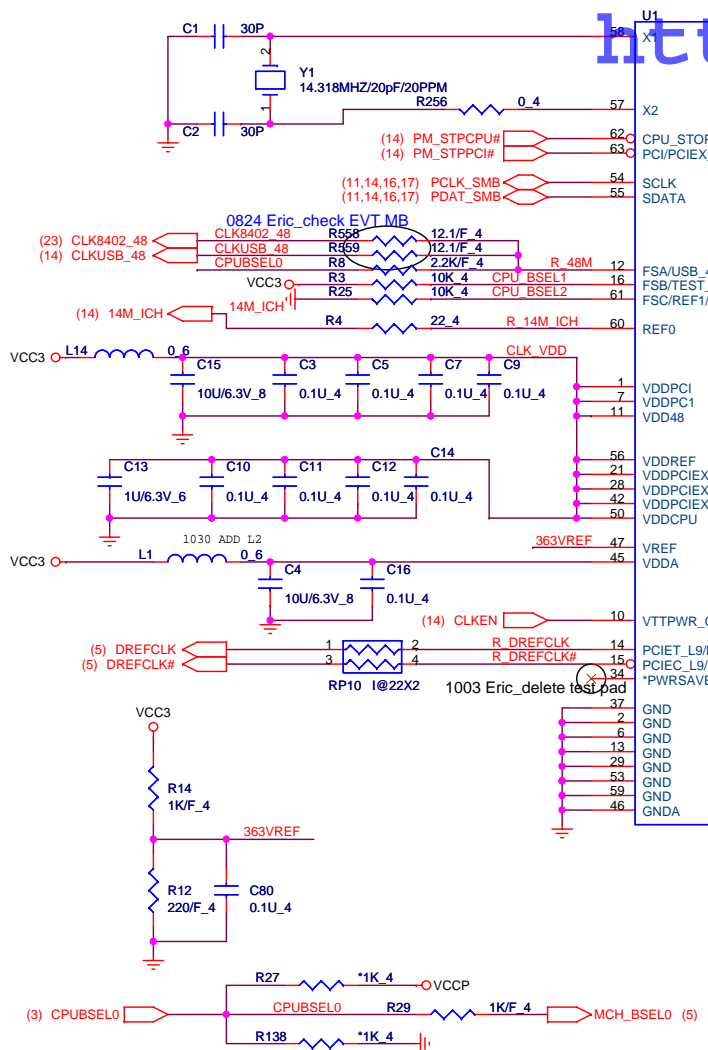


GD1 Block Diagram

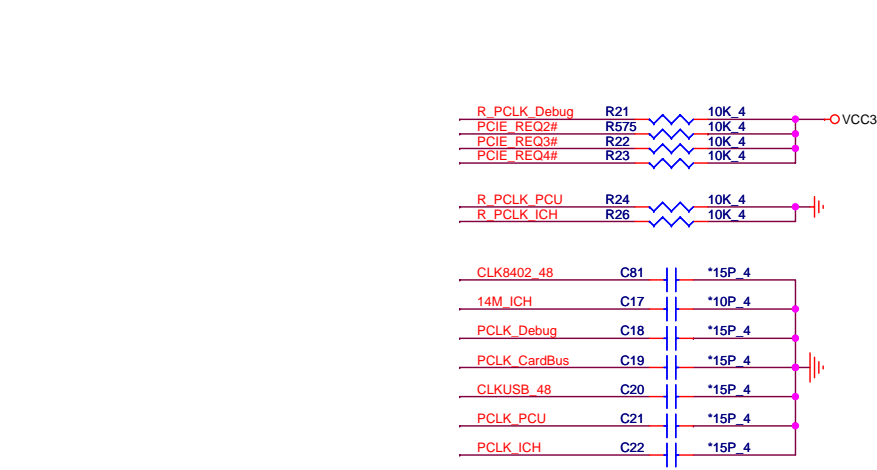
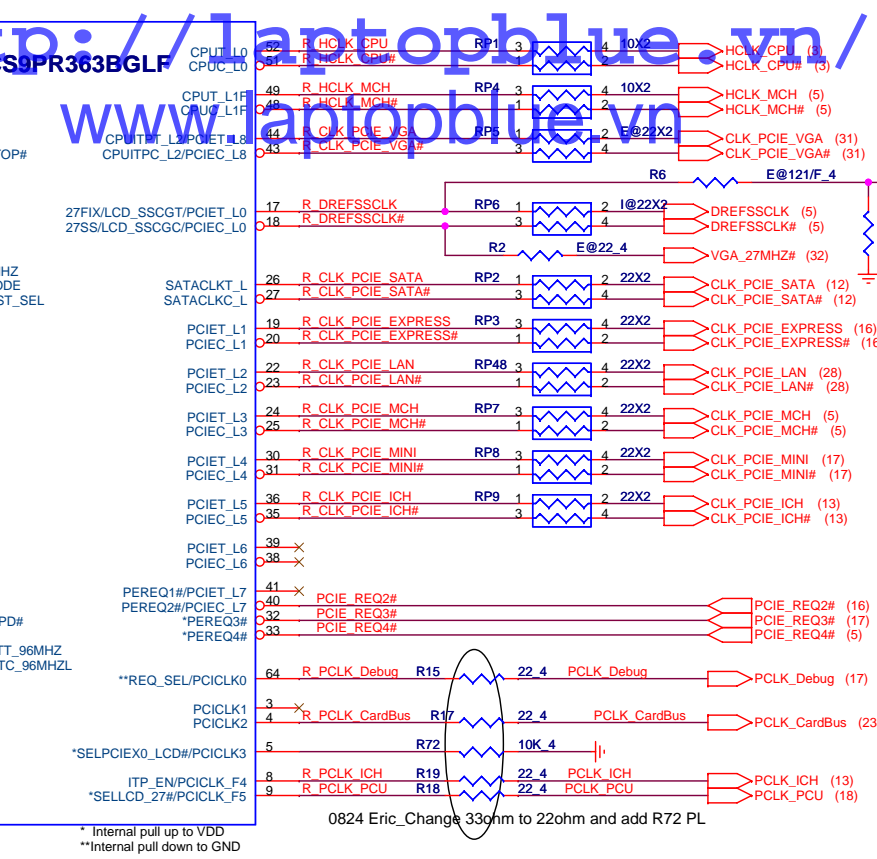
<http://laptopblue.vn/>
www.laptopblue.vn

1





FSC BSEL2	FSB BSEL1	FSA BSEL0	CPU	SRC	PCI	REF	USB	DOT	Spread %
0	0	0	266.66	100	33.33	14.318	48	96	0.5 Down
0	0	1	133.33	100	33.33	14.318	48	96	0.5 Down
* 0	1	0	200.00	100	33.33	14.318	48	96	0.5 Down
0	1	1	166.66	100	33.33	14.318	48	96	0.5 Down
1	0	0	333.33	100	33.33	14.318	48	96	0.5 Down
1	0	1	100.00	100	33.33	14.318	48	96	0.5 Down
1	1	0	400.00	100	33.33	14.318	48	96	0.5 Down
1	1	1	200.00	100	33.33	14.318	48	96	0.5 Down



Signal		965GM	965PM
VGA_27MHZ	R6	NI	121
VGA_27MHZ#	R7	NI	71.5
CLK_PCIE_VGA	RP5	NI	22X2
DREFSSCLK	RP6	22X2	NI
DREFCLK	RP10	22X2	NI

PIN 5 R494	PIN 9 R26	PIN 14/15	PIN 17/18
LO (10K)	LO (10K)	DOT96	LCD
HI (NC)	HI (NC)	DOT96	PCIEX0

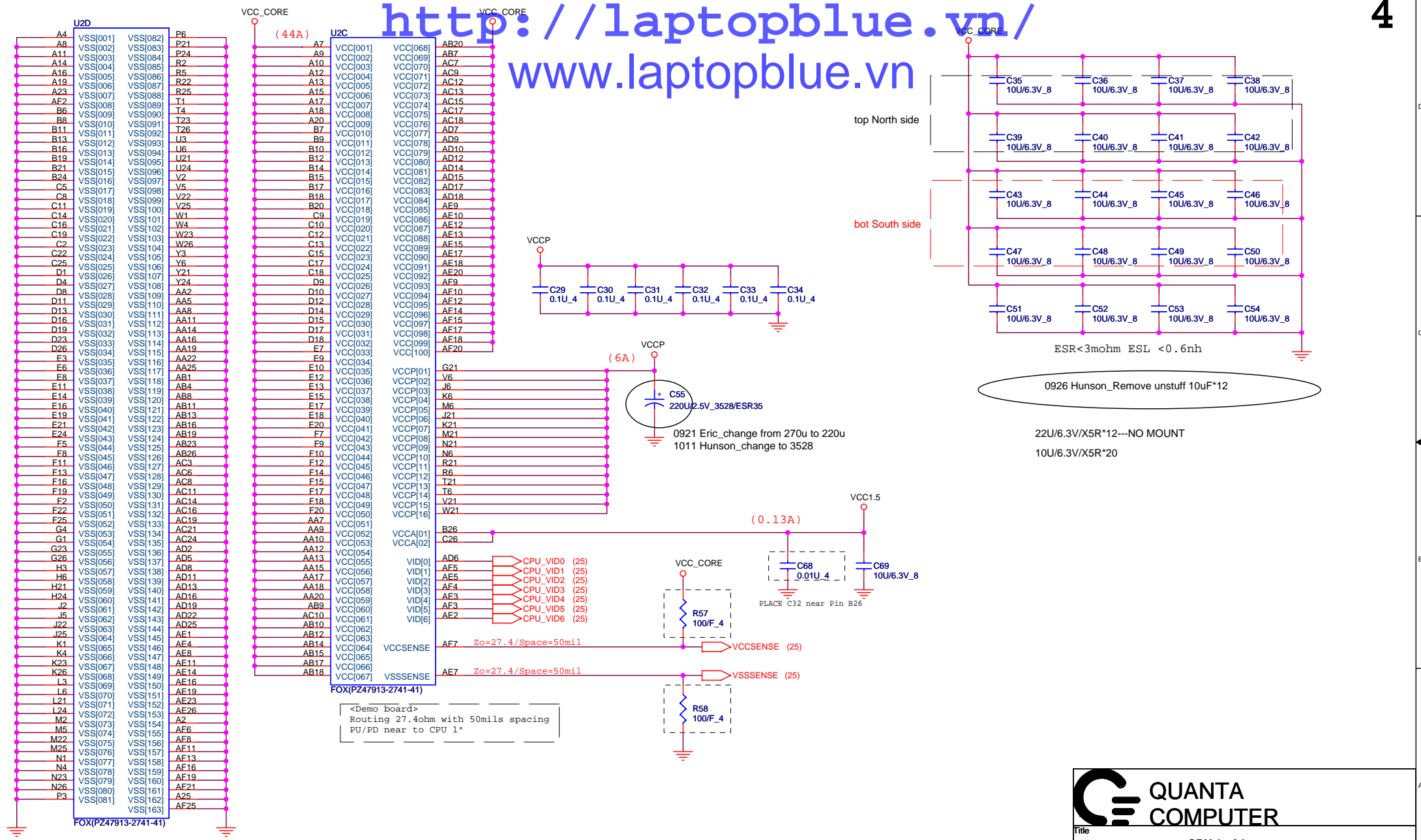
ITP_EN(PIN8)	
LOW : PIN43/44 SRC	
HIGH : PIN43,44 CPUTIP	

PCIE_REQ1#	PCIE_L0	PCIE_L6	
PCIE_REQ2#	PCIE_L1	PCIE_L8	
PCIE_REQ3#	PCIE_L2	PCIE_L4	
PCIE_REQ4#	PCIE_L3	PCIE_L5	PCIE_L7

Title	
CLOCK GENERATOR	
Size	Document Number
B	GD1 Main Board
Date:	Wednesday, November 15, 2006
Sheet	2 of 35
Rev	1A

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

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top North side

bot South side


ESR<3mohm ESL <0.6nh

0926 Hunson_Remove unstuff 10uF*12

220/6.3V/X5R*12---NO MOUNT

10U/6.3V/X5R*20

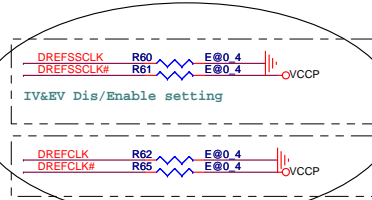
<Demo board>
Routing 27.4ohm with 50mils spacing
PU/PD near to CPU 1"



QUANTA
COMPUTER

Title		
CPU 2 of 2		
Size	Document Number	Rev
B	GD1 Main Board	1A
Date:	Tuesday, November 14, 2006	Sheet 4 of 35

- 1.Level 1 Environment-related Substances Should NEVER be Used.
- 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



0921 Edward_follow design Guide 1.1 page195

RSVD

MUXING

DDR

CLK

CFG

DMI

VID

ME

NC

MISC

GIAO TIẾP VỚI RAM HỆ THỐNG

GIAO TIẾP TRUYỀN DỮ LIỆ VỚI SB

VCC3

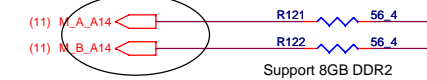
1012 Hunson_ADD R178_0R for G965 ES1 (stay at 0.7V durring booting)

QUANTA COMPUTER

GMCH HOST1 of 5

GD1 Main Board

Date: Tuesday, November 14, 2006 Sheet 5 of 35



(5) MCH_CFG_5 MCH_CFG_5 R123 *4.02K/F_4

(5) MCH_CFG_9 MCH_CFG_9 R124 *4.02K/F_4

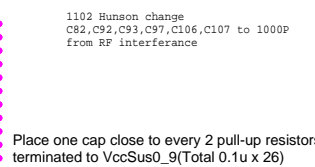
(5) MCH_CFG_12 MCH_CFG_12 R125 *4.02K/F_4

(5) MCH_CFG_13 MCH_CFG_13 R126 *4.02K/F_4

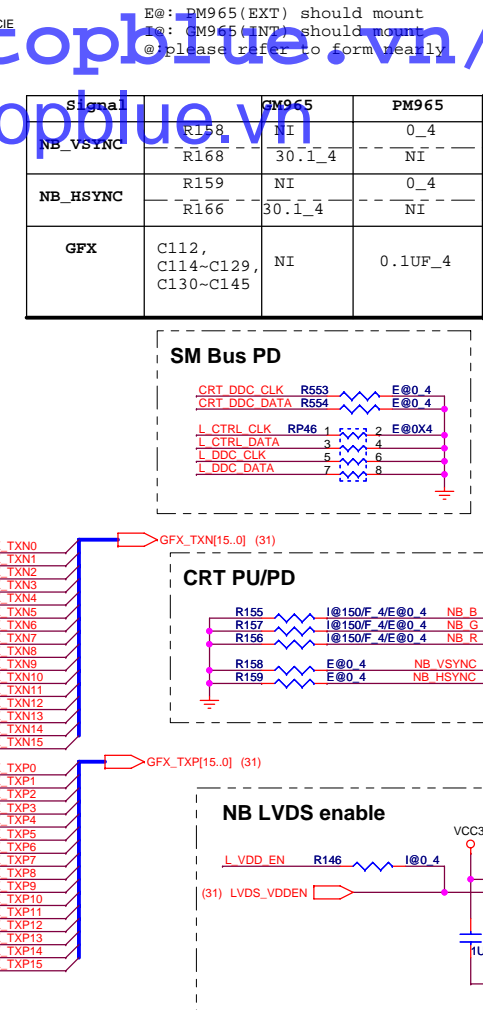
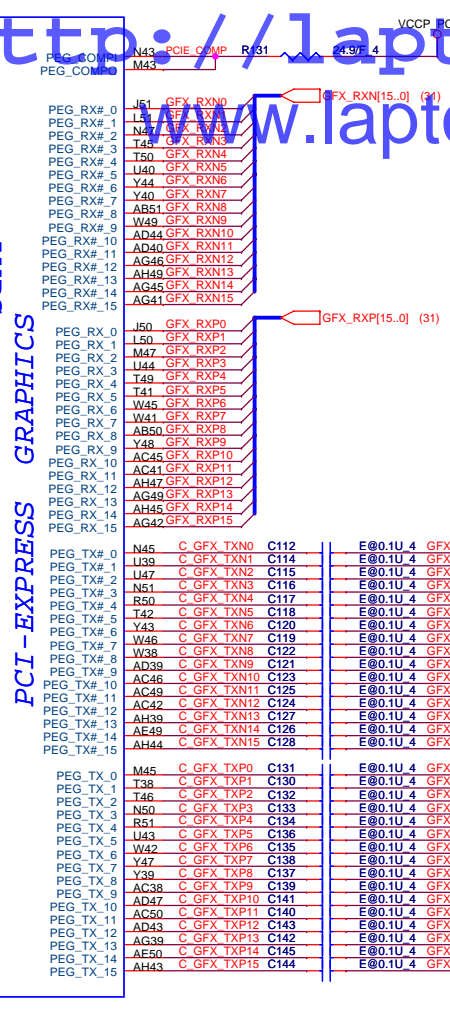
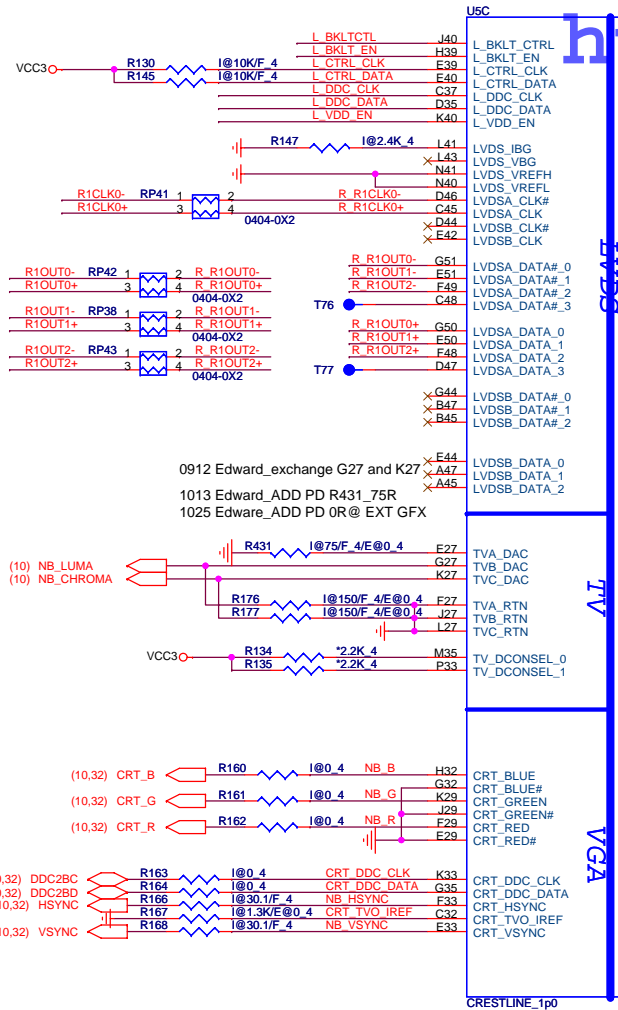
(5) MCH_CFG_16 MCH_CFG_16 R127 *4.02K/F_4

(5) MCH_CFG_19 MCH_CFG_19 R128 *4.02K/F_4 VCC3

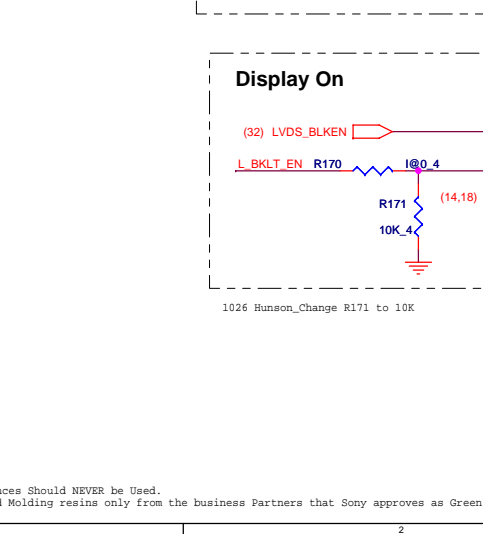
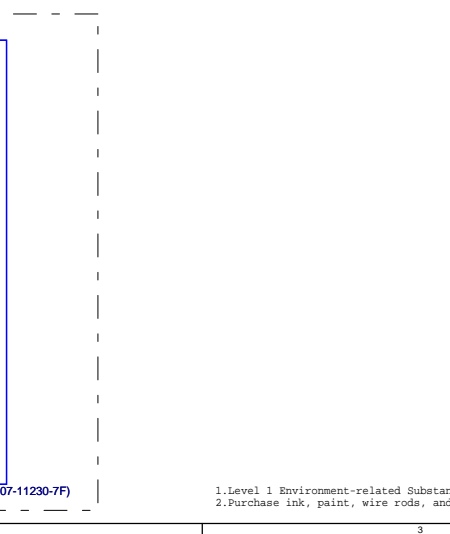
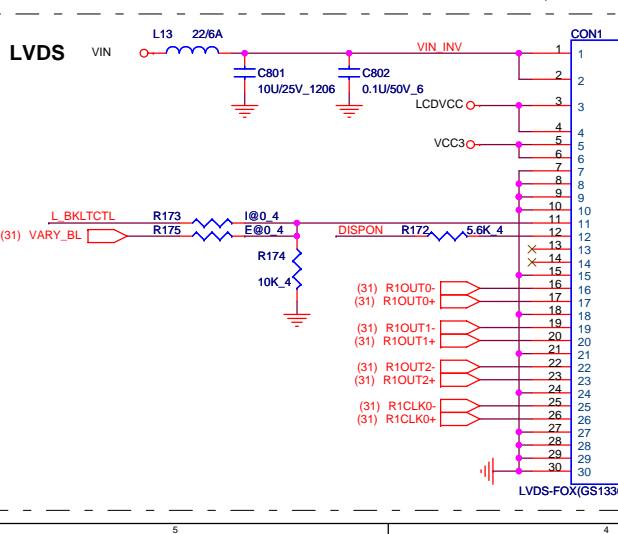
(5) MCH_CFG_20 MCH_CFG_20 R129 *4.02K/F_4 VCC3



Title				GMCH DDR II 2 of 5			
Size		Document Number					Rev
		GD1 Main Board					1A
Date:		Tuesday, November 14, 2006		Sheet		6 of 35	



Signal		965GM	965PM
NB_B	R160 R155	0_4 150_4	NI 0_4
NB_G	R161 R157	0_4 150_4	NI 0_4
NB_R	R162 R156	0_4 150_4	NI 0_4
L_IBG	R147	2.4K_4	NI
LVDS	RP38 RP41-RP43	0X2	NI
L_BKLT_EN	R170	0_4	NI
L_CTRL_CLK	R130 RP46	10K NI	NI 0X4
L_CTRL_DATA	R145 RP46	10K NI	NI 0X4
LVDS_VDDEN	R146	0_4	NI
DDC2BC	R163 R553	0_4 NI	NI 0_4
DDC2BD	R164 R554	0_4 NI	NI 0_4
CRT_TVO_IREF	R167	1.3K_4	0
L_DDC_CLK	RP46	NI	0X4
L_DDC_DATA	RP46	NI	0X4
BRIGHT	R173 R175	0_4 NI	NI 0_4



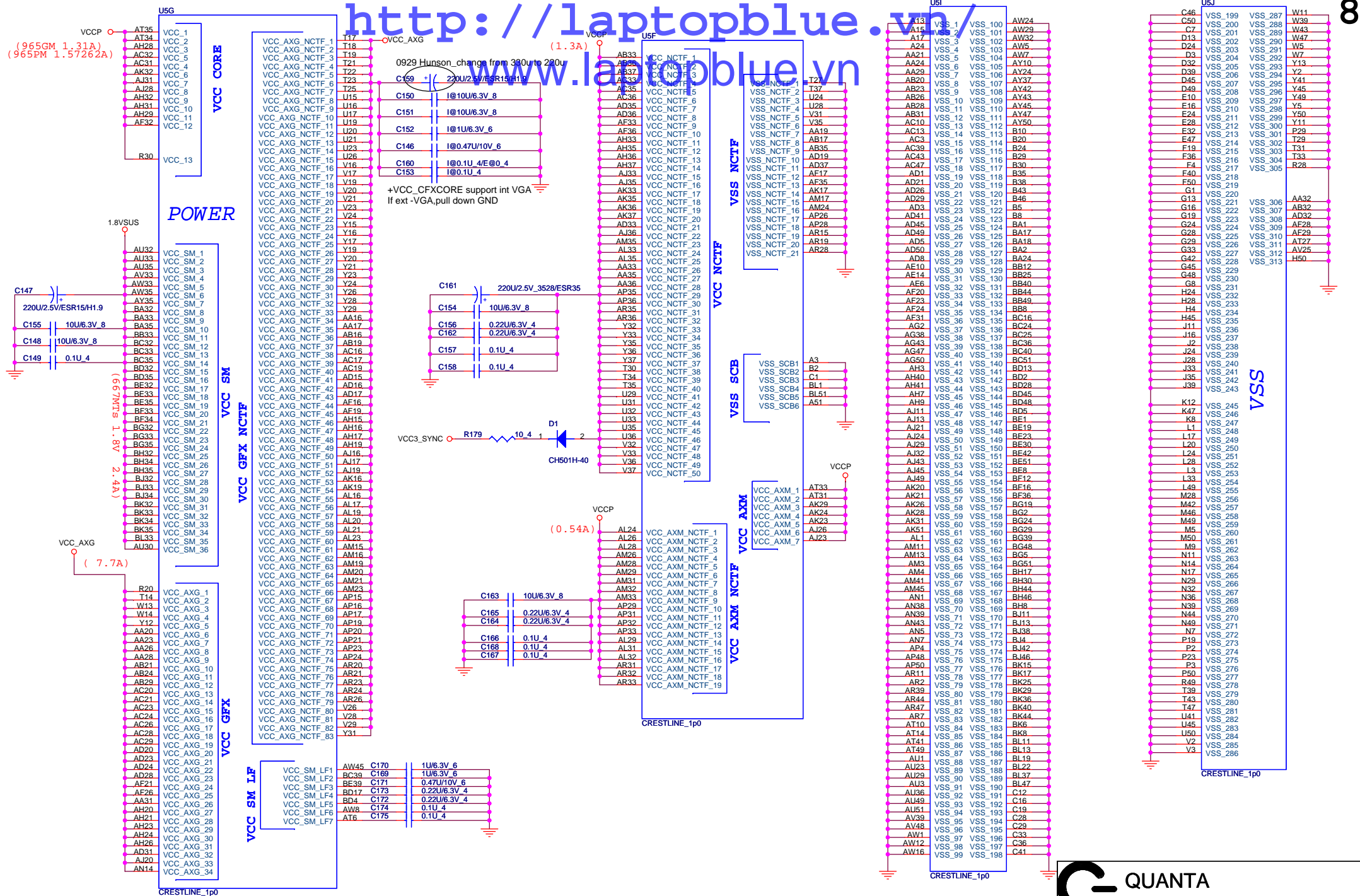
QUANTA COMPUTER


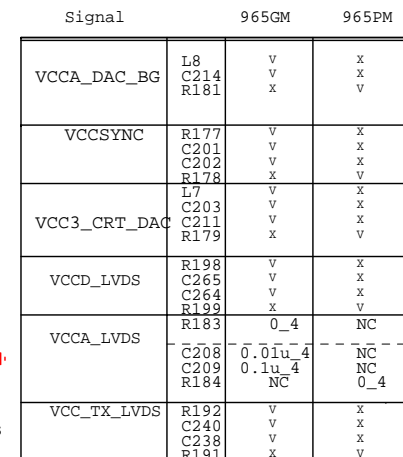
Title: **GMCH DMI VEDIO 3 of 5**

Size: **Document Number** **Rev 1A**

Date: **Tuesday, November 14, 2006** Sheet **7** of **35**

1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



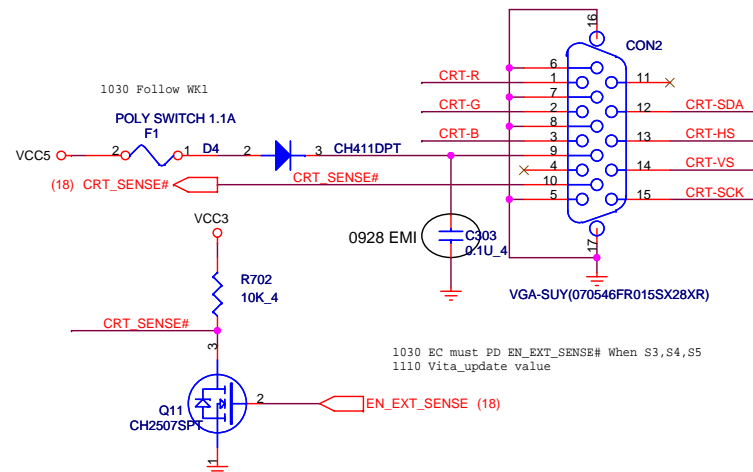
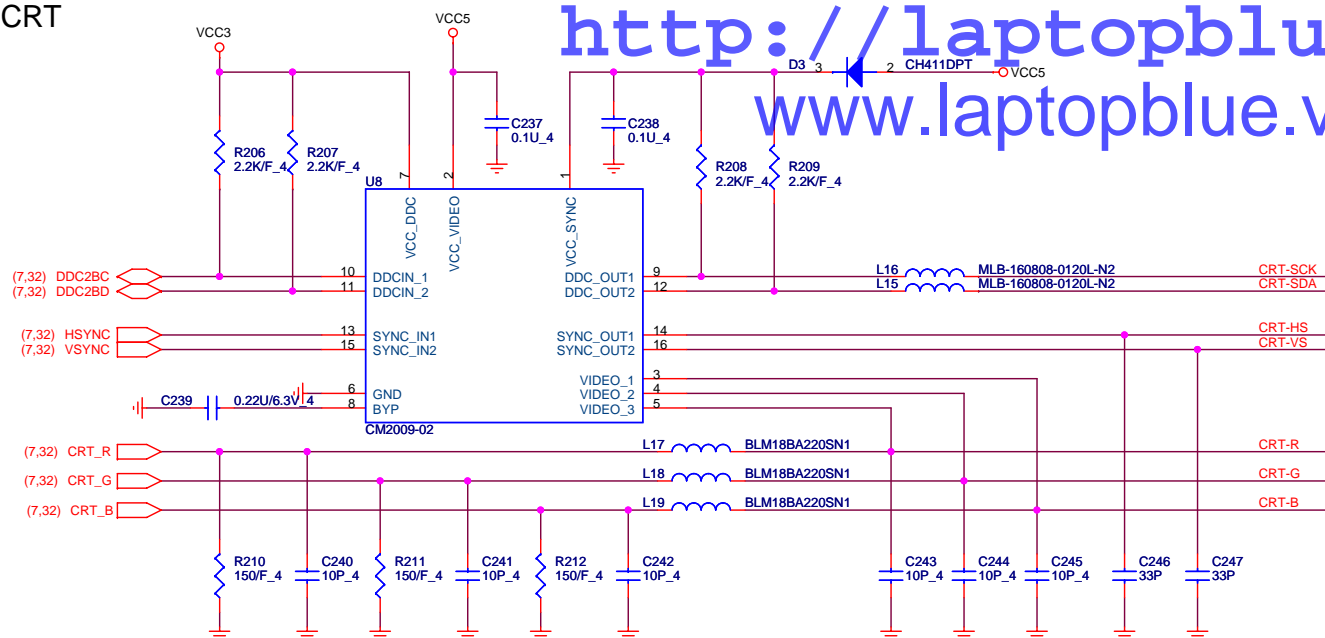


QUANTA
COMPUTER

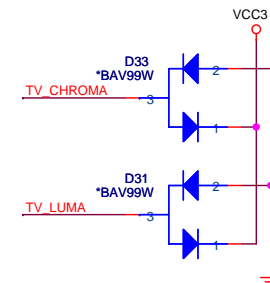
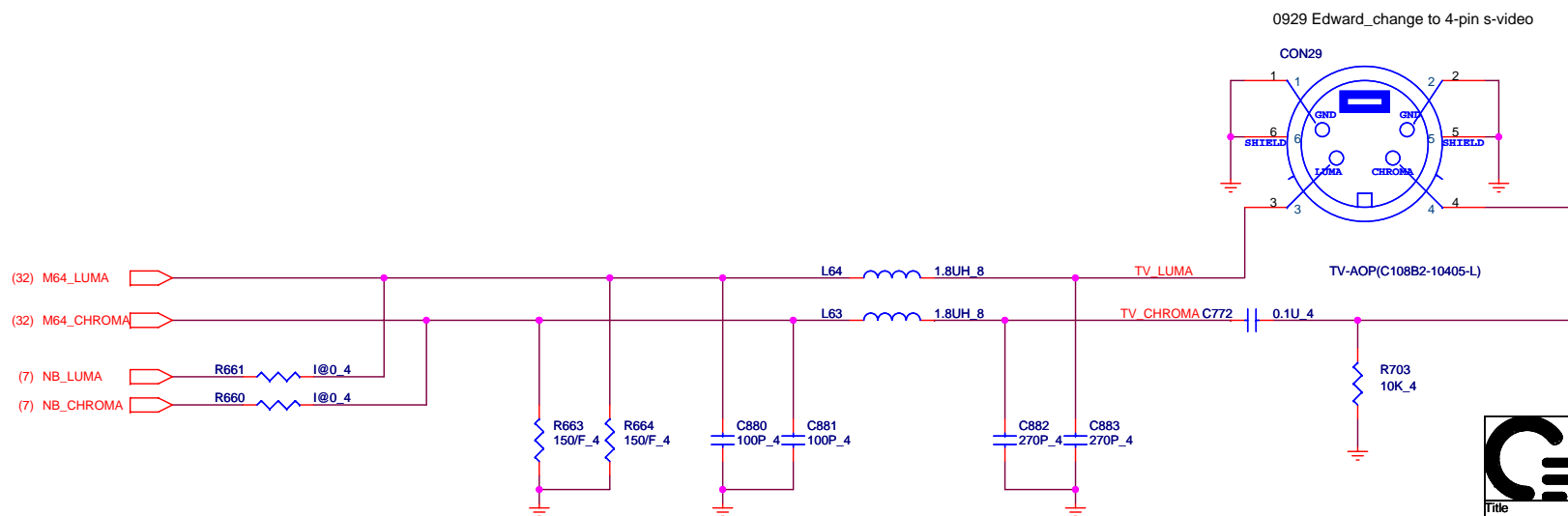
Title **GMCH Power 2 5 of 5**

Size B	Document Number GD1 Main Board	Rev 1A
-----------	--	-----------


Date: Tuesday, November 14, 2006 Sheet 9 of 35

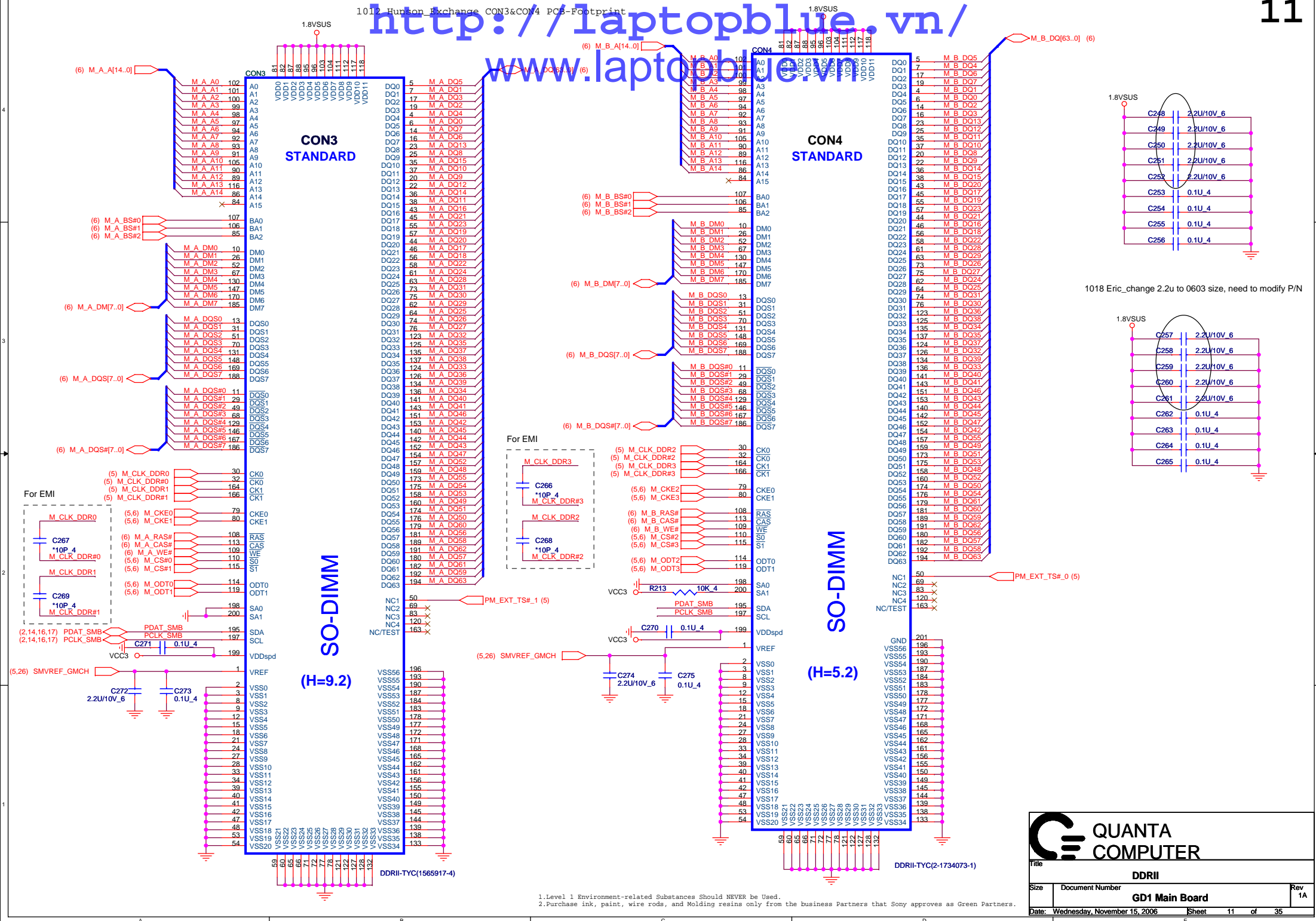


S-VIDEO_OUT



1. Level 1 Environment-related Substances Should NEVER be Used.
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 QUANTA COMPUTER			
Title			
CRT & S-VIDEO			
Size	Document Number		Rev
B	GD1 Main Board		1A
Date:	Wednesday, November 15, 2006	Sheet	10 of 35



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www.laptopblue.vn

XÁC NHẬN THỜI GIAN THẬT

ket nối với IO

CLEAR CMOS.

KHÔI ÂM THANH.

KHÔI HDD

KHÔI DVD

CÁC LỆNH LIÊN QUAN CPU

XOR Chain Entrance Strap

ICH_RSV0	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal operation(Default)
1	1	Set PCIE port config bit 1

ICH8 strap

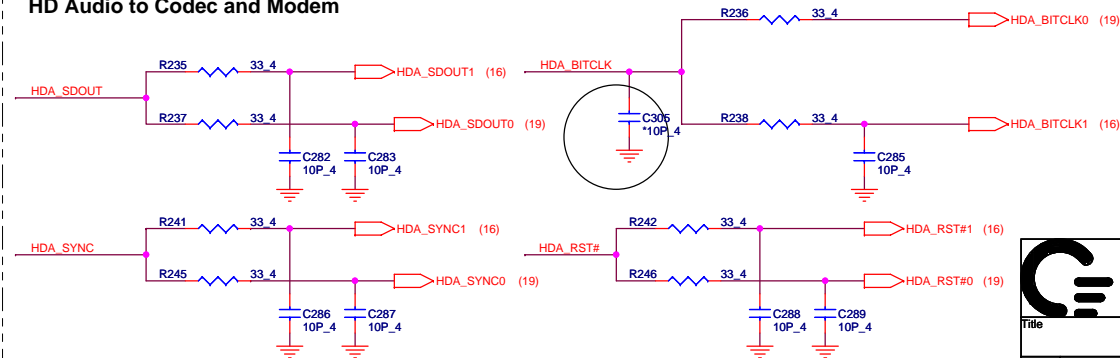
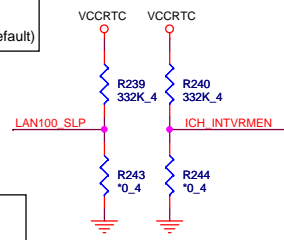
ICH8-M LAN100_SLP Strap
 (Internal VR for VccLAN1_05 and VccCL1_05)

LAN100_SLP	Low = Internal VR disable High = Internal VR enable(Default)
------------	---

ICH_INTVRMEN	Low = Disable High = Enable(Default)
--------------	---

ICH8-M Internal VR Enable strap
 (Internal VR for Vccsus1_05, VccSus1_5 and VccCL1_5)

HD Audio to Codec and Modem



1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

QUANTA COMPUTER

Title: **ICH8-M HOST 1 of 4**

Size: **Document Number**

GD1 Main Board

Date: **Tuesday, November 14, 2006** Sheet **12** of **35**

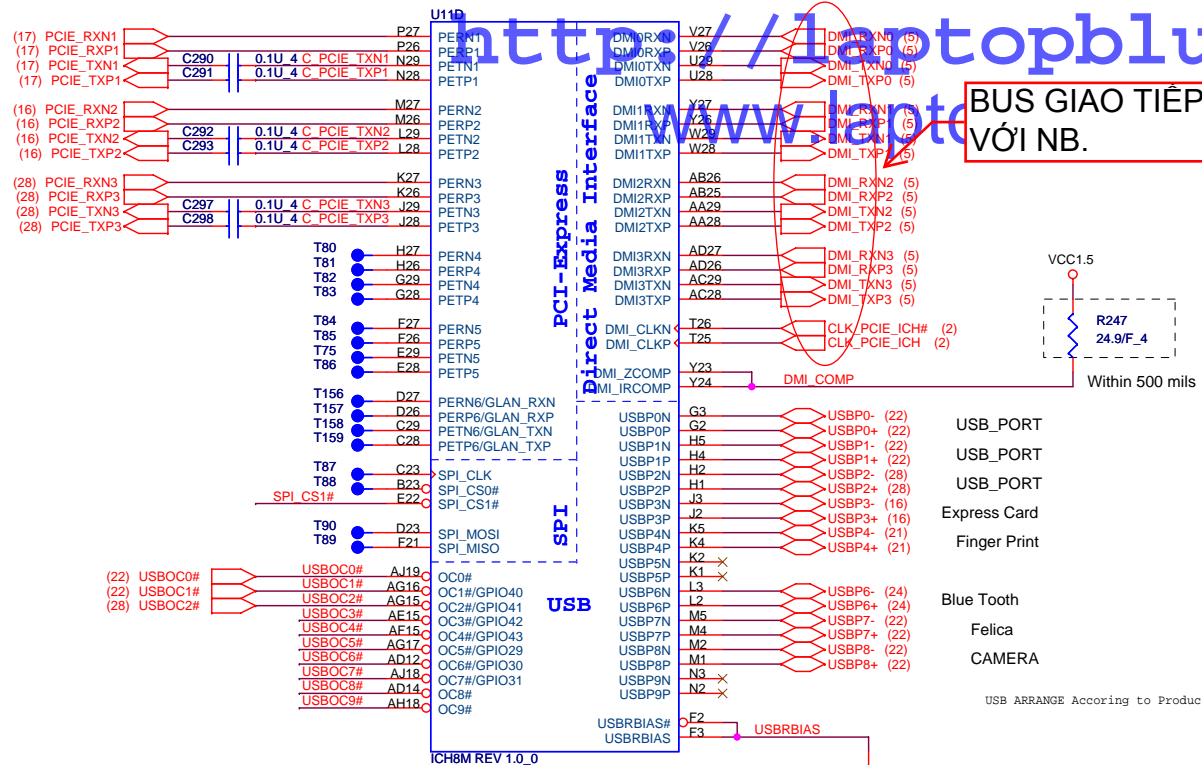
<http://laptopblue.vn/>

BUS GIAO TIẾP VỚI NB.

WireLess Lan

Express card

10/100 Lan



PCI bus info.

PCI DEVICE	IDSEL#	REQ# / GNT#	Interrupts
T18402	AD23	REQ0# / GNT0#	PIRQ E/F/G

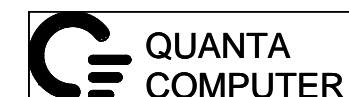
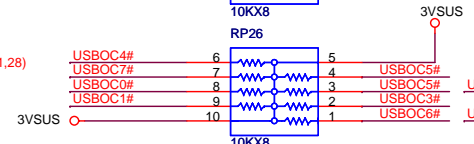
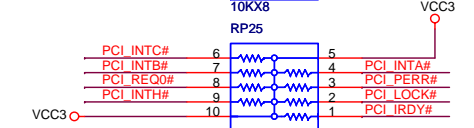
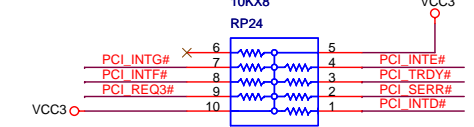
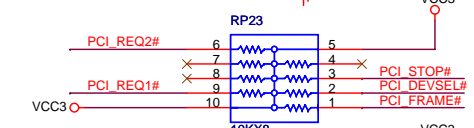
A16 SWAP Override strap

PCI_GNT#3	Low = A16 swap override enabled High = Default
-----------	---

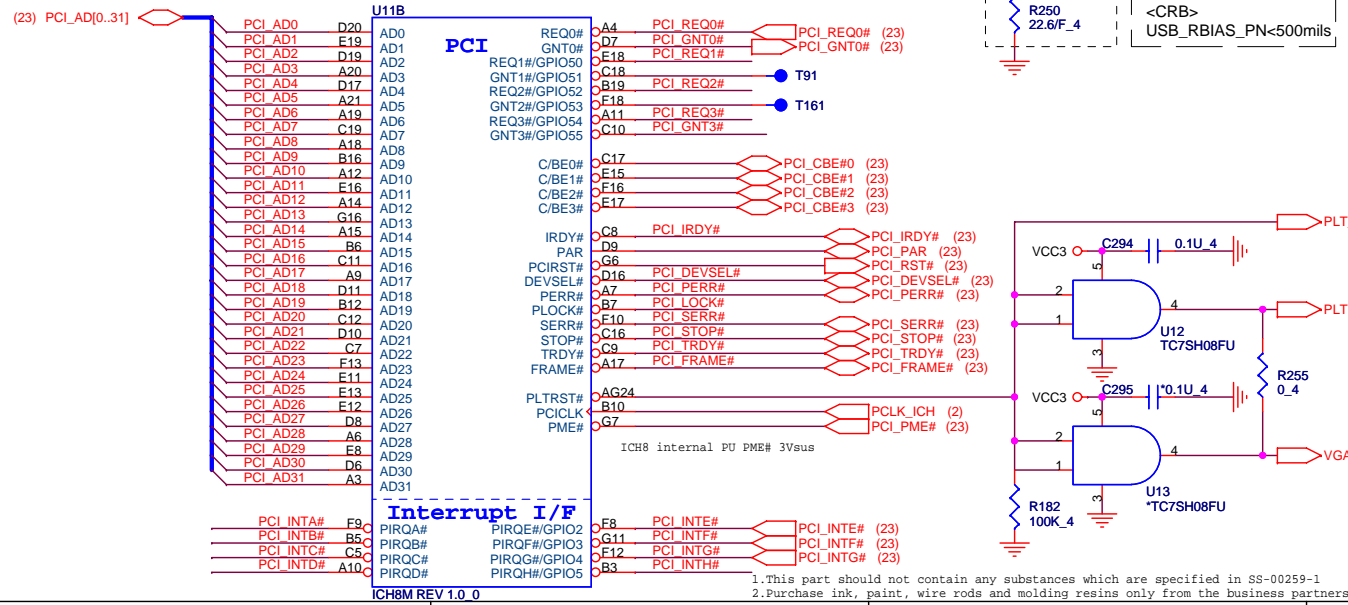


ICH8 Boot BIOS select

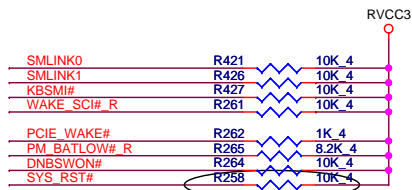
PCI_GNT#0	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC(Default)



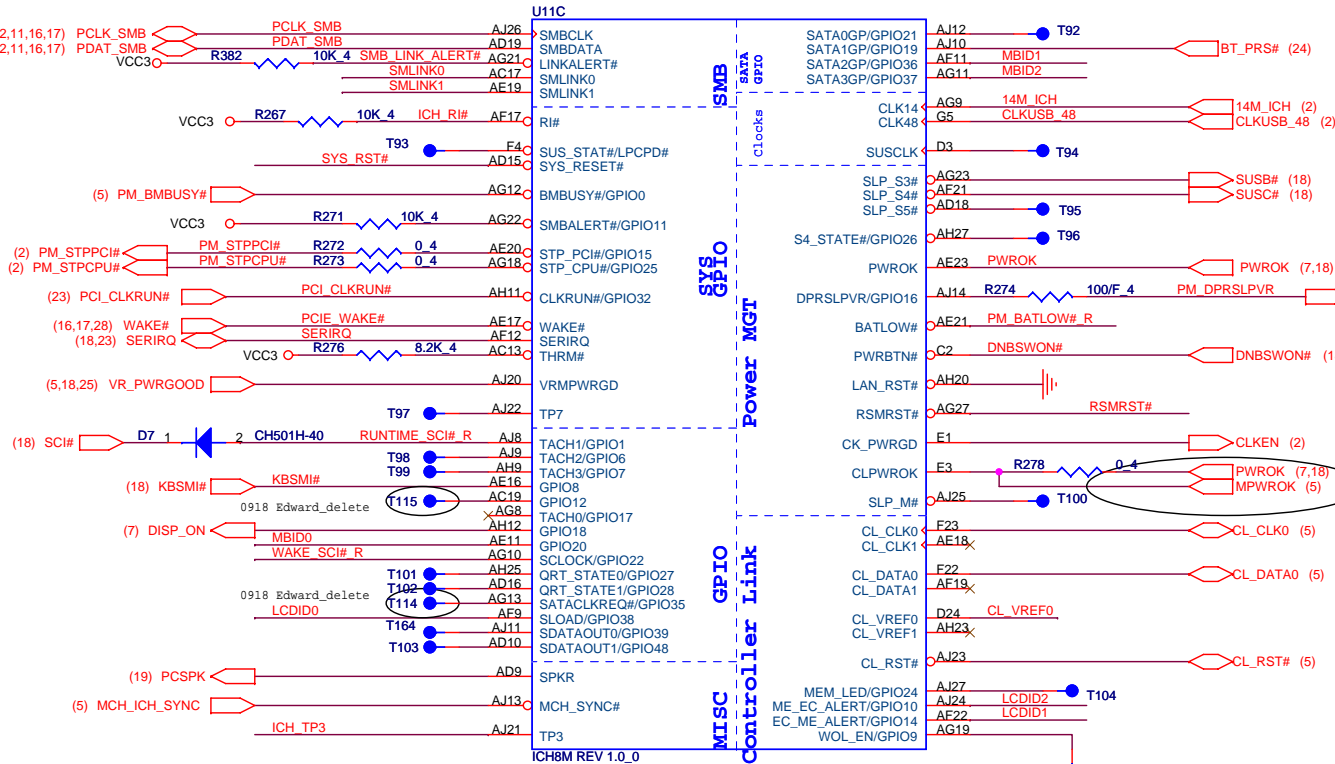
Title			ICH8-M PCIE 2 of 4
Size	Document Number	Rev	1A
B	GD1 Main Board		
Date:	Tuesday, November 14, 2006	Sheet	13 of 35



1. This part should not contain any substances which are specified in SS-00259-1.
2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

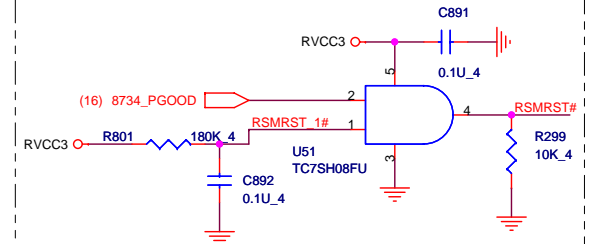


0915 Edward.Tsai follow intel check list



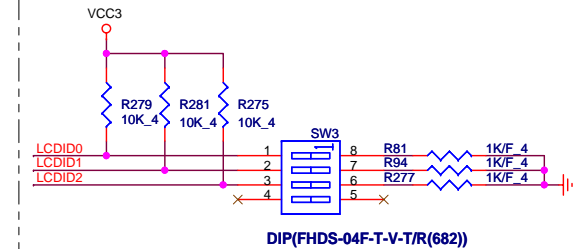
RSMRST# must be after RVCC3 10 ms

RSMRST#

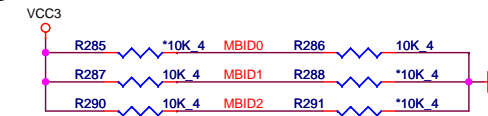


LCD ID

1012 Edward_Change SW3 PN#

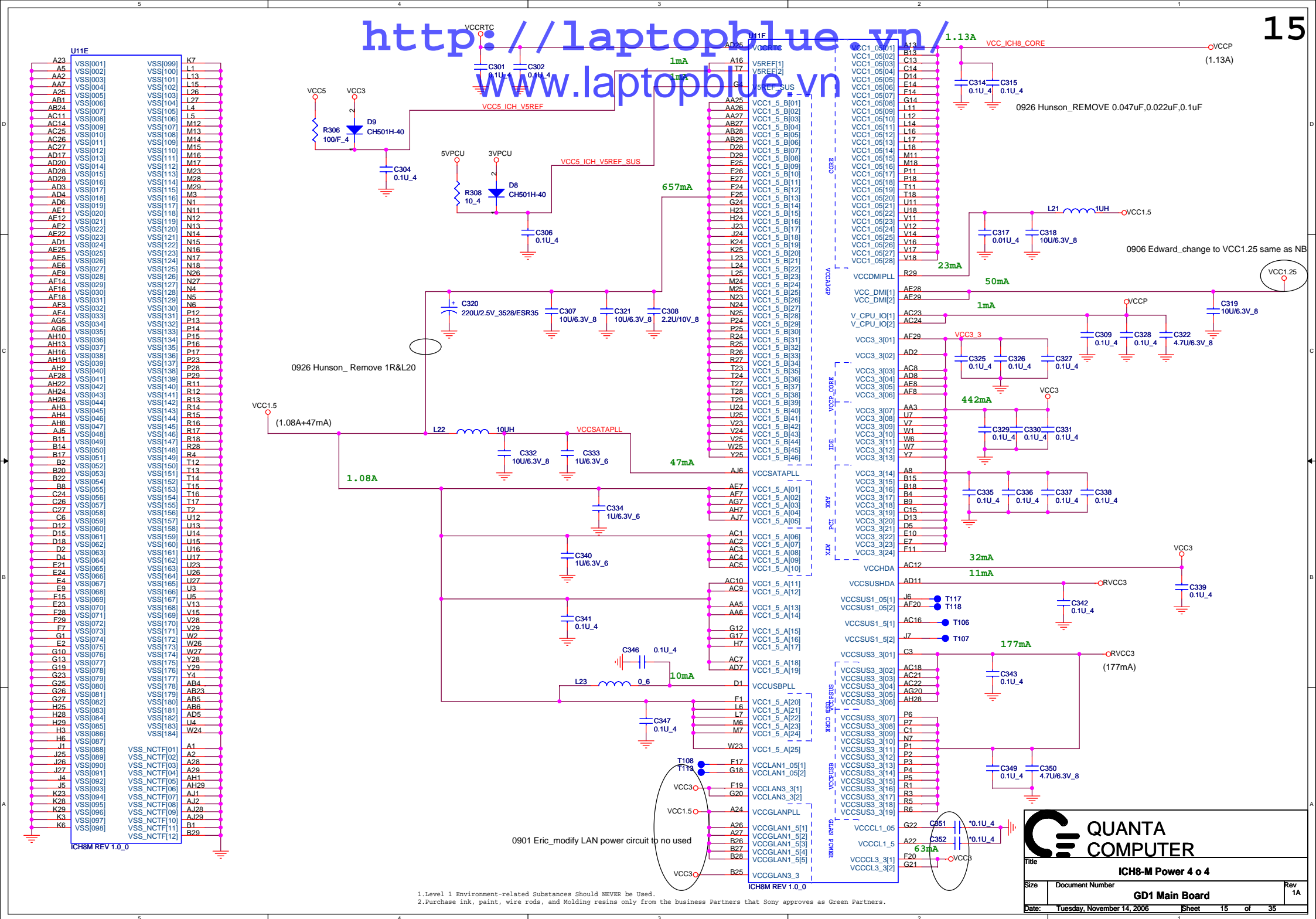


MB ID

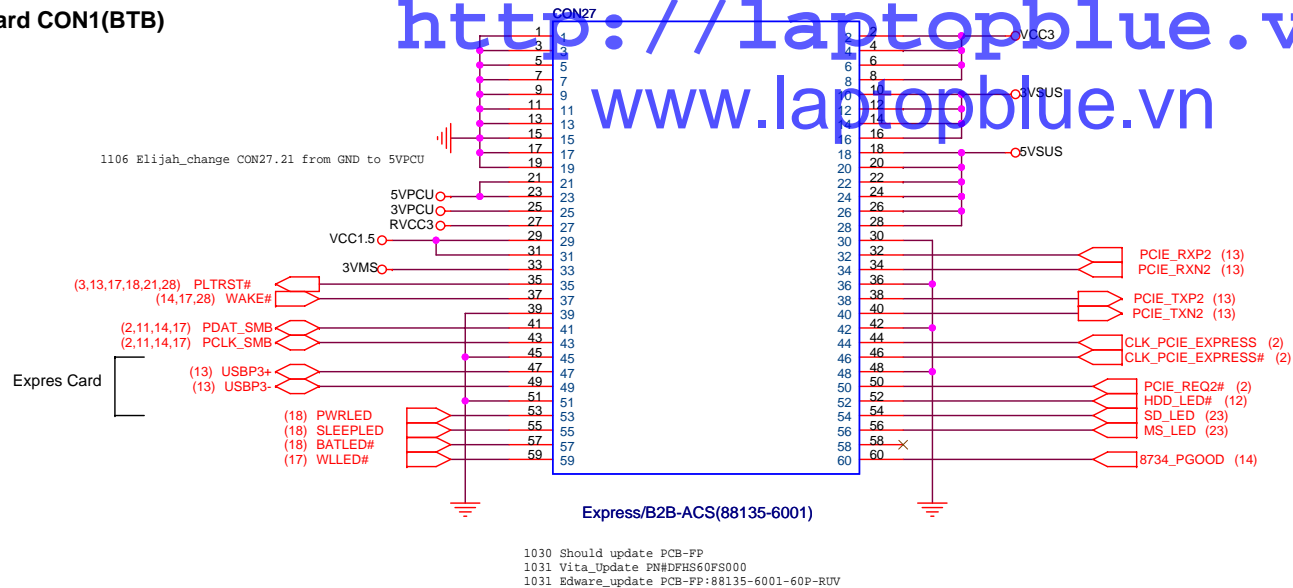


Title			ICH8-M GPIO 3 of 4
Size	Document Number	Rev	
B	GD1 Main Board	1A	
Date:	Wednesday, November 15, 2006	Sheet	14 of 35

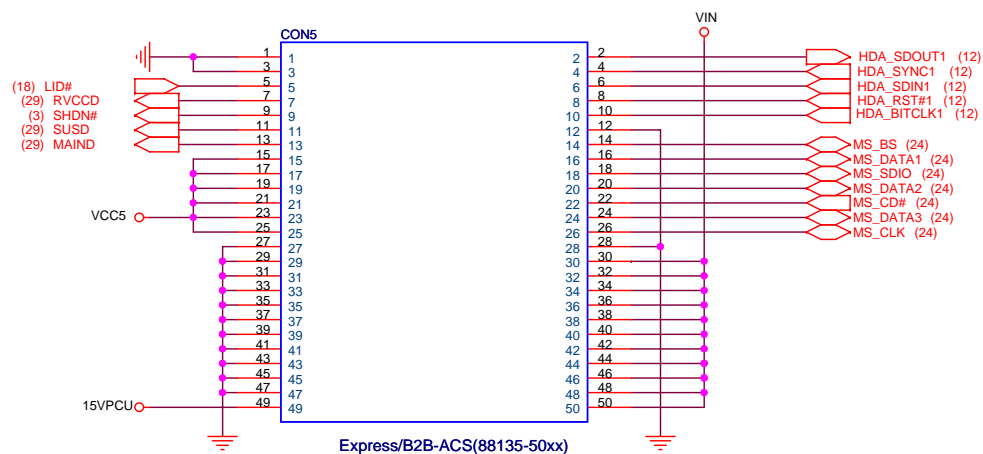
1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



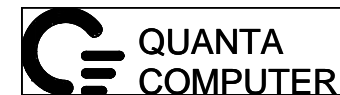
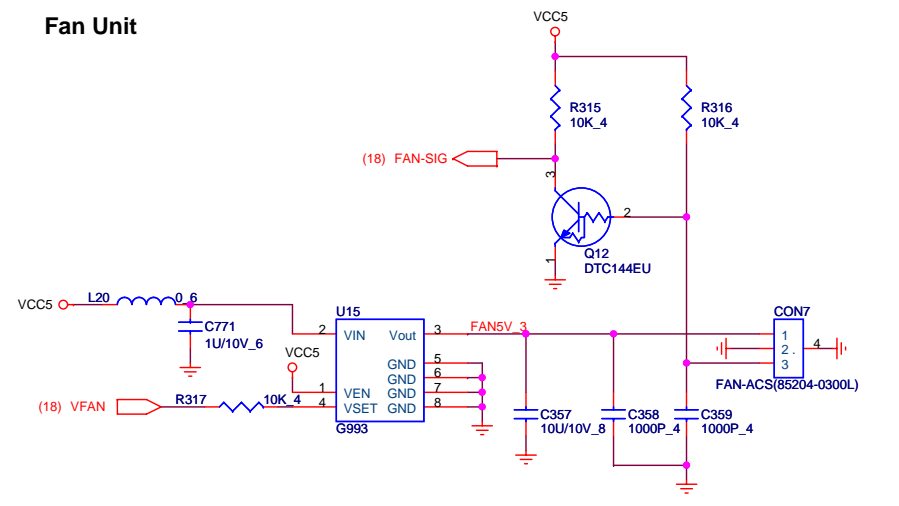
Express Board CON1(BTB)



Express Board CON2(BTB)



Fan Unit



Title		
Express Card		
Size	Document Number	Rev
B	GD1 Main Board	1A
Date:	Tuesday, November 14, 2006	Sheet 16 of 35

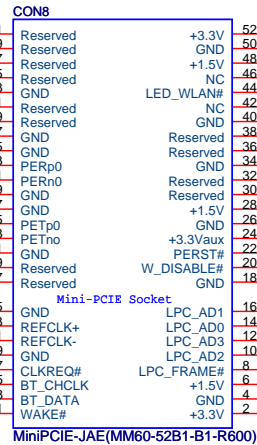
- This part should not contain any substances which are specified in SS-00259-1
- Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

<http://laptopblue.vn/>
www.laptopblue.vn

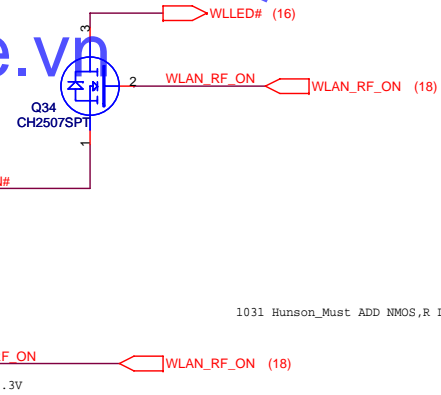
LED_GP :LED WLEN LINK
Output Current : 7mA
Blink rate : 1 flash per every 3 sec.

WLSW:
Low : disable the radio.
High : Enable the radio.

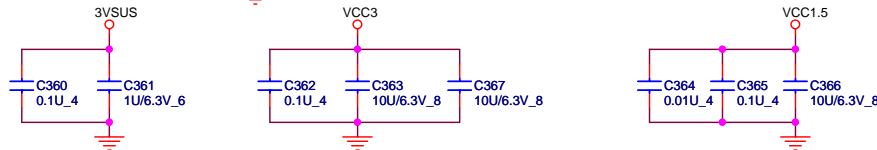
Mini PCI-E Card FOR WL




LED WLAN# MAX sink current=16mA
(recommend=1mA)
Drive in LED#B
+3.3V:1A
+1.5V:500mA
+3.3Vaux:5mA



1031 Hunson_Must ADD NMOS,R LED in Express/B



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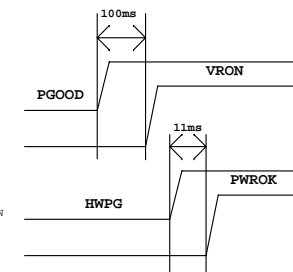
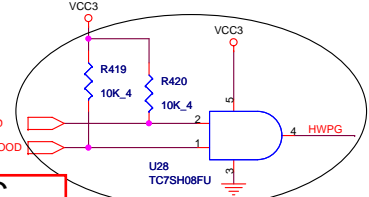
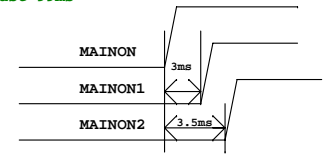
 QUANTA COMPUTER			
Title			
MINIPCIE / MDC			
Size	Document Number		
B	GD1 Main Board		
Date: Wednesday, November 15, 2006			Rev 1A
Sheet 17 of 35			

I/O Address	Index	Data
BADDR1-0	1 0	2E 2F
	1 1	4E 4F
0 0	HCFCGBAH, HCFCGBAL	(HCFCGBAH, HCFCGBAL)+1
0 1	XOR-TEST	

BT_PEN# : SHBM(If = 0 Enable share host BIOS memory)

PWROK must be after HWPG at least 10ms
VRON must be after PGOOD at least 99ms

MAINON <-- 3ms --> MAION1
MAINON <-- 3.5ms --> MAION2



**PWR TU CAC
KHOI NGUON
+TU NGUON
CPU BAO VE.**

0901 Hunson_volumute is low active

0831 Eric_BT_PEN change to high active

1013 Hunson Add Batt SCK&SDA

1107 Hunson_FAE recommend PD 10K on EVT version

1016 Hunson_Change EC to WPC8763L

QUANTA COMPUTER

Title: **WPC8763L & SPI FLASH**

Size: Document Number: **GD1 Main Board** Rev 1A

Date: Tuesday, November 14, 2006 Sheet 18 of 35

giao tiếp với SB

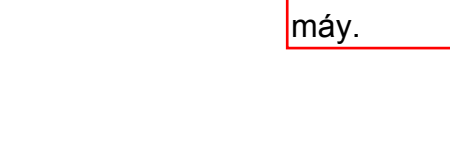
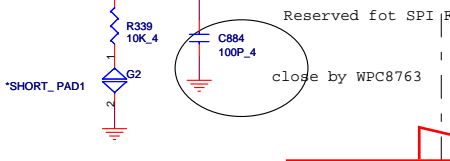
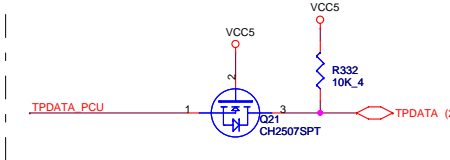
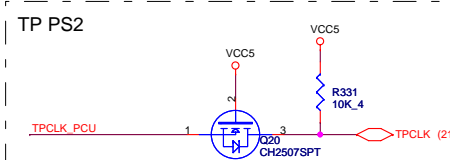
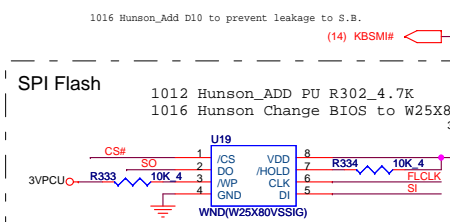
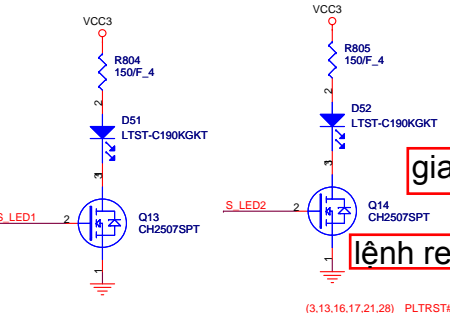
lệnh reset từ SB

giao tiếp rom bios

matrix 8 X 16 (kb)

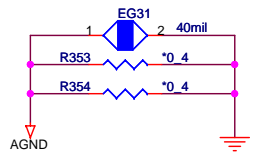
lên vi đk trên mặt máy.

osc 32,768hz.



JEN0(PIN24)	JENK(PIN53)	Functionality of PINS (17,20,21,23,25,27)	Functionality of PINS (47,48,50,51,52)
NC	NC	GPIO	KEYBOARD OUTPUT
10K PD	NC	JTAG Signal	KEYBOARD OUTPUT
NC	10K PD	GPIO	JTAG Signal
10K PD	10K PD	Illegal Strap combination	

Audio decoupling



1101 FAE comment

(20) EAPD

Low enable speaker

High enable HP

0928 EMI

1101 FAE comment

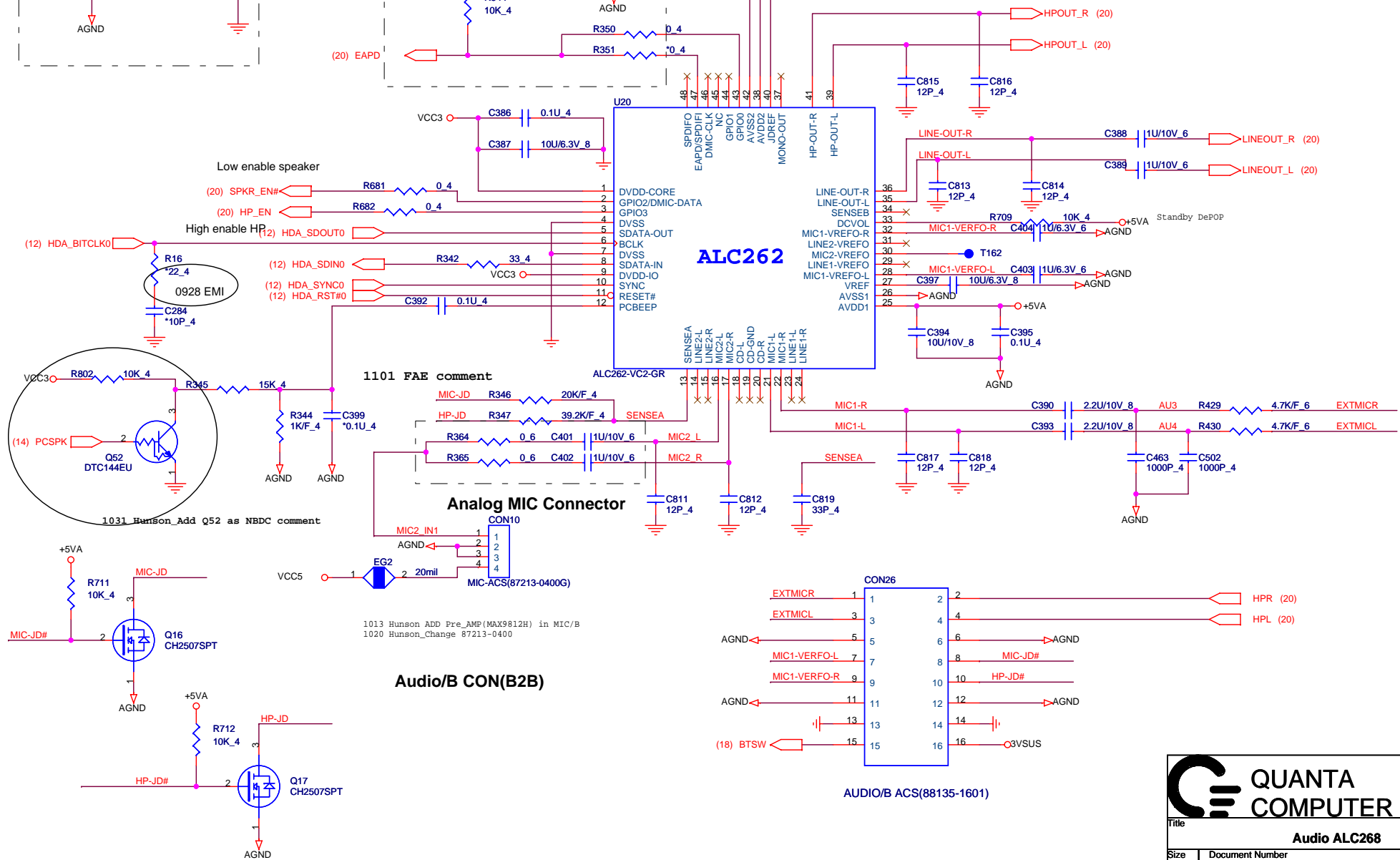
Analog MIC Connector

1013 Hunson ADD Pre_AMP(MAX9812H) in MIC/B
1020 Hunson_Change 87213-0400

Audio/B CON(B2B)

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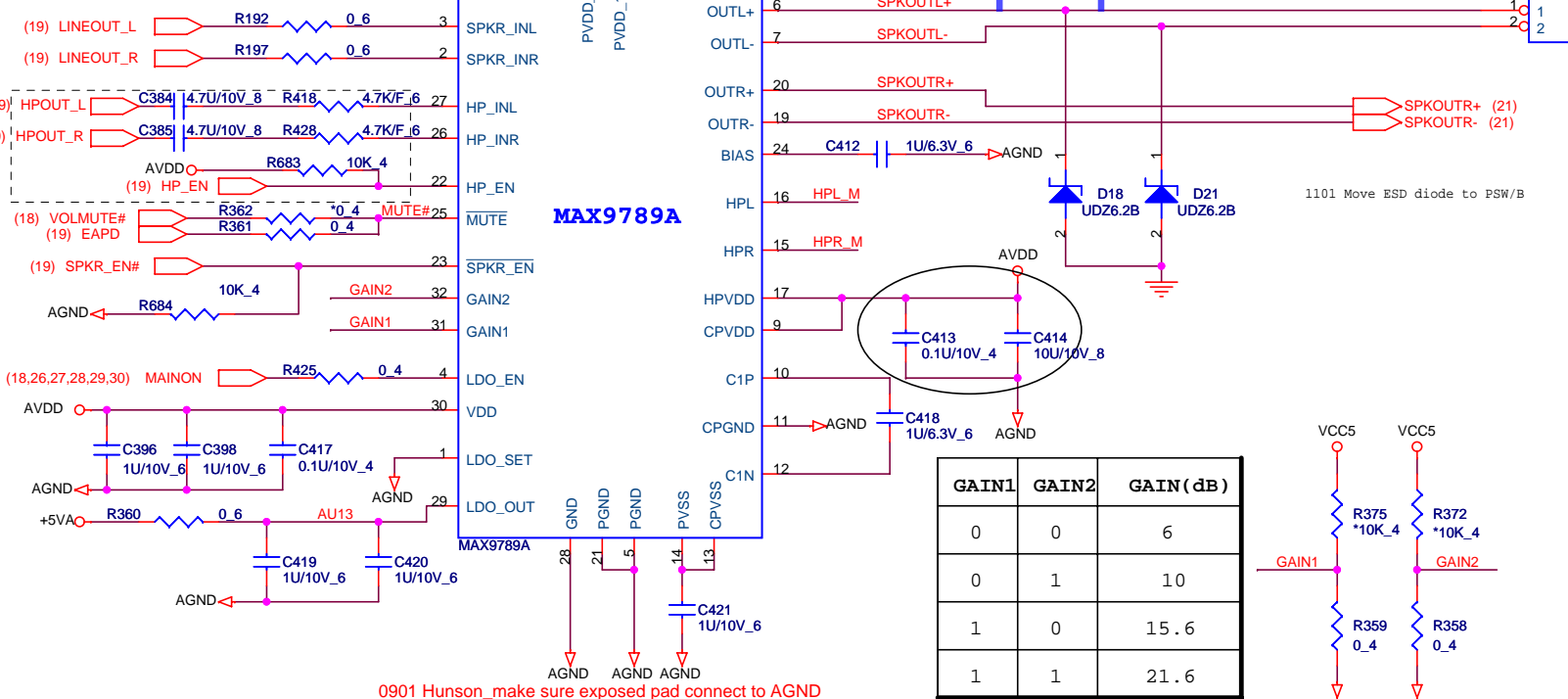
AUDIO/B ACS(88135-1601)

QUANTA
COMPUTER

Title				
Audio ALC268				
Size	Document Number			Rev
Custom	GD1 Main Board			1A
Date:	Tuesday, November 14, 2006	Sheet	19	of 35

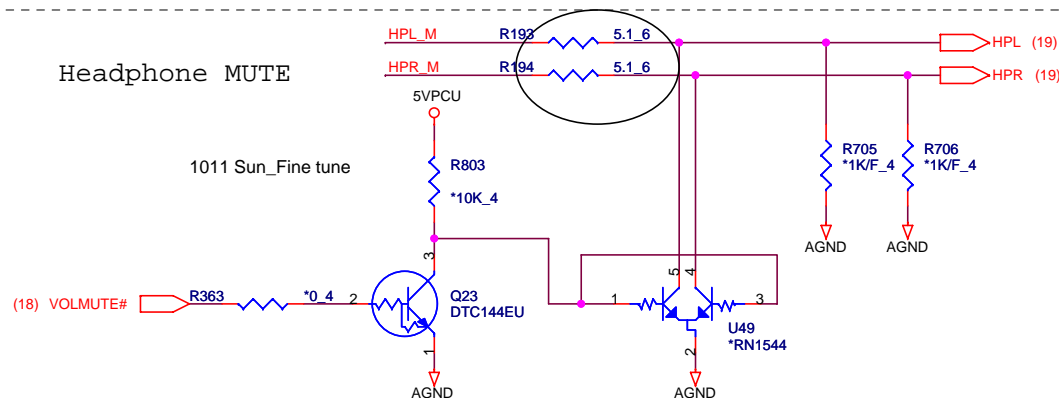
0904 Hunson_best Frequency response
 with 9789A from FAE comment

0904 Hunson_FAE advise reserve 0R



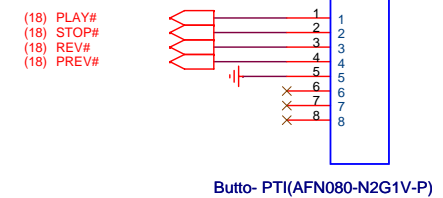
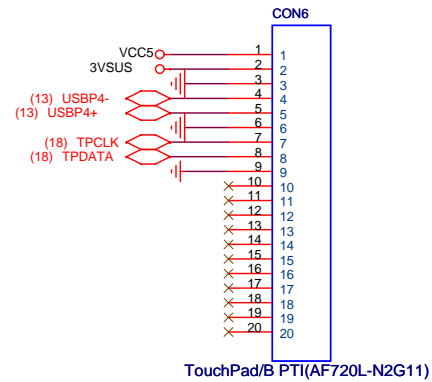
0901 Hunson_make sure exposed pad connect to AGND

Headphone MUTE

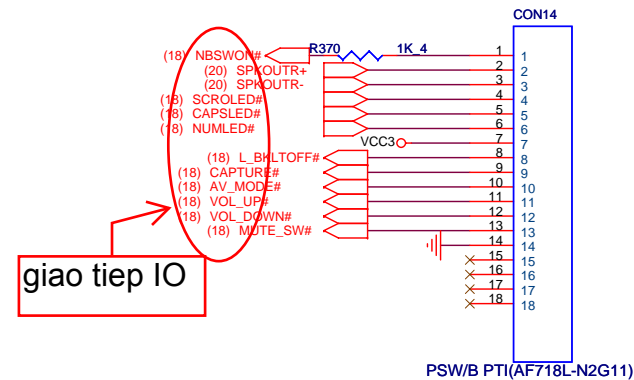
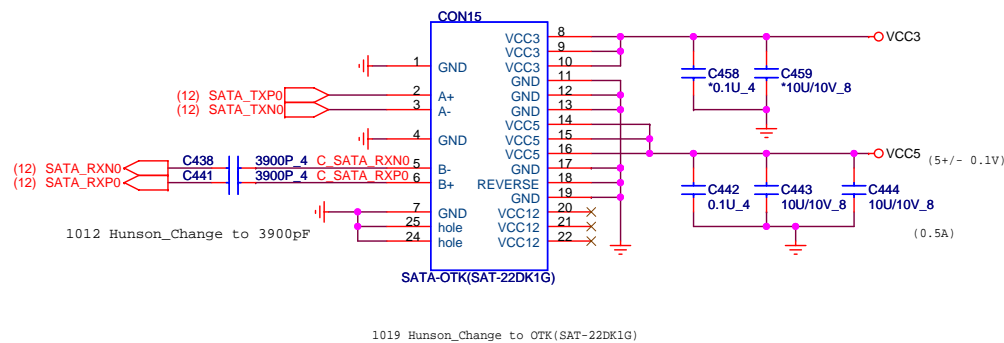


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Button CON(FFC)



PSW Board CON(FFC)



giao tiếp IO



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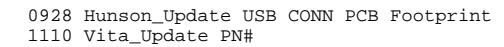
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Size	Document Number
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GD1 Main Board

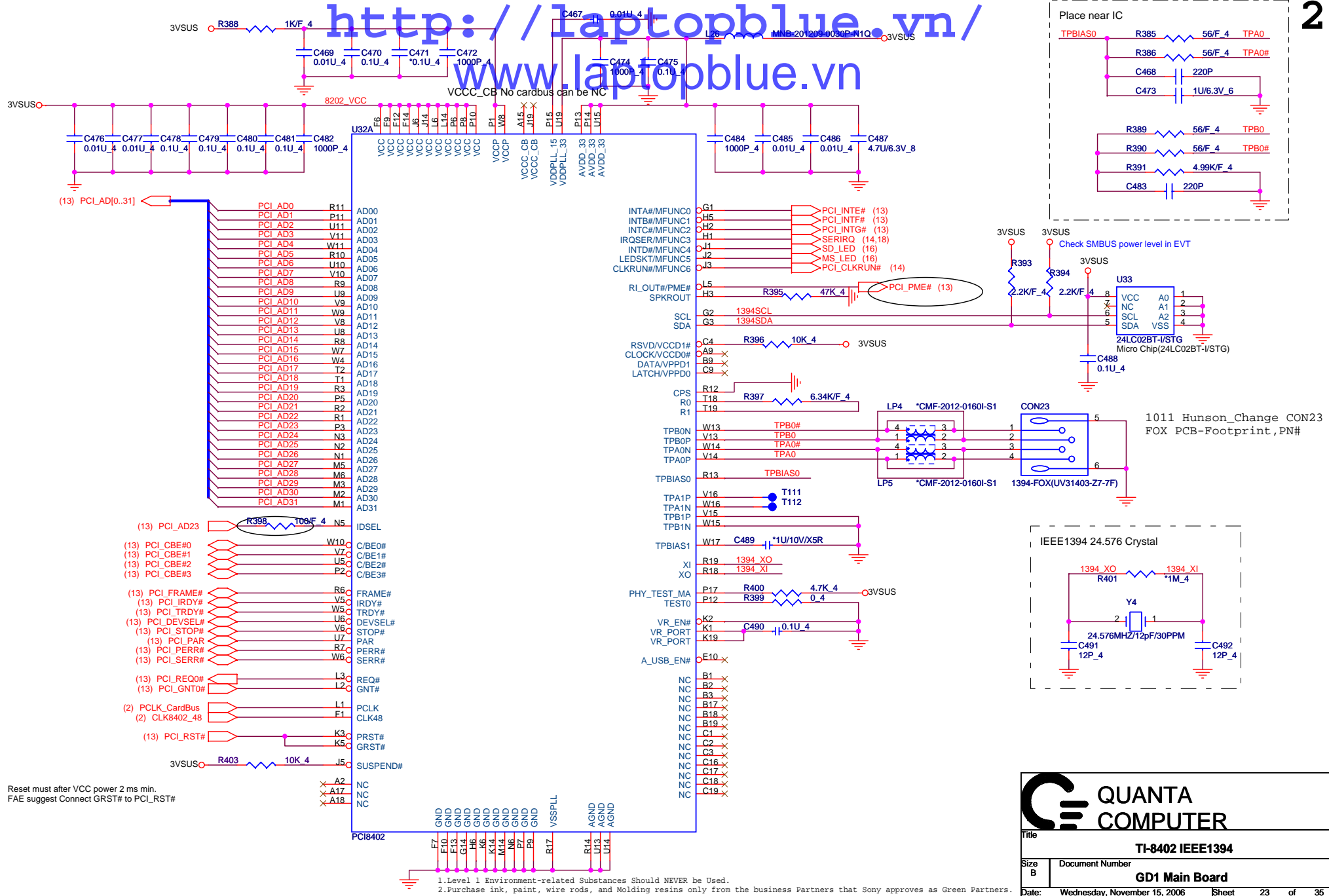
Date: Wednesday, November 15, 2006 Sheet 21 of 35

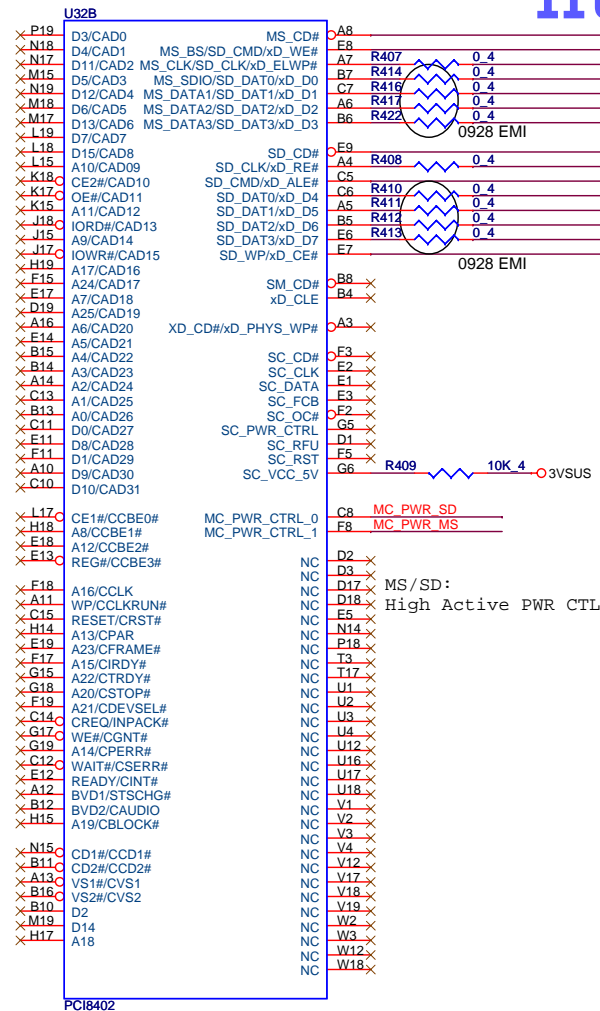
1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



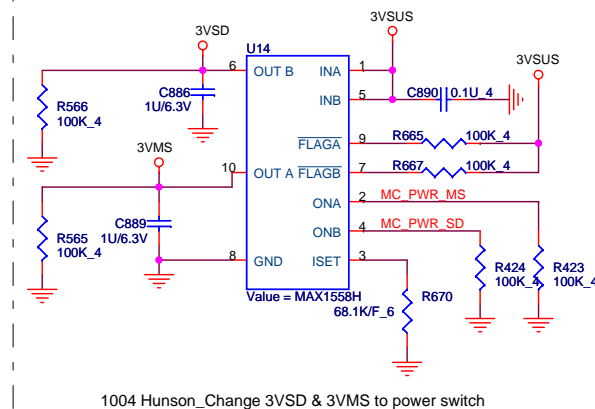
1. Level 1 Environment-related Substances Should NEVER be Used.
2. Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners

Rev
1A

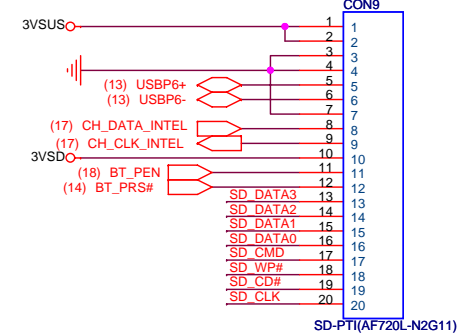




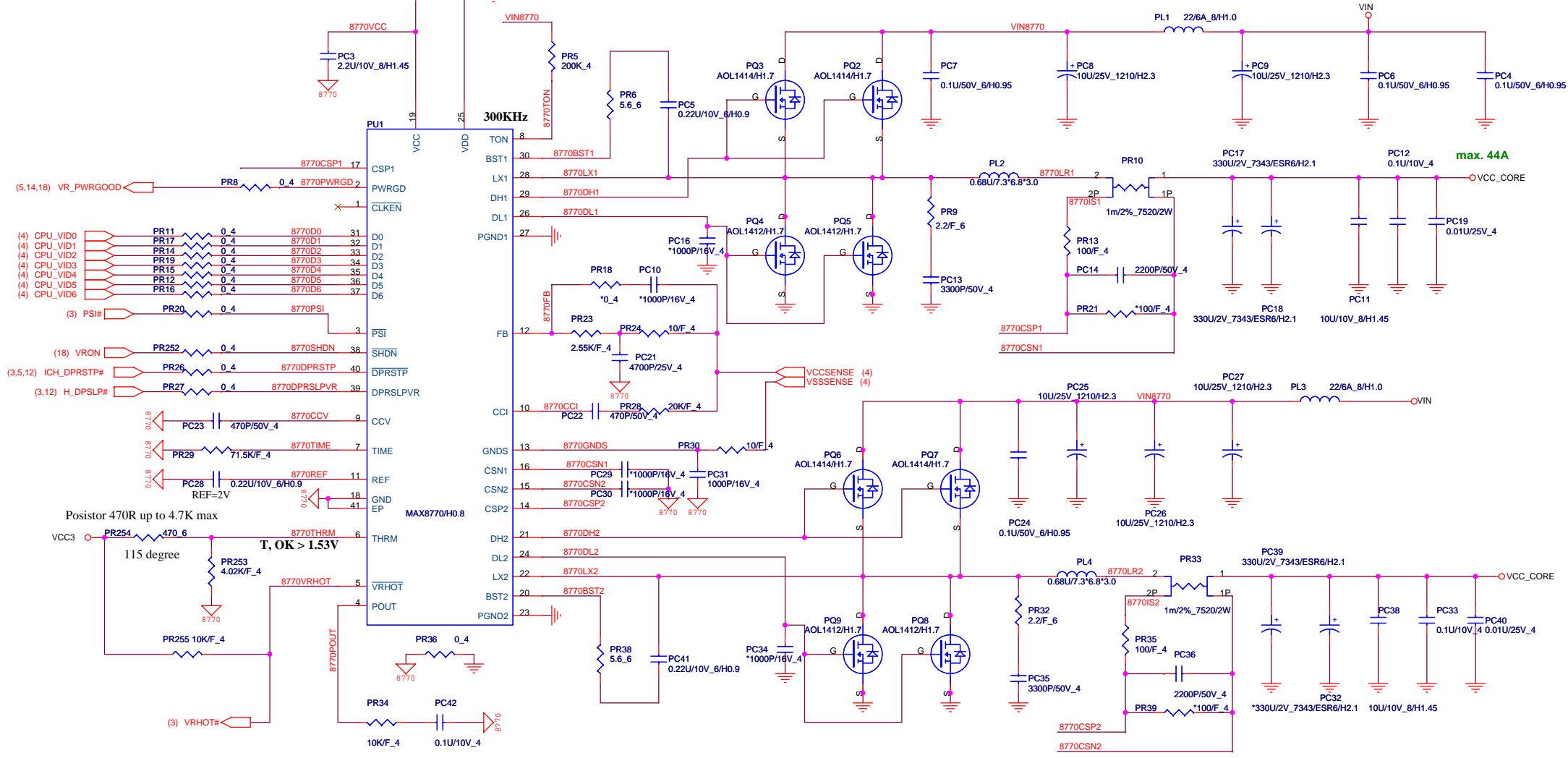
SD & MS Power Switch



SD CON(FPC)



1031 Hunson_Change pin assignment
 FAE comment:MS/SD_clock must have 3W space away DATA[0:3]

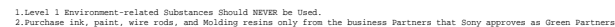


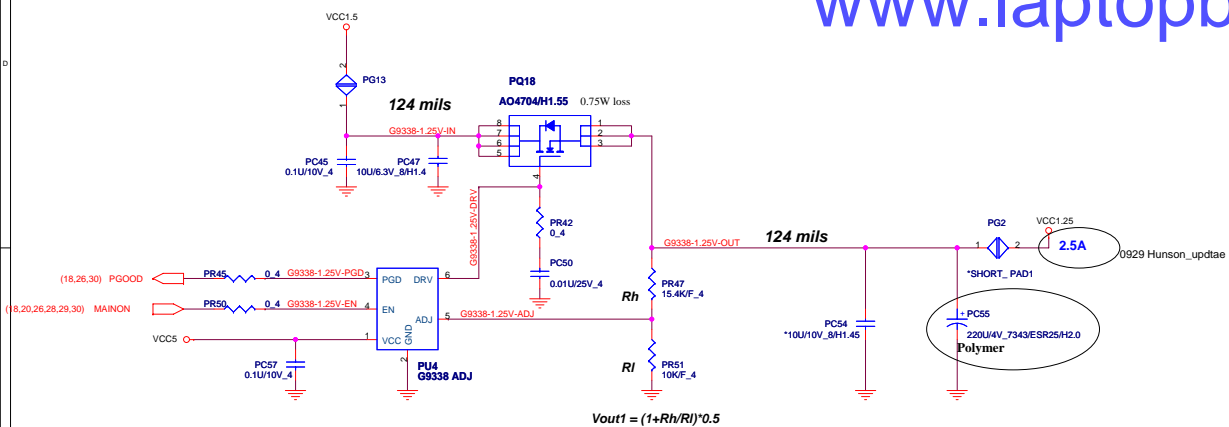
In progress

QUANTA
COMPUTER

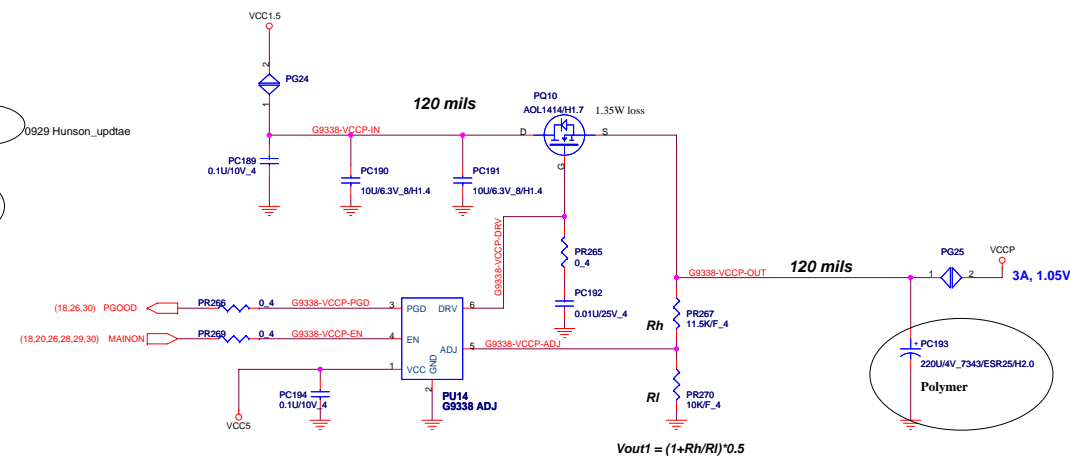
Title		CPU CORE (MAX8770)	
Size	Document Number	RD3 Main Board	
Custom		Rev 1A	
Date:	Tuesday, November 14, 2006	Sheet	25 of 35

1. Level 1 Environment-related Substances should NEVER be Used.
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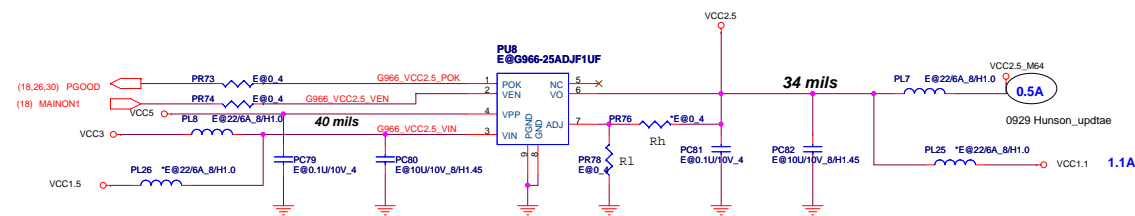
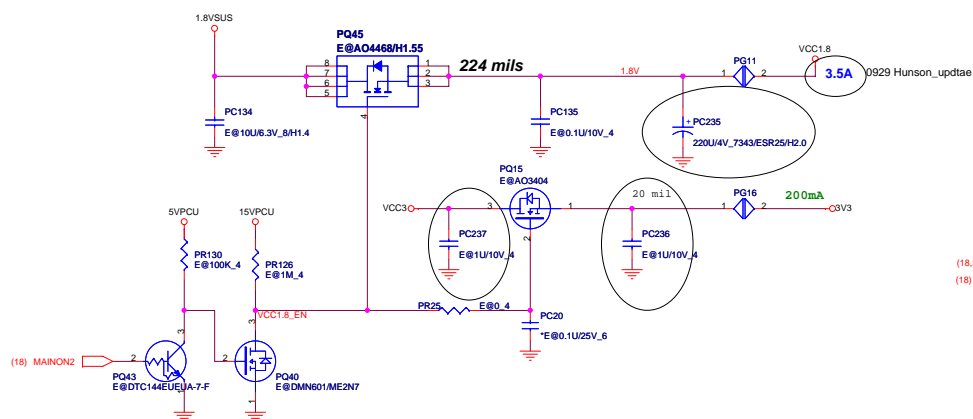




$$V_{out1} = (1 + R_h/R_I) \cdot 0.5$$



$$V_{out1} = (1 + R_h/R_I) \cdot 0.5$$



$$V_o = (0.8 \cdot (R_h + R_I)) / R_I$$

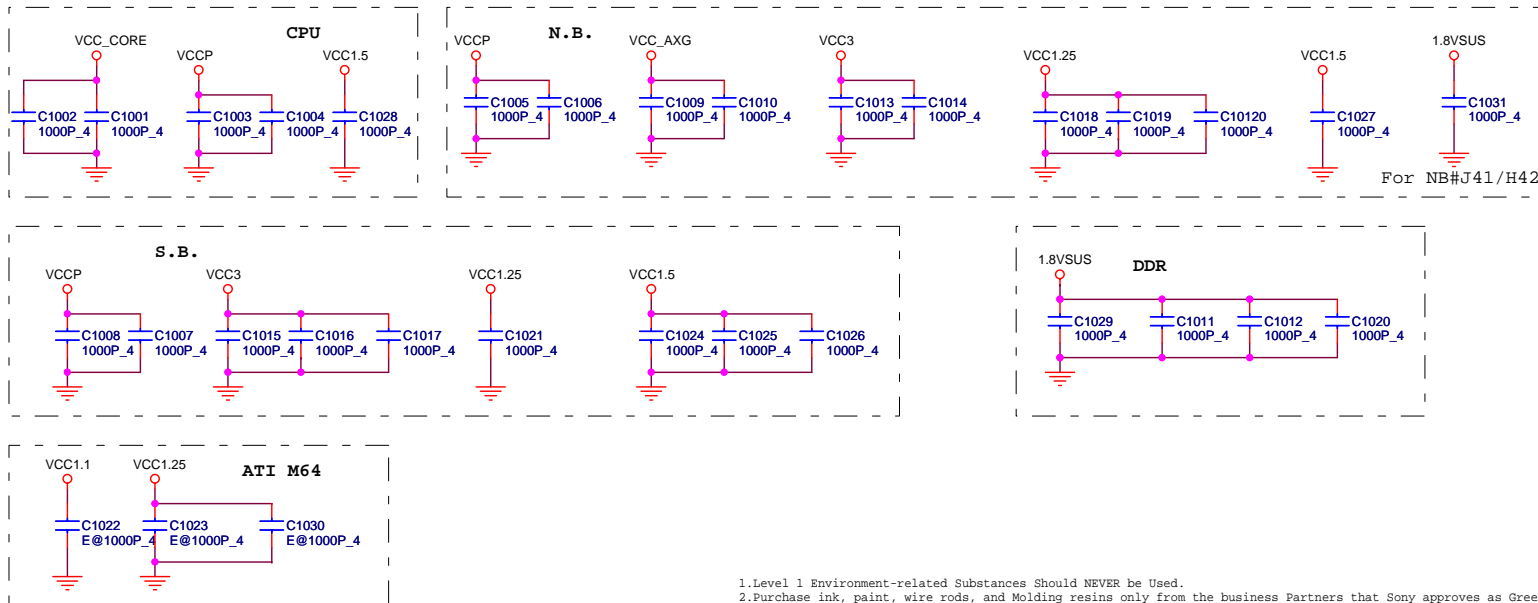
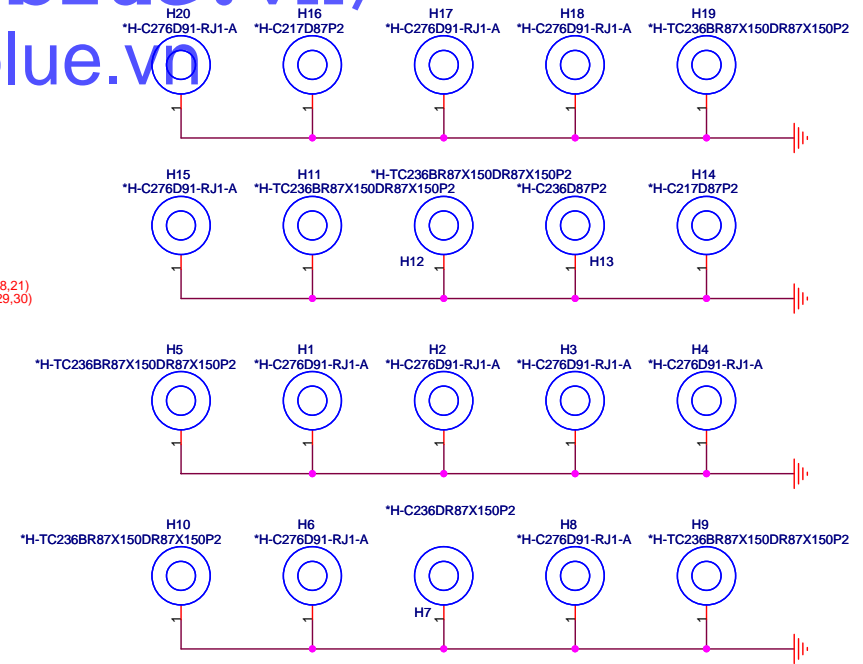
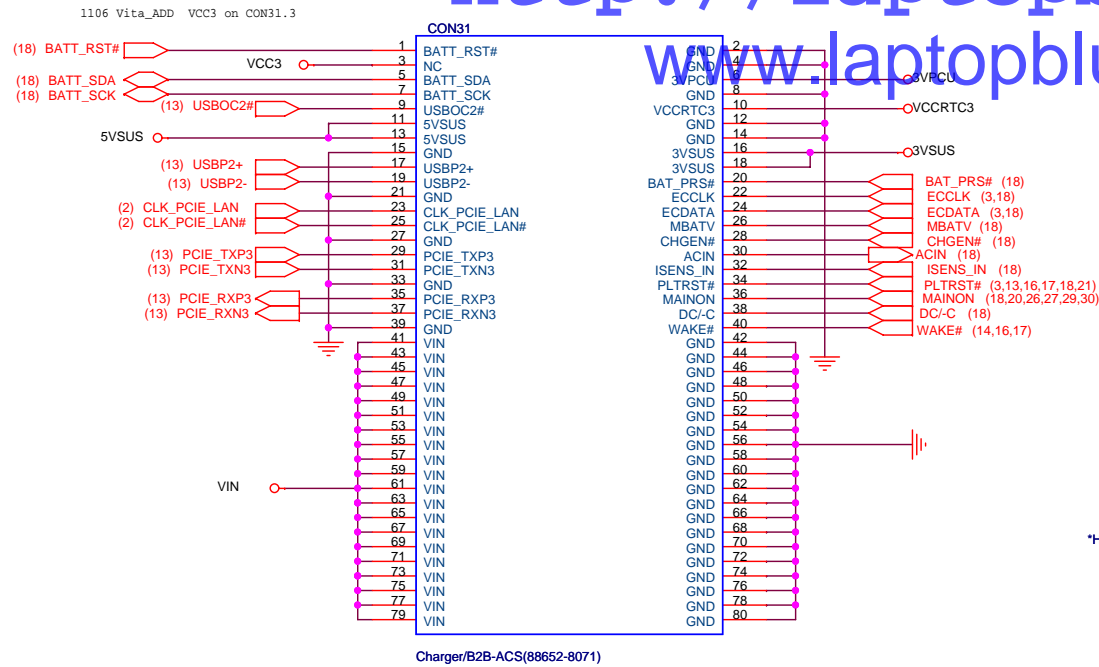
ADJ=GND, V_o = Fix 2.5V

In progress

QUANTA COMPUTER			
VCCP & VCC1.5 & VCC1.25			
File	RD3 Main Board		
Size	Document Number	Rev 1A	
Date	Tuesday, November 14, 2006	Sheet	27 of 35

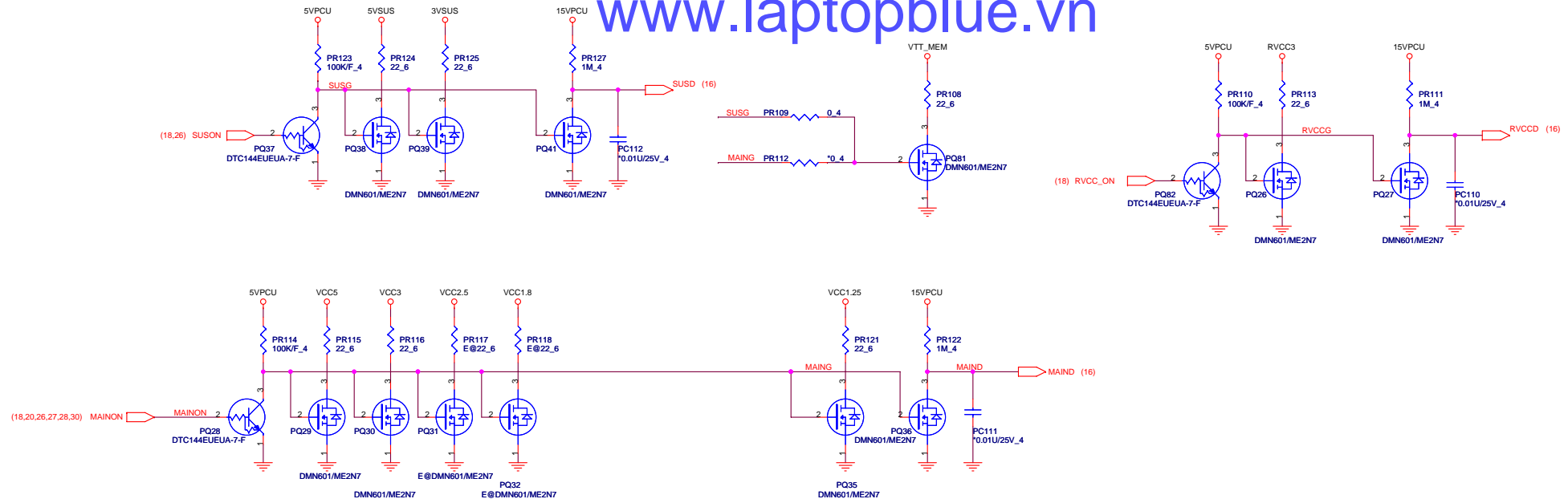
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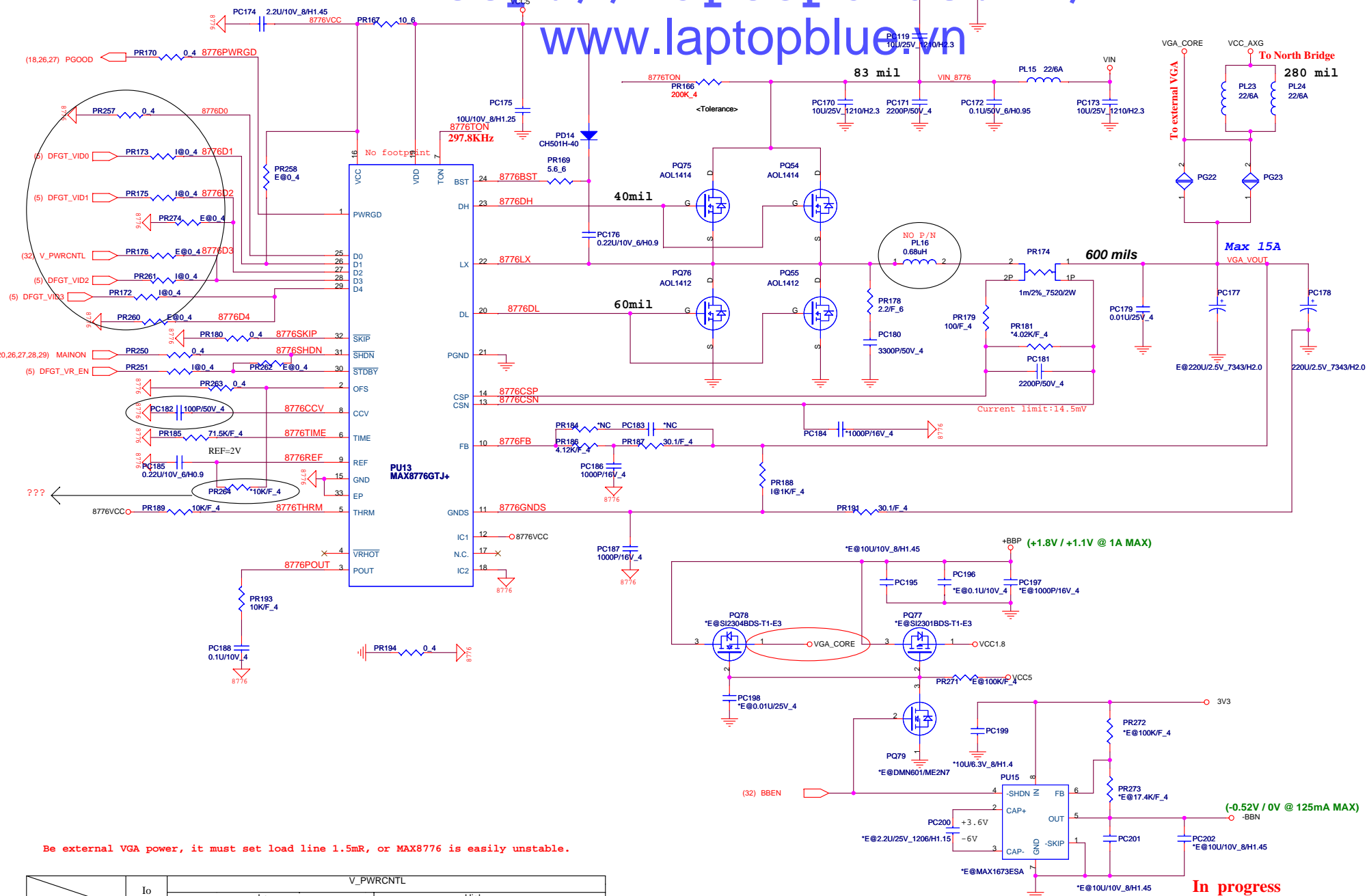
QUANTA COMPUTER			
Title Power Charger & LAN CON			
Size B	Document Number		Rev 1A
GD1 Main Board			
Date: Wednesday, November 15, 2006		Sheet 28	of 35



In progress

 QUANTA COMPUTER		
Title: 3VPCU & 5VPCU		
Size: Custom	Document Number: RD3 Main Board	Rev: 1A
Date: Wednesday, November 15, 2006	Sheet: 29	of 35

1.Level 1 Environment-related Substances Should NEVER be Used.
 2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



Be external VGA power, it must set load line 1.5mR, or MAX8776 is easily unstable.

	Io	V_PWRCNTL	
		Low	High
Ext :ATI M71-M74	15A	1.2V (D4,D3,D2,D1,D0=0,0,0,1,0)	1.0V (D4,D3,D2,D1,D0=0,1,0,1,0)
Int :North Bridge	7.7A	VID	

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U3001A

PART 1 OF 7

GFX_TXP0	AK33	PCIE_RX0P	PCIE_TX0P	AG31	C GFX_RXP0	C3000	E@0.1U_4	GFX_RXP0	E@0.1U_4	GFX_RXN0	AG30	C GFX_RXN0	C3001	E@0.1U_4	GFX_RXN0
GFX_TXN0	AJ33	PCIE_RX0N	PCIE_TX0N	AG30	C GFX_RXN0	C3001	E@0.1U_4	GFX_RXN0	E@0.1U_4	GFX_RXN0	AG30	C GFX_RXN0	C3001	E@0.1U_4	GFX_RXN0
GFX_TXP1	AJ35	PCIE_RX1P	PCIE_TX1P	AF31	C GFX_RXP1	C3002	E@0.1U_4	GFX_RXP1	E@0.1U_4	GFX_RXN1	AF30	C GFX_RXN1	C3003	E@0.1U_4	GFX_RXN1
GFX_TXN1	AJ34	PCIE_RX1N	PCIE_TX1N	AF30	C GFX_RXN1	C3003	E@0.1U_4	GFX_RXN1	E@0.1U_4	GFX_RXN1	AF30	C GFX_RXN1	C3003	E@0.1U_4	GFX_RXN1
GFX_TXP2	AH35	PCIE_RX2P	PCIE_TX2P	AF28	C GFX_RXP2	C3004	E@0.1U_4	GFX_RXP2	E@0.1U_4	GFX_RXN2	AF27	C GFX_RXN2	C3005	E@0.1U_4	GFX_RXN2
GFX_TXN2	AH34	PCIE_RX2N	PCIE_TX2N	AF27	C GFX_RXN2	C3005	E@0.1U_4	GFX_RXN2	E@0.1U_4	GFX_RXN2	AF27	C GFX_RXN2	C3005	E@0.1U_4	GFX_RXN2
GFX_TXP3	AG35	PCIE_RX3P	PCIE_TX3P	AD31	C GFX_RXP3	C3006	E@0.1U_4	GFX_RXP3	E@0.1U_4	GFX_RXN3	AD30	C GFX_RXN3	C3007	E@0.1U_4	GFX_RXN3
GFX_TXN3	AG34	PCIE_RX3N	PCIE_TX3N	AD30	C GFX_RXN3	C3007	E@0.1U_4	GFX_RXN3	E@0.1U_4	GFX_RXN3	AD30	C GFX_RXN3	C3007	E@0.1U_4	GFX_RXN3
GFX_TXP4	AF33	PCIE_RX4P	PCIE_TX4P	AD28	C GFX_RXP4	C3008	E@0.1U_4	GFX_RXP4	E@0.1U_4	GFX_RXN4	AD27	C GFX_RXN4	C3009	E@0.1U_4	GFX_RXN4
GFX_TXN4	AE33	PCIE_RX4N	PCIE_TX4N	AD27	C GFX_RXN4	C3009	E@0.1U_4	GFX_RXN4	E@0.1U_4	GFX_RXN4	AD27	C GFX_RXN4	C3009	E@0.1U_4	GFX_RXN4
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GFX_TXN5	AE34	PCIE_RX5N	PCIE_TX5N	AB30	C GFX_RXN5	C3011	E@0.1U_4	GFX_RXN5	E@0.1U_4	GFX_RXN5	AB30	C GFX_RXN5	C3011	E@0.1U_4	GFX_RXN5
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GFX_TXN6	AD34	PCIE_RX6N	PCIE_TX6N	AB27	C GFX_RXN6	C3013	E@0.1U_4	GFX_RXN6	E@0.1U_4	GFX_RXN6	AB27	C GFX_RXN6	C3013	E@0.1U_4	GFX_RXN6
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GFX_TXN12	U33	PCIE_RX12N	PCIE_TX12N	V27	C GFX_RXN12	C3025	E@0.1U_4	GFX_RXN12	E@0.1U_4	GFX_RXN12	V27	C GFX_RXN12	C3025	E@0.1U_4	GFX_RXN12
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GFX_TXN13	U34	PCIE_RX13N	PCIE_TX13N	U30	C GFX_RXN13	C3027	E@0.1U_4	GFX_RXN13	E@0.1U_4	GFX_RXN13	U30	C GFX_RXN13	C3027	E@0.1U_4	GFX_RXN13
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E@ATI-M64-M

Clock

PCIE_REFCLKP

PCIE_REFCLKN

RSVD_1

RSVD_2

PERSTB

Calibration

PCIE_CALRN

PCIE_CALRP

PCIE_CALI

(2) CLK_PCIE_VGA

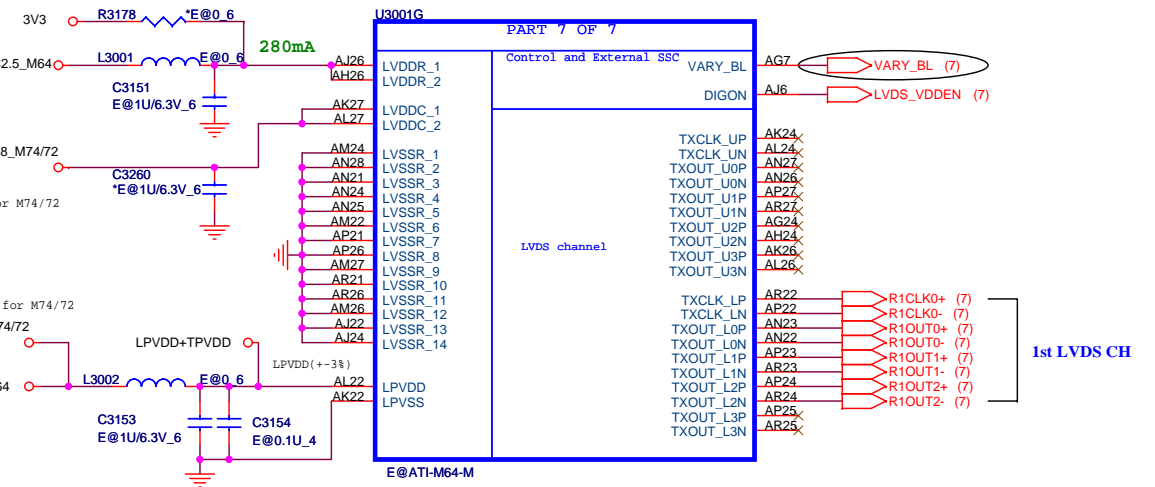
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T3027

T3028

(13) VGA_PLTRST#

AM32



QUANTA
COMPUTER

Title

ATI M64M PCIE

Size

Document Number

GD1 Main Board

Rev

1A

Date:

Tuesday, November 14, 2006

Sheet

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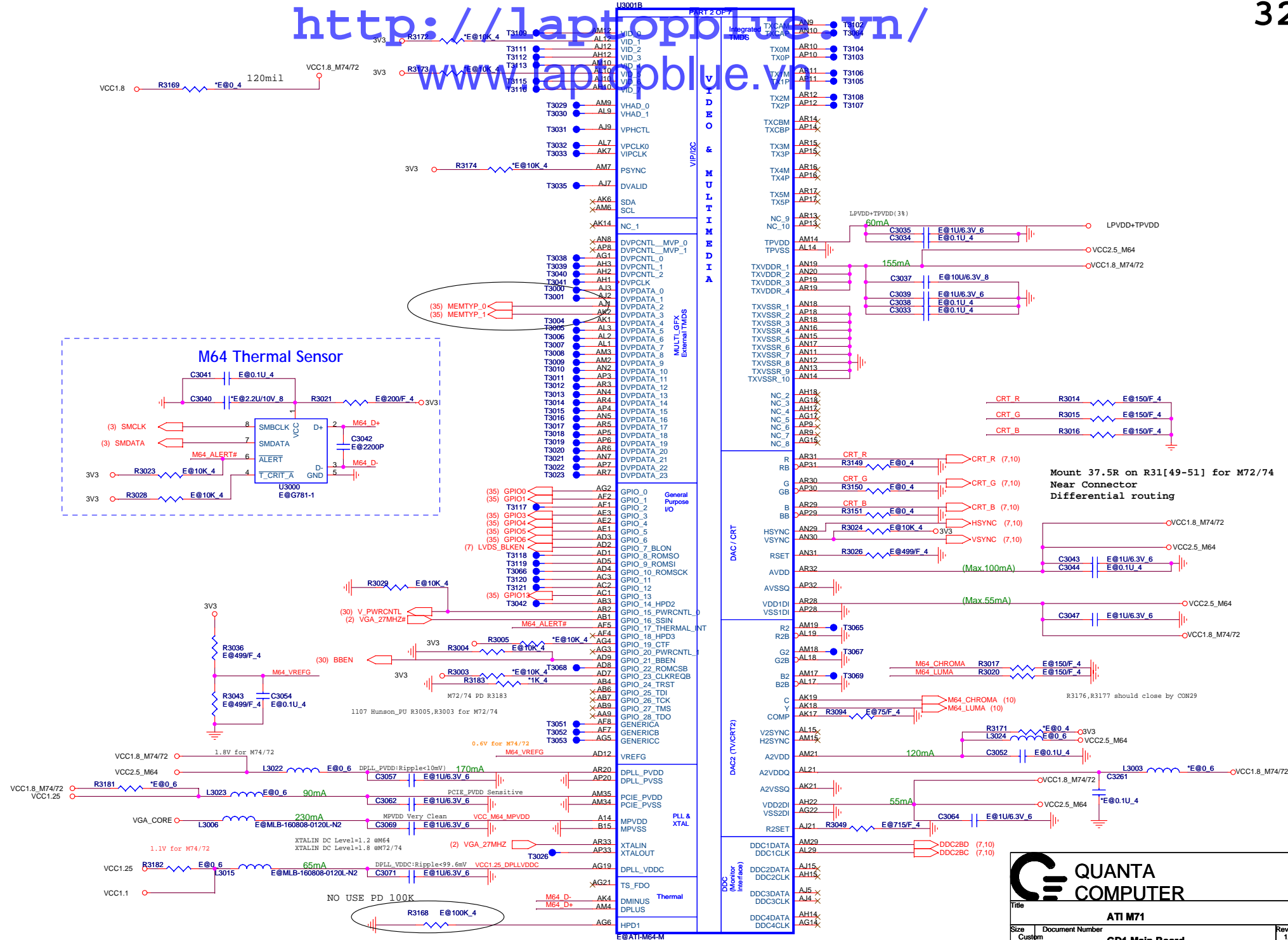
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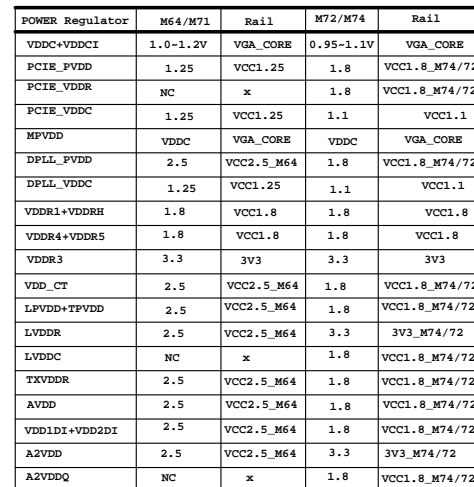
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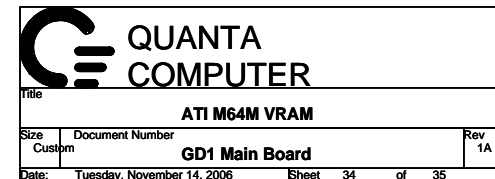
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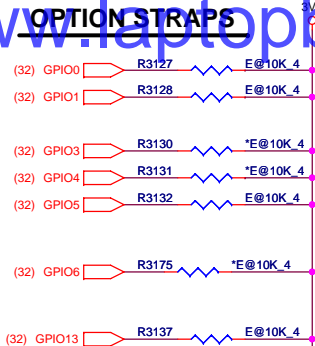






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M64-M Strap



VRAM Straps			REV. 0.1
STRAPS	PIN	DESCRIPTION	VALUE
MEMTYPE[1:0]	GPIO 27,26	Memory identification for BIOS 00 - Infineon GDDR3 : 2M x 32 bits x 4 banks(Default) 01 - SAMSUNG GDDR3 : 2M x 32 bits x 4 banks 10 - RESERVED 11 - RESERVED	00

MEMTYPE1	MEMTYPE0	R3124	R3125	R3122	R3123	
0	0	NC	10K	NC	10K	00 - Infineon GDDR3 : 2M x 32 bits x 4 banks
0	1	NC	10K	10K	NC	01 - SAMSUNG GDDR3 : 2M x 32 bits x 4 banks
1	0	10K	NC	NC	10K	10 - RESERVED
1	1	10K	NC	10K	NC	11 - RESERVED