

<http://laptopblue.vn/>

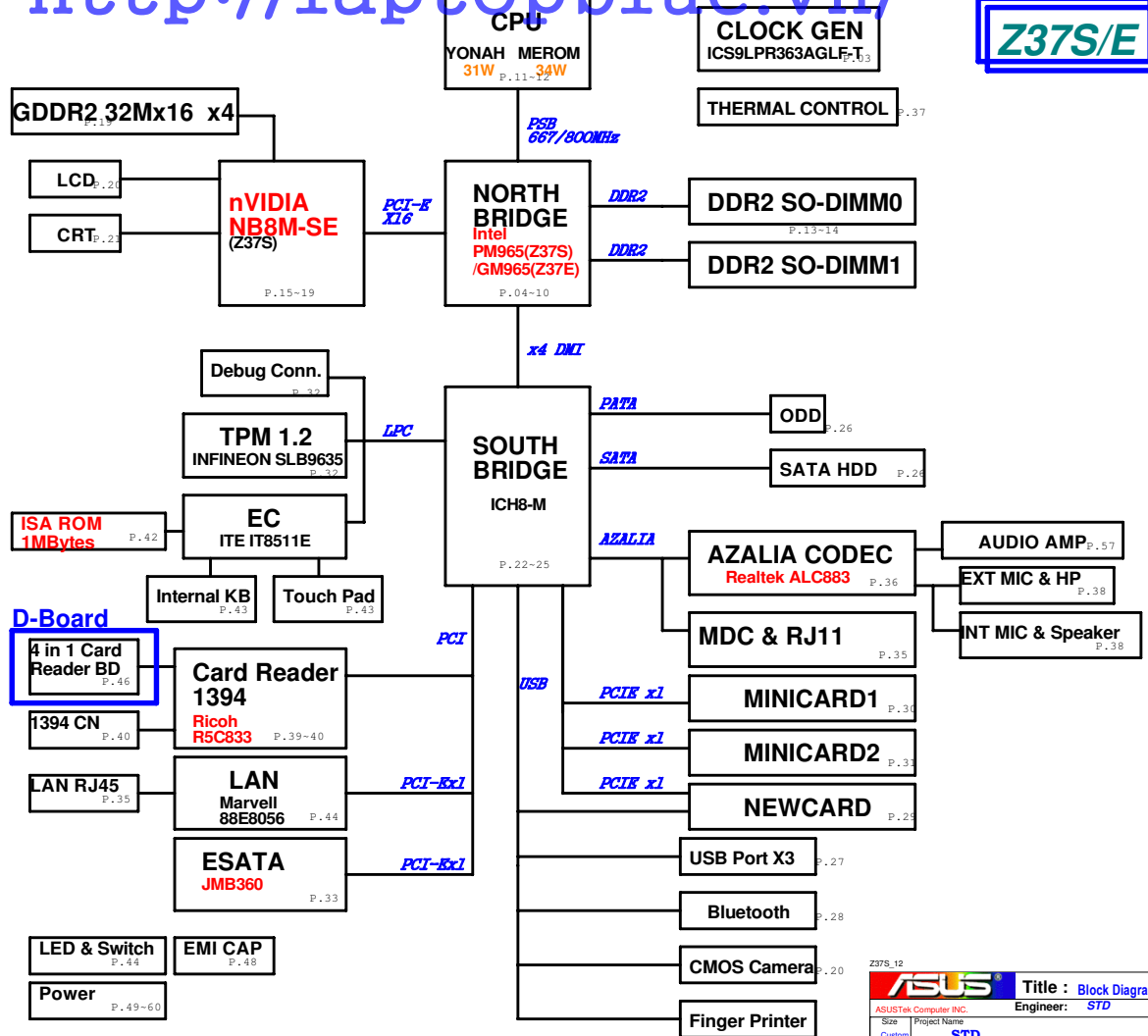


PART for BOM only		
Function	ASUS Partnumber	LOCATION
RTC BTY	07G016412032	
GPU NB8M	02G190012900	

Temp Modify
1012: Delete EC23,31,32,34,39 already have in location
Delete EC35,36 No space to add
Delete GC114,115,120,121,122,123,116,117,118,119 No space to add

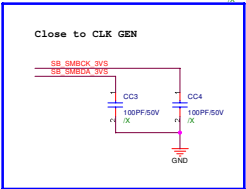
Z37S_12			
Title		Revision	
Size	Document Number	Rev	
A3	STD	2.0G	
Date	2007.11.28	Sheet	1 of 60

<< Kennedy_Zhang >>



ASUS		Title : Block Diagram	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 15 SEP 2007	Sheet 2 of 80		

<< Kennedy_Zhang >>



Z37S_12

		Title : ICS9LPR363AGLF-T	
ASUSTek Computer INC.		Engineer: STD	
Size Custom	Project Name STD	Rev 2.0	

Order: 81811, 1514 94, 3007 E-board: 3 ref: 60

(PM965)
(GM965)

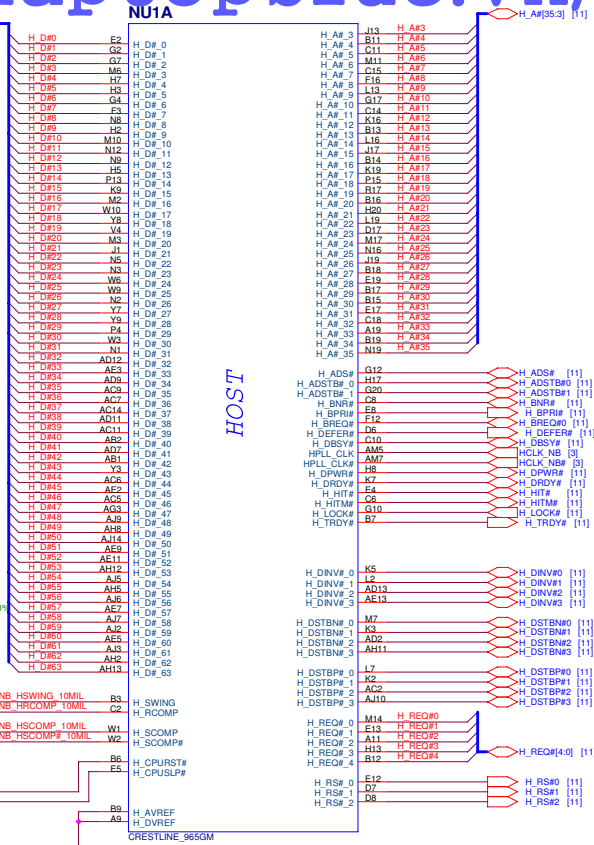

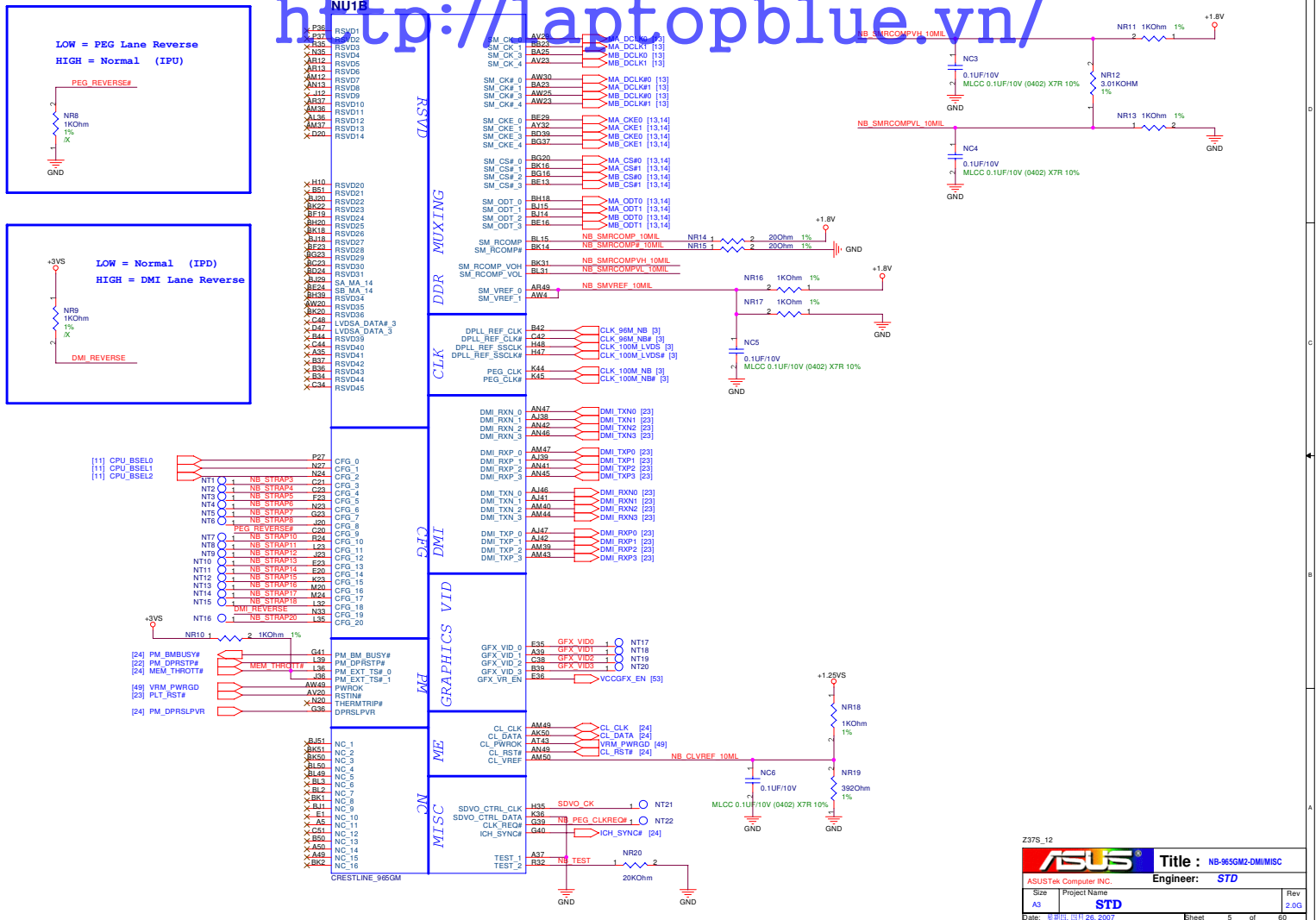


Figure 1 Schematic representation of the Hsp70 gene structure and its expression. The top part shows the gene structure with exons as boxes and introns as lines with arrows. The bottom part shows the expression of Hsp70 isoforms in various tissues, with red boxes indicating the presence of a specific isoform.

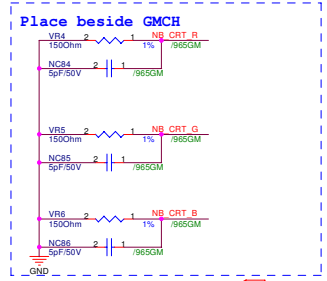
Gene/Region	Exon 1	Exon 2	Exon 3	Exon 4	Exon 5	Exon 6	Exon 7	Exon 8	Exon 9	Exon 10	Exon 11	Exon 12	Exon 13	Exon 14	Exon 15	Exon 16	Exon 17	Exon 18	Exon 19	Exon 20	Exon 21	Exon 22	Exon 23	Exon 24	Exon 25	Exon 26	Exon 27	Exon 28	Exon 29	Exon 30	Exon 31	Exon 32	Exon 33	Exon 34	Exon 35	Exon 36	Exon 37	Exon 38	Exon 39	Exon 40	Exon 41	Exon 42	Exon 43	Exon 44	Exon 45	Exon 46	Exon 47	Exon 48	Exon 49	Exon 50	Exon 51	Exon 52	Exon 53	Exon 54	Exon 55	Exon 56	Exon 57	Exon 58	Exon 59	Exon 60	Exon 61	Exon 62	Exon 63	Exon 64	Exon 65	Exon 66	Exon 67	Exon 68	Exon 69	Exon 70	Exon 71	Exon 72	Exon 73	Exon 74	Exon 75	Exon 76	Exon 77	Exon 78	Exon 79	Exon 80	Exon 81	Exon 82	Exon 83	Exon 84	Exon 85	Exon 86	Exon 87	Exon 88	Exon 89	Exon 90	Exon 91	Exon 92	Exon 93	Exon 94	Exon 95	Exon 96	Exon 97	Exon 98	Exon 99	Exon 100	Exon 101	Exon 102	Exon 103	Exon 104	Exon 105	Exon 106	Exon 107	Exon 108	Exon 109	Exon 110	Exon 111	Exon 112	Exon 113	Exon 114	Exon 115	Exon 116	Exon 117	Exon 118	Exon 119	Exon 120	Exon 121	Exon 122	Exon 123	Exon 124	Exon 125	Exon 126	Exon 127	Exon 128	Exon 129	Exon 130	Exon 131	Exon 132	Exon 133	Exon 134	Exon 135	Exon 136	Exon 137	Exon 138	Exon 139	Exon 140	Exon 141	Exon 142	Exon 143	Exon 144	Exon 145	Exon 146	Exon 147	Exon 148	Exon 149	Exon 150	Exon 151	Exon 152	Exon 153	Exon 154	Exon 155	Exon 156	Exon 157	Exon 158	Exon 159	Exon 160	Exon 161	Exon 162	Exon 163	Exon 164	Exon 165	Exon 166	Exon 167	Exon 168	Exon 169	Exon 170	Exon 171	Exon 172	Exon 173	Exon 174	Exon 175	Exon 176	Exon 177	Exon 178	Exon 179	Exon 180	Exon 181	Exon 182	Exon 183	Exon 184	Exon 185	Exon 186	Exon 187	Exon 188	Exon 189	Exon 190	Exon 191	Exon 192	Exon 193	Exon 194	Exon 195	Exon 196	Exon 197	Exon 198	Exon 199	Exon 200	Exon 201	Exon 202	Exon 203	Exon 204	Exon 205	Exon 206	Exon 207	Exon 208	Exon 209	Exon 210	Exon 211	Exon 212	Exon 213	Exon 214	Exon 215	Exon 216	Exon 217	Exon 218	Exon 219	Exon 220	Exon 221	Exon 222	Exon 223	Exon 224	Exon 225	Exon 226	Exon 227	Exon 228	Exon 229	Exon 230	Exon 231	Exon 232	Exon 233	Exon 234	Exon 235	Exon 236	Exon 237	Exon 238	Exon 239	Exon 240	Exon 241	Exon 242	Exon 243	Exon 244	Exon 245	Exon 246	Exon 247	Exon 248	Exon 249	Exon 250	Exon 251	Exon 252	Exon 253	Exon 254	Exon 255	Exon 256	Exon 257	Exon 258	Exon 259	Exon 260	Exon 261	Exon 262	Exon 263	Exon 264	Exon 265	Exon 266	Exon 267	Exon 268	Exon 269	Exon 270	Exon 271	Exon 272	Exon 273	Exon 274	Exon 275	Exon 276	Exon 277	Exon 278	Exon 279	Exon 280	Exon 281	Exon 282	Exon 283	Exon 284	Exon 285	Exon 286	Exon 287	Exon 288	Exon 289	Exon 290	Exon 291	Exon 292	Exon 293	Exon 294	Exon 295	Exon 296	Exon 297	Exon 298	Exon 299	Exon 300	Exon 301	Exon 302	Exon 303	Exon 304	Exon 305	Exon 306	Exon 307	Exon 308	Exon 309	Exon 310	Exon 311	Exon 312	Exon 313	Exon 314	Exon 315	Exon 316	Exon 317	Exon 318	Exon 319	Exon 320	Exon 321	Exon 322	Exon 323	Exon 324	Exon 325	Exon 326	Exon 327	Exon 328	Exon 329	Exon 330	Exon 331	Exon 332	Exon 333	Exon 334	Exon 335	Exon 336
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		Title : NB-965GM1-FSB	
ASUSTek Computer INC.		Engineer: STD	
Size Custom	Project Name STD		R 2
Date: 2007/04/26		Sheet 4	of 60

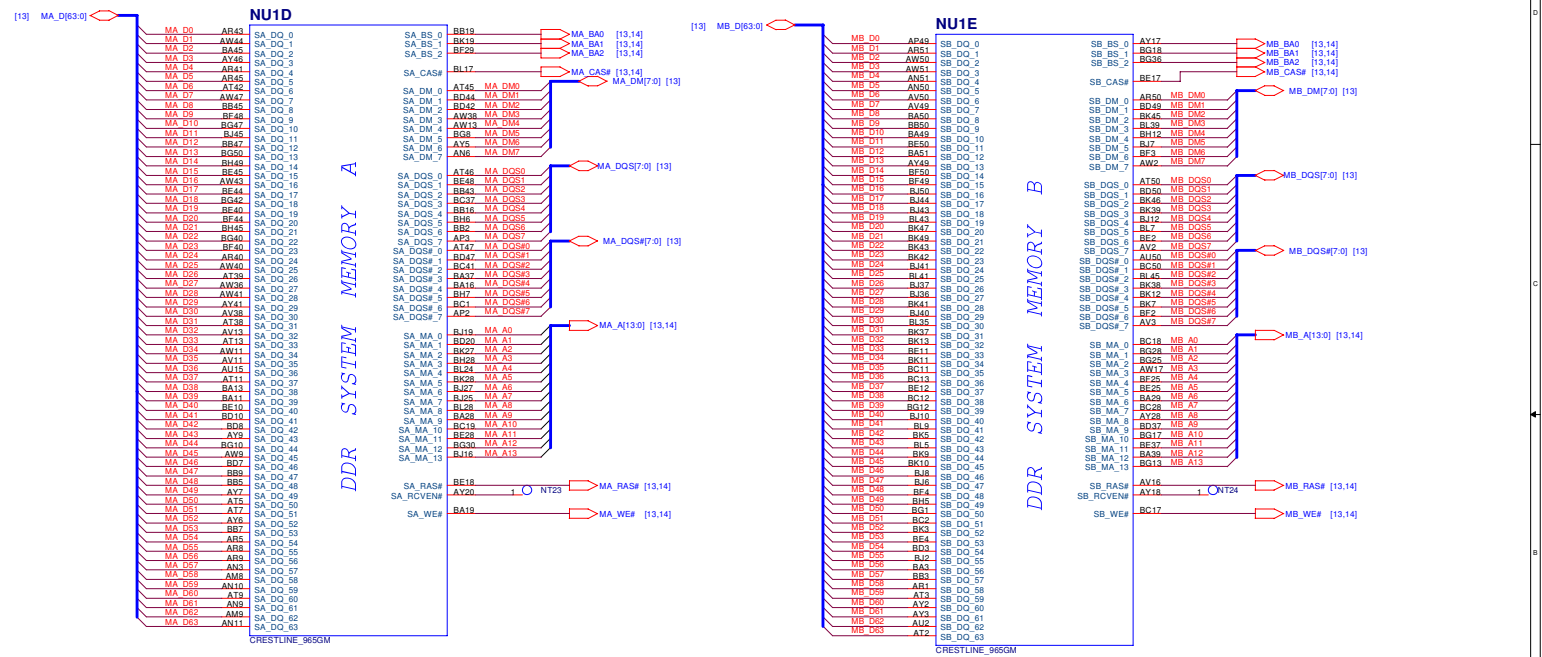
<< Kennedy_Zhang >>



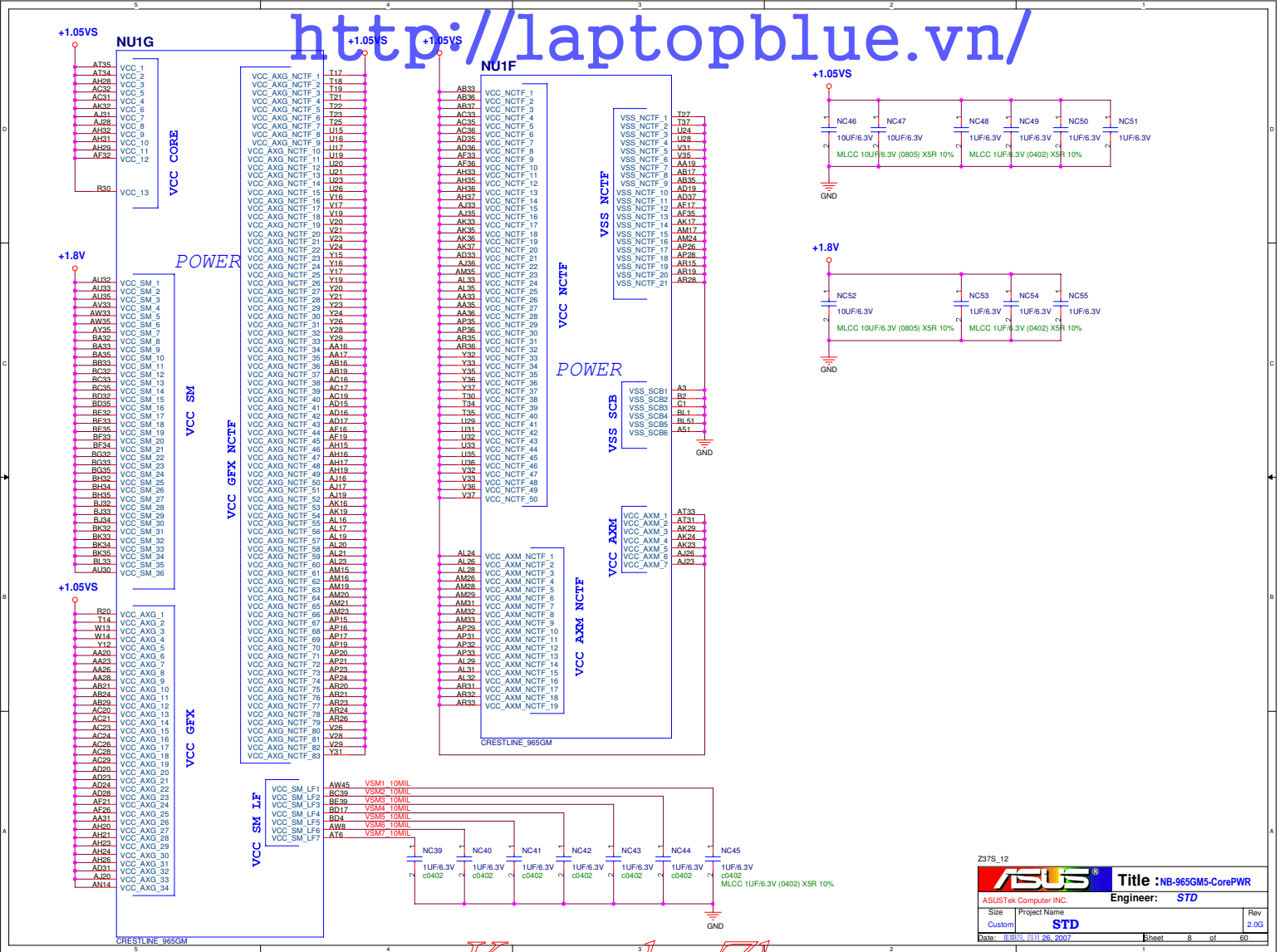
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<< Kennedy_Zhang >>



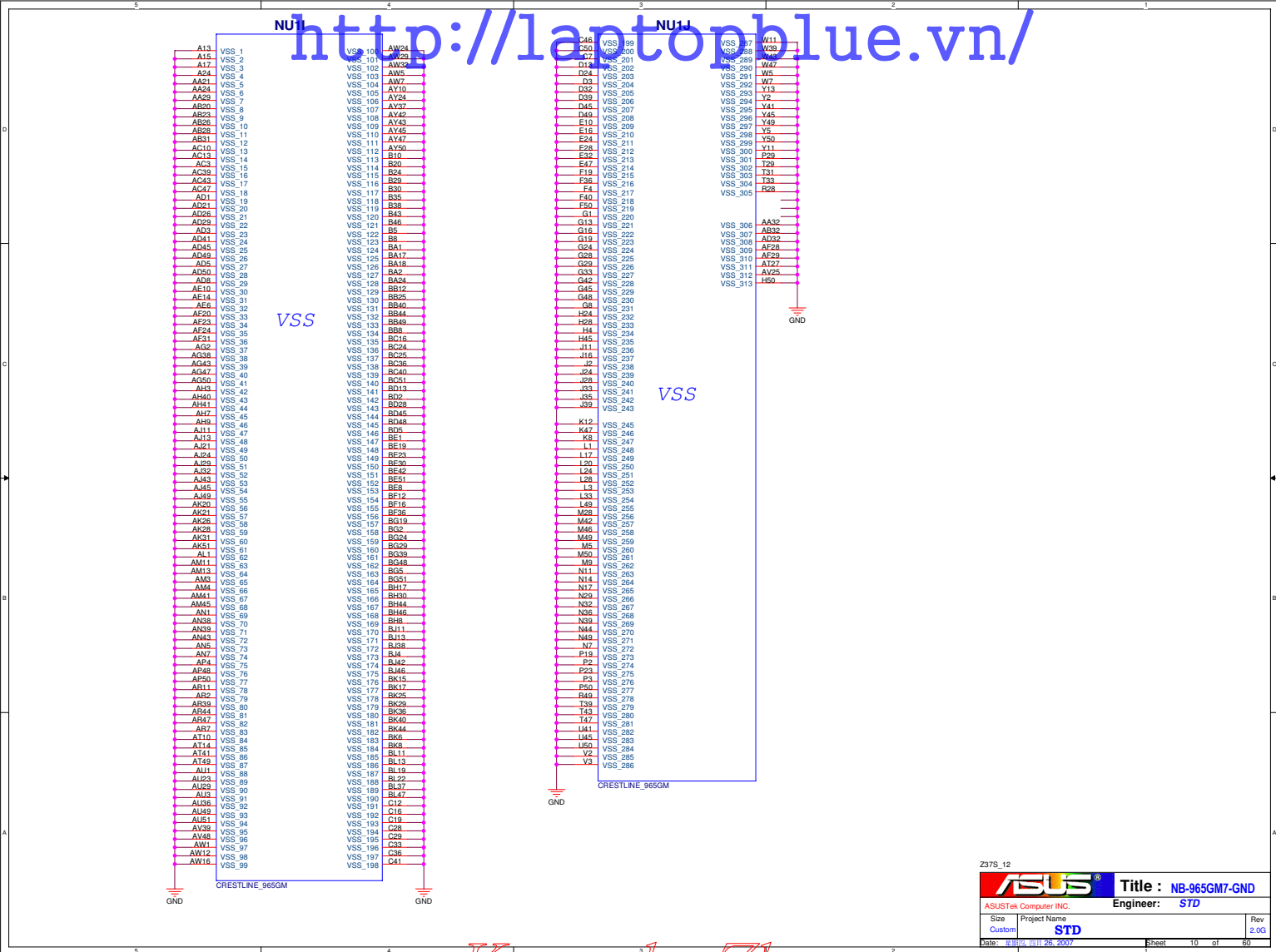
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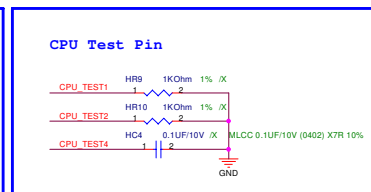
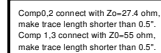


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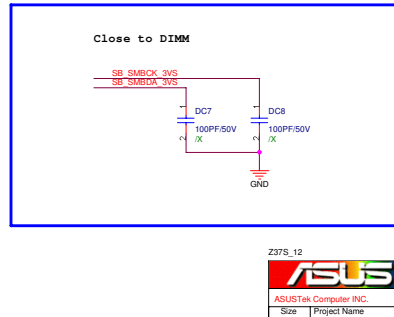
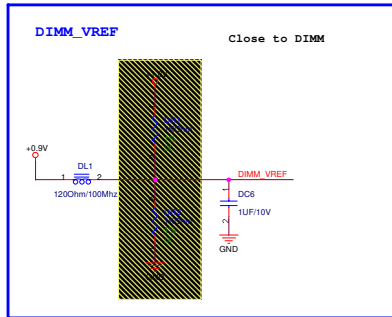
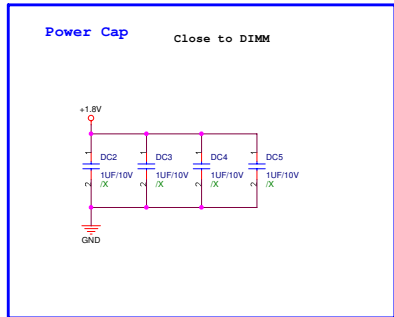
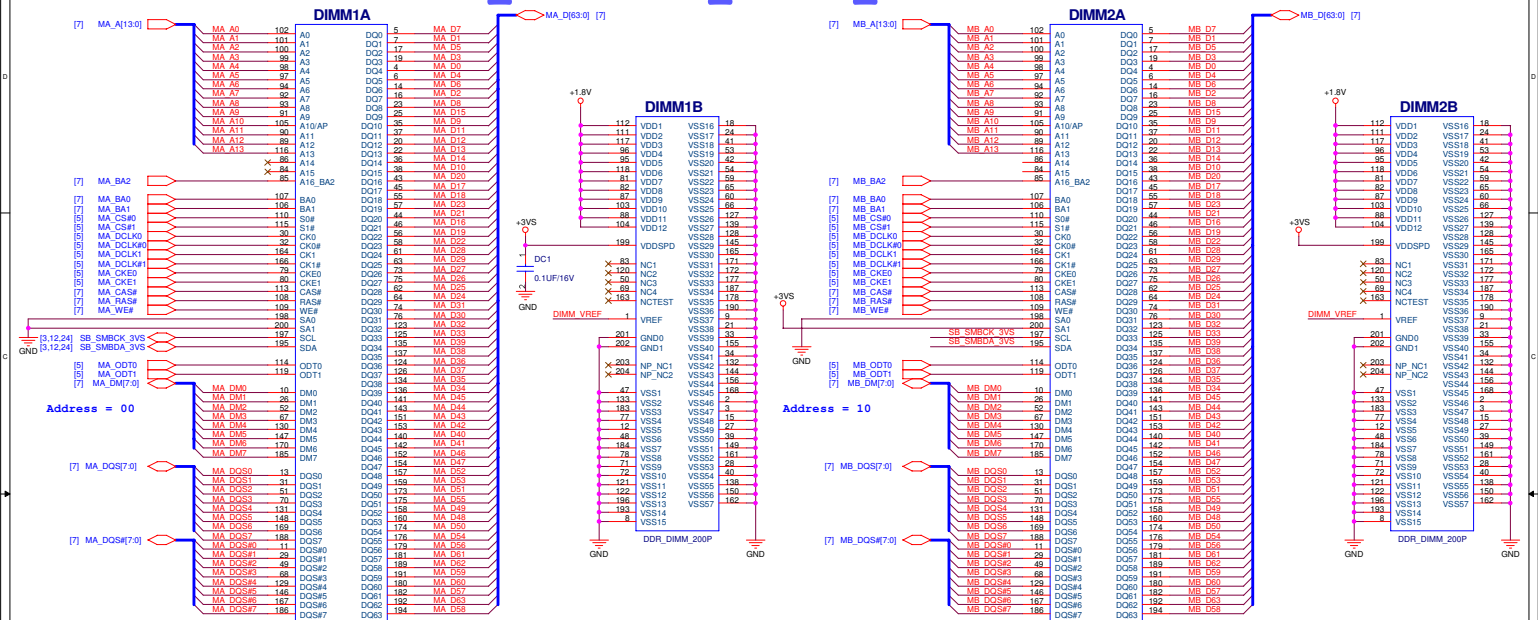
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ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 2007.04.26	Sheet 8	of 60	



[illegible]

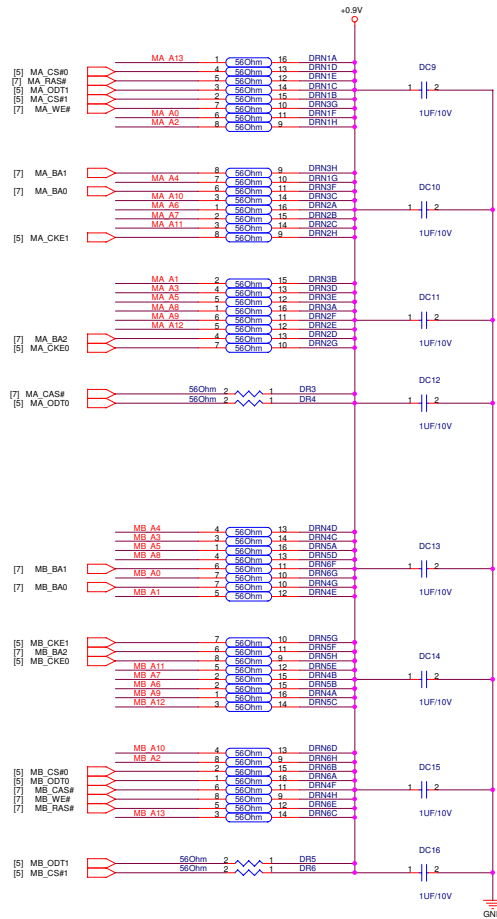






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[7] MA_A13[0]
[7] MB_A13[0]



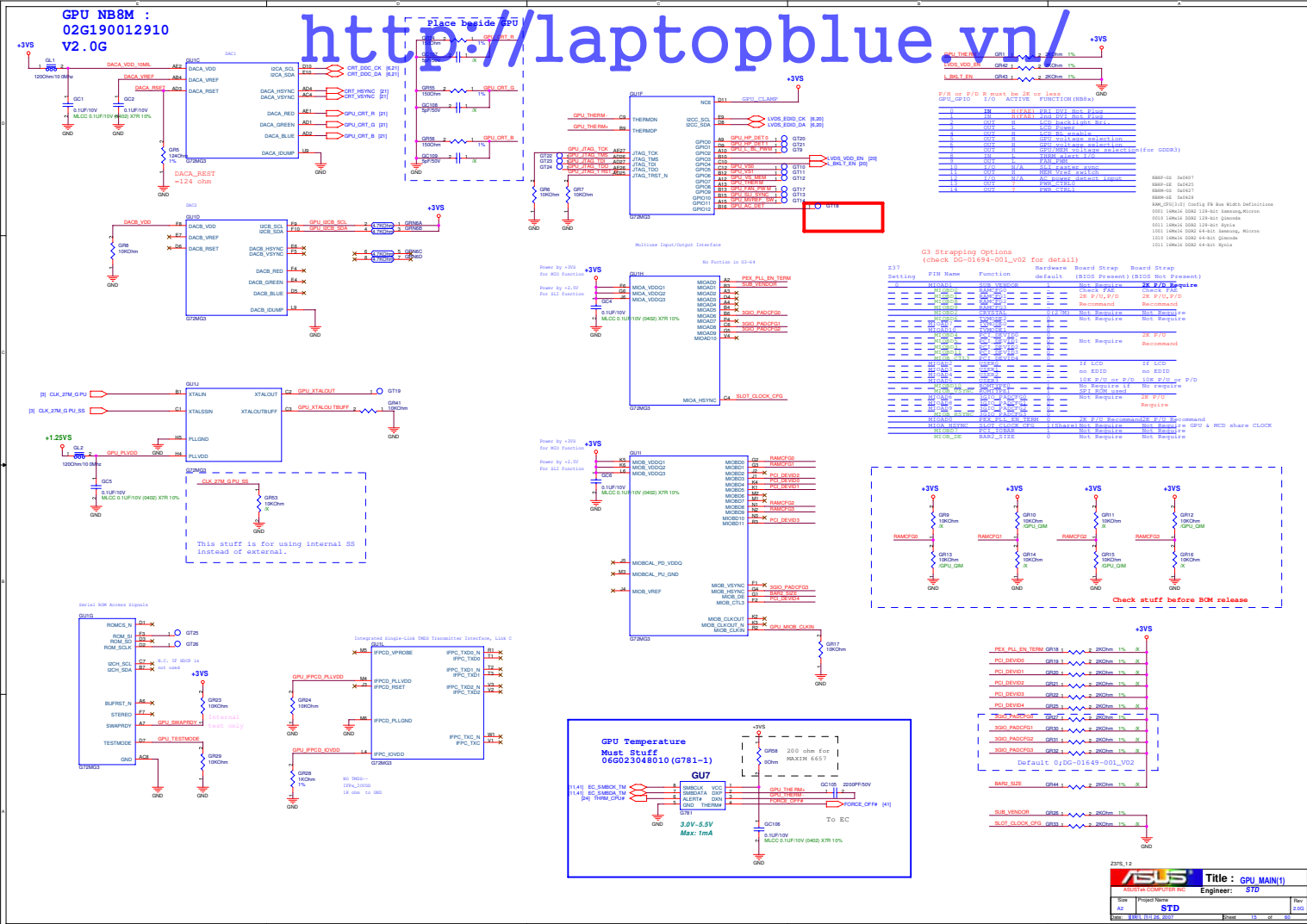
Z37S_12

ASUS		Title : DDR2 TERMINATION	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 11/01/2007	Sheet 14	of 80	

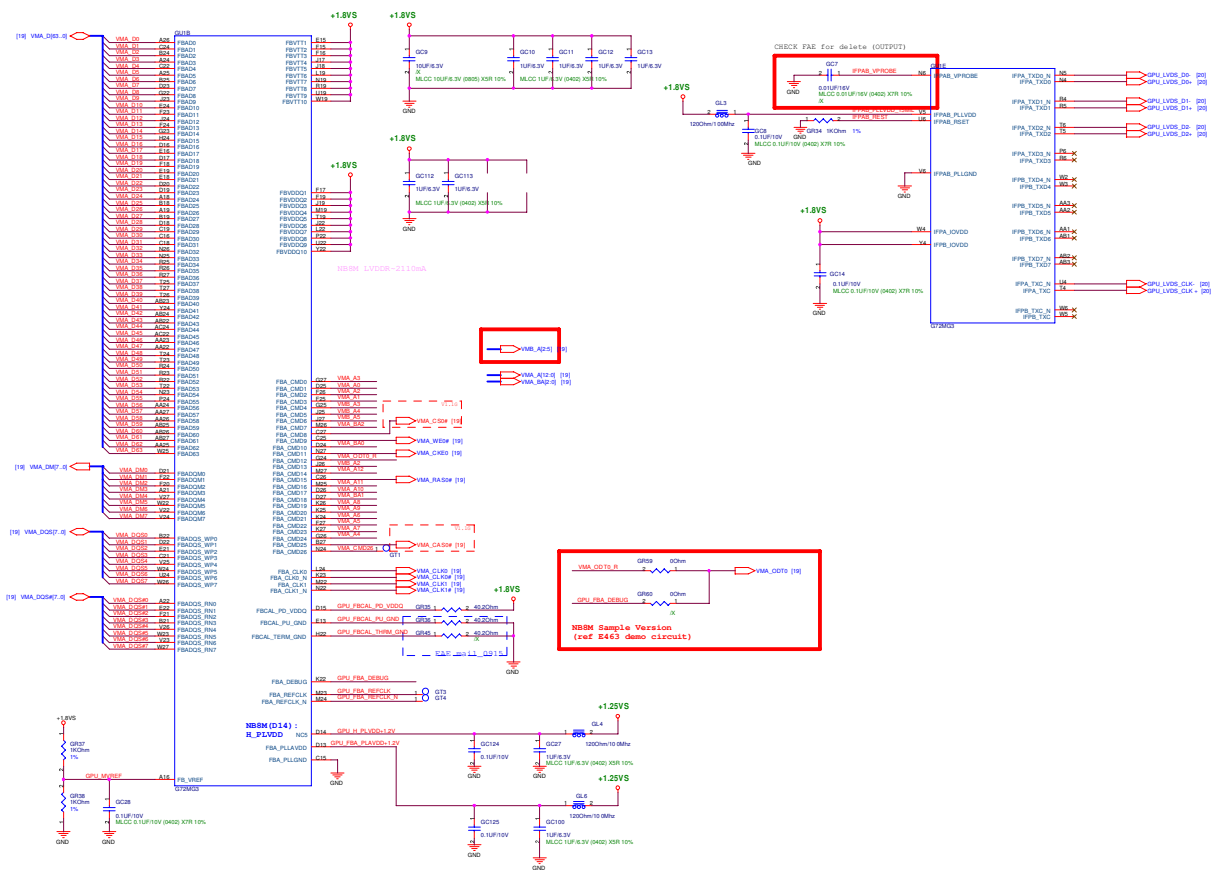
<< Kennedy_Zhang >>

GPU NB8M :
02G190012910
V2.0G

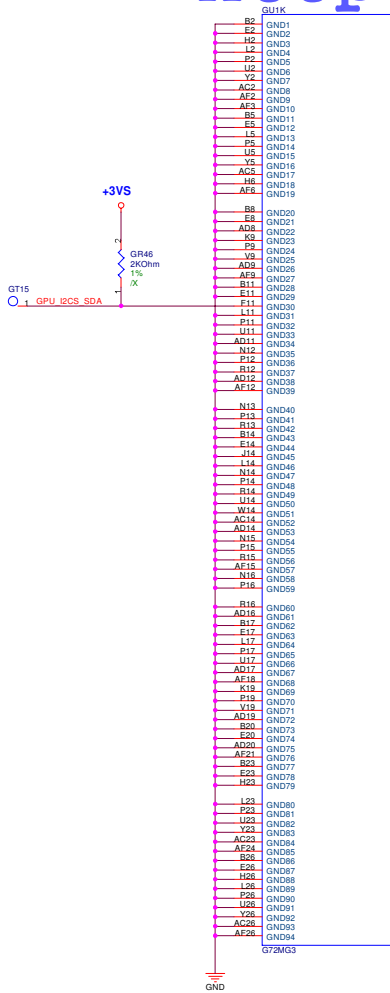
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<< Kennedy_Zhang >>



Z37S_12		Title : GPU_POWER_GND(4)	
ASUS		Engineer: STD	
ASUS COMPUTER INC	Project Name	Rev	
A3	STD	2.0G	
Date: 11/11/2007	Sheet: 18	of	90

<< Kennedy_Zhang >>

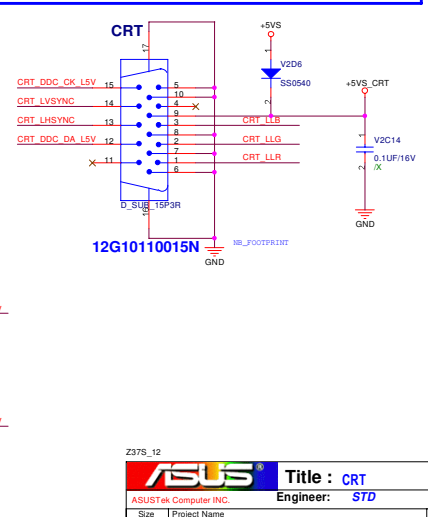
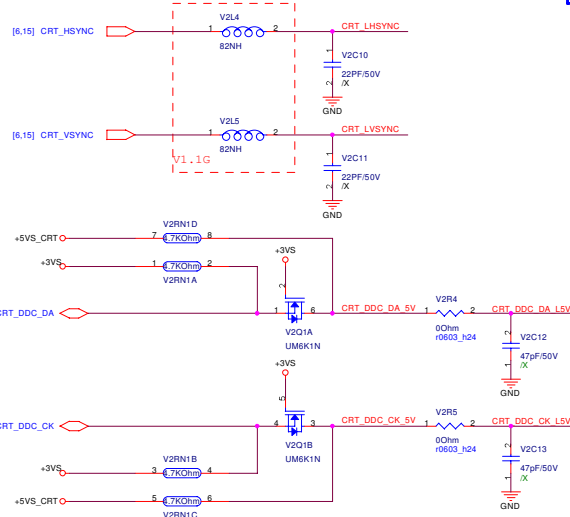
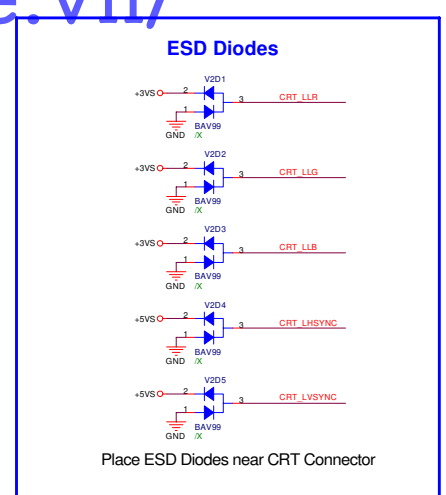
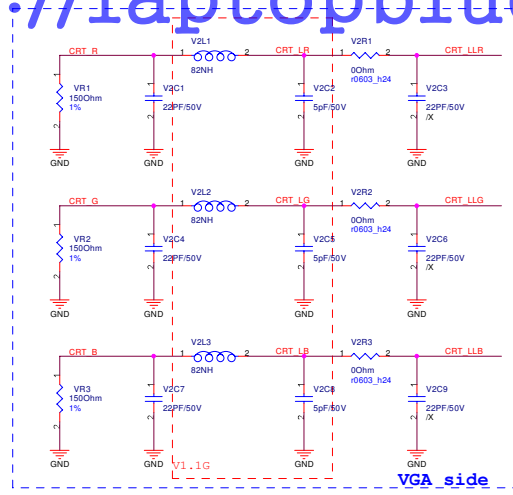
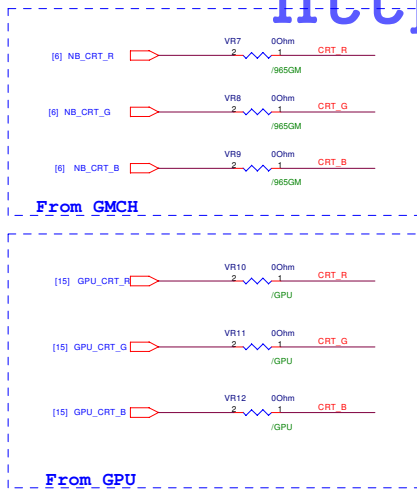
```

- Prevent DDRII enter nuknown mode,
  before CPU initial DDRII

```



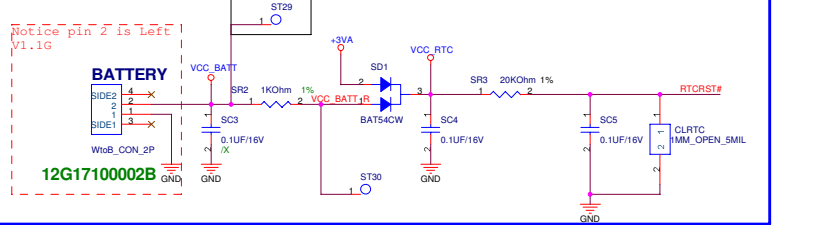
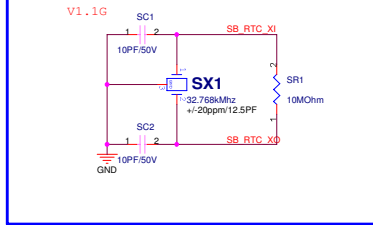
<< Kennedy_Zhang >>



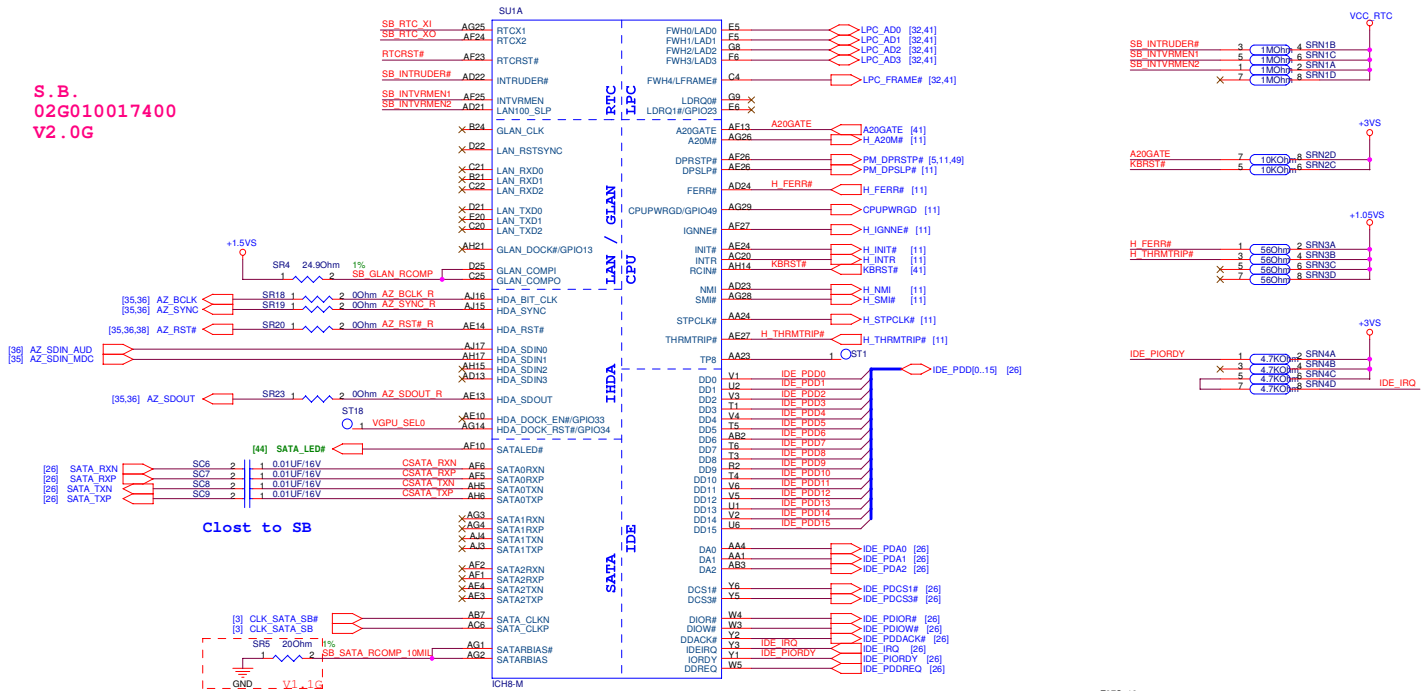
2375_12		Title : CRT	
ASUSTek Computer Inc.		Engineer: STD	
Size	A3	Project Name	STD
Date	10/28/2007	Rev	2.0G
Sheet		21	of 60

<< Kennedy_Zhang >>

RTC BATTERY



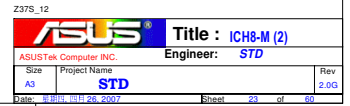
S.B.
02G010017400
V2.0G

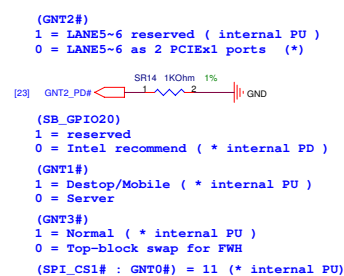


Z37S_12

ASUS		Title : ICH8-M (1)	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.2G	
Date: 2007.04.26	Sheet 22	of 60	

<< Kennedy_Zhang >>





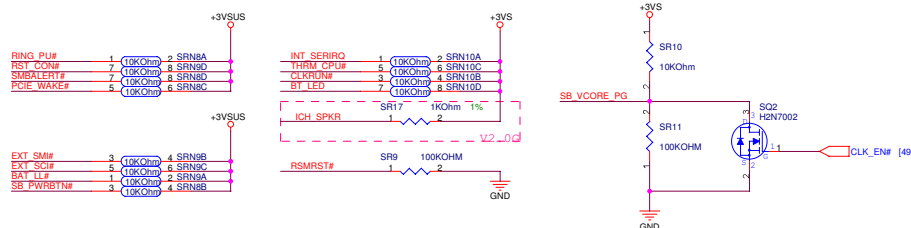
	GNT0#	SPI_CS1
SPI	0	1
PCI	1	0
LPC	1	1

```
(INTVRMEN, LAN100_SLP)
1 = Enabled internal VRMs for VccSus1_05, 1_5 and
VccCL1_5, 1_05 and VccLAN1_05 ( * )
0 = Disabled internal VRMs
```

```
(ICH_SATALED#)
1 = LANE1~4 normal ( * internal PU )
0 = LANE1~4 reverse
```

(SPKR)
1 = Disabled TCO Reboot
0 = Enabled TCO Reboot (* internal PD)

```
(TP_GPIO33) : Not used for ICH8M
1 = Enabled Descriptor Security (* internal PU)
0 = Disabled Descriptor Security
```

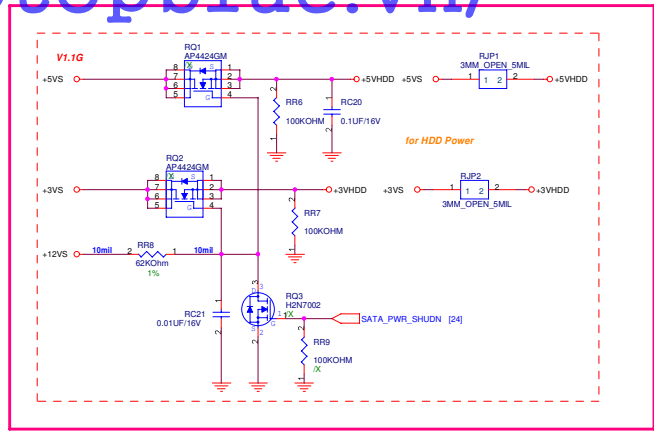
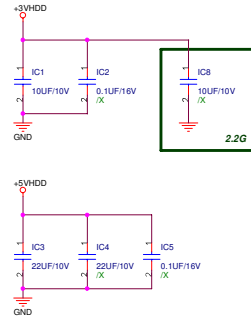
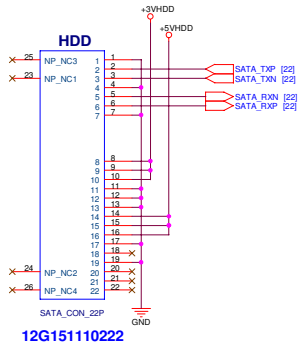


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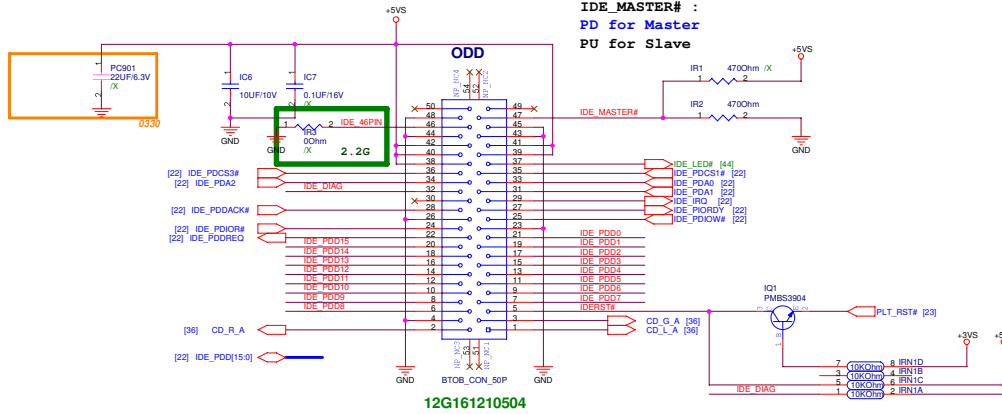


SATA HDD

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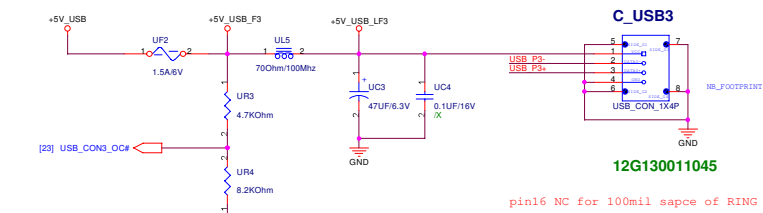
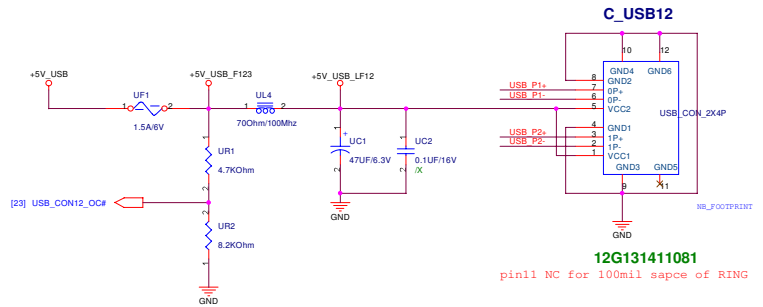
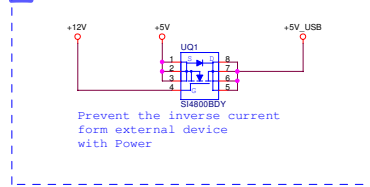
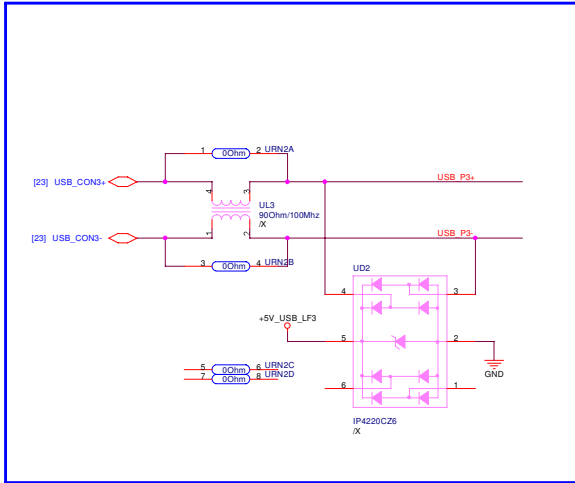
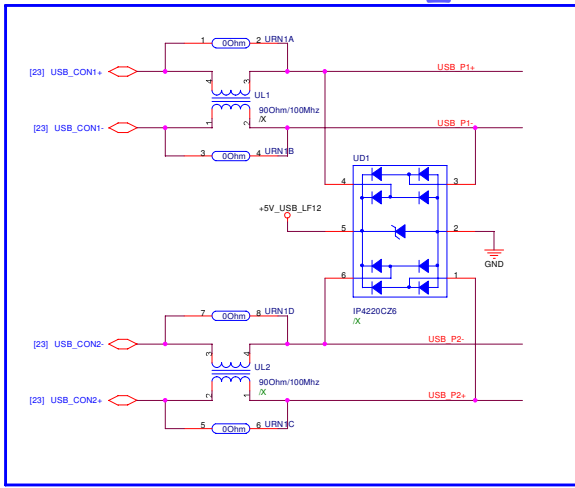
ODD



Z37S_12

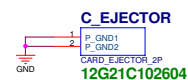
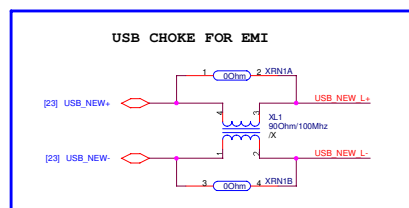
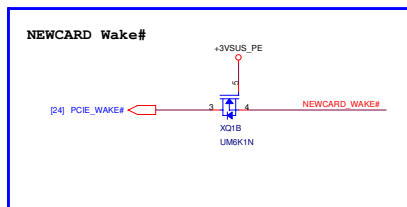
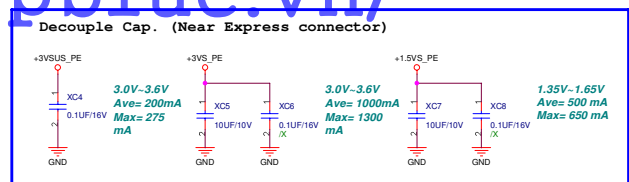
ASUS		Title : SATA-HDD & ODD	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	2.00
Custom	STD		
Date: 10/25/2007	Sheet 26 of 30		

<< Kennedy_Zhang >>



Z375_12		Title : USB CONN	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
A3	STD	2.0G	
Date: 8/28/11	11/18/2007	Sheet 27	of 60

<< Kennedy_Zhang >>



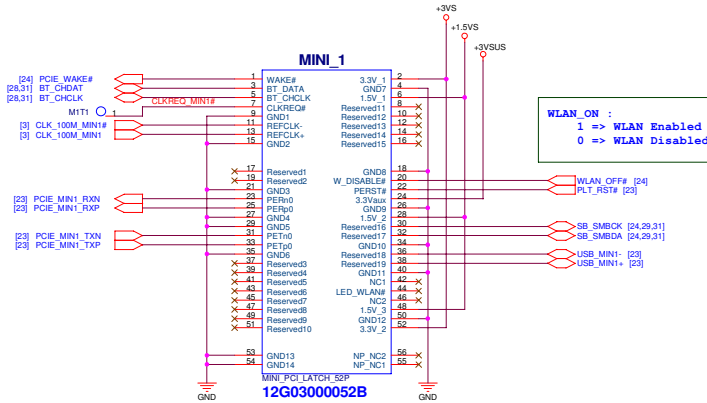
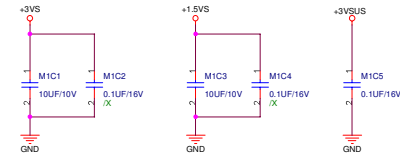
<< Kennedy_Zhang >>

Decouple Cap. (Near C_MINICARD1)

+3.003V~+3.597V
Max= 750 mA

+1.425V~+1.575V
Max= 375 mA

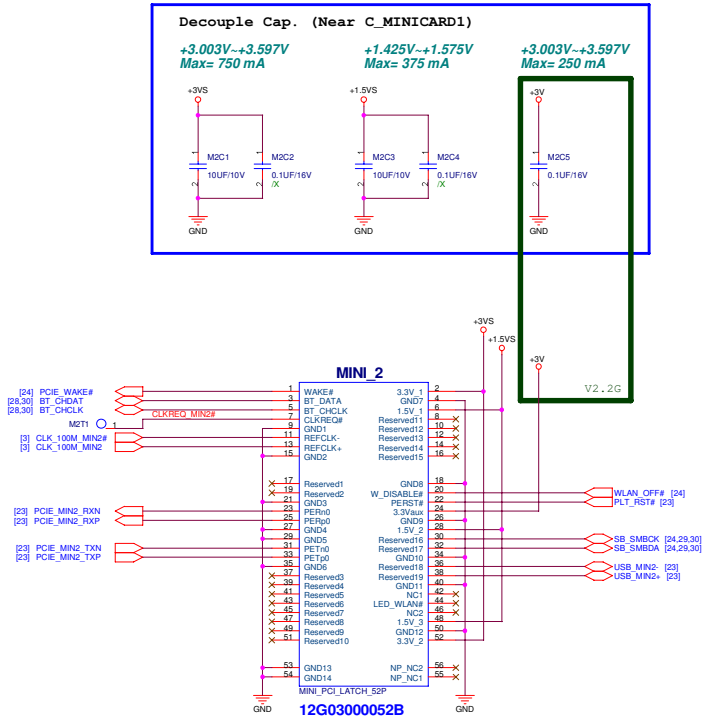
+3.003V~+3.597V
Max= 250 mA



Z376_12

ASUS		Title : MINI CARD	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 11/11/2017	Sheet	30	of 50

<< Kennedy_Zhang >>

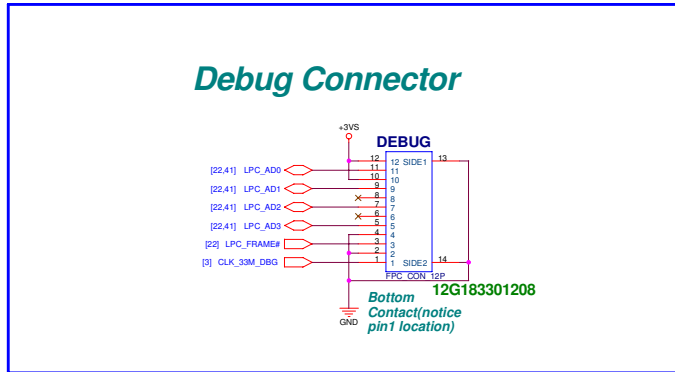
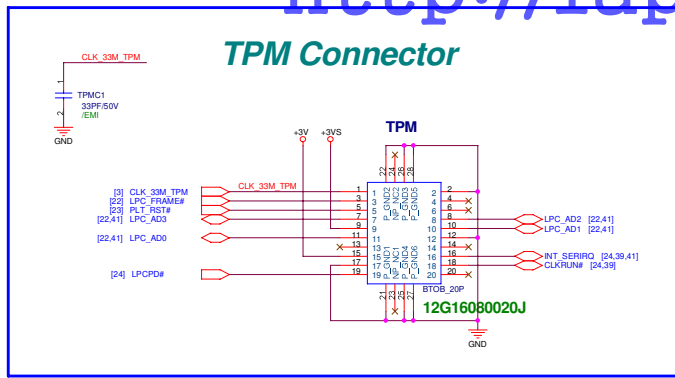


WLAN_ON :
1 => WLAN Enabled
0 => WLAN Disabled

Z375_12

ASUS		Title : MINI CARD	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name		Rev
Custom	STD		2.00
Date: APR 11 2017		Sheet	31 of 50

<< Kennedy_Zhang >>



Z37S_12

ASUS		Title : TPM	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 10/27/2007	Sheet: 32	of	30

<< Kennedy_Zhang >>


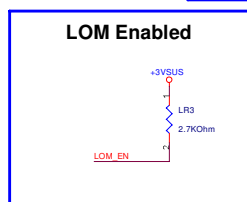
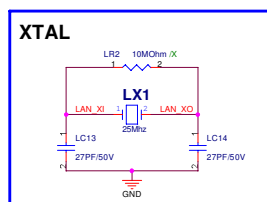
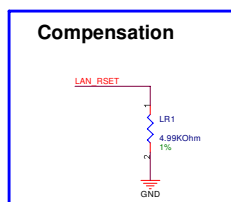
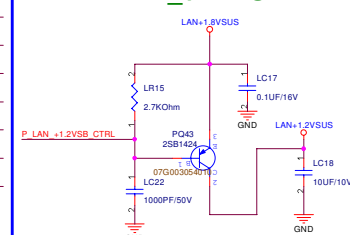
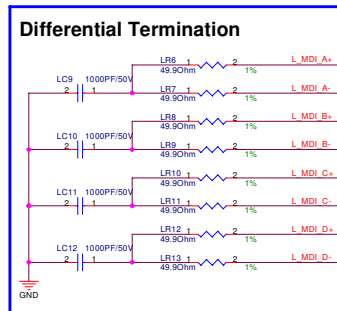
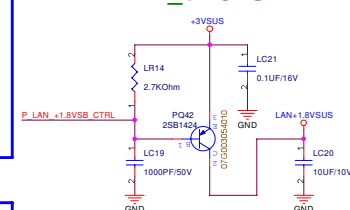
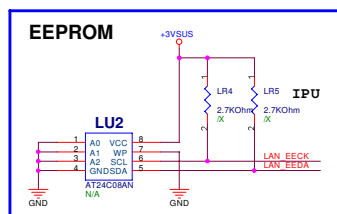
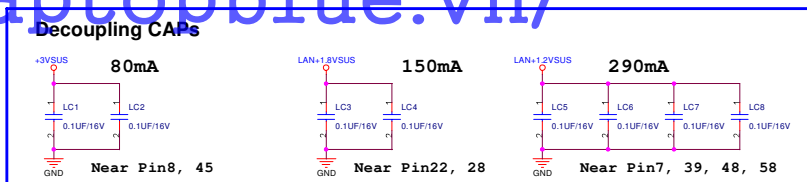


Diagram showing the connection for pins 12 and 13. Pin 12 is connected to NP_NC1 and Pin 13 is connected to NP_NC2. Both connections are marked with a red 'X'.

Place near connector

SATA_CON_7P



Pin	MDI			MDIX		
	100BASE-T	100BASE-TX	10BASE-T	100BASE-T	100BASE-TX	10BASE-T
MDIO/MDI0	Rx_C0A	TX0	TX0	Rx_C0A	R0A	R0A
MDIO/MDI1	SP_C0B	R0B	R0B	Rx_C0B	TX0	TX0
MDIO/MDI2	Rx_C0C	unused	unused	Rx_C0C	unused	unused
MDIO/MDI3	Rx_C0D	unused	unused	Rx_C0D	unused	unused

Z37S 12



v1.1G
| RING

RJ45_RJ11

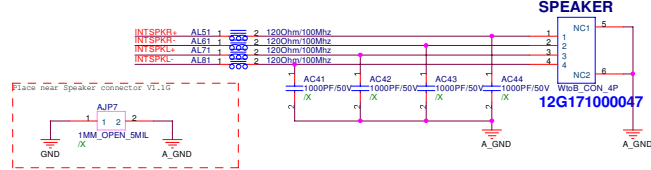
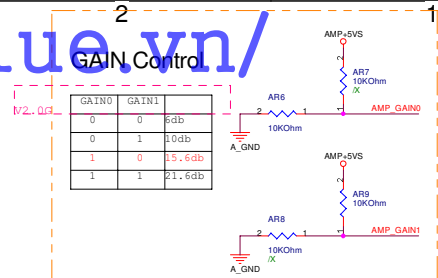
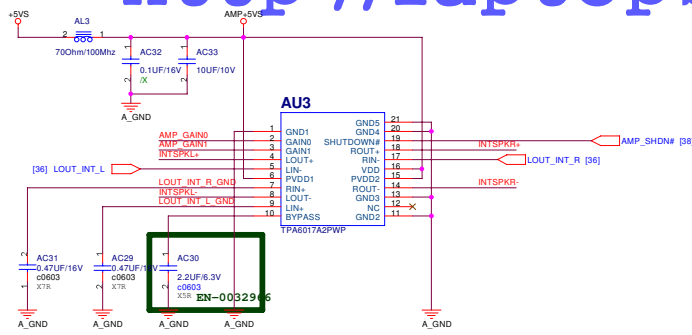
12	12	P_GND2
11	11	NP_NC2

LTRLN3	8	8
LYRLP3	7	7
LTRLN1	6	6
LYRLN2	5	5
LYRLP2	4	4
LTRLP1	3	3
LTRLN0	2	2
LYRLP0	1	1

NP_NC1 13
P_GND1 15

pin16 NC for 100mil sapce of RING

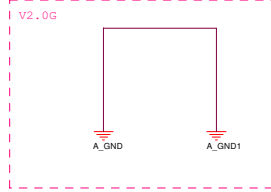
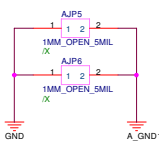
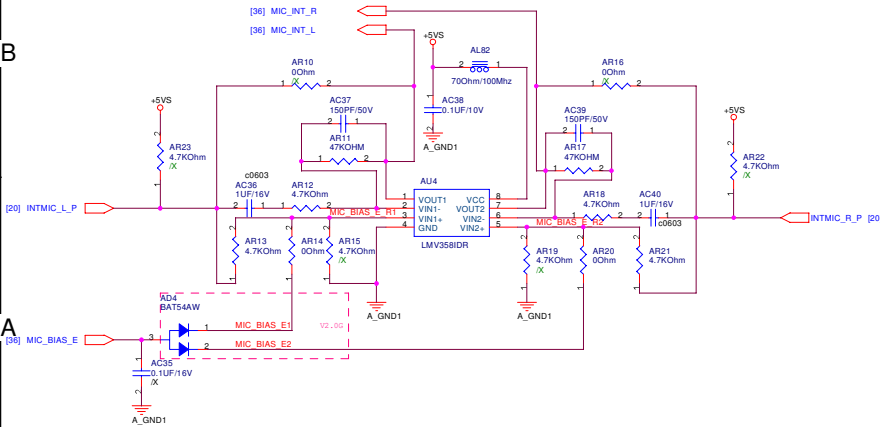
**BOM :
12G14211125**

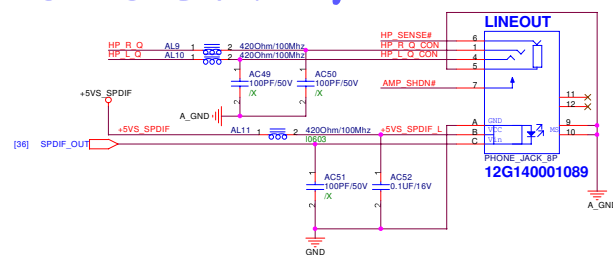


Internal MIC Amp.

FL = 33.86kHz, FH = 22.5kHz

Place Near INTMIC Connector





http://laptopblue.vn/

Headphone & S/PDIF Jack

Microphone-In Jack

Anti POP for LINE_OUT

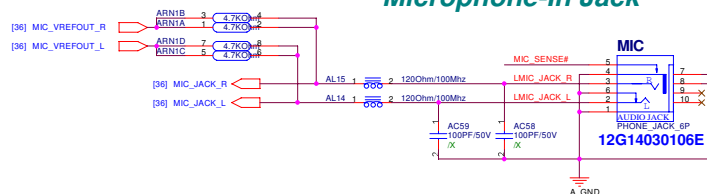
Jack Plug-in Detection

ASUS Title : Audio_3 Engineer: STD

ASUSTek Computer INC. Project Name: STD Rev: 2.0G

Date: 08/03/2007 Sheet: 38 of 60

<< Kennedy_Zhang >>



http://laptopblue.vn/

Headphone & S/PDIF Jack

Microphone-In Jack

Anti POP for LINE_OUT

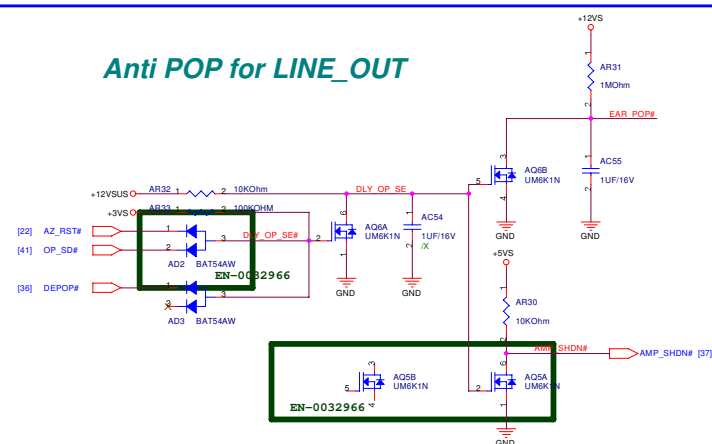
Jack Plug-in Detection

ASUS Title : Audio_3 Engineer: STD

ASUSTek Computer INC. Project Name: STD Rev: 2.0G

Date: 08/03/2007 Sheet: 38 of 60

<< Kennedy_Zhang >>



http://laptopblue.vn/

Headphone & S/PDIF Jack

Microphone-In Jack

Anti POP for LINE_OUT

Jack Plug-in Detection

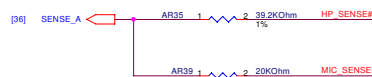
ASUS Title : Audio_3 Engineer: STD

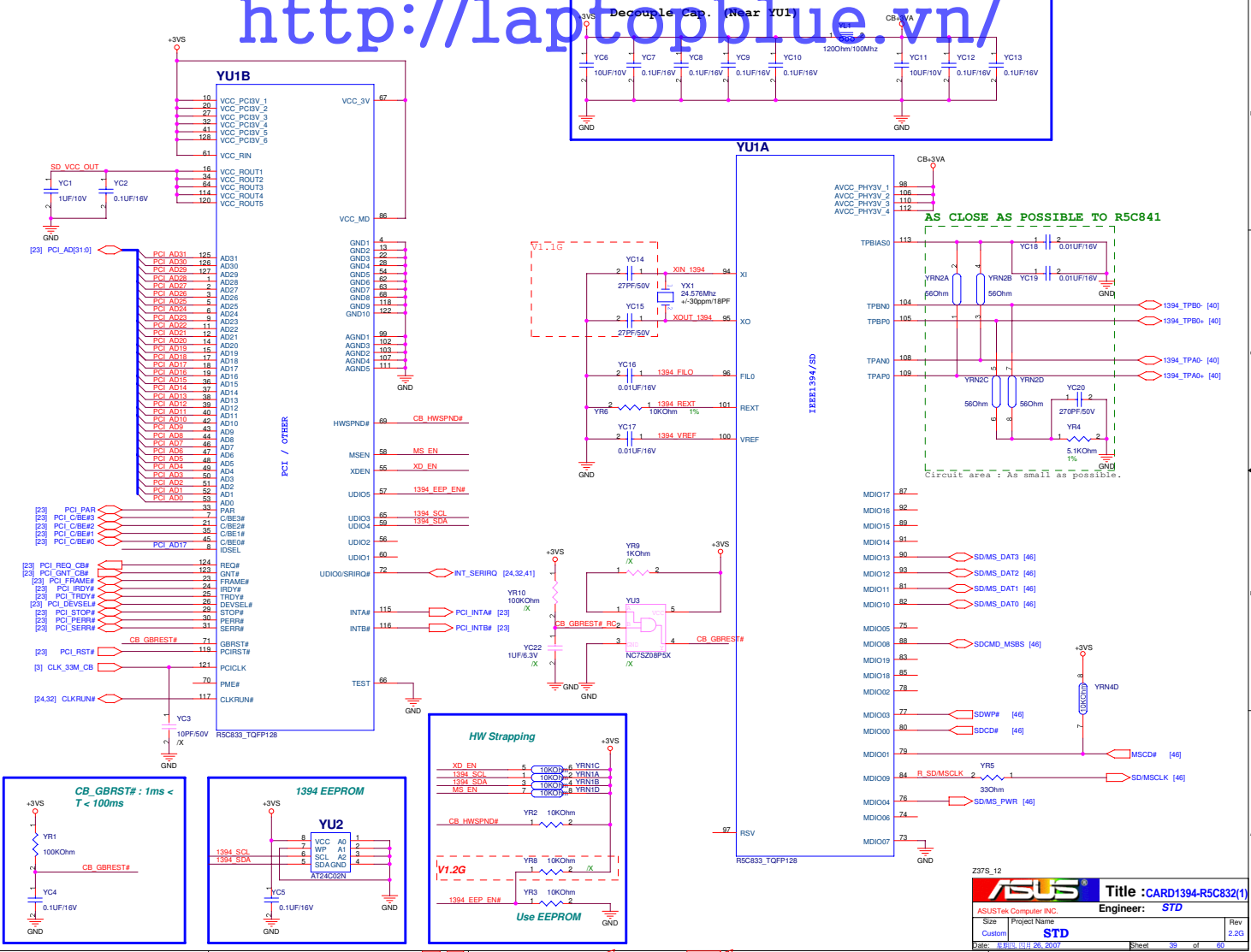
ASUSTek Computer INC. Project Name: STD

Date: 08/03/2007 Sheet 38 of 60

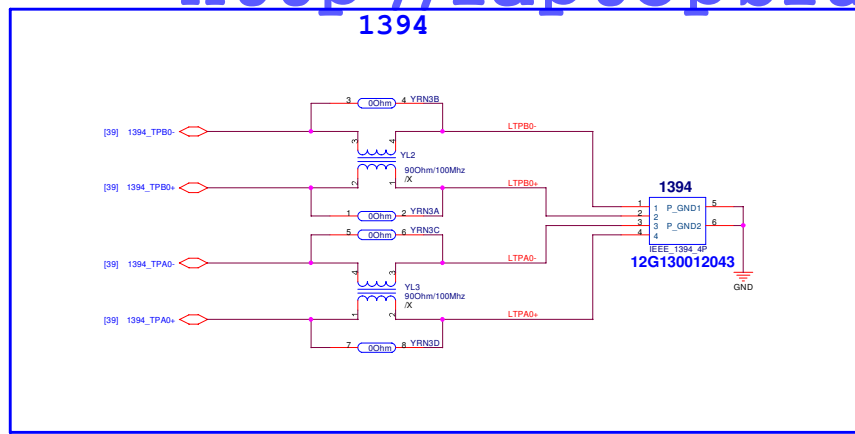
Rev 2.0G

<< Kennedy_Zhang >>





<< Kennedy_Zhang >>



Z375_12

ASUS		Title 1394 CON	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name		Rev
A3	STD		2.0G
Date: 11/26/2007		Sheet 40 of 60	

<< Kennedy_Zhang >>

ISA ROM

EC Hardware Strapping

[FA3/ BADDR1 : FA2/ BADDR0]

00: PNPCNG Access Register Pair Are 002Eh and 002Fh
01: PNPCNG Access Register Pair Are 004Eh and 004Fh
10: PNPCNG Access Register Pair Are Determined by EC Domain Registers SWCBALR and SWCBAHR.
11: Reserved



Note: Sampled at VSTBY Power Up Reset

FA4/ PPEN

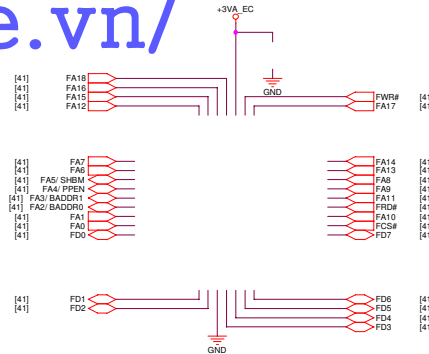
0: Normal
1: KBS Interface Pins Are Switched to Parallel Port Interface for In-System Programming



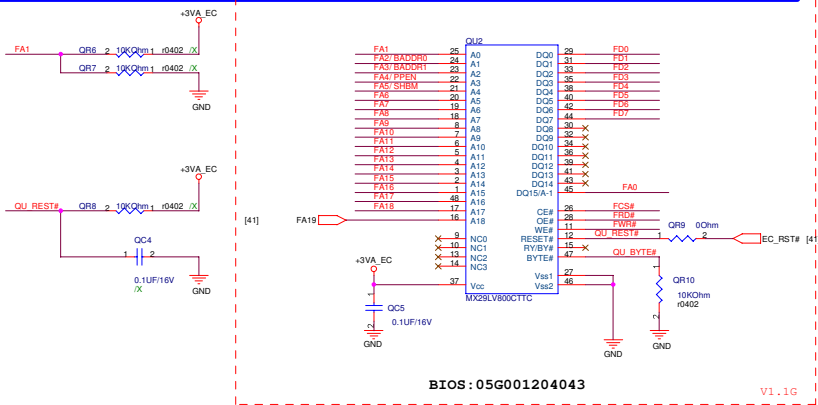
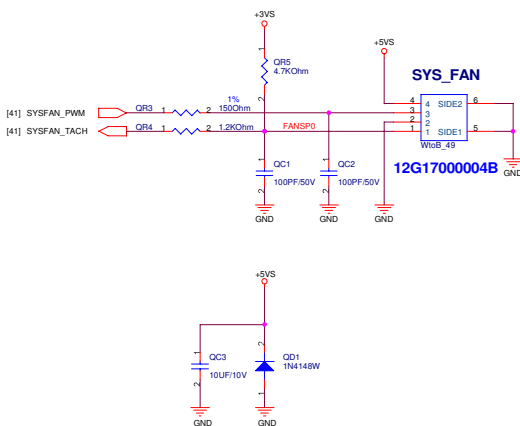
FA5/ SHBM

0: Disable Shared Memory with Host BIOS

1: Enable Shared Memory with Host BIOS



System Fan Connector



BIOS: 05G001204043

V1.1G

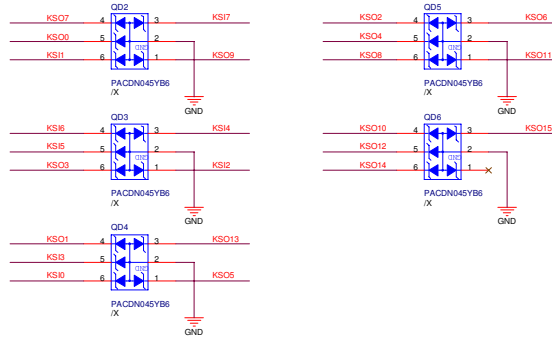
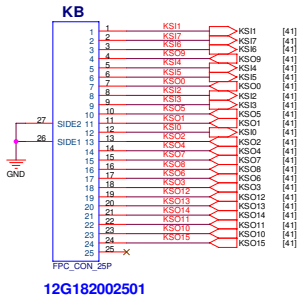
Z37S 12

ASUS		Title : ISA FLASH ROM	
ASUSTek Computer INC		Engineer: STD	
Size	Project Name		Rev
Custom	STD		2.0G
Date: 06/05/2007		Sheet: 45	of 60

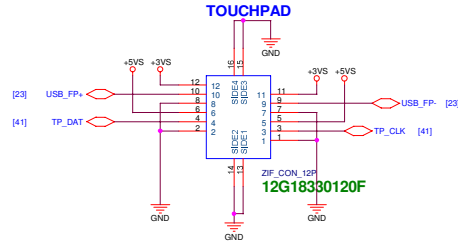
<< Kennedy_Zhang >>

FOR EMI/ESD

Keyboard Connector



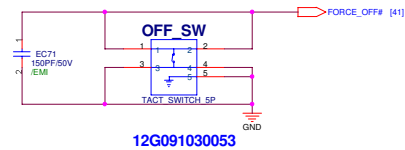
Fingerprint & TouchPad Connector



Z378_12

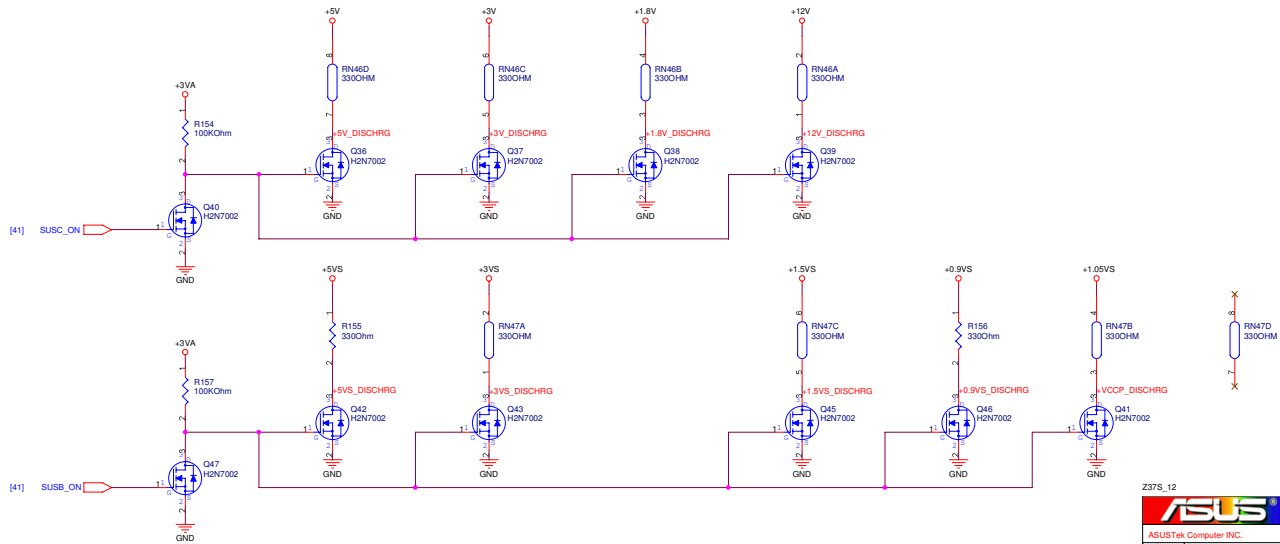
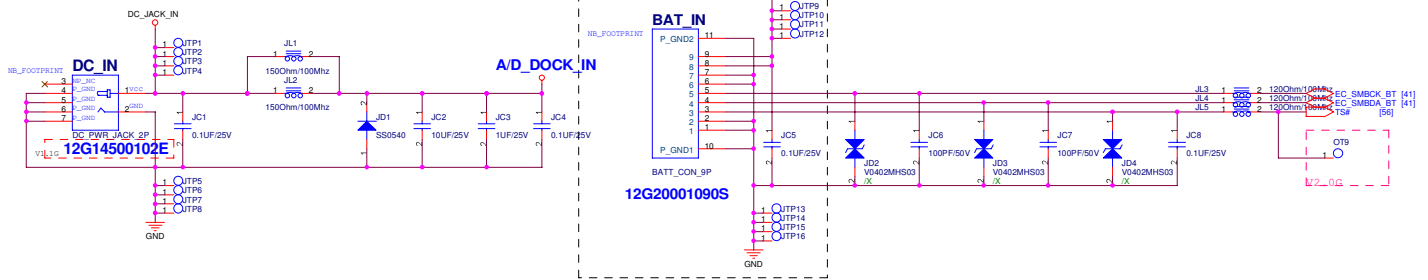
ASUS		Title : Touch Pad & KB	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.0G	
Date: 11/14/13 25:2007		Sheet	43 of 50

<< Kennedy_Zhang >>



Z375_12		Title : LED & SW	
ASUSTek Computer INC.		Engineer: STD	
Size Custom	Project Name STD	Rev 2.0G	
Date: 6月8日 04月 26, 2007	Sheet	44	of 60

DC-IN Connector

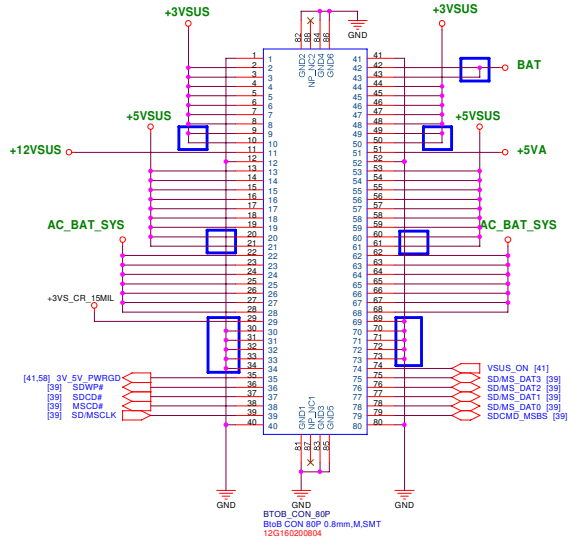


Z37S_12

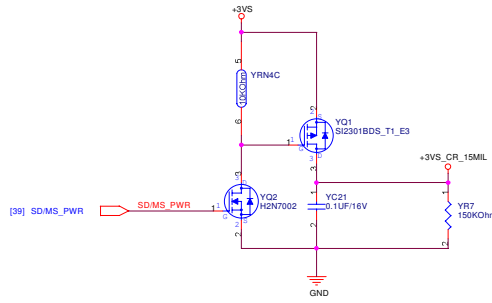
ASUS		Title : DC IN & BAT IN	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.0G	
Date: 10/10/2007	Sheet: 45	of 60	

<< Kennedy_Zhang >>

CARDBD_CON



BOM : 12G160200805 --8mmHigh

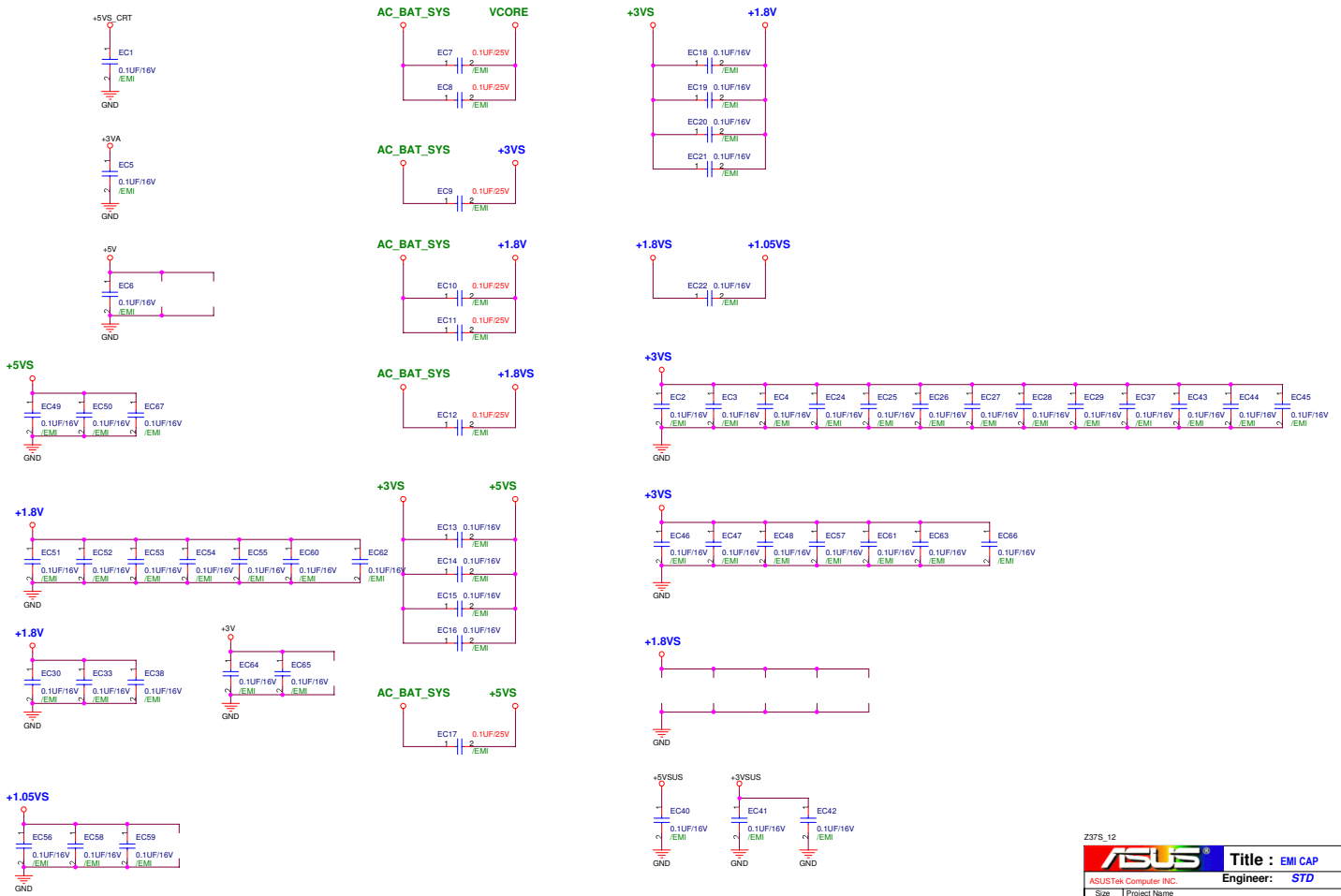


Z376_12

ASUS		Title 4 in 1 CARD READER	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name		Rev
A3	STD		2.0G
Date: 11/26/2007		Sheet 48	of 60

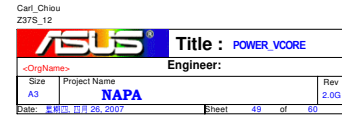
<< Kennedy_Zhang >>

<< Kennedy_Zhang >>

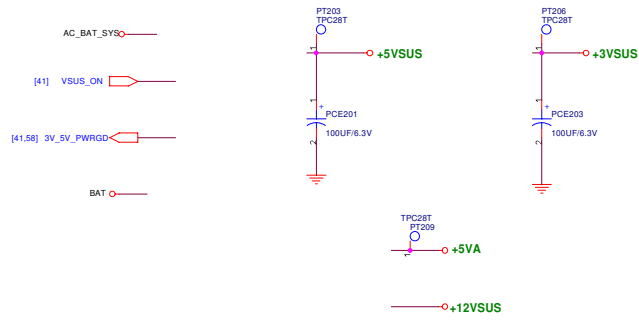


Z37S_12		Title : EMI CAP	
ASUSTek Computer INC.		Engineer: STD	
Size	Project Name	Rev	
Custom	STD	2.00	
Date: 11/01/13 11:28:2007		Sheet	48 of 60

<< Kennedy_Zhang >>



Put these elements close to daughter board !!

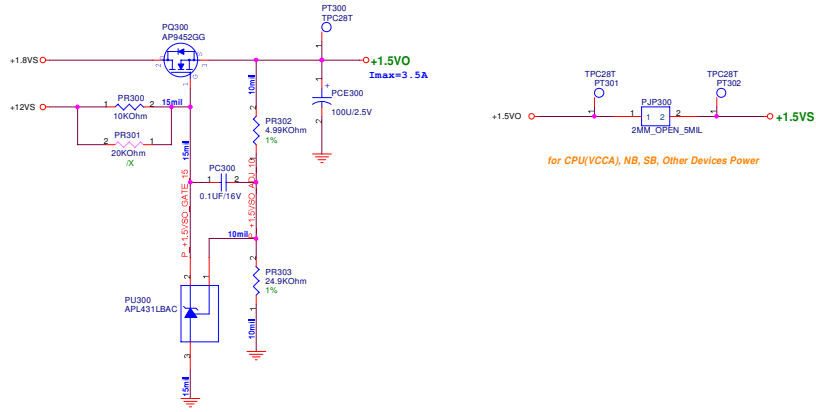


Z375_12

		Title : POWER_SYSTEM	
<OrigName>		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.0G	
Date: 18/01/2007		Sheet	50 of 50

<< Kennedy_Zhang >>

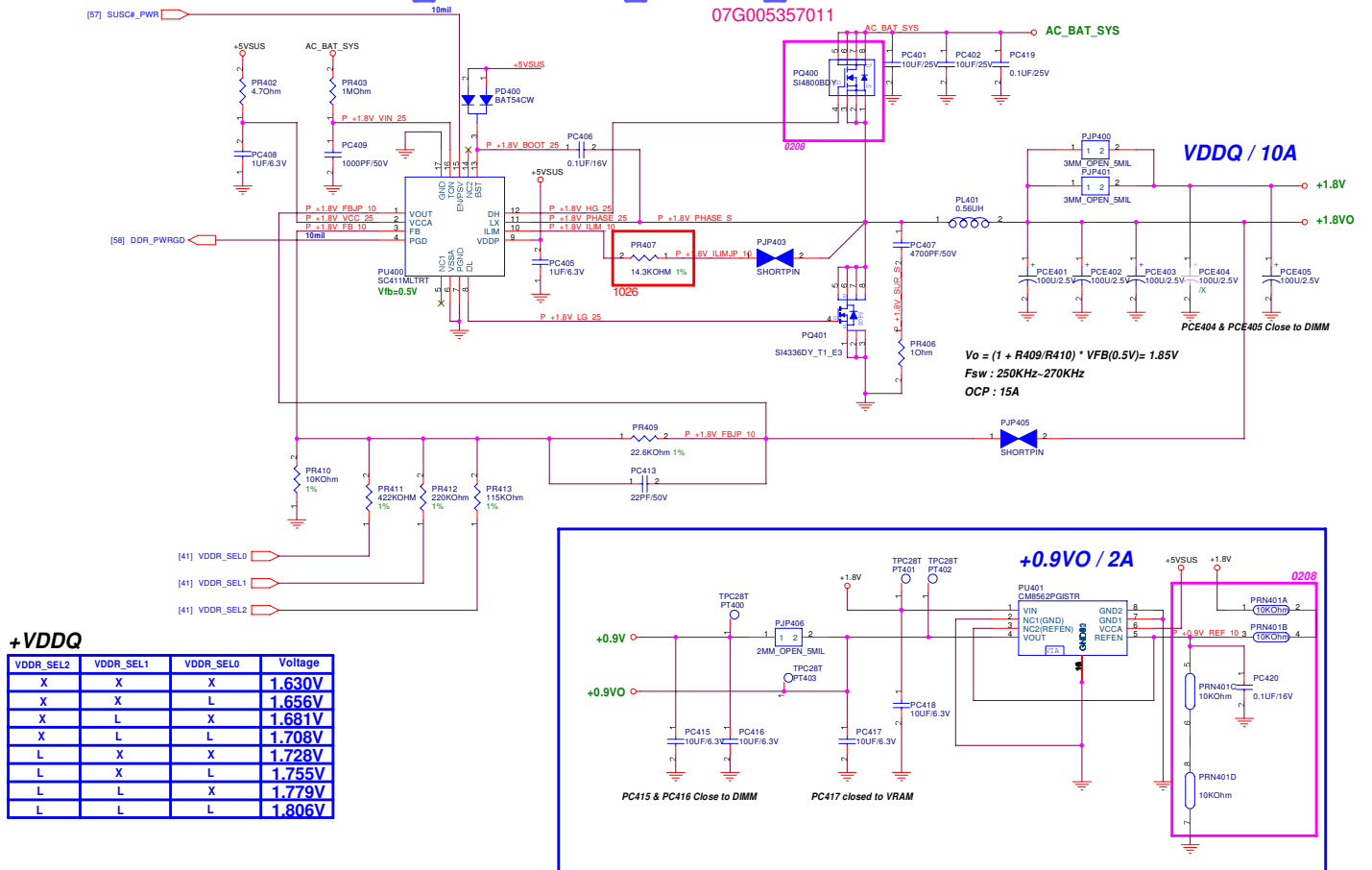
+1.5VS / 3.5A



Z37S_12

ASUS		Title : POWER_IO_1.5VS & 1.05VS	
<OrgName>		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.05	
Date: 8/10/2012 28/2002		Sheet	51 of 80

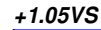
<< Kennedy_Zhang >>



Z37S_12

ASUS		Title : POWER_I/O, DDR & VTT	
-OrigName-		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.0G	
Date:	10/11/2007	Sheet	60 of 60

<< Kennedy_Zhang >>



VGMCH_SEL0	VGMCH_SEL1	Voltage
L	L	1.093V
X	L	1.001V
L	X	0.949V
X	X	0.857V

<< Kennedy_Zhang >>

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Z375_12



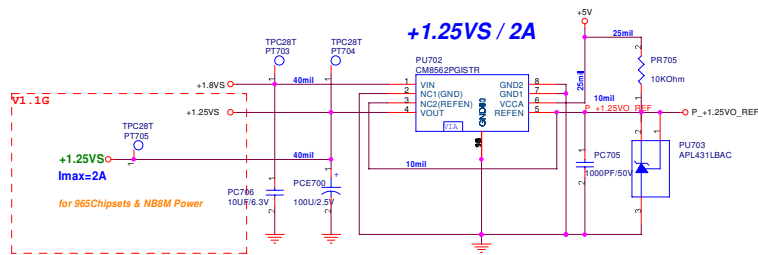
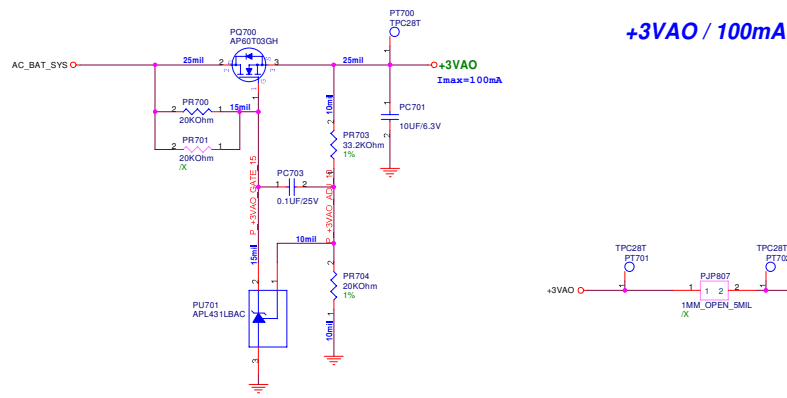
Title : POWER, VGA, CORE & RAM

Engineer:

Size	Project Name	Rev
A3	NAPA	2.0G

Date: 18/01/2017 28/01/2017 Sheet 54 of 60

<< Kennedy_Zhang >>

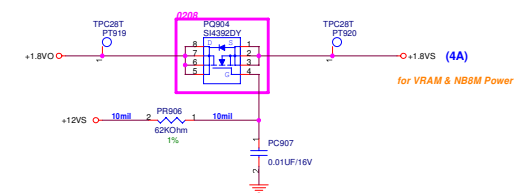
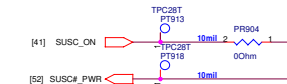


Z378_12

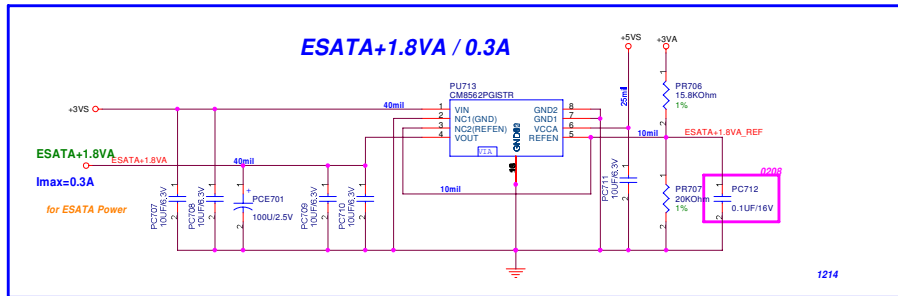
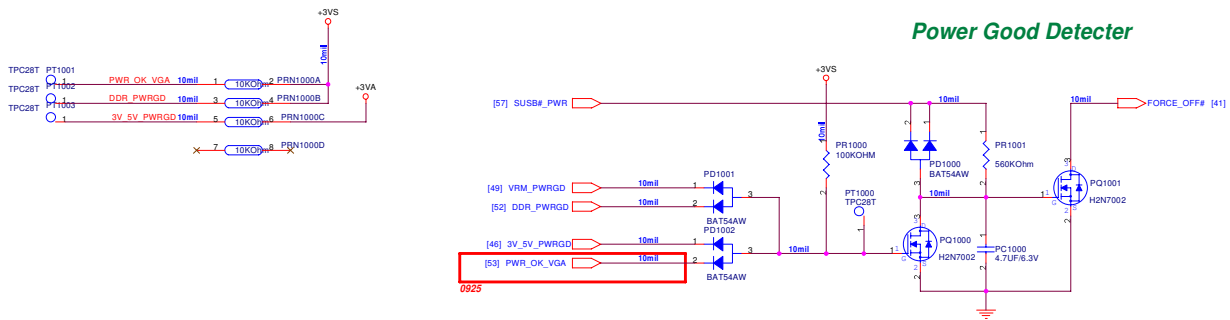
		Title : POWER_I/O_+3VA & +2.5V	
<OrgName>		Engineer:	
Size	Project Name	Rev	
A3	NAPA	2.0G	
Date: 星期四, 11月 26, 2007		Sheet	55 of 60

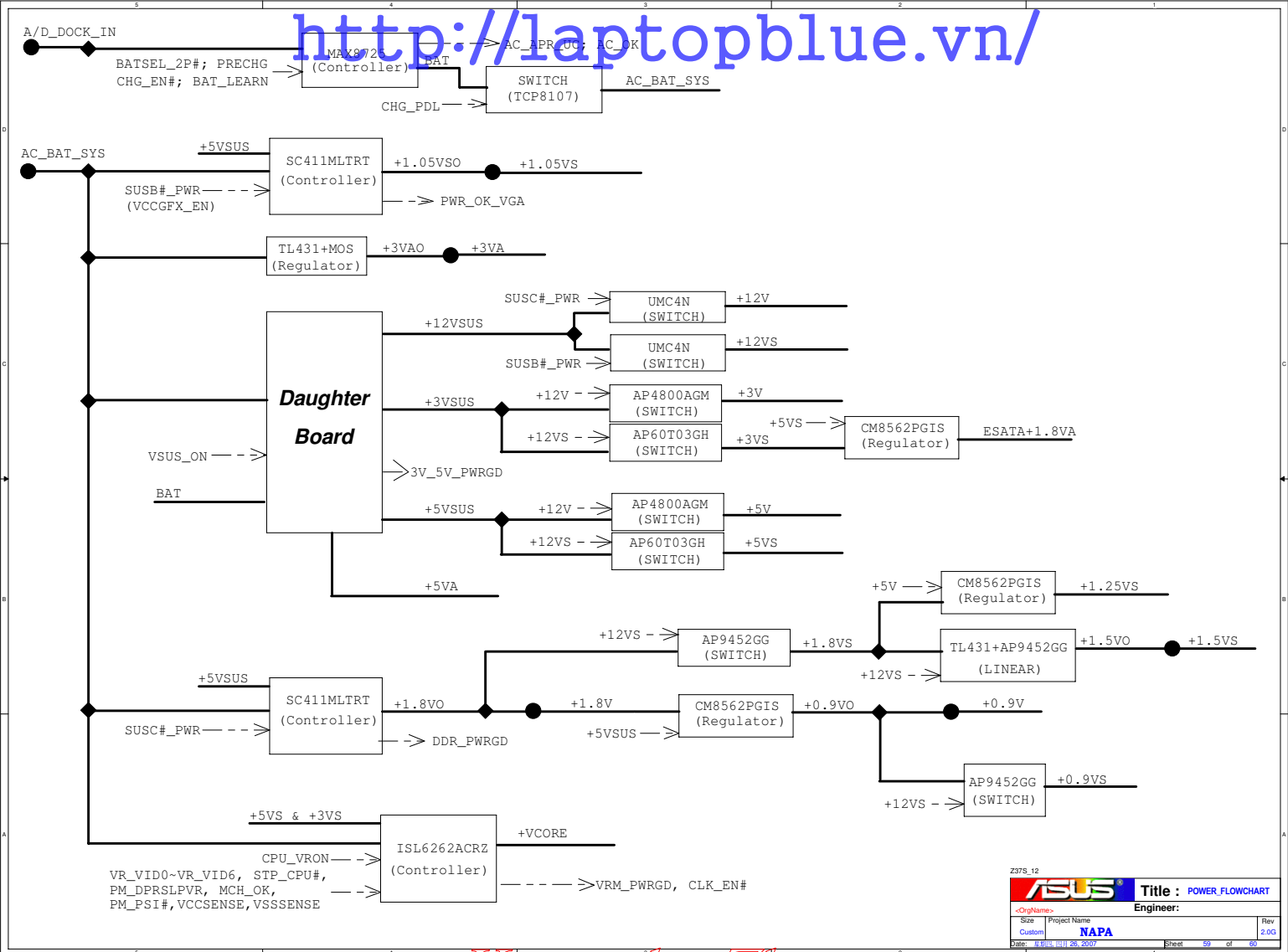
<< Kennedy_Zhang >>

<< Kennedy_Zhang >>




<< Kennedy_Zhang >>





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Z37S 12

		Title : POWER_SIGNAL	
<OrigName>		Engineer: Carl_Chiou	
Size	Project Name		Rev
Custom	NAPA		2.00
Date: 10/10/2007	Sheet 60 of 60		

<< Kennedy_Zhang >>