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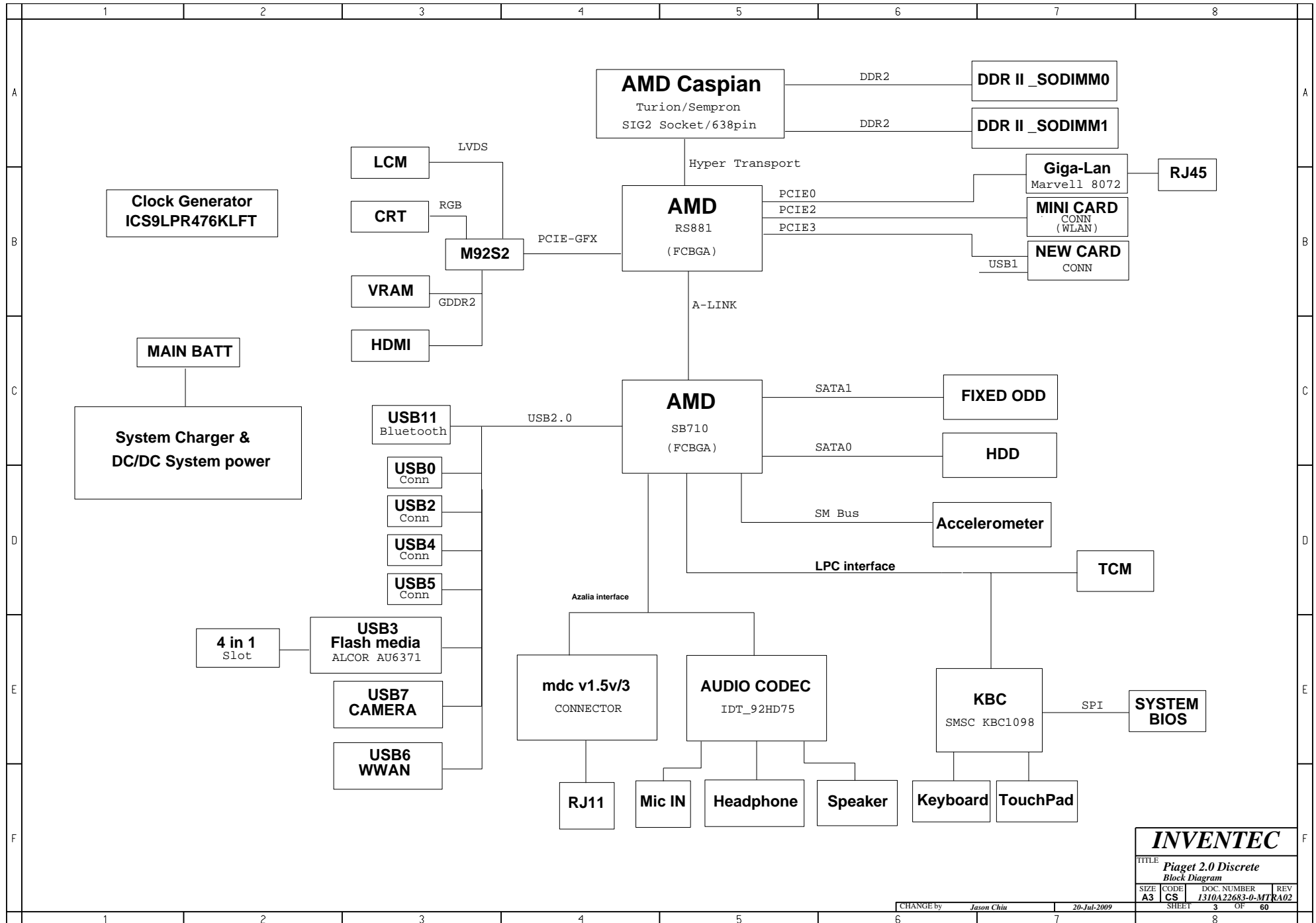
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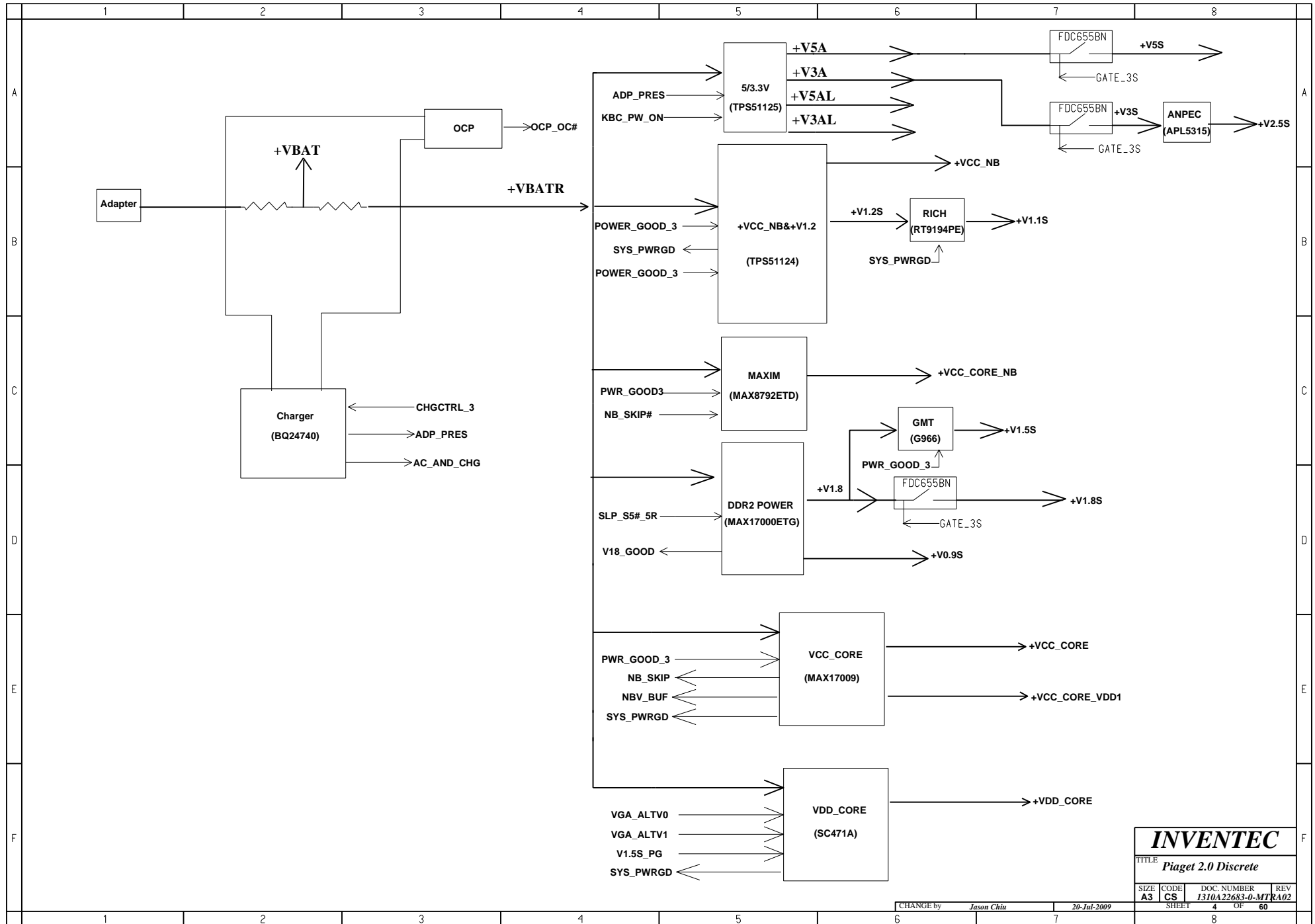
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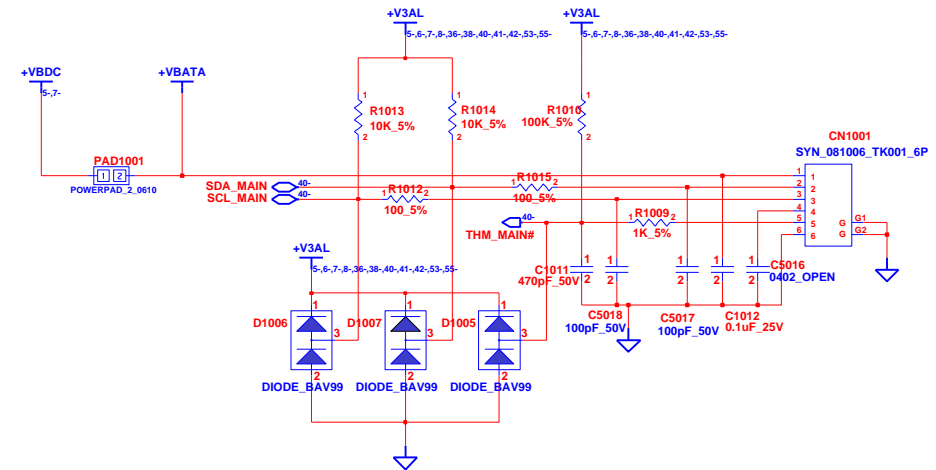
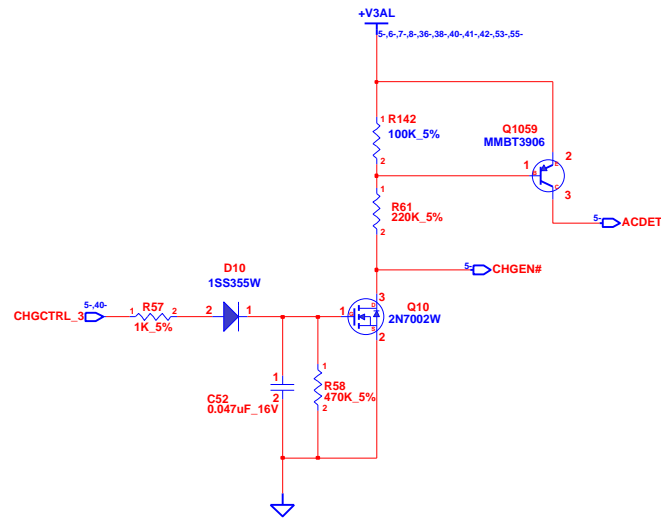
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<i>Piaget 2.0 Discrete</i>			
<i>Table OF Content</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTR	RA02
SHEET		2	OF 60

CHANGE by Jason Chiu 20-Jul-2009



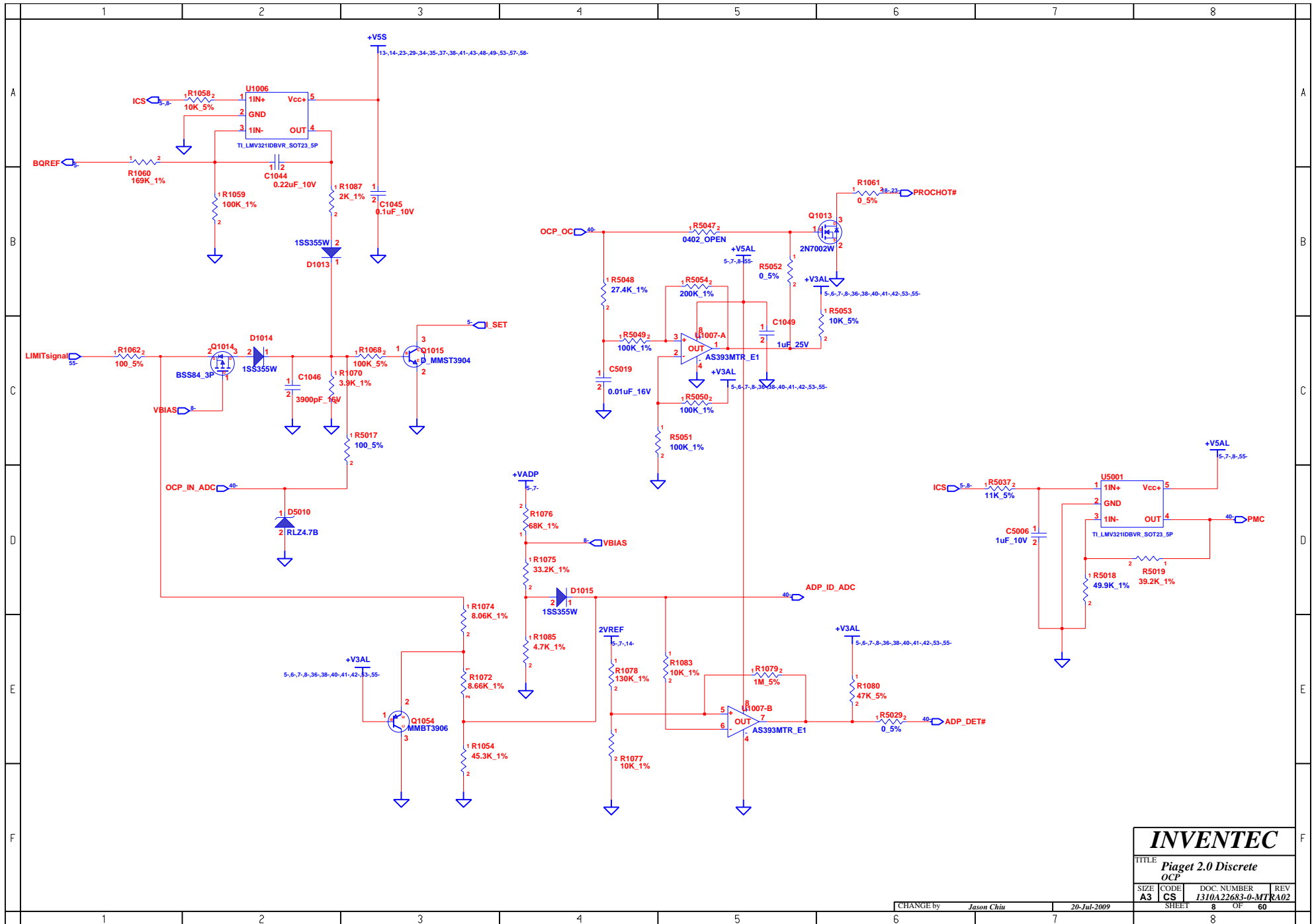




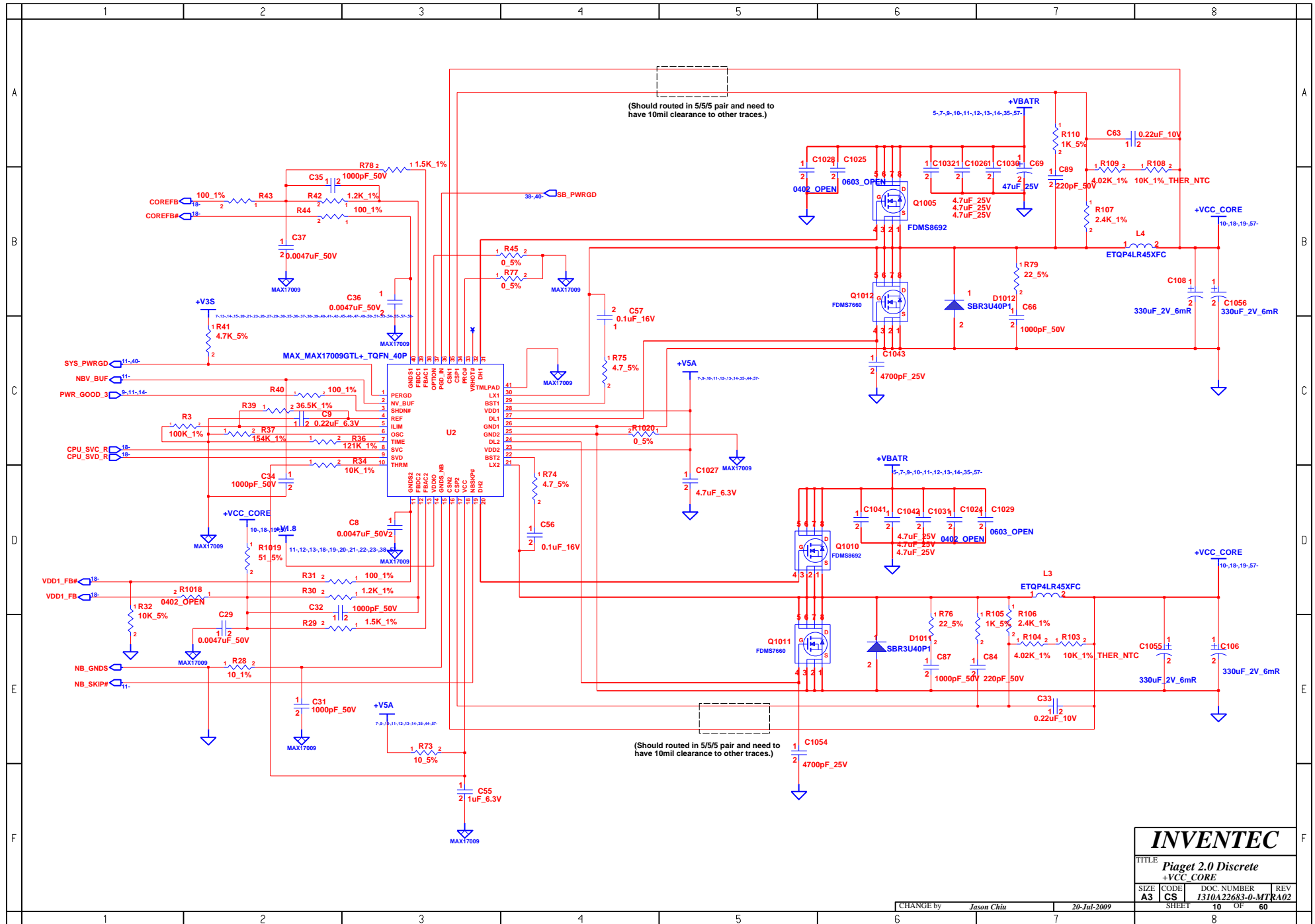
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SIZE A3	CODE CS	DOC. NUMBER <i>1310A22683-0-MTR</i>	REV RA02
CHANGE by <i>Jason Chin</i>		20-Jul-2009	
SHEET		6	OF 60

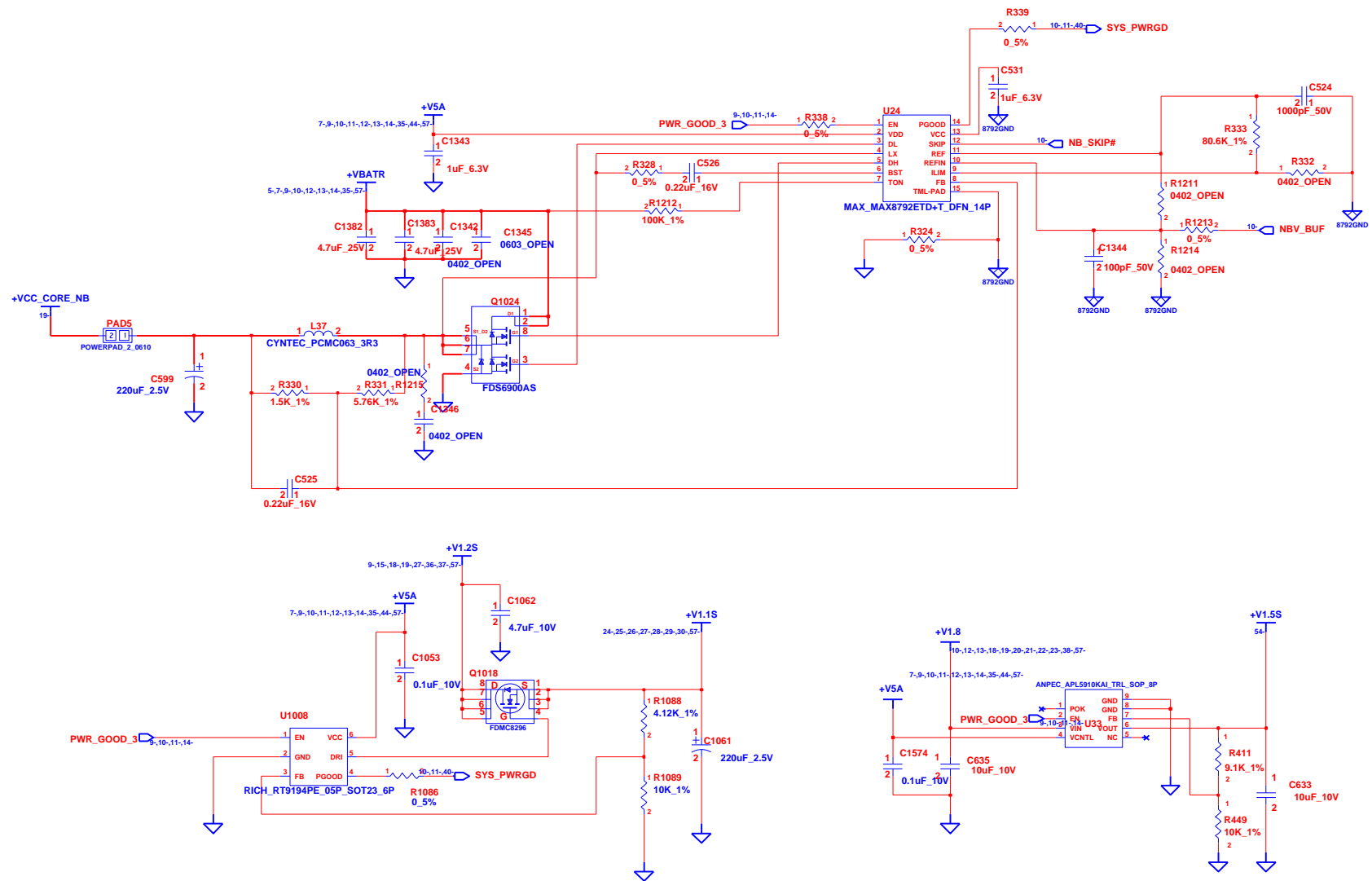
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<i>Piaget 2.0 Discrete</i> +V5A,+V3A&+V2.5S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTRA02	
SHEET		7	OF 60

CHANGE by	<i>Jason Chiu</i>	<i>12-Aug-2009</i>
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INVENTEC			
TITLE Piaget 2.0 Discrete OCP			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	SHEET 8 OF 60

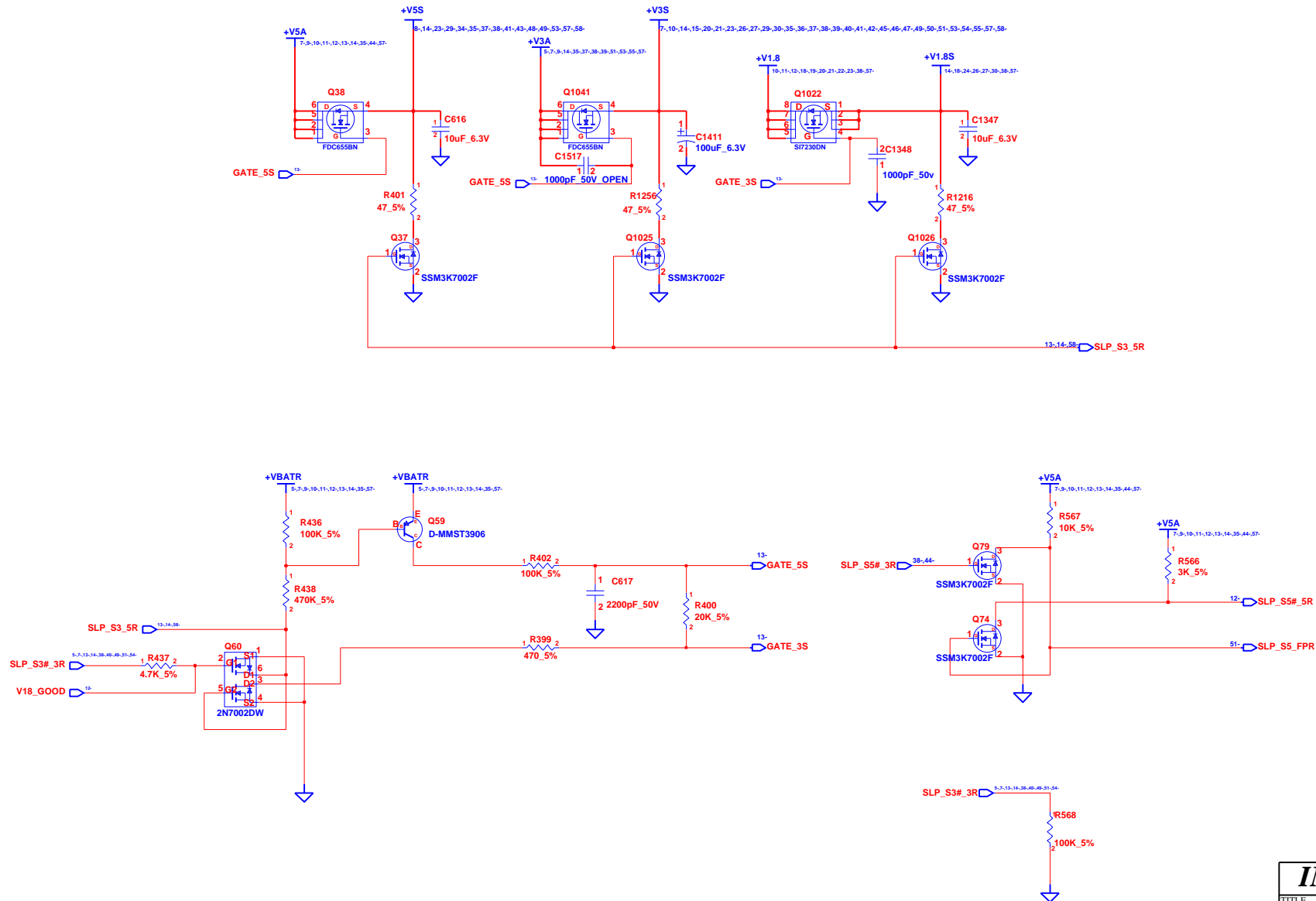




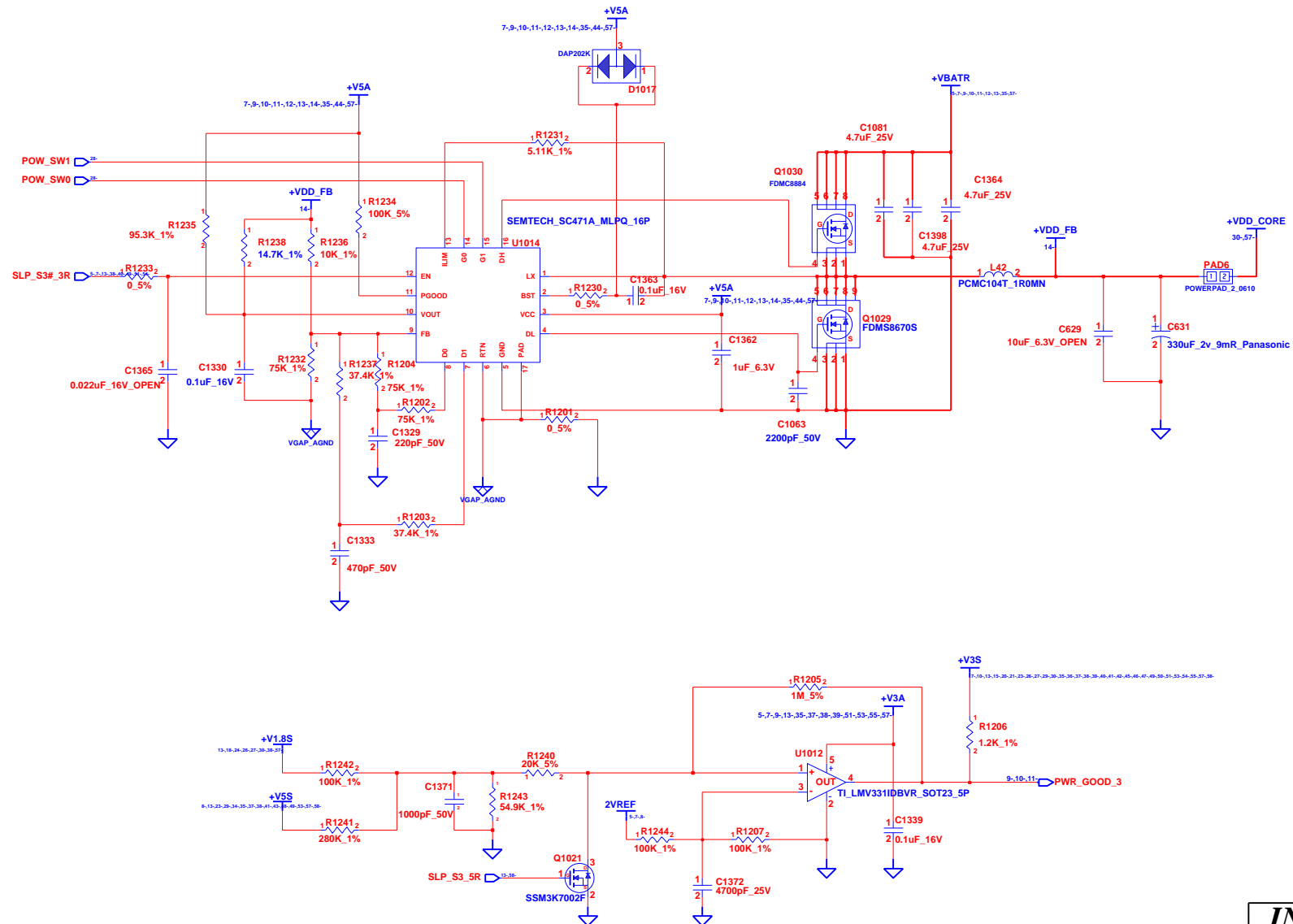
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CHANGE by <i>Jason Chiu</i>		20-Jul-2009	
SHEET 11		OF 60	

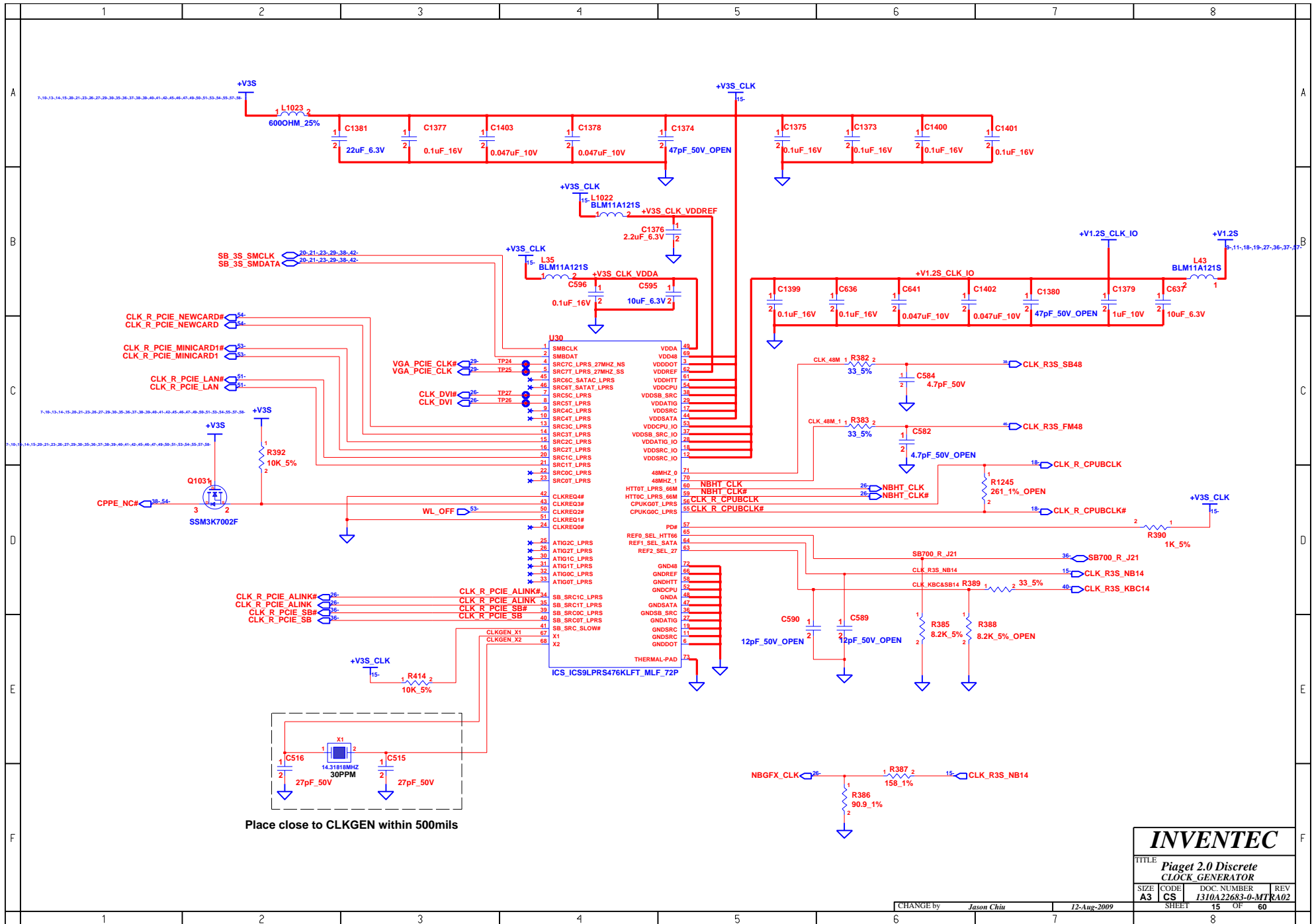
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INVENTEC			
TITLE Piaget 2.0 Discrete POWER(Sleep)			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET 13		OF 60	



INVENTEC			
TITLE Piaget 2.0 Discrete POWER(Sequence)			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		14-Aug-2009	
SHEET 14		OF 60	



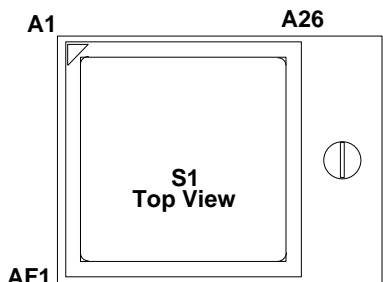
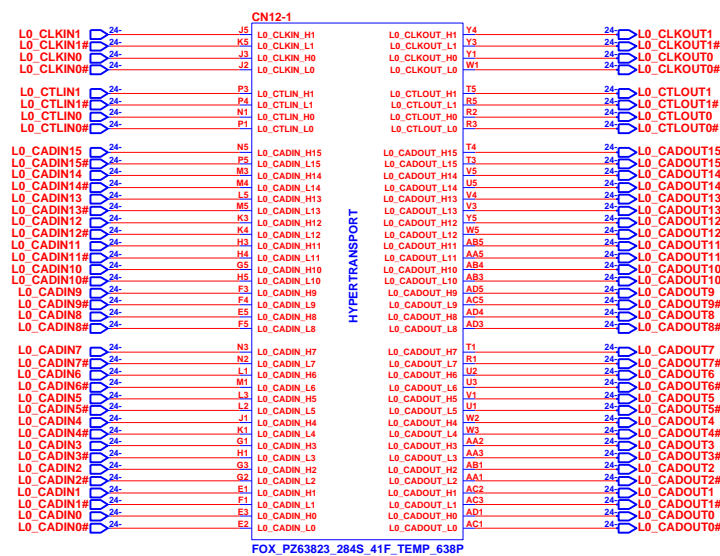
INVENTEC

TITLE
**Piaget 2.0 Discrete
CLOCK GENERATOR**

SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
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CHANGE by *Jason Chiu* 12-Aug-2009

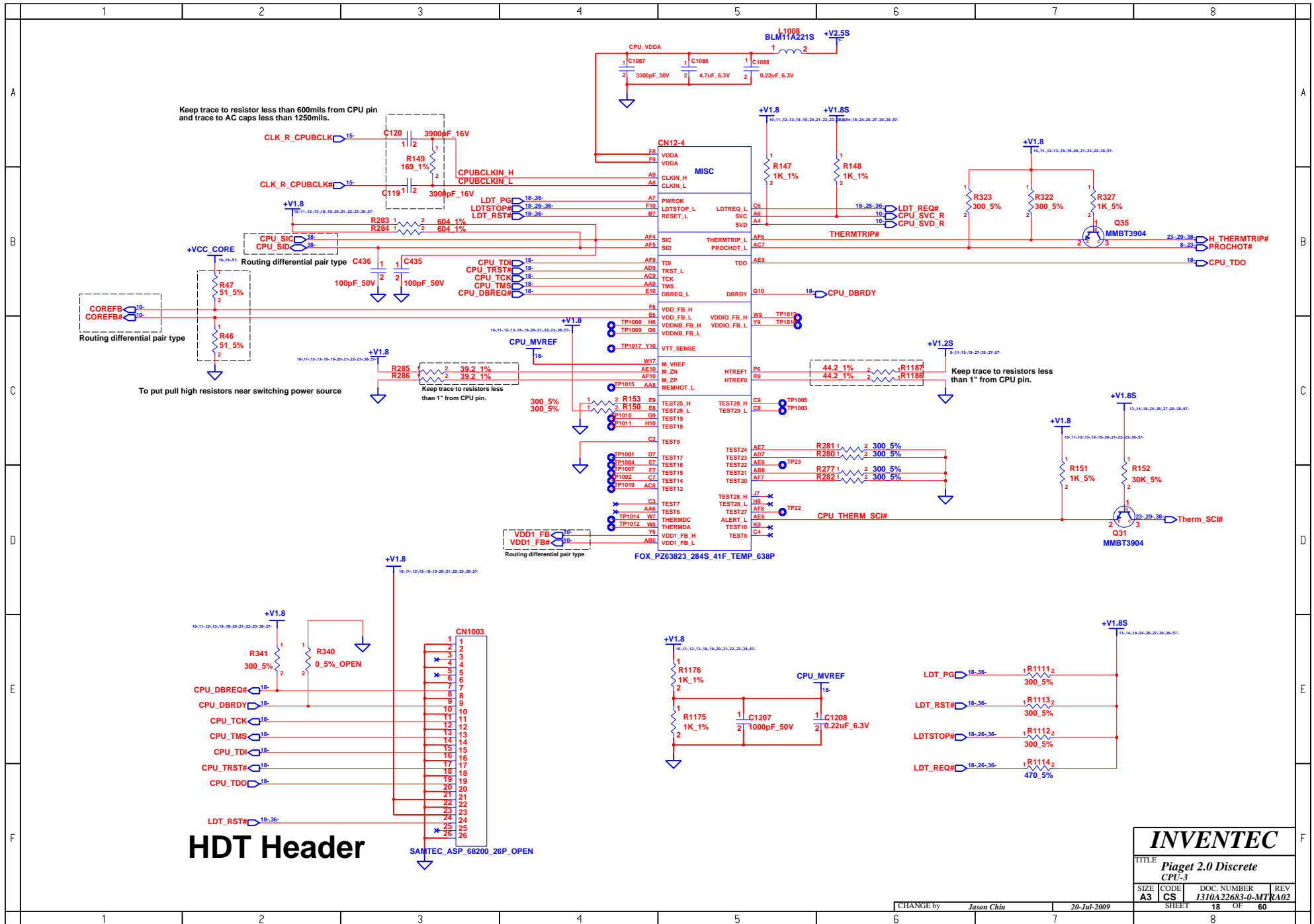
SHEET 15 OF 60

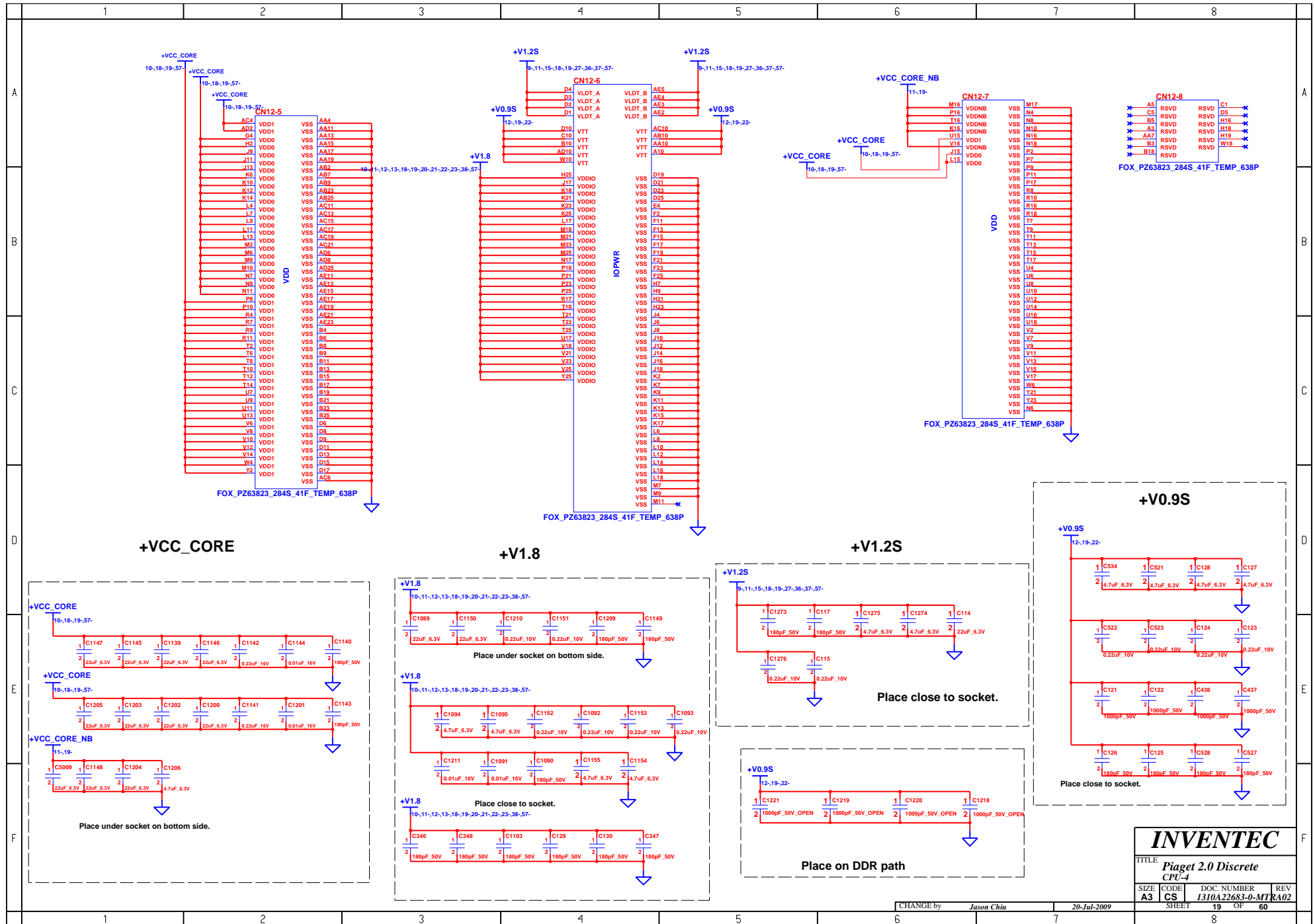


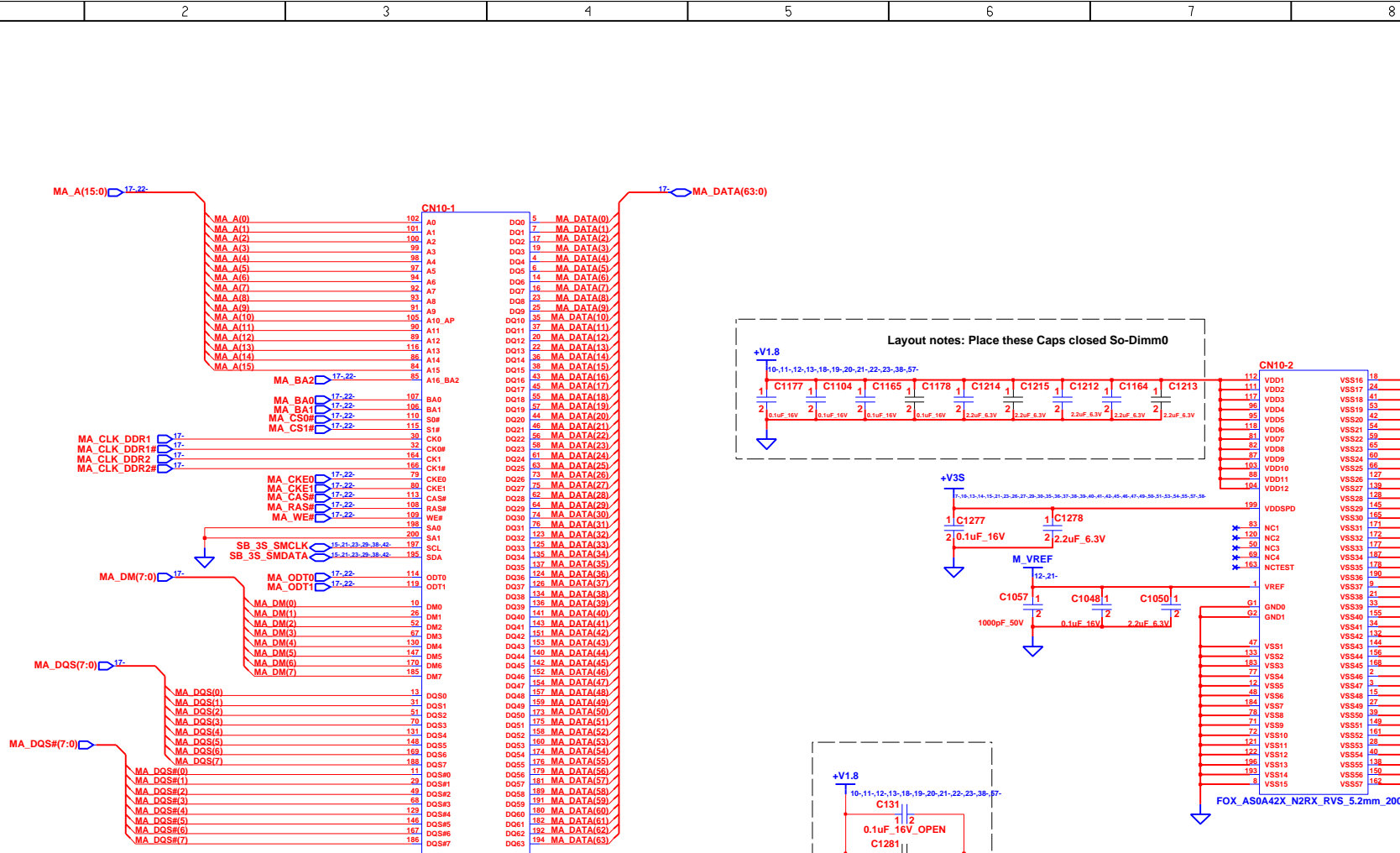
Layout: Add stitching caps if crossing plane split.

INVENTEC			
TITLE Piaget 2.0 Discrete CPU-1			
SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	SHEET 16 OF 60

CHANGE by	Jason Chiu	20-Jul-2009
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SO DIMM_5.2mm

For EMI Test

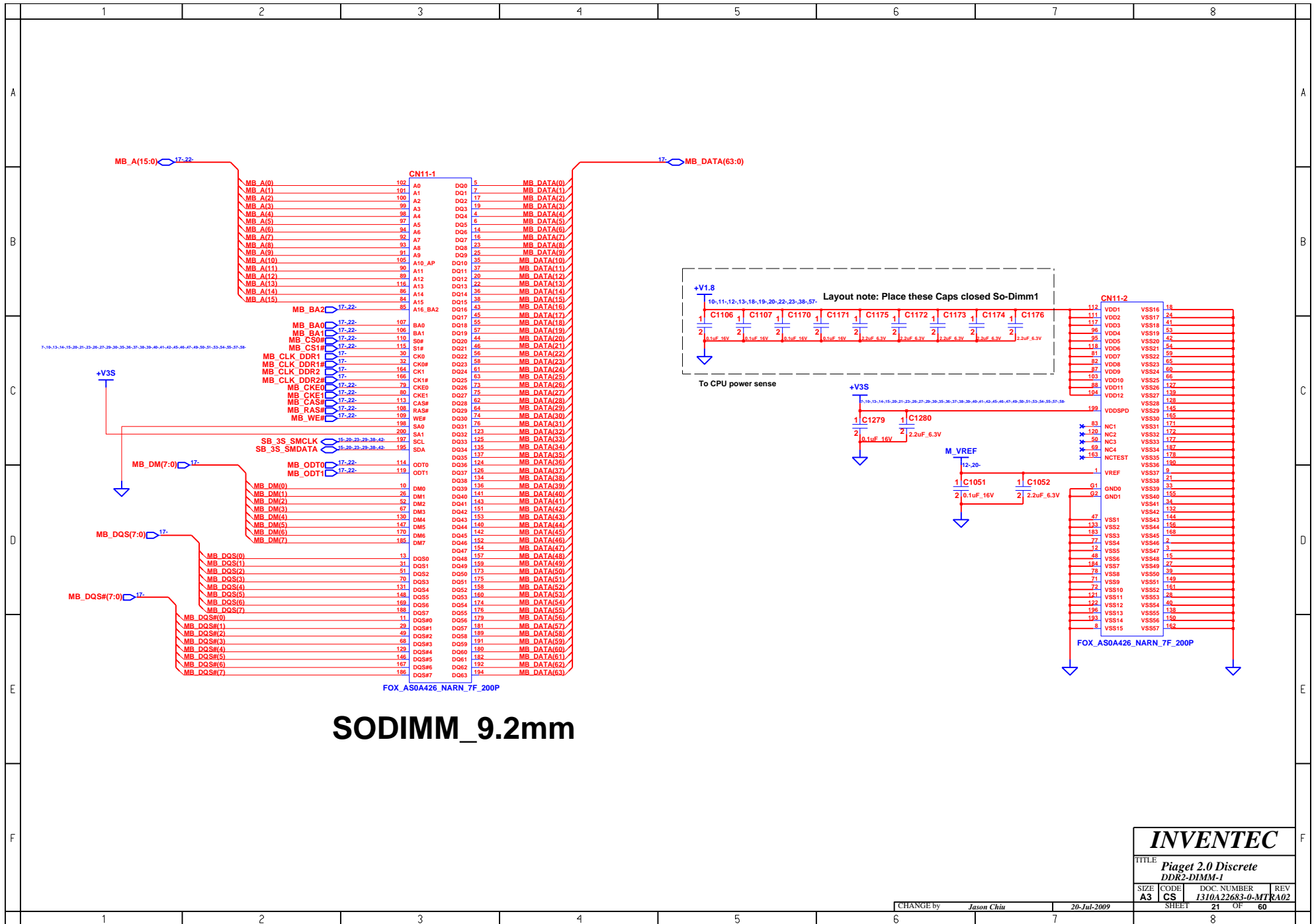
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DDR2-DIMM-0

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A3	CS	I310A22683-0-MTR	A02

CHANGE by: Jason Chiu 20-Jul-2009

SHEET 20 OF 60



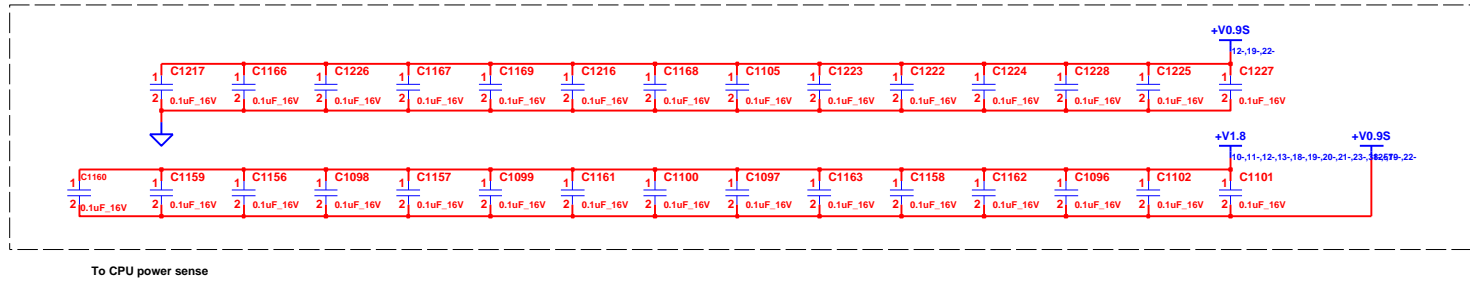
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TITLE
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DDR2-DIMM-1**

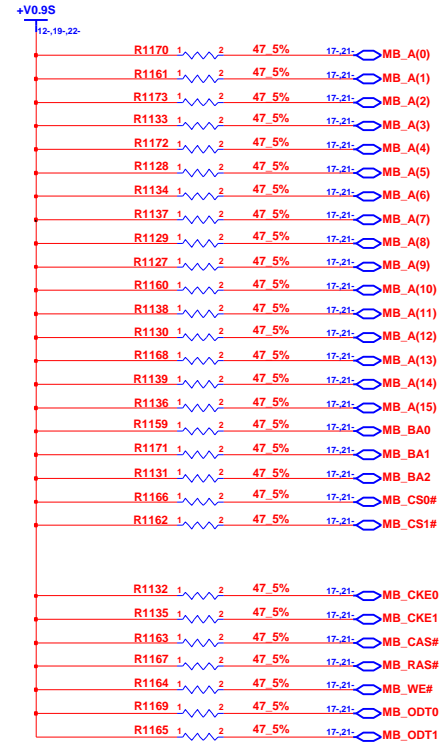
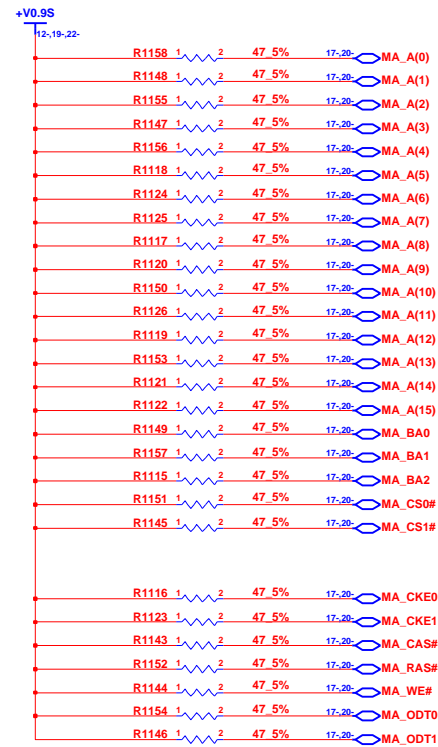
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A3	CS	I310A22683-0-MT	RA02

CHANGE by Jason Chiu 20-Jul-2009

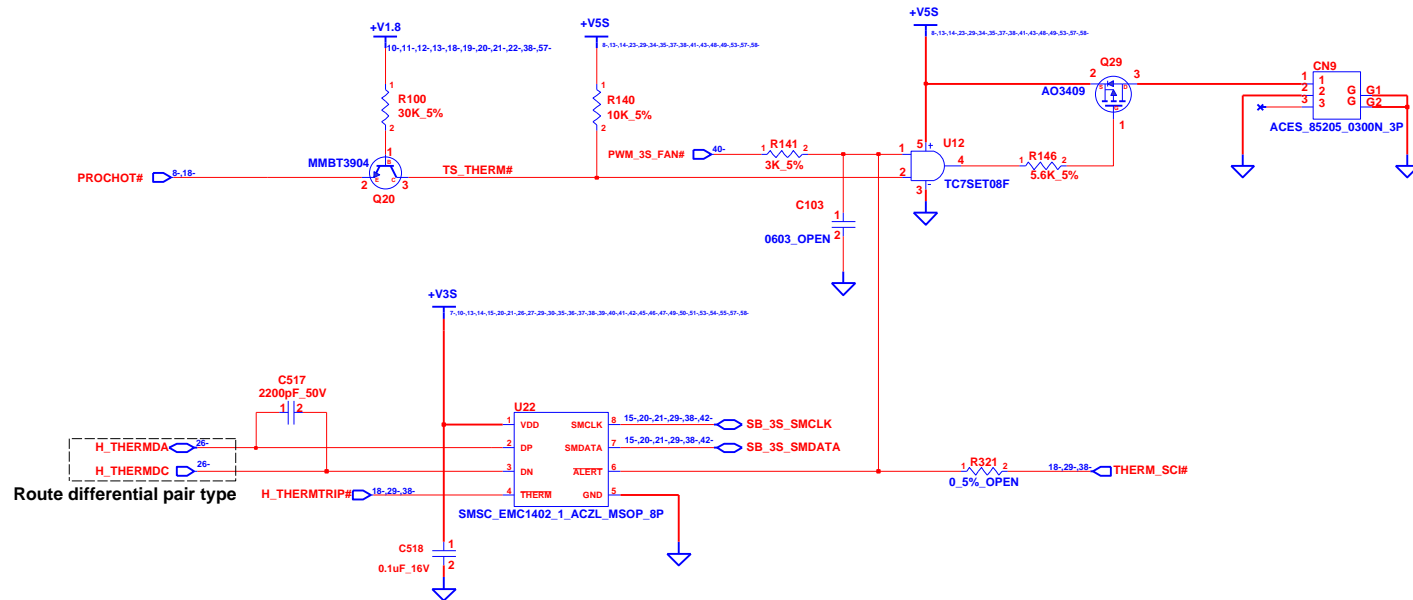
SHEET 21 OF 60



Place CAPs close to DIMM

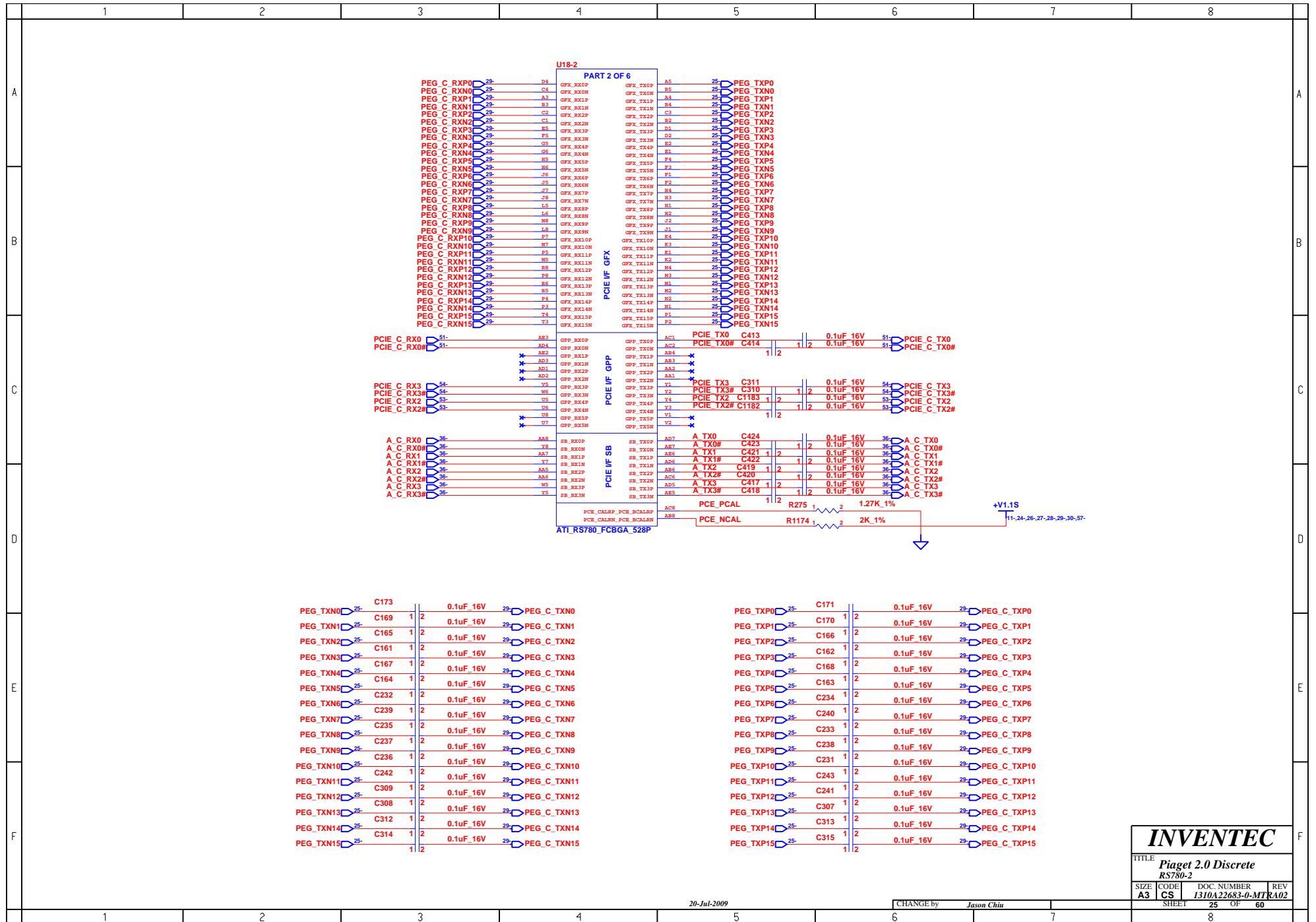


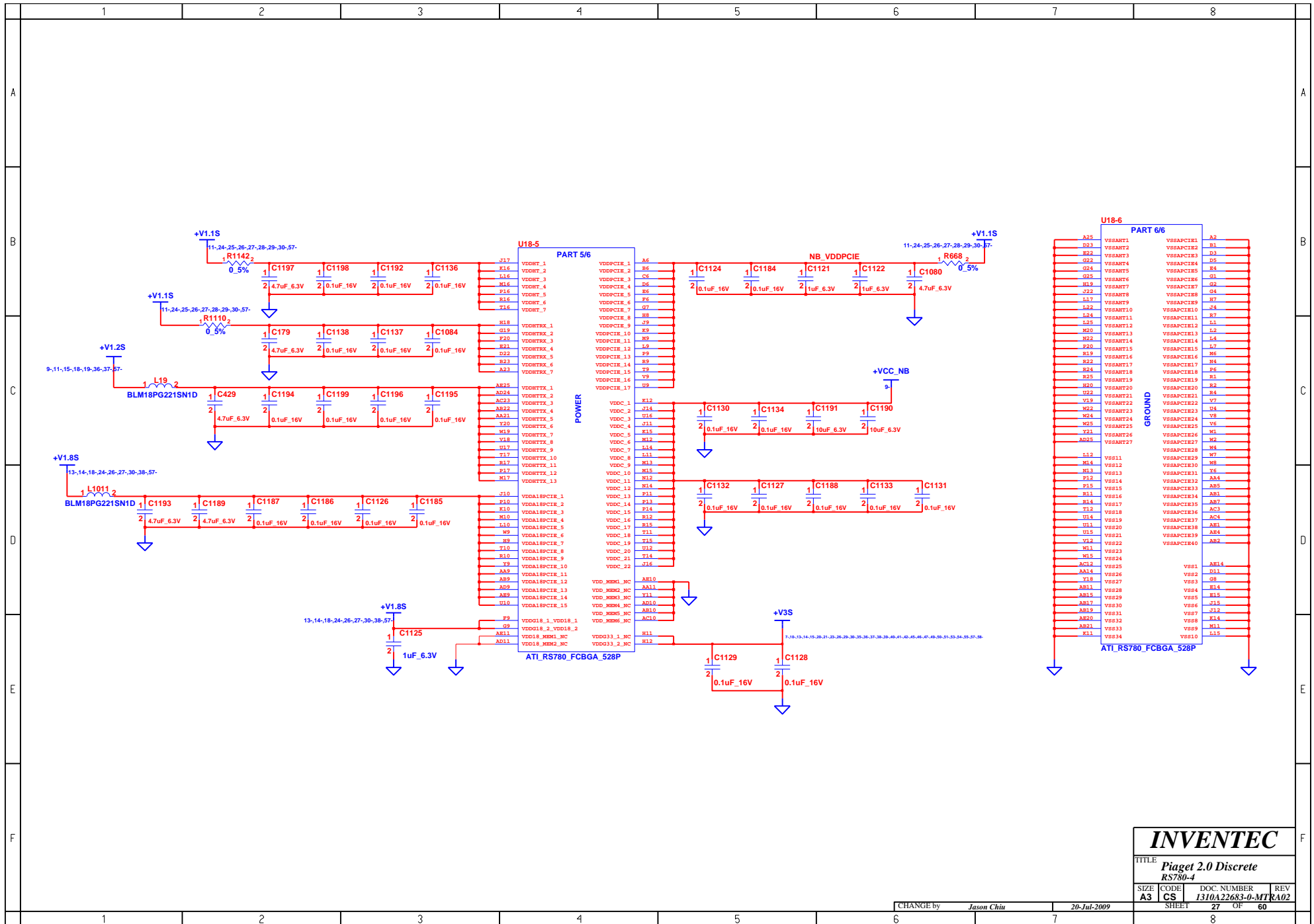
INVENTEC			
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SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MT	REV RA02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET		22	OF 60



LAYOUT Note: Put the thermal sensor close to CPU.

INVENTEC			
TITLE			
Piaget 2.0 Discrete			
THERMAL & FAN CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MTR	RA02
CHANGE by		20-Jul-2009	
SHEET		23	OF 60

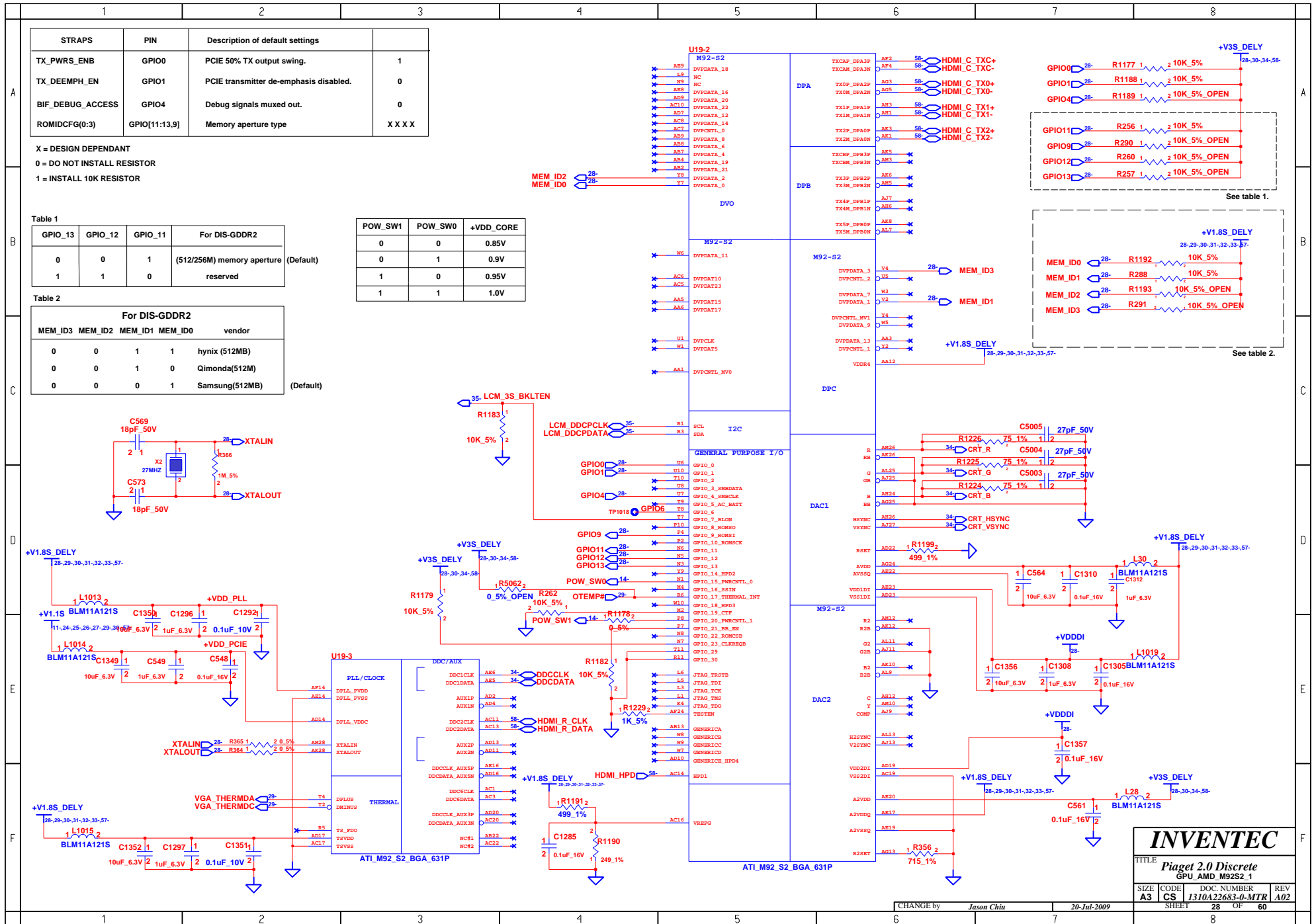


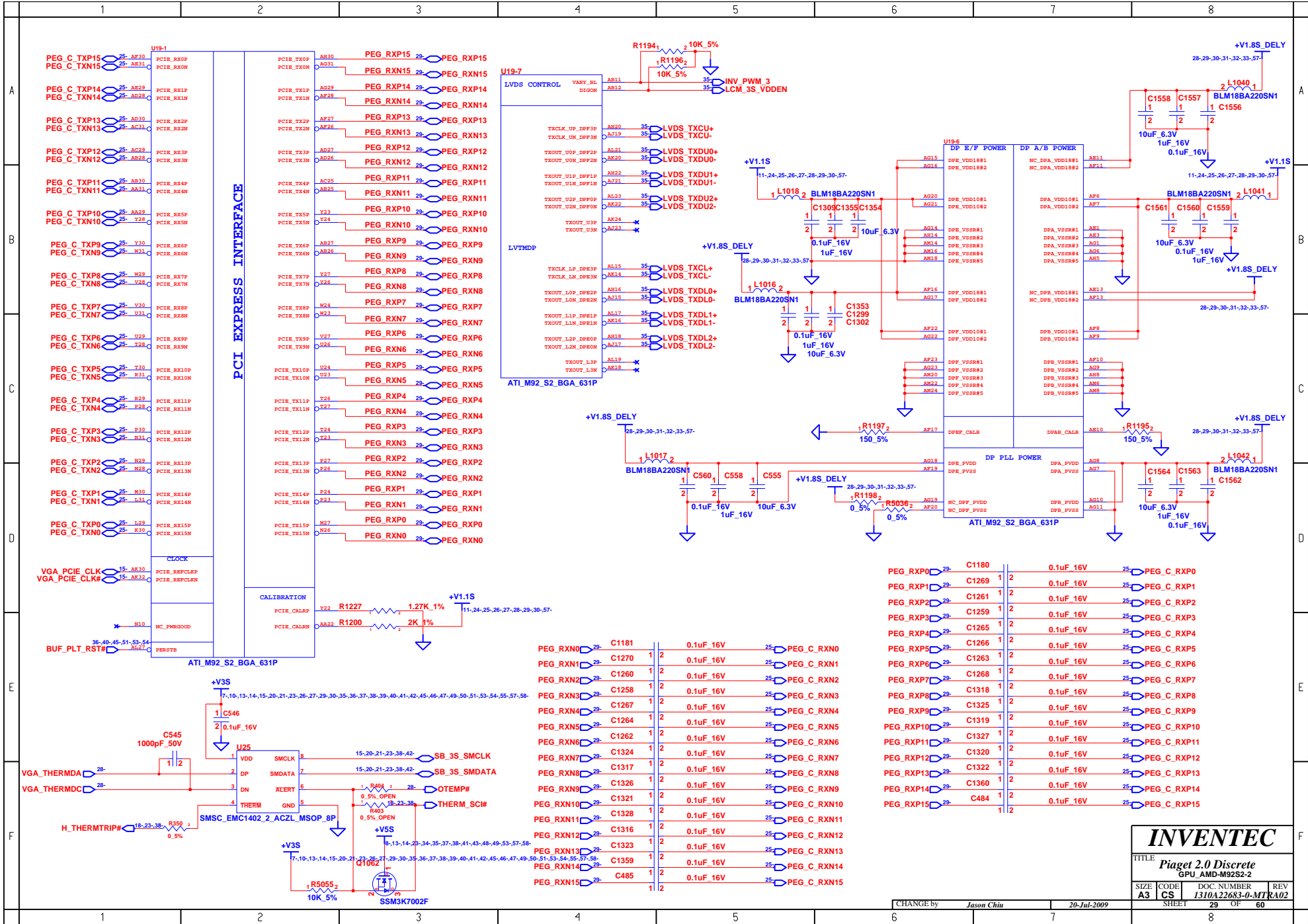


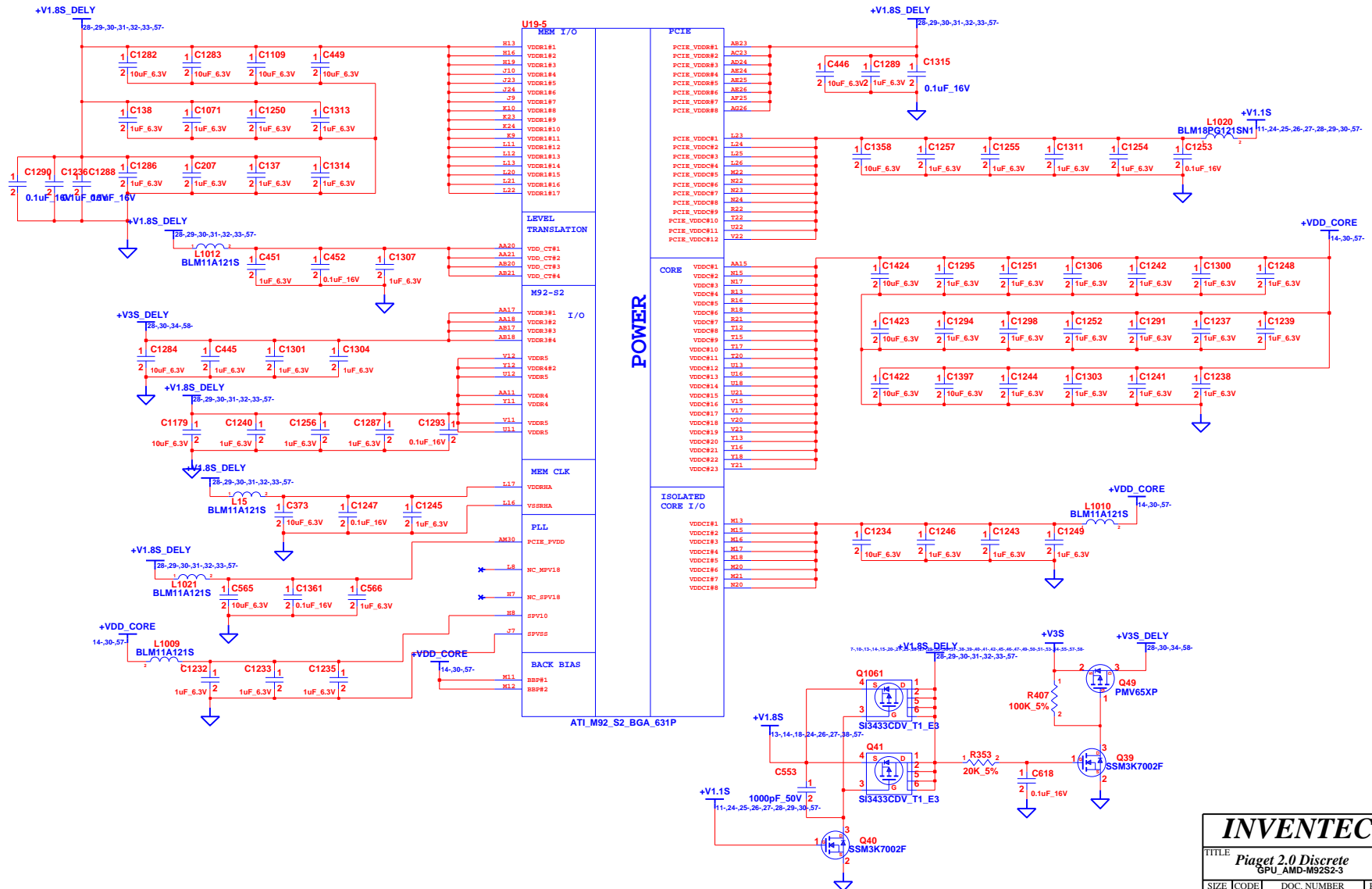
INVENTEC

TITLE Piaget 2.0 Discrete RS780-4			
SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MT	REV A02
SHEET 27		OF 60	

CHANGE by Jason Chiu 20-Jul-2009



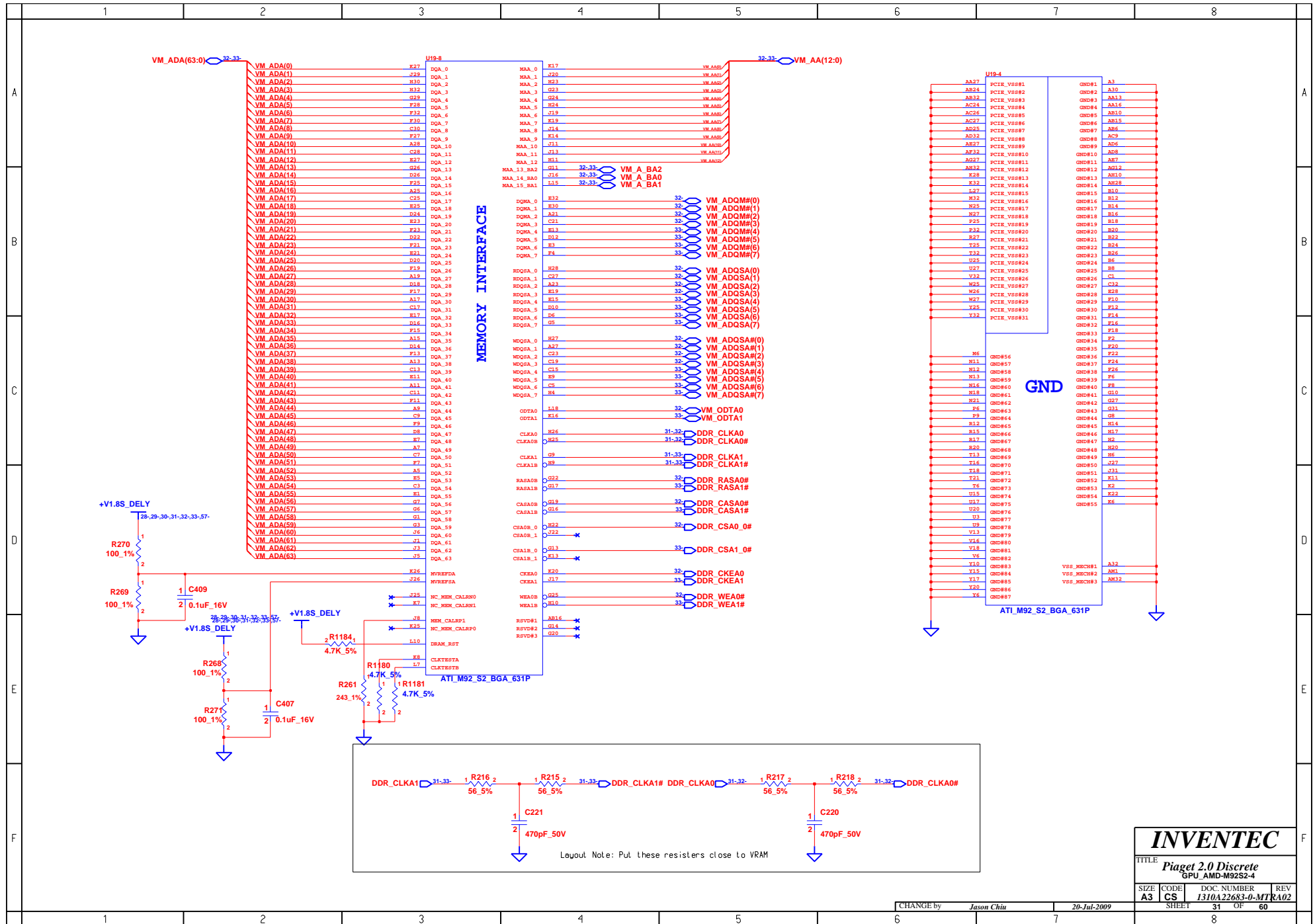


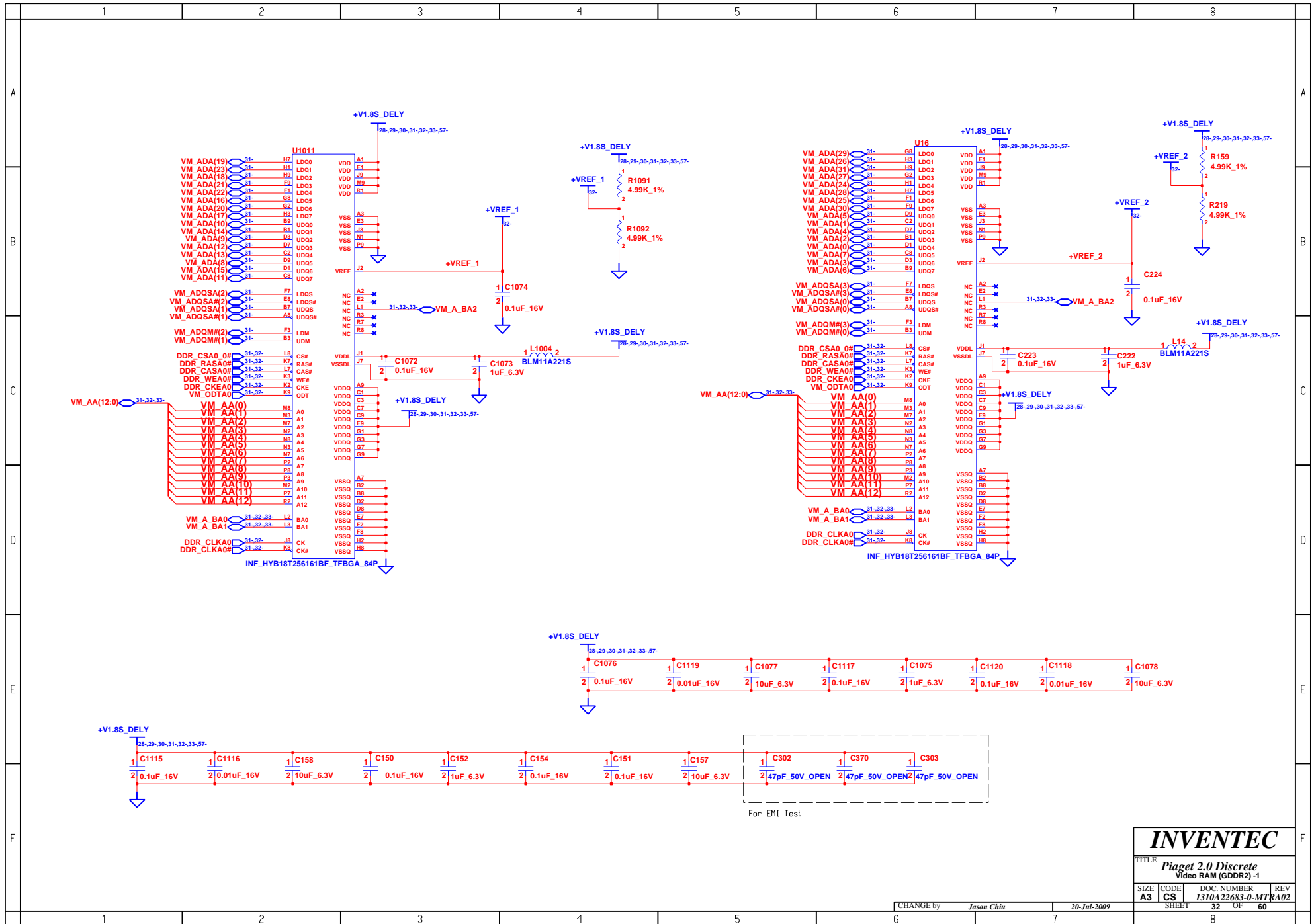


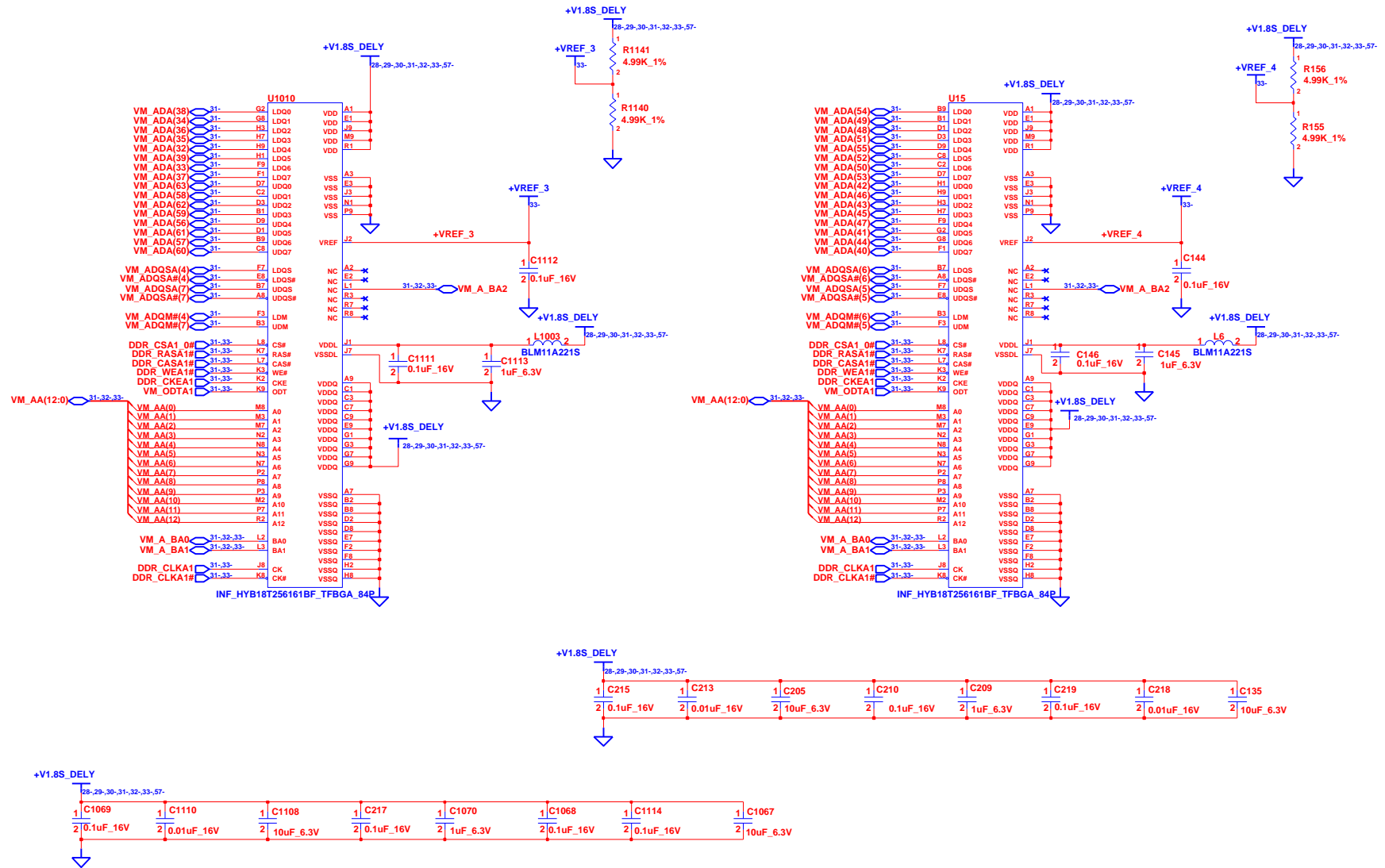
INVENTEC

TITLE			
Piaget 2.0 Discrete			
GPU_AMD-M92S2-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MTR	A02
SHEET		30	OF 60

CHANGE by Jason Chiu 20-Jul-2009



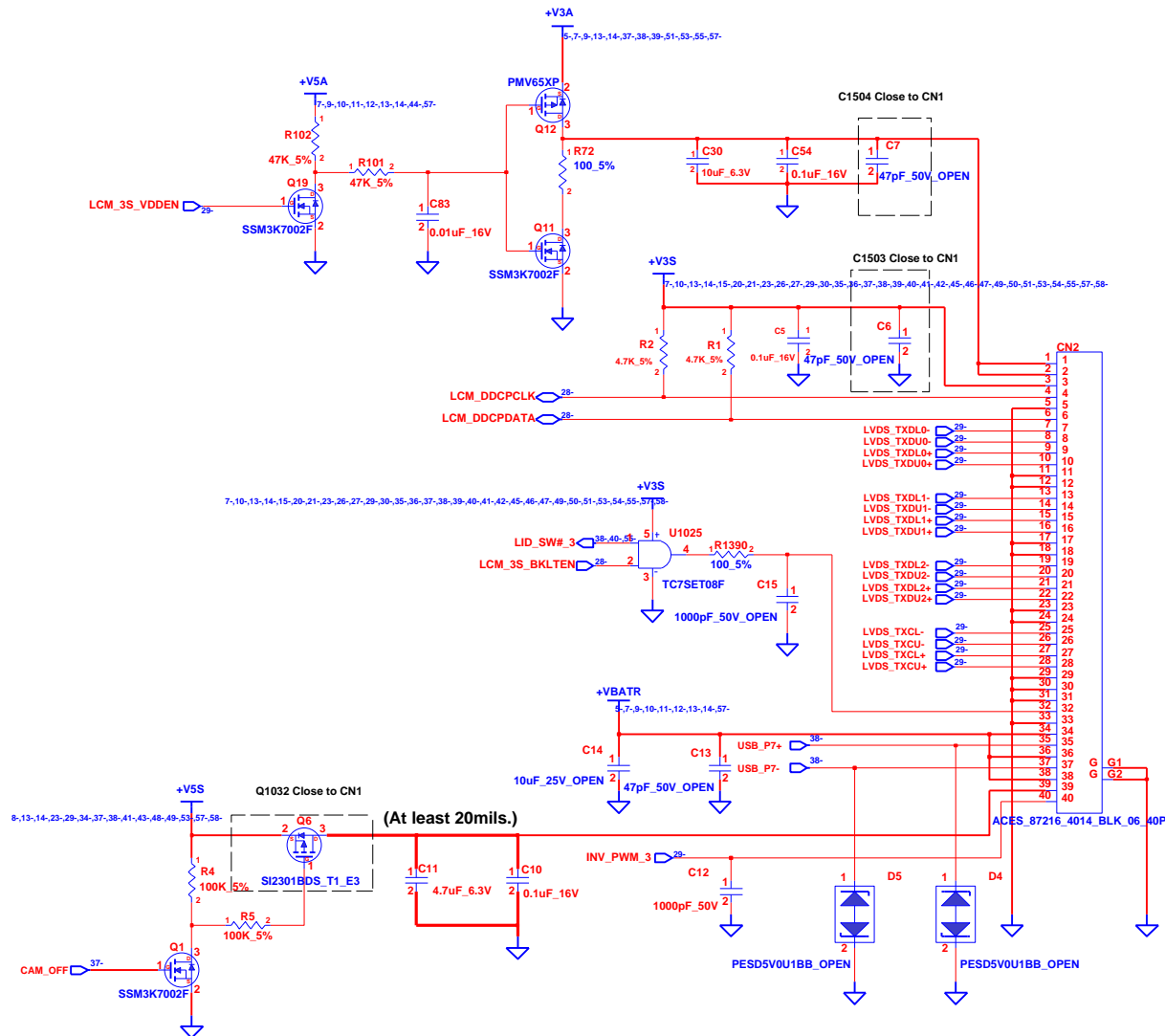




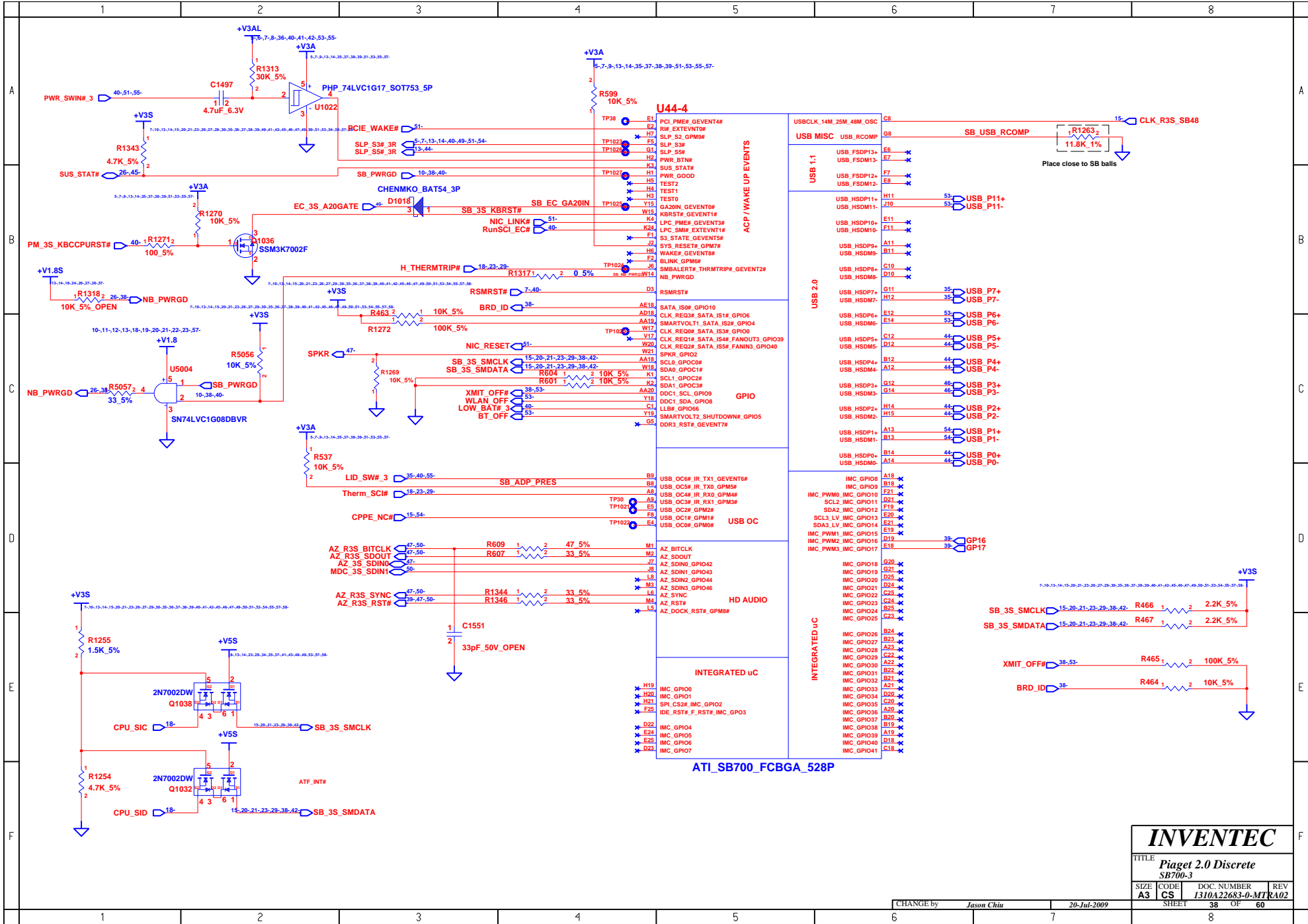
INVENTEC

TITLE			
Piaget 2.0 Discrete Video RAM (GDDR2)-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A22683-0-MT	RA02
SHEET		33	OF 60

CHANGE by Jason Chiu 20-Jul-2009



INVENTEC			
TITLE: Piaget 2.0 Discrete LCD CONN & WEBCAM CONN			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22683-0-MTR	REV: A02
CHANGE by: Jason Chiu		20-Jul-2009	
SHEET: 35		OF: 60	

**INVENTEC**

TITLE	<i>Piaget 2.0 Discrete</i> <i>SB700-3</i>
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTRA02	

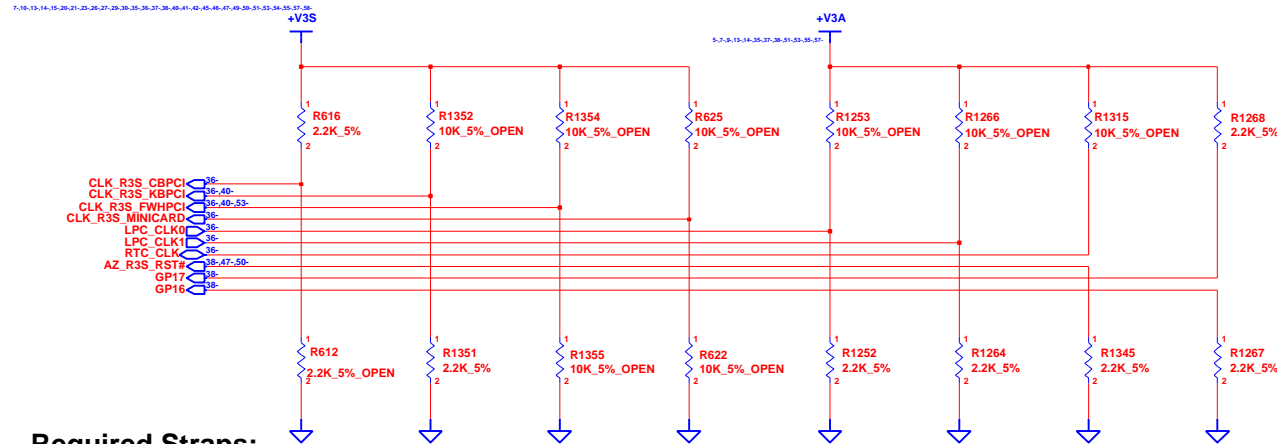
CHANGE by	<i>Jason Chiu</i>
-----------	-------------------

20-Jul-2009

CS	1310A22683-0-MTR
SHEET	38 OF 60

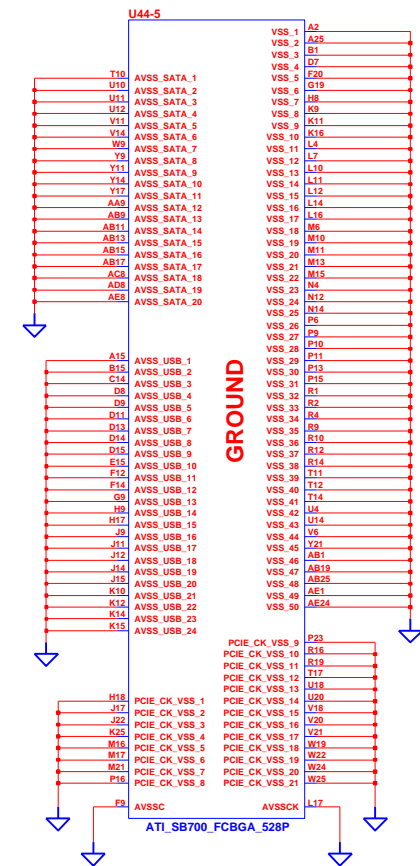
38 OF 60

38 OF 60



Required Straps:

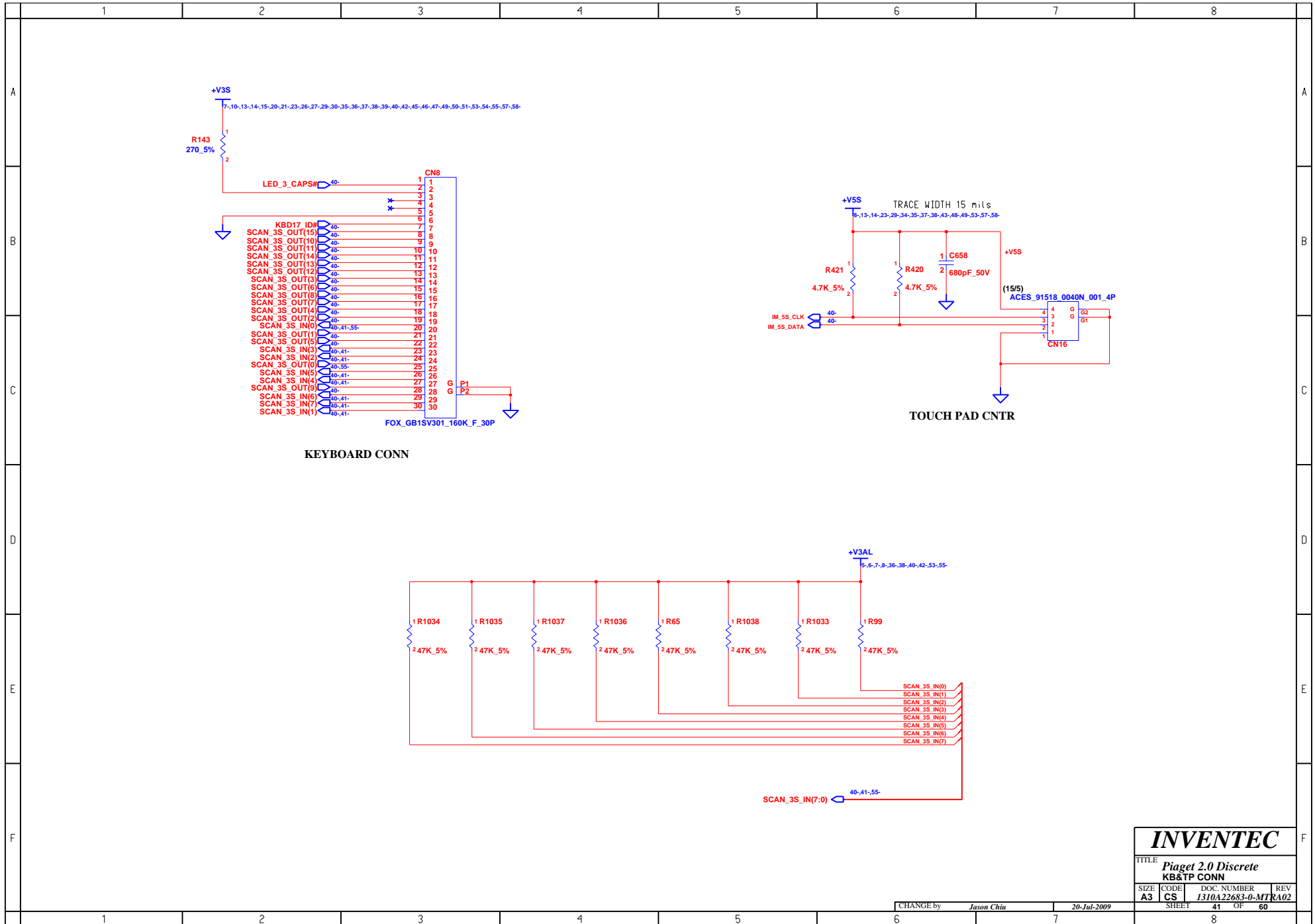
	CLK_R3S_CBPCI	CLK_R3S_KBPCI	CLK_R3S_FWHPCI	CLK_R3S_MINICARD	LPC_CLK0	LPC_CLK1	RTC_CLK	AZ_RST#	GP17 GP16
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	ENABLE PCI MEM BOOT	CLKGEN ENABLED	INTERNAL RTC DEFAULT	EC ENABLED	ROM TYPE H, H = Reserved H, L = SPI ROM
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			DISABLE PCI MEM BOOT DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK	EC DISABLED DEFAULT	L, H = LPC ROM L, L = FWH ROM

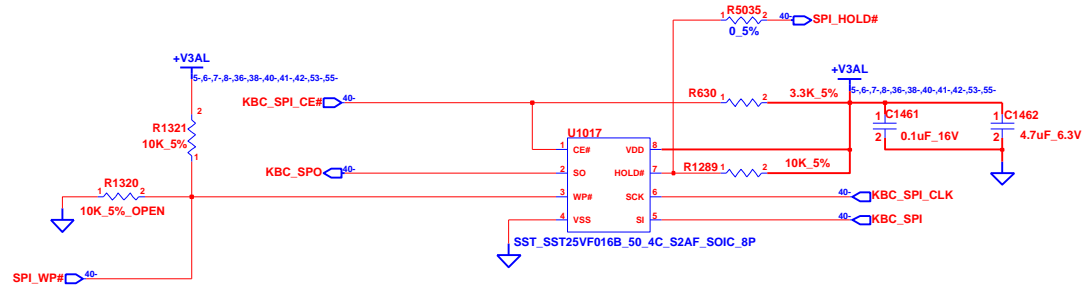


INVENTEC

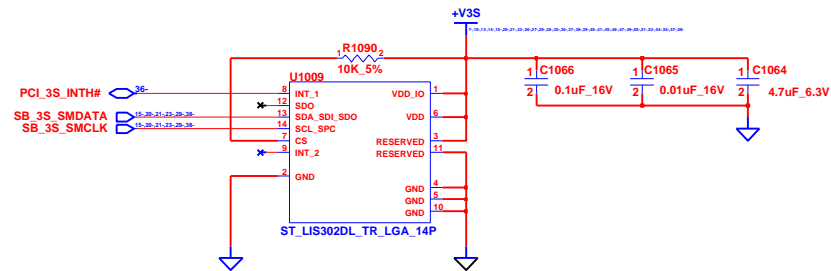
TITLE Piaget 2.0 Discrete SB700-4			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
SHEET 39		OF 60	

CHANGE by **Jason Chin** 20-Jul-2009





SPI Flash



Accelerometer

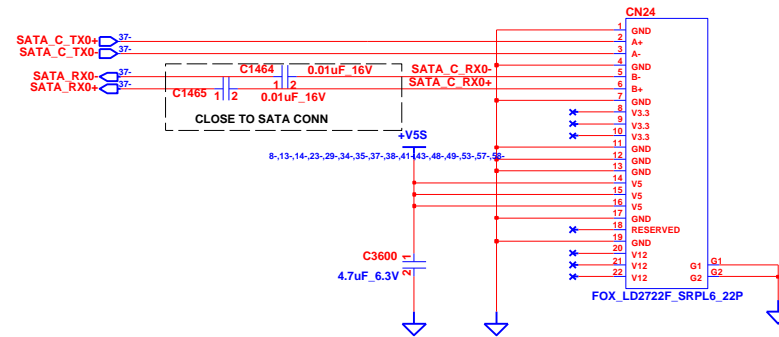
INVENTEC

TITLE
**Piaget 2.0 Discrete
SPI & ACCELEROMETER**

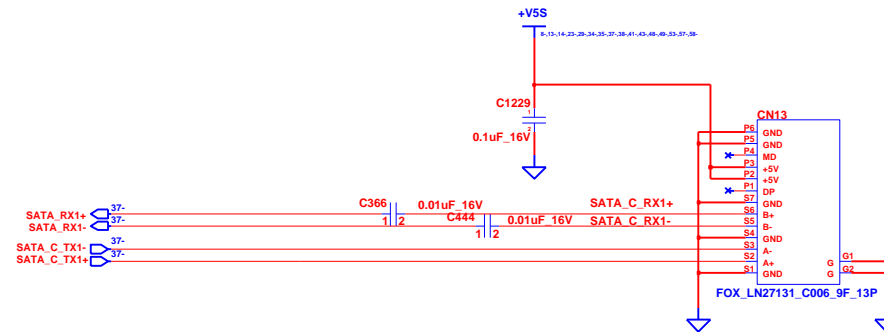
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
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CHANGE by *Jason Chin* 20-Jul-2009

SHEET 42 OF 60

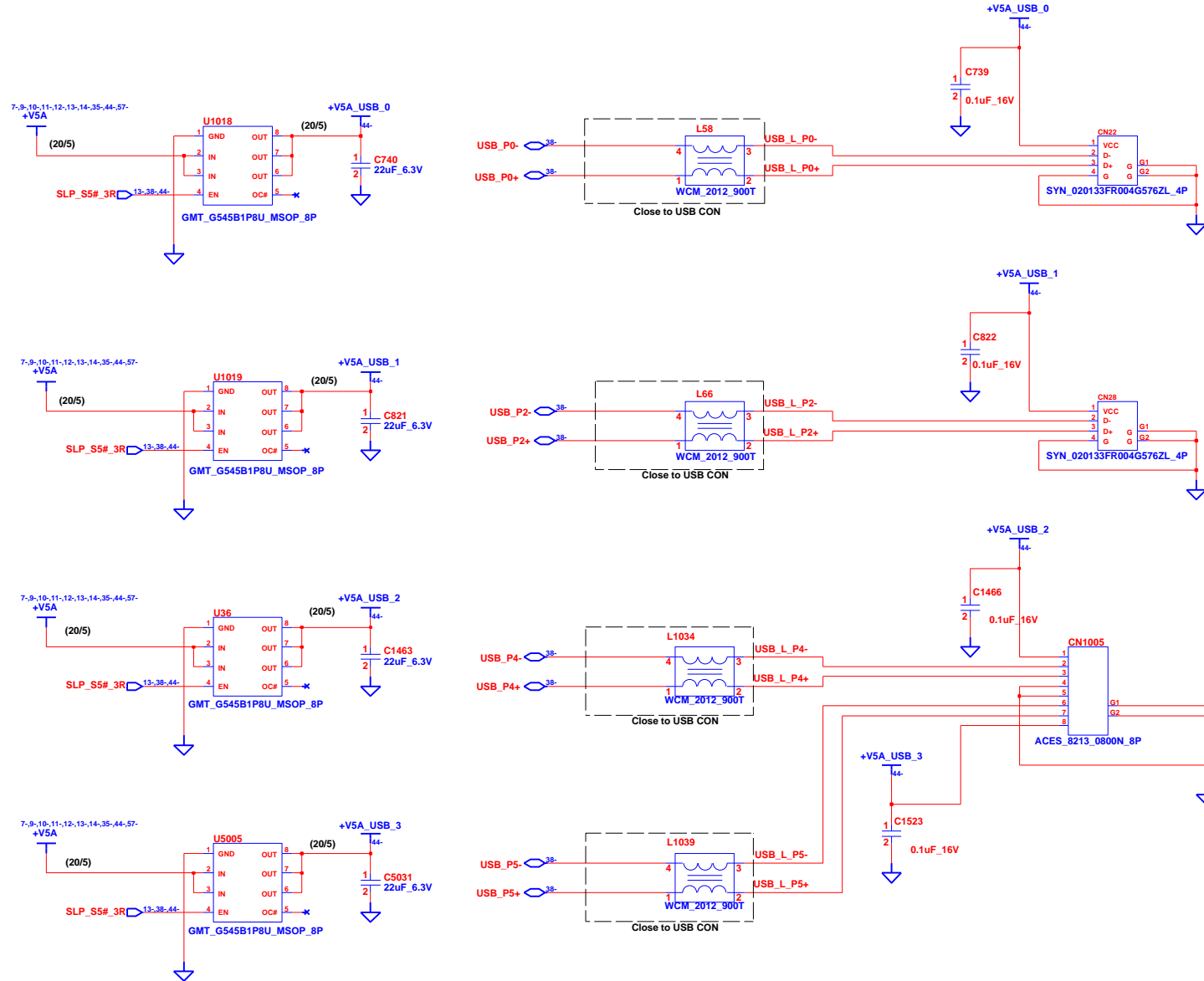


HDD CONNECTOR

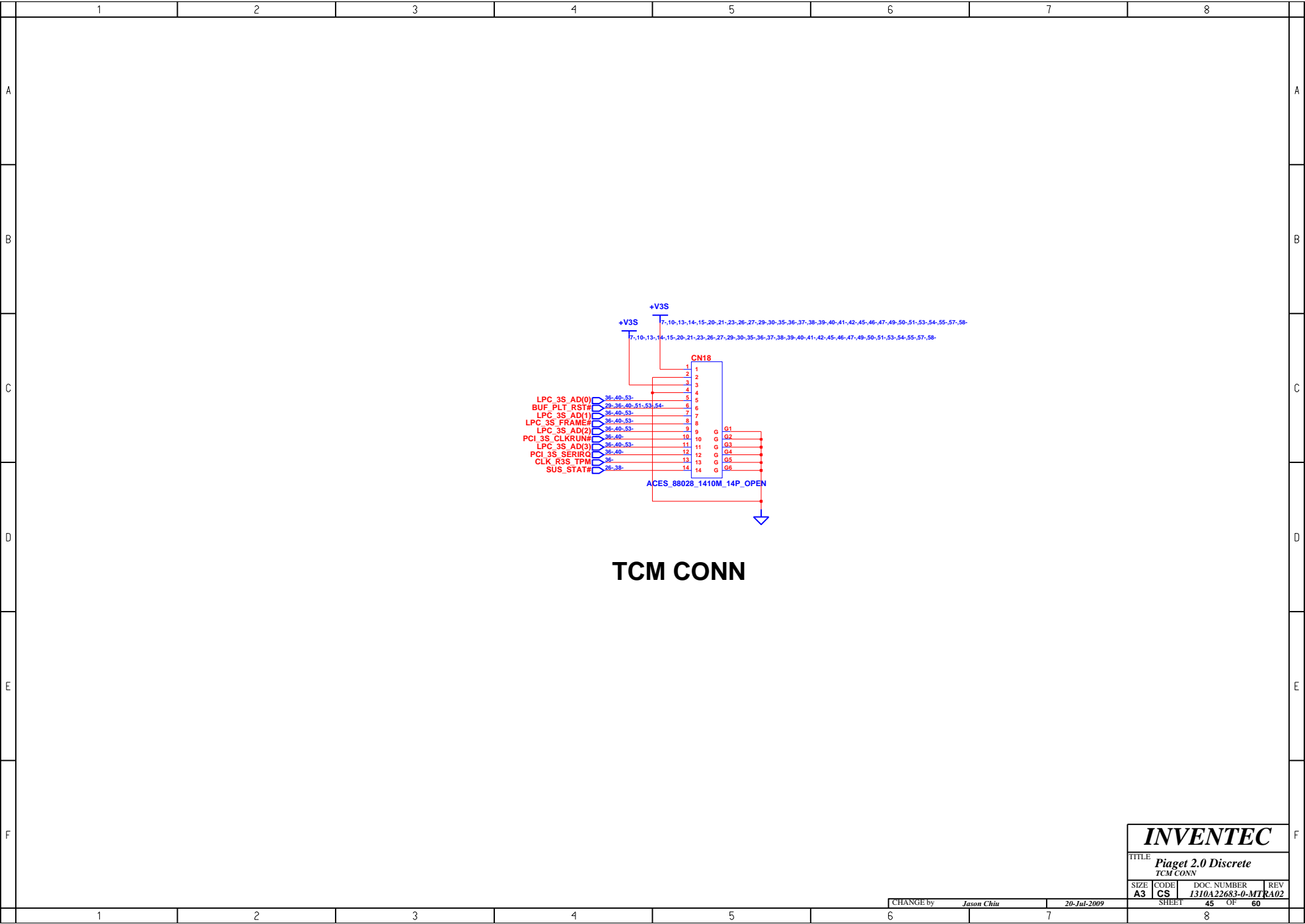


ODD SATA CONNECTOR

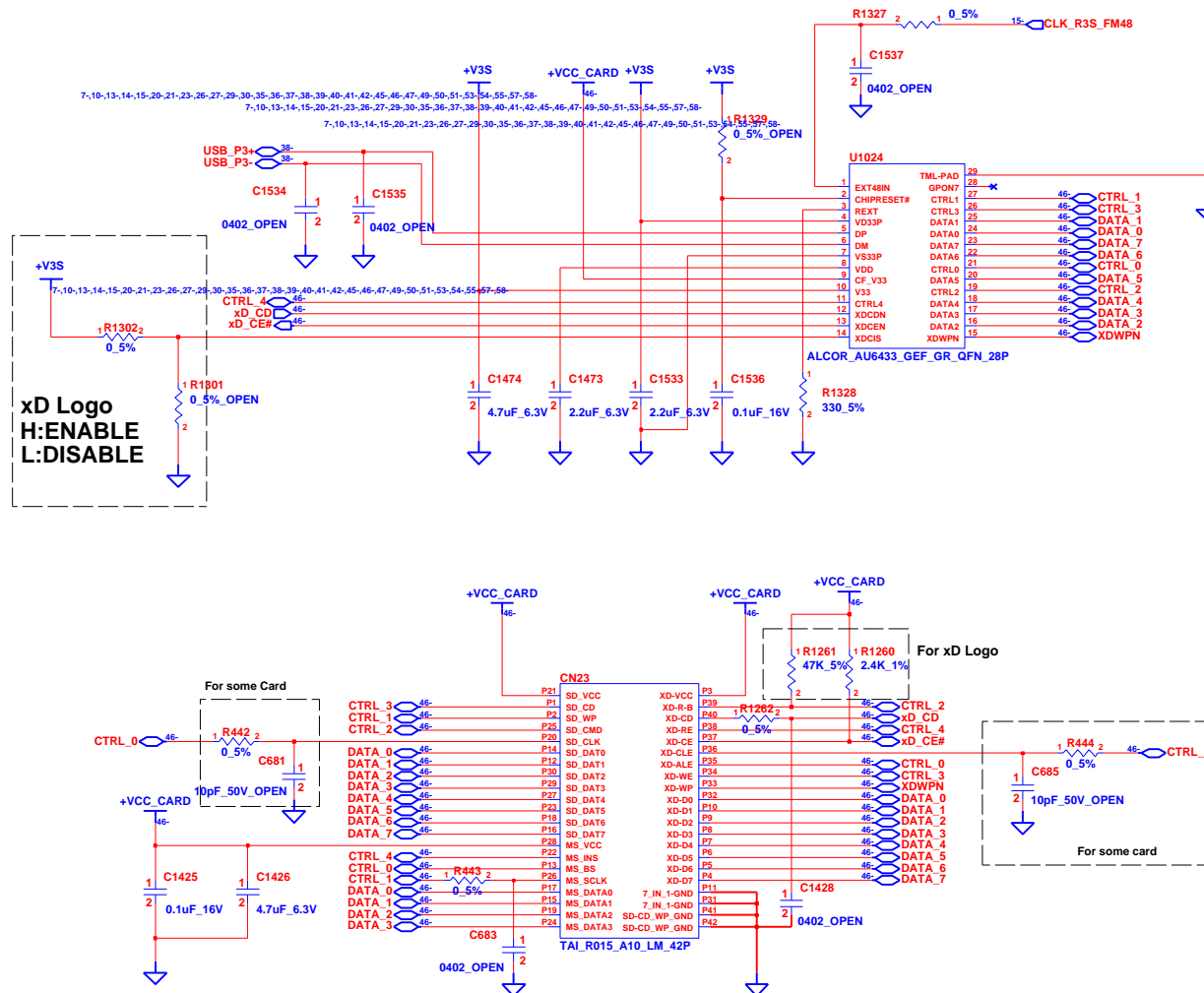
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TITLE Piaget 2.0 Discrete HDD & ODD CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET		43	OF 60



INVENTEC			
TITLE Piaget 2.0 Discrete USB CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET		44 OF 60	



INVENTEC			
TITLE <i>Piaget 2.0 Discrete</i> TCM CONN			
SIZE A3	CODE CS	DOC. NUMBER <i>1310A22683-0-MTR</i>	REV A02
CHANGE by <i>Jason Chiu</i>		20-Jul-2009	
SHEET		45	OF 60



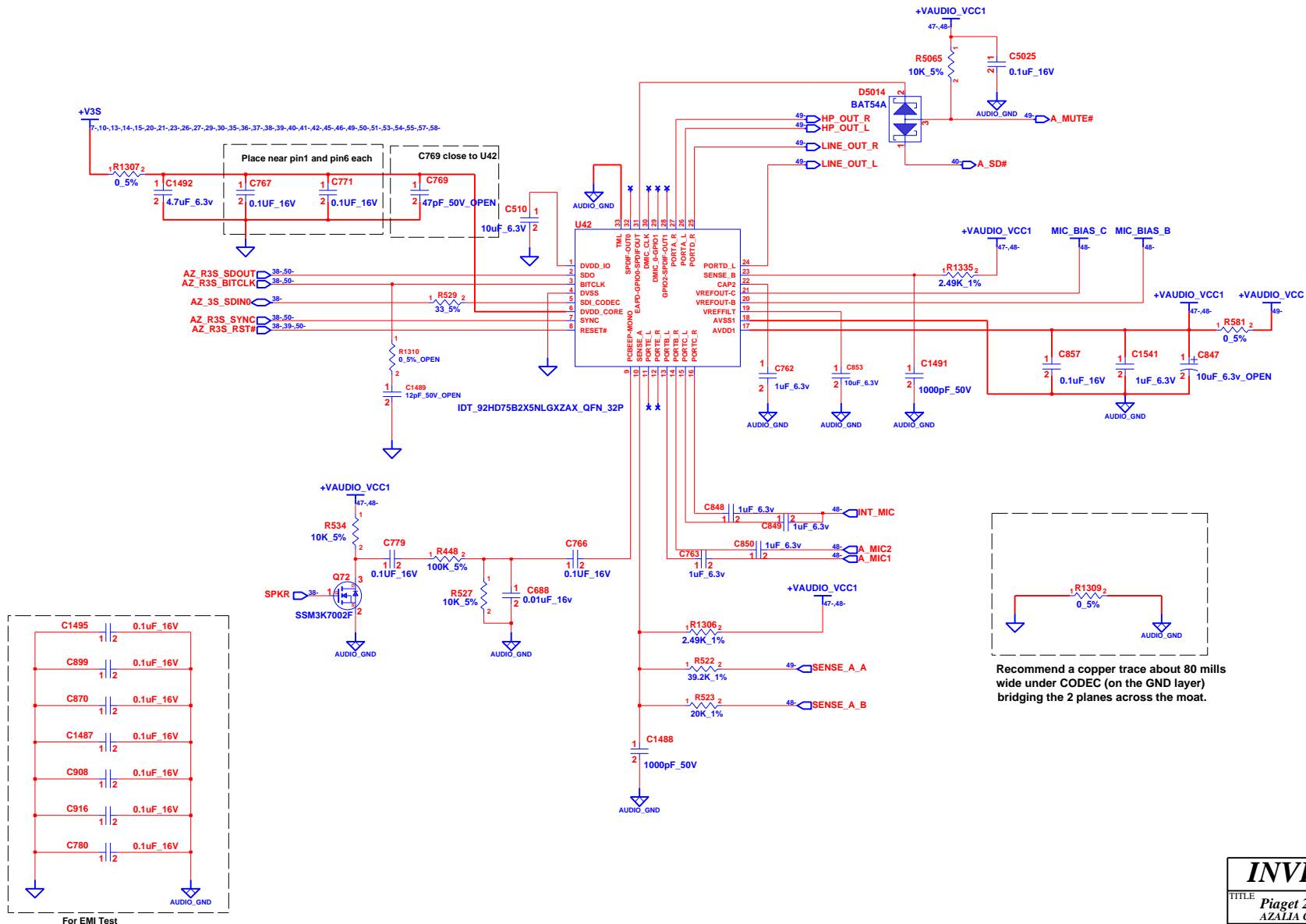
4 IN 1 Card Reader
(SD/MMC/MS/xD)

INVENTEC

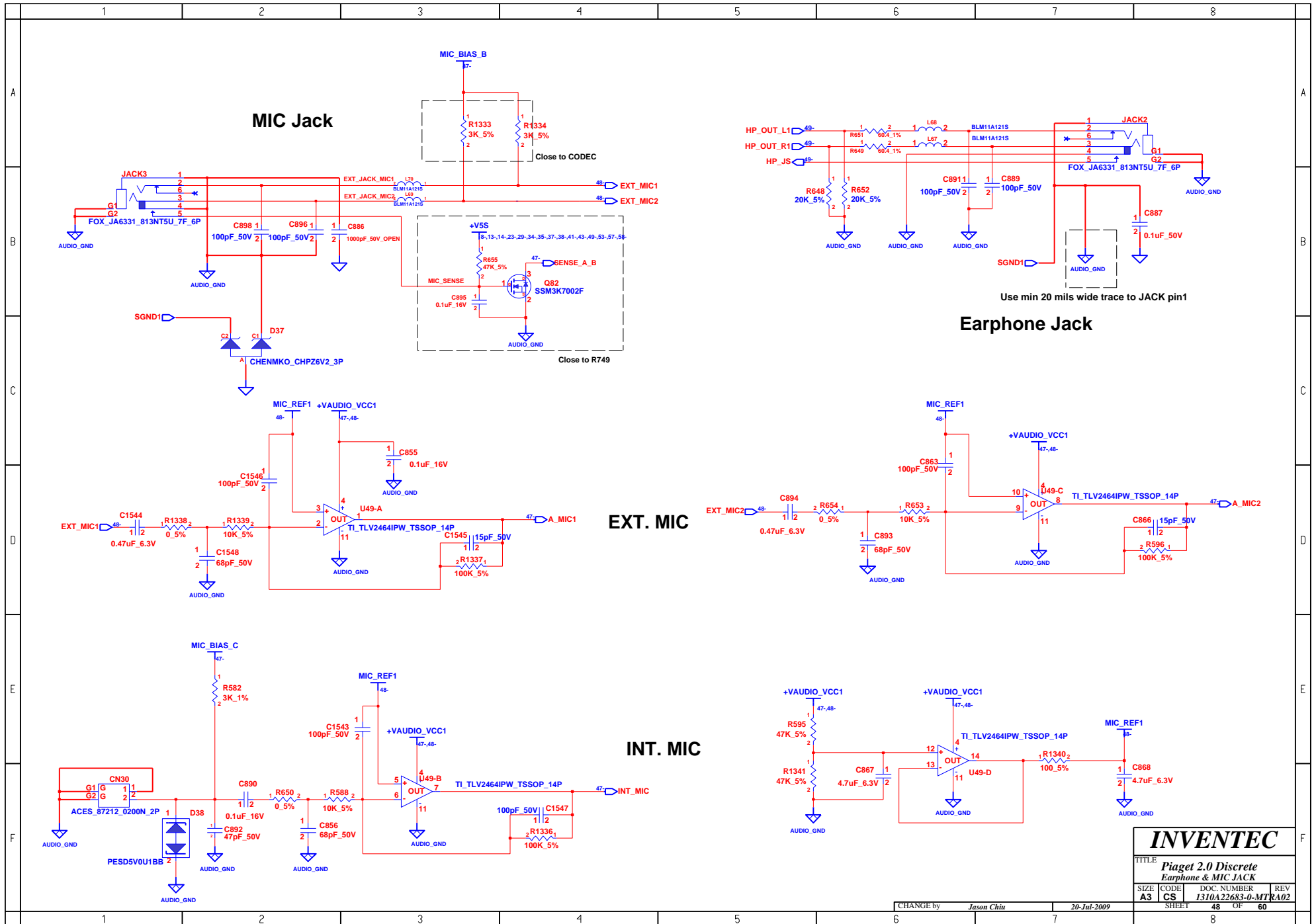
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 SIZE: **A3** CODE: **CS** DOC. NUMBER: **I310A22683-0-MTR** REV: **A02**

CHANGE by: **Jason Chiu** 20-Jul-2009

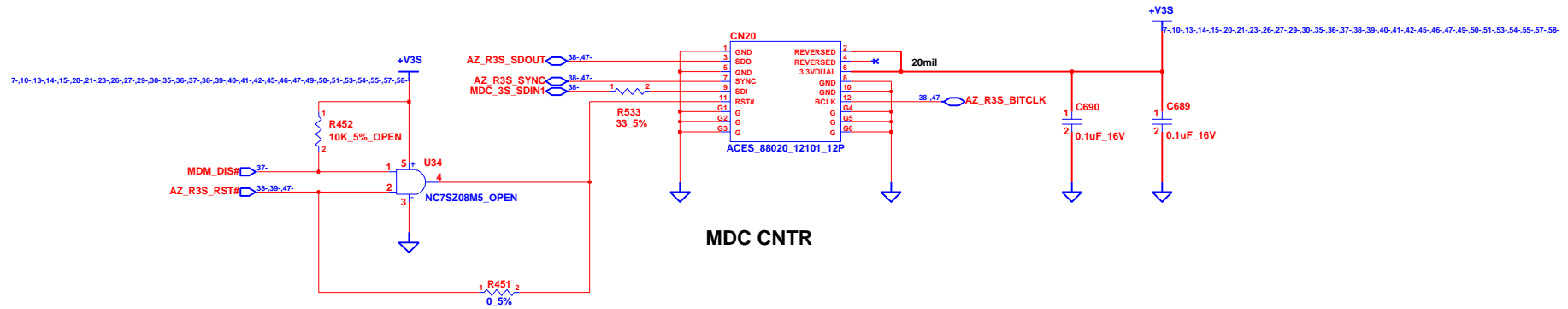
SHEET: **46** OF: **60**



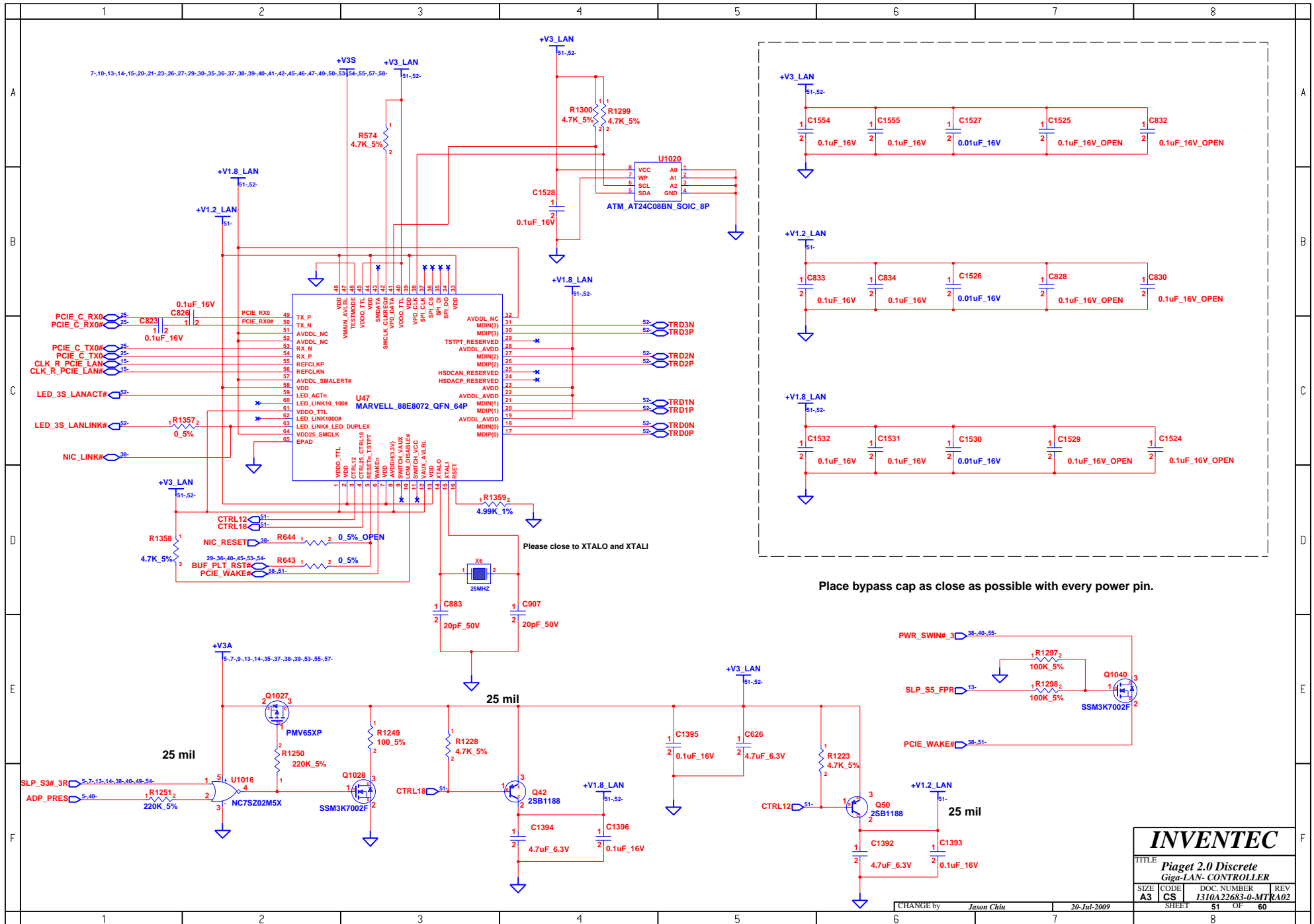
INVENTEC			
TITLE Piaget 2.0 Discrete AZALIA CODEC			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by Jason Chiu		20-Jul-2009	
SHEET		47 OF 60	



INVENTEC			
TITLE: Piaget 2.0 Discrete Earphone & MIC JACK			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A22683-0-MTR	REV: A02
CHANGE by: Jason Chiu		20-Jul-2009	
SHEET: 48		OF: 60	



INVENTEC			
TITLE <i>Piaget 2.0 Discrete</i> <i>MDC CNTR</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>1310A22683-0-MTR</i>	REV RA02
CHANGE by <i>Jason Chiu</i>		20-Jul-2009	
SHEET		50	OF 60



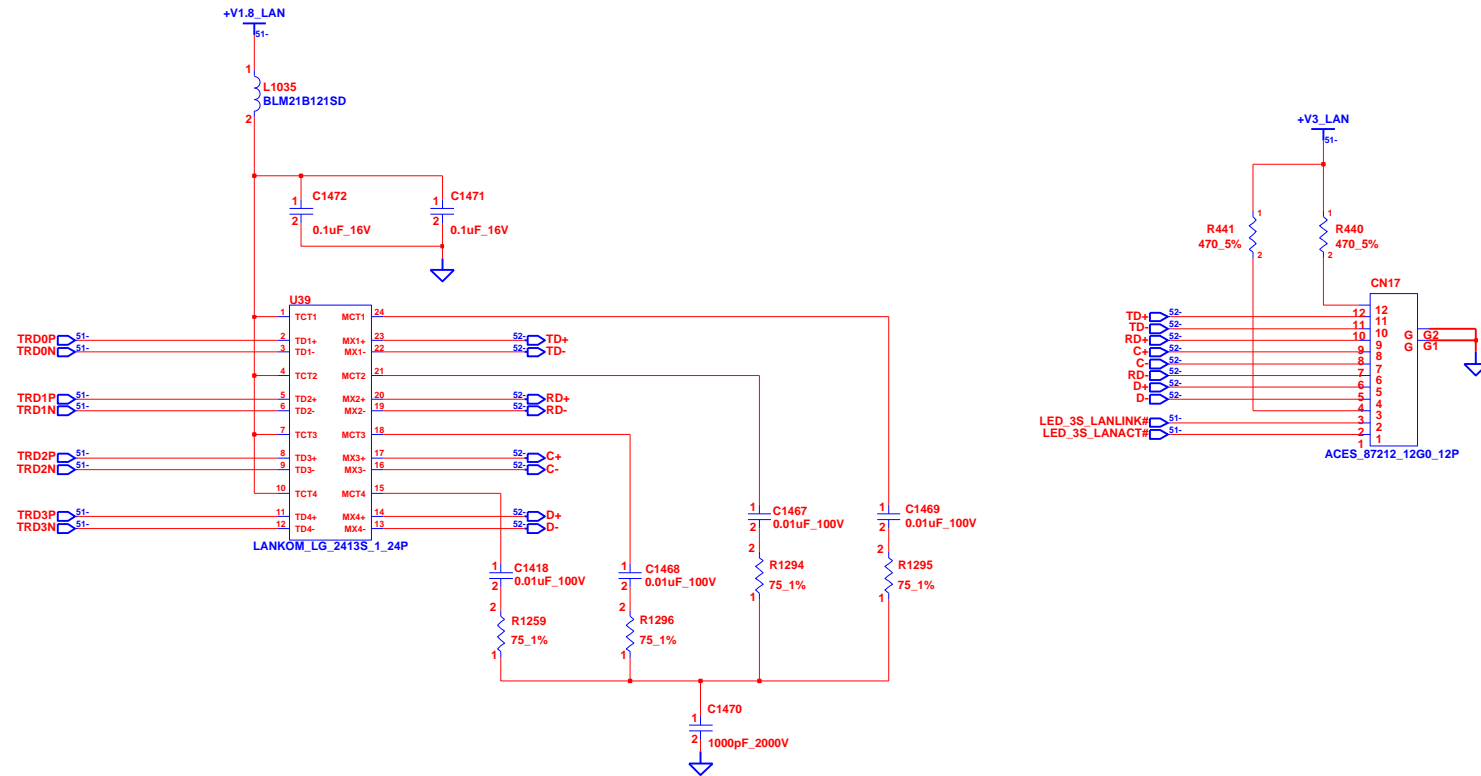
INVENTEC

TITLE: **Piaget 2.0 Discrete Giga-LAN- CONTROLLER**

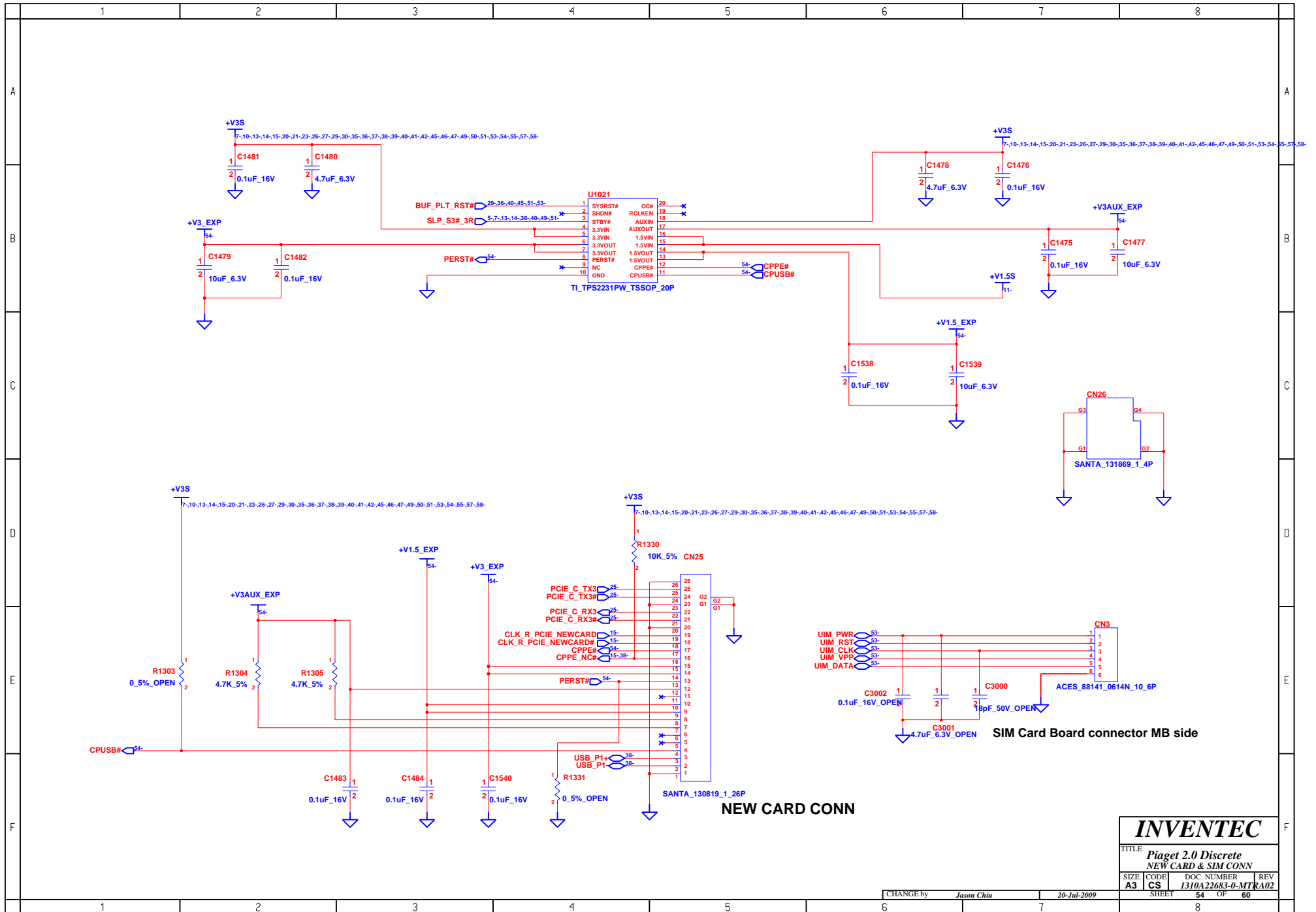
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
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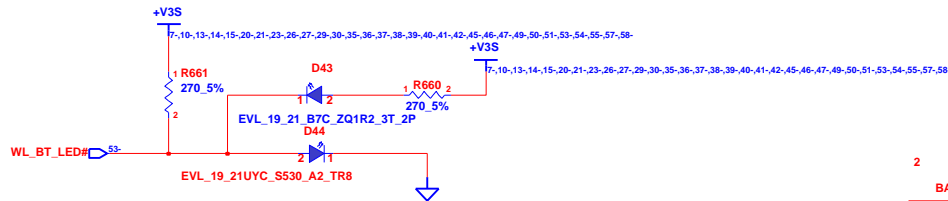
CHANGE by: Jason Chiu 20-Jul-2009

SHEET 51 OF 60

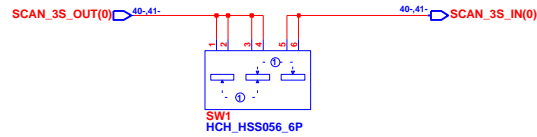


INVENTEC			
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Piaget 2.0 Discrete			
NIC 10/100/1000-RJ45 CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTR	RA02
CHANGE by		20-Jul-2009	
Jason Chin		SHEET	
6		52 OF 60	

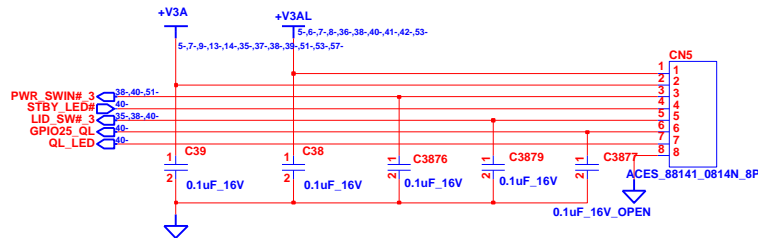




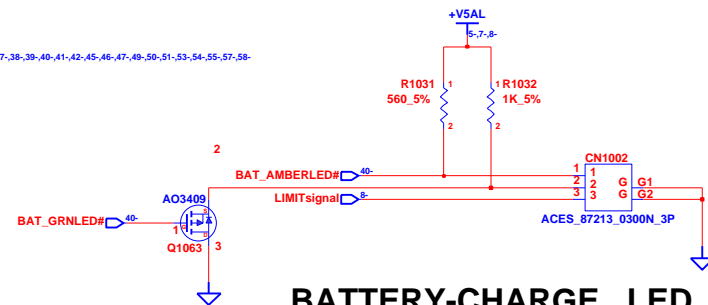
WIRELESS LED



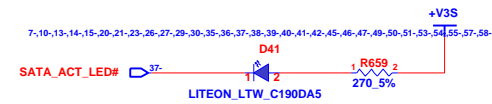
WIRELESS BUTTON



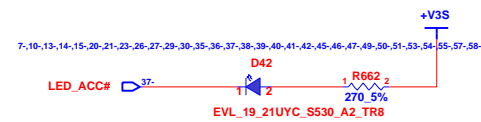
Swth Board & LED Board CONN



BATTERY-CHARGE LED



HDD & ODD LED



ACCELEROMTER LED

INVENTEC			
TITLE Piaget 2.0 Discrete W/B & SW/B & LED/B CONN			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV RA02
CHANGE by Jason Chin		20-Jul-2009	
SHEET		55	OF 60

	1	2	3	4	5	6	7	8		
A			 CPU		 MINI CARD				A	
B									B	
C			 GPU		 MDC				C	
D									D	
E			 MAIN BOARD						E	
F									F	
	1	2	3	4	5	6	7	8		

CHANGE by

Jason Chiu

20-Jul-2009

SHEET

56

OF

60

TITLE

Piaget 2.0 Discrete

SCREW

SIZE

A3

CODE

CS

DOC. NUMBER

1310A22683-0-MT

REV

RA02

INVENTEC			
TITLE Piaget 2.0 Discrete SCREW			
SIZE A3	CODE CS	DOC. NUMBER I310A22683-0-MTR	REV A02
CHANGE by <i>Jason Chin</i>		20-Jul-2009	
SHEET		56	OF 60

HDMI CONN

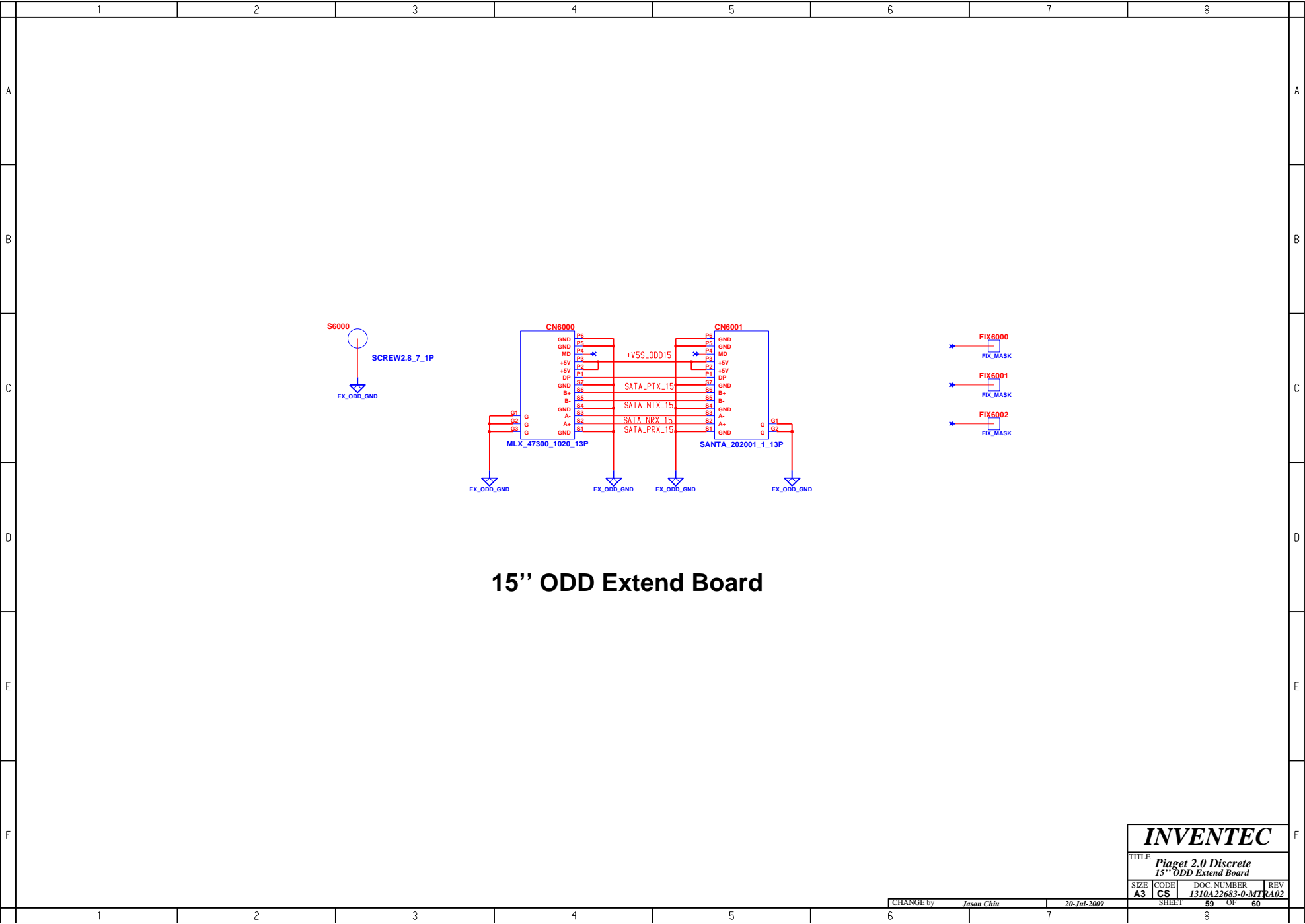
INVENTEC

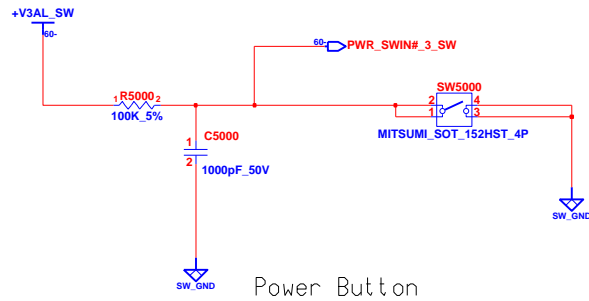
TITLE	<i>Piaget 2.0 Discrete TCM</i>
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SIZE A3	CODE CS	DOC. NUMBER 1310A22683-0-MTRA02	REV
SHEET		58	OF 60

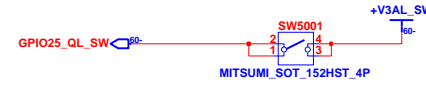
CHANGE by	Jason Chiu	20-Jul-2009
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8

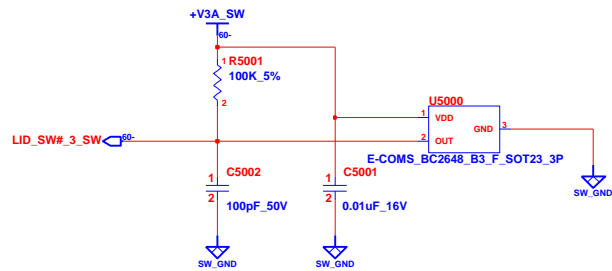




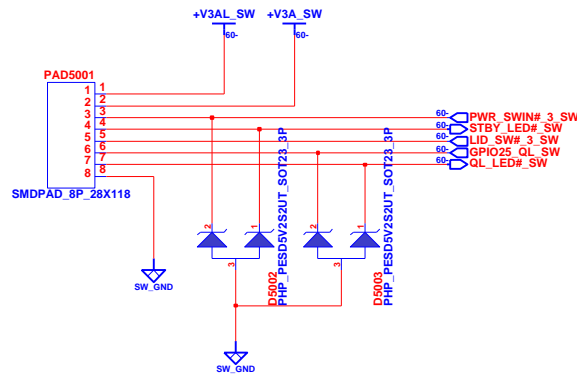
Power Button



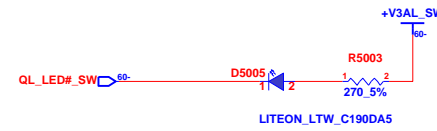
Switch & LED Board



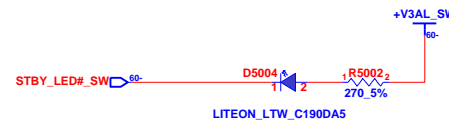
LID Switch



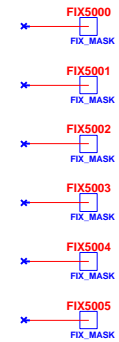
Switch & LED Board



Quick Look LED



Power/Standby LED



INVENTEC			
TITLE			
Piaget 2.0 Discrete 15" ODD Extend Board			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A22683-0-MTR	A02
CHANGE by		20-Jul-2009	
SHEET		60	OF 60