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Project Code & Schematics Subject: M790 Main Board_6L

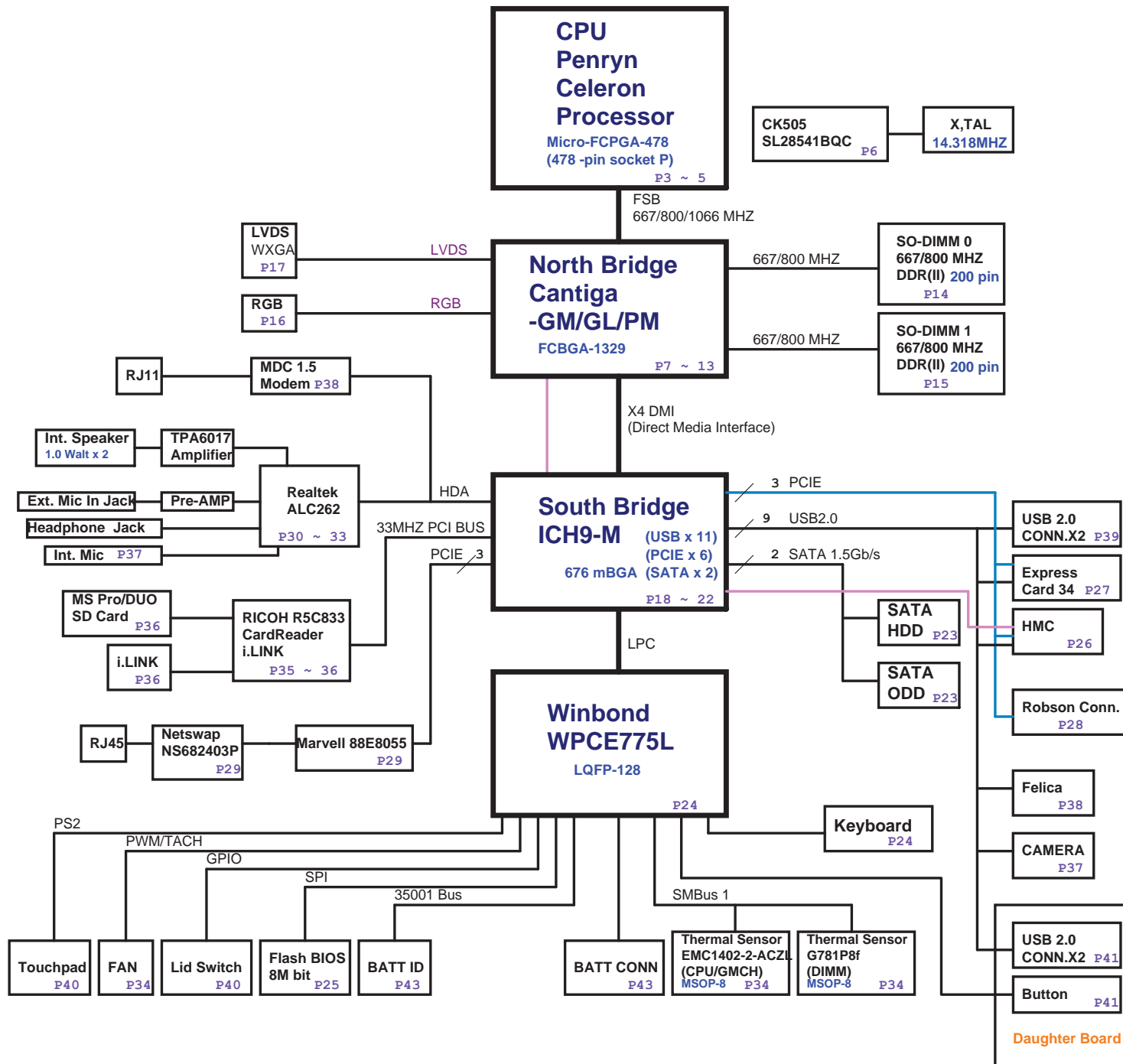
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U/B P/N: 1P-1086J02-6010 (IRIS)

P/B P/N: 1P-1086J03-6010 (IRIS)

R/B P/N: 1P-1086J01-6010 (IRIS)

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TI CHARGER BQ24751 P.63	
INPUTS	OUTPUTS
DC_IN	BT+ DCBATOUT

SYSTEM DC/DC TPS51125RGER P.64	
INPUTS	OUTPUTS
DCBATOUT	+5VALW +5VALW_LDO +3VALW +ECVCC +15V_ATW

SYSTEM DC/DC SC411 P.65	
INPUTS	OUTPUTS
DCBATOUT	+1_5VRUN +1_05VM

SYSTEM DC/DC TPS51116RGER P.66	
INPUTS	OUTPUTS
DCBATOUT	+1_8VSUS +0_9VSUS

CPU DC/DC ISL6262A P.67	
INPUTS	OUTPUTS
DCBATOUT	VHORE

SYSTEM DC/DC APL5912 P.70	
INPUTS	OUTPUTS
+1_5VRUN	PEX_VDD

SYSTEM DC/DC TPS51117 P.70	
INPUTS	OUTPUTS
DCBATOUT	NV_VDD

SYSTEM DC/DC MAX8776 P.71	
INPUTS	OUTPUTS
DCBATOUT	+VGF_XORE

7 H_AA[3..35]

7 H_ADSTB#0
7 H_REQ#4..0

7 H_ADSTB#1

19 H_A20M#

19 H_FERR#

19 H_IGNNE#

19 H_STPCLK#

19 H_INTR

19 H_NMI

19 H_SMI#

TP15 20MIL -1 TP CPU RSVD01 M4
TP18 20MIL -1 TP CPU RSVD02 N5
TP7 20MIL -1 TP CPU RSVD03 T2
TP11 20MIL -1 TP CPU RSVD04 V3
TP5 20MIL -1 TP CPU RSVD05 B2
TP14 20MIL -1 CPU TEST7 C3
TP6 20MIL -1 TP CPU RSVD07 D2
TP25 20MIL -1 TP CPU RSVD08 D22
TP10 20MIL -1 TP CPU RSVD09 D3
TP19 20MIL -1 TP CPU RSVD10 F6

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H_A#4 L5
H_A#5 L4
H_A#6 K5
H_A#7 M3
H_A#8 N2
H_A#9 J1
H_A#10 N3
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H_A#13 L2
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H_A#17 Y2
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H_A#32 W3
H_A#33 AA4
H_A#34 AB2
H_A#35 AA3

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H_REQ#1 H2
H_REQ#2 K2
H_REQ#3 J3
H_REQ#4 L1

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CPU SOCKET_478P
FOX_P24782A-274M-01

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CONTROL
LOCK#
RESET#
RS[0]#
RS[1]#
RS[2]#
TRDY#
HIT#
HITM#

REQ[0]#
REQ[1]#
REQ[2]#
REQ[3]#
REQ[4]#

ADDR GROUP 1
STV#
XDP/TP SIGNAL#
XDP BPM#0
XDP BPM#1
XDP BPM#2
XDP BPM#3
XDP BPM#4
XDP BPM#5
XDP TCK
XDP TDI
XDP TDO
XDP TMS
XDP TRST#
XDP DBR#

THERMAL
PROCHOT#
THERMDA
THERMDC
THERMTRIP#

H CLK
BCLK[0]
BCLK[1]

RESERVED
RSVD[01]
RSVD[02]
RSVD[03]
RSVD[04]
RSVD[05]
RSVD[06]
RSVD[07]
RSVD[08]
RSVD[09]
RSVD[10]

H1 ADS# 7
E2 BNR# 7
G5 BPR# 7
H5 DEFER# 7
F21 DRDY# 7
E1 DBSY# 7
F1 BREQ#0 7
D20 IERR#
B3 INIT# 19
H4 LOCK# 7
C1 H_CPURST#
F3 H_RS#0
F4 H_RS#1
G3 H_RS#2
G2 H_TRDY# 7
G6 H_HIT# 7
F4 H_HITM# 7

AD4 XDP BPM#0 1
AD3 XDP BPM#1 1
AD1 XDP BPM#2 1
AC4 XDP BPM#3 1
AC2 XDP BPM#4 1
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AB3 XDP TDO
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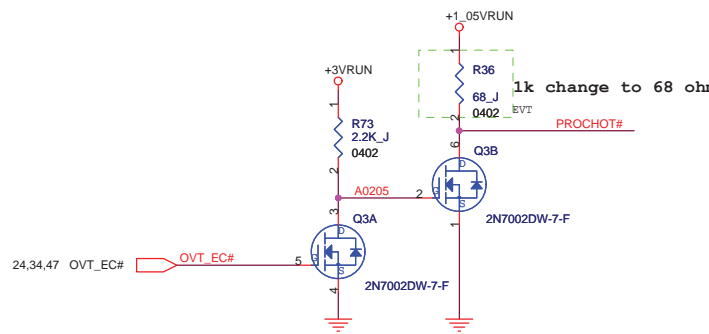
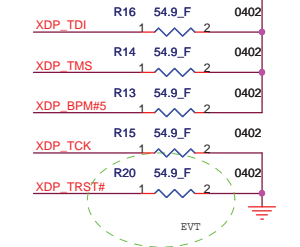
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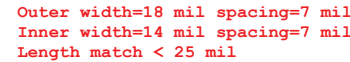
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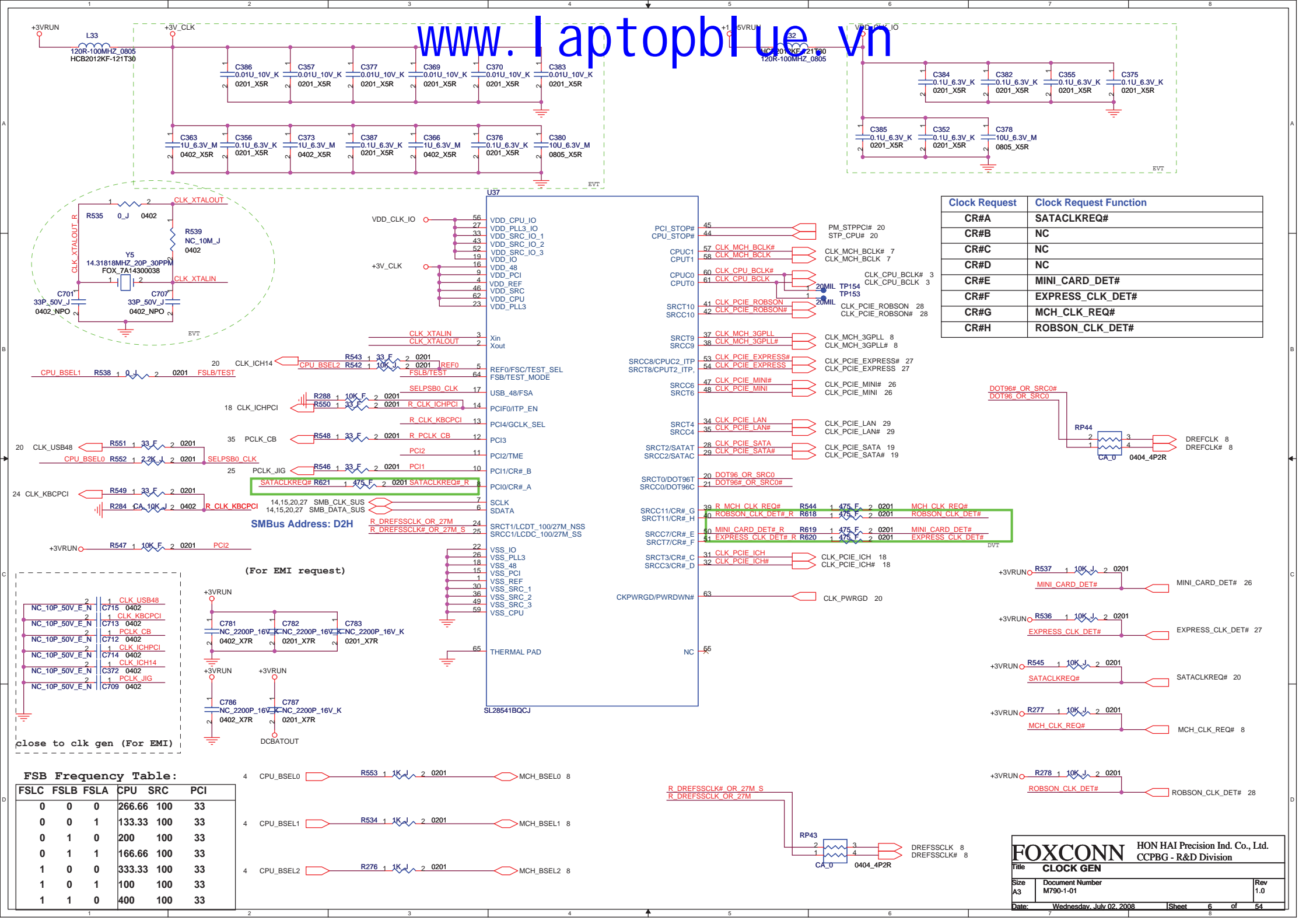
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TP5 20MIL -1 TP CPU RSVD05 B2
TP14 20MIL -1 CPU TEST7 C3
TP6 20MIL -1 TP CPU RSVD07 D2
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TP10 20MIL -1 TP CPU RSVD09 D3
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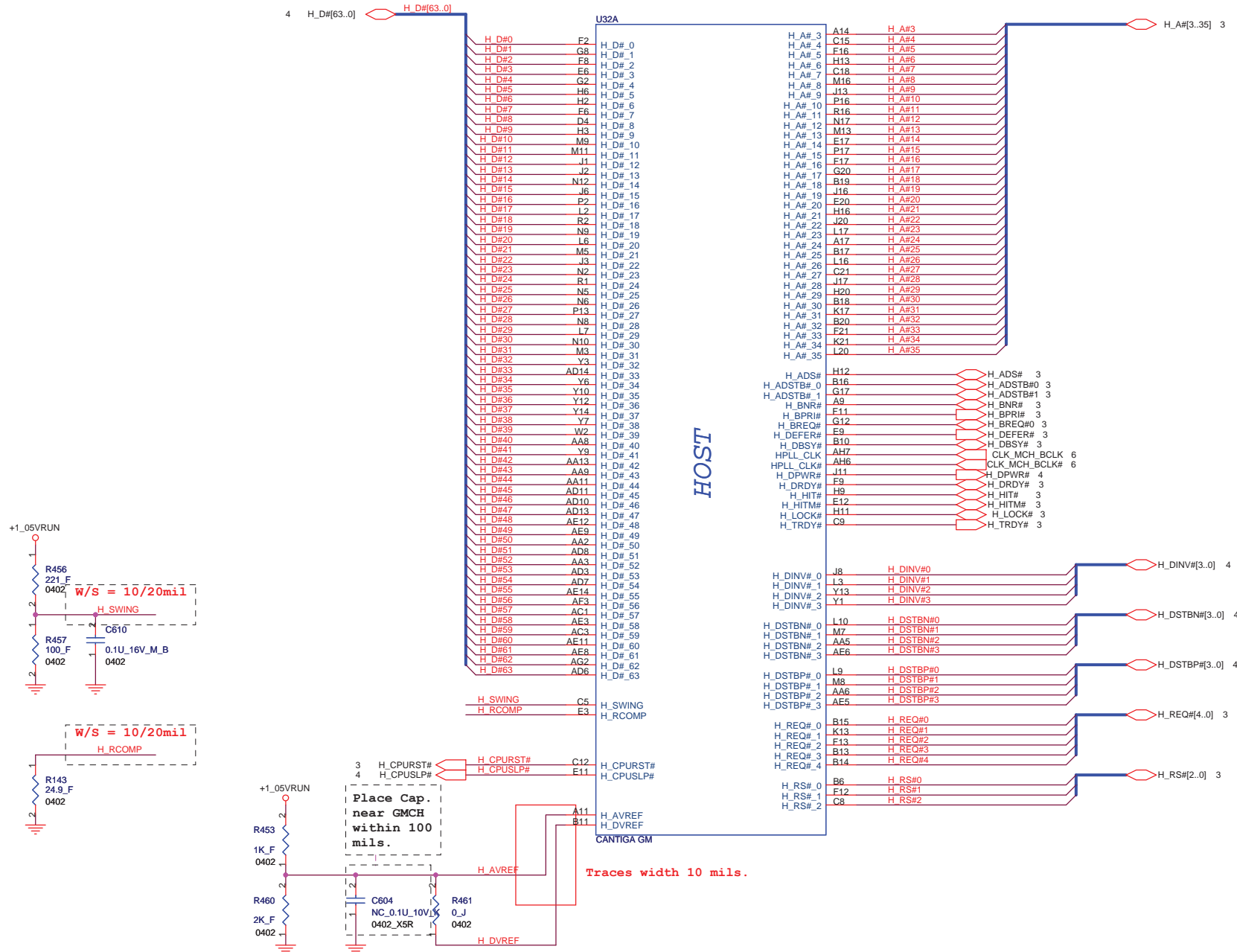
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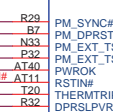




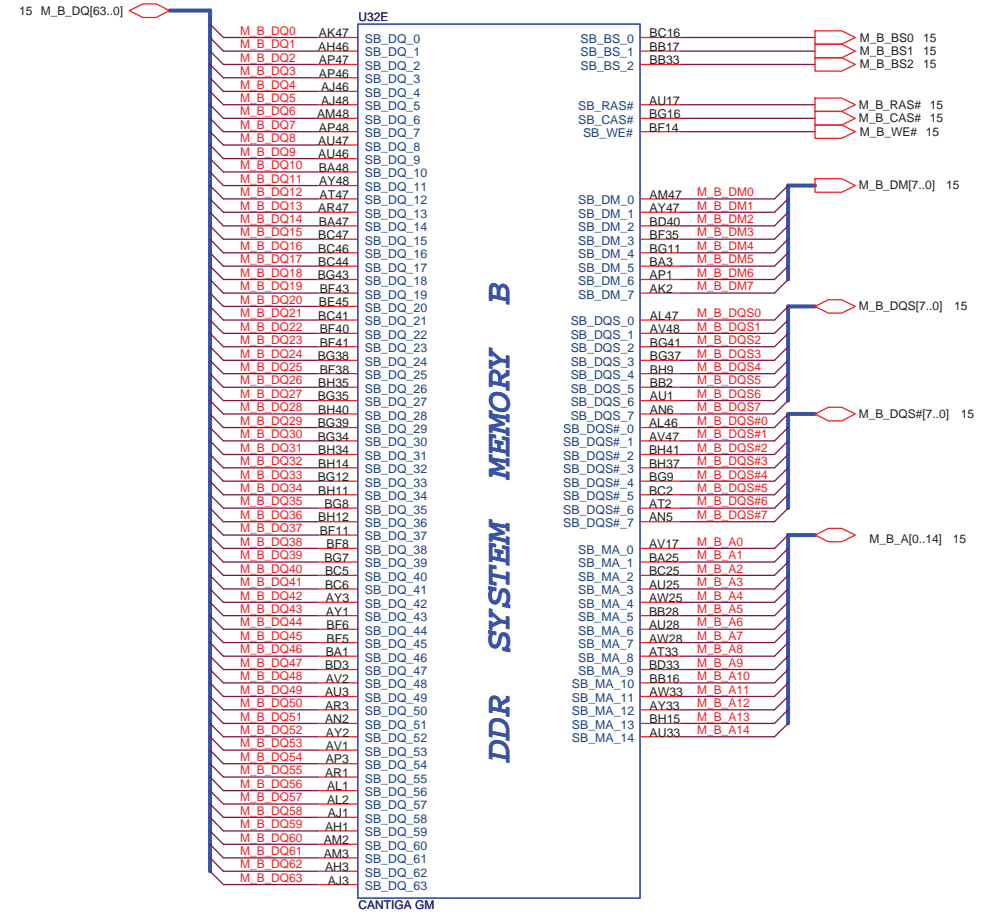
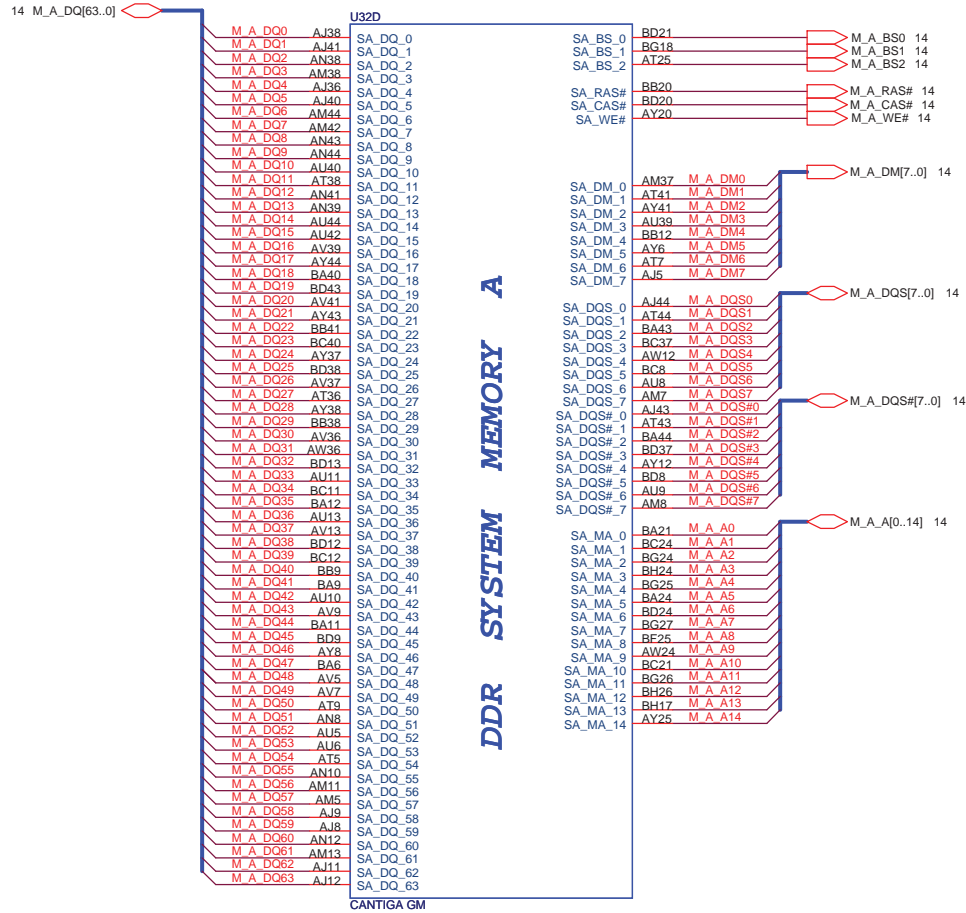


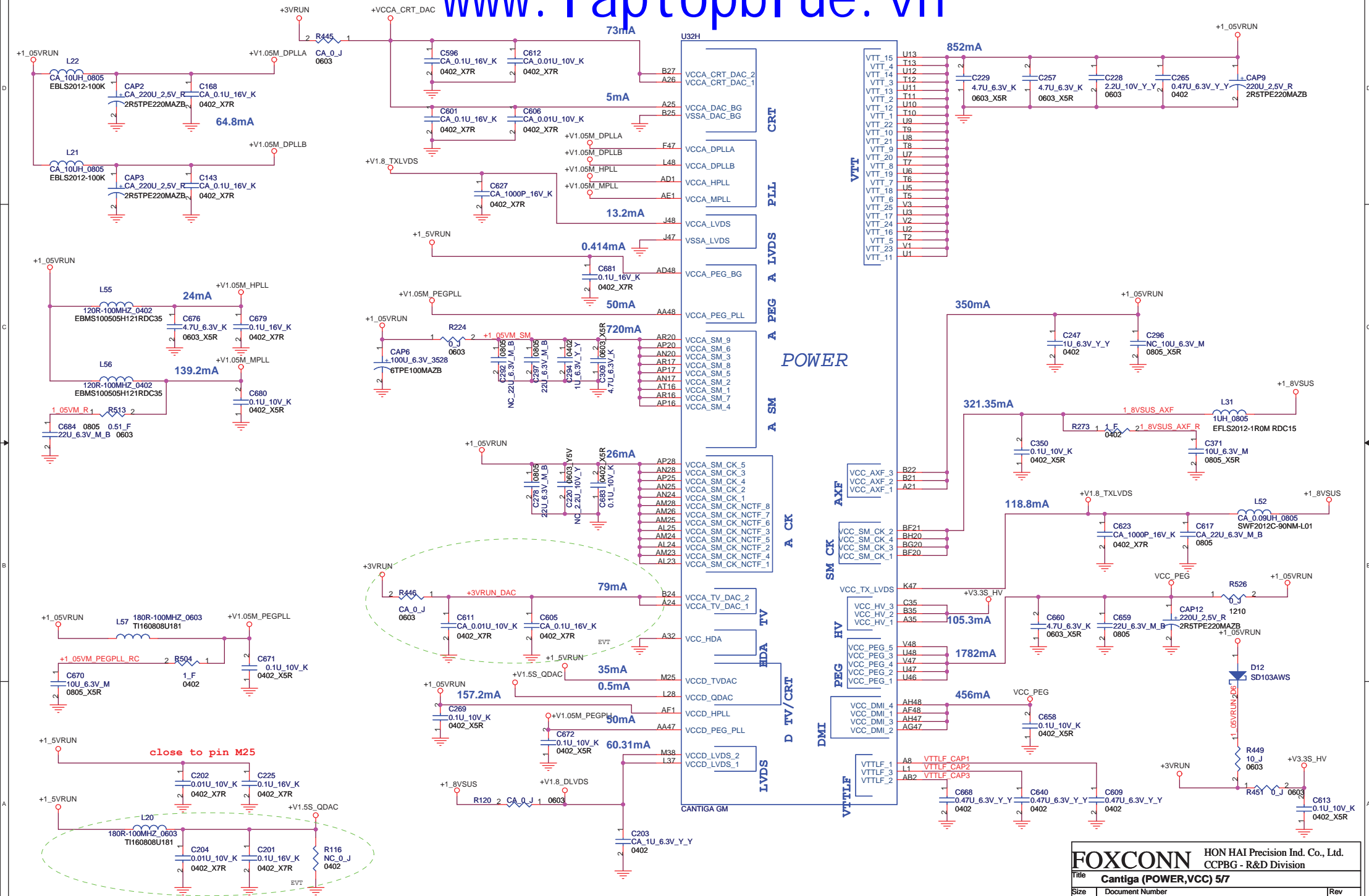


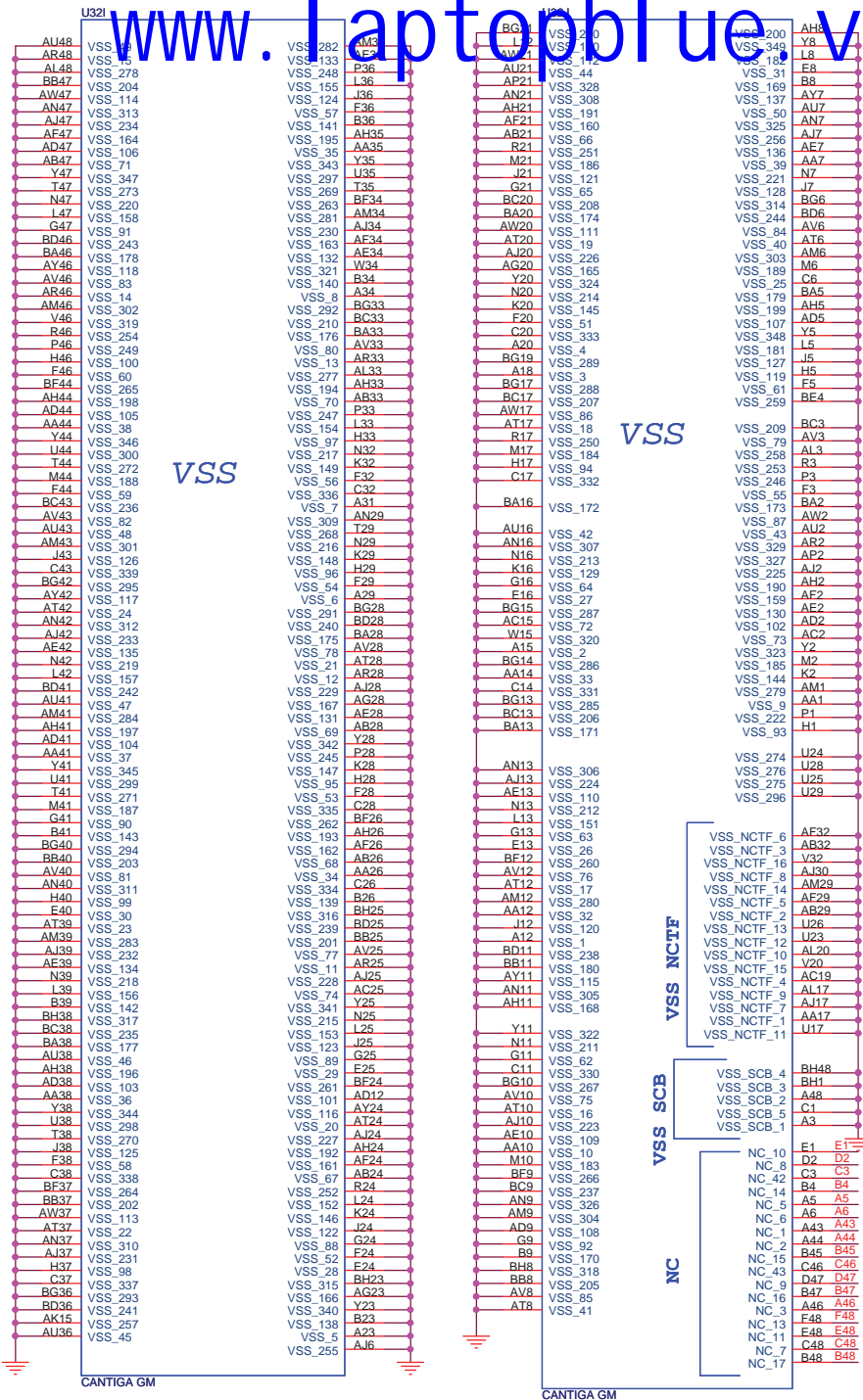
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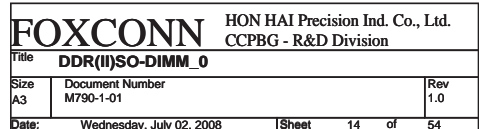


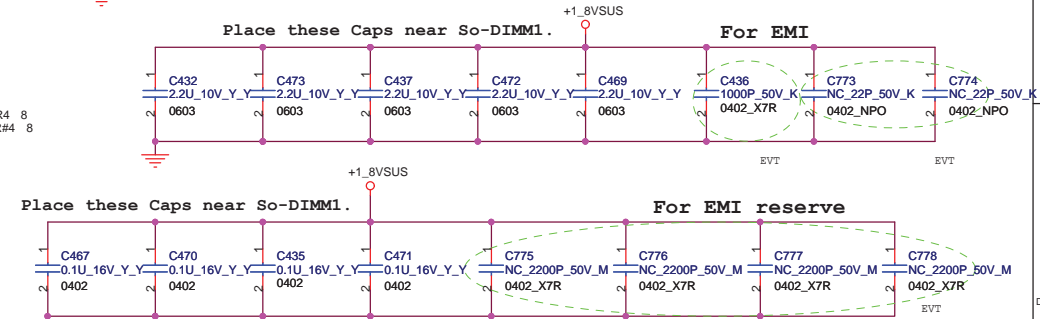
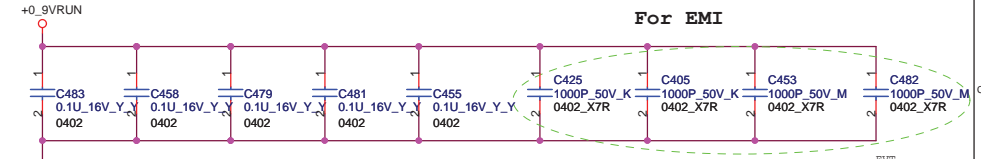
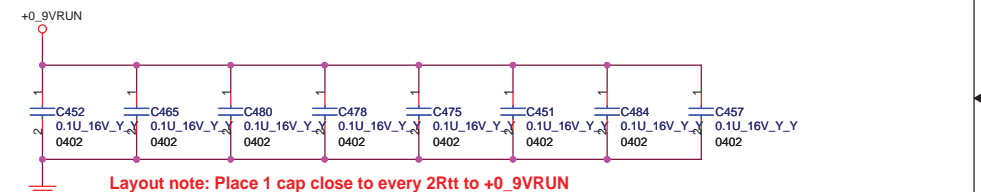
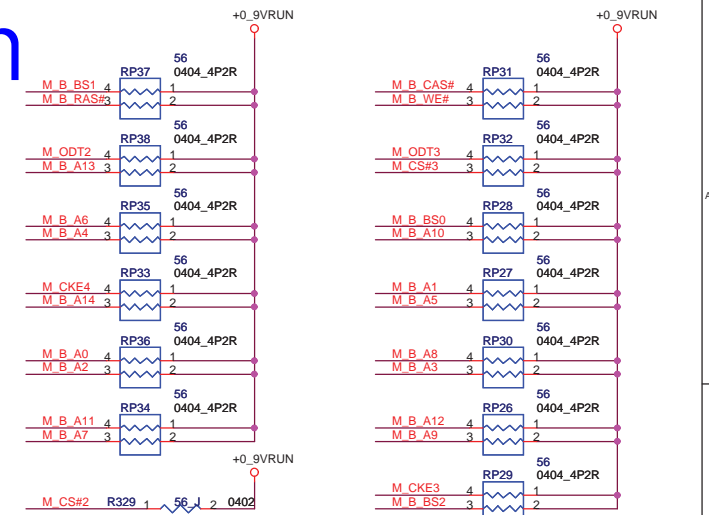
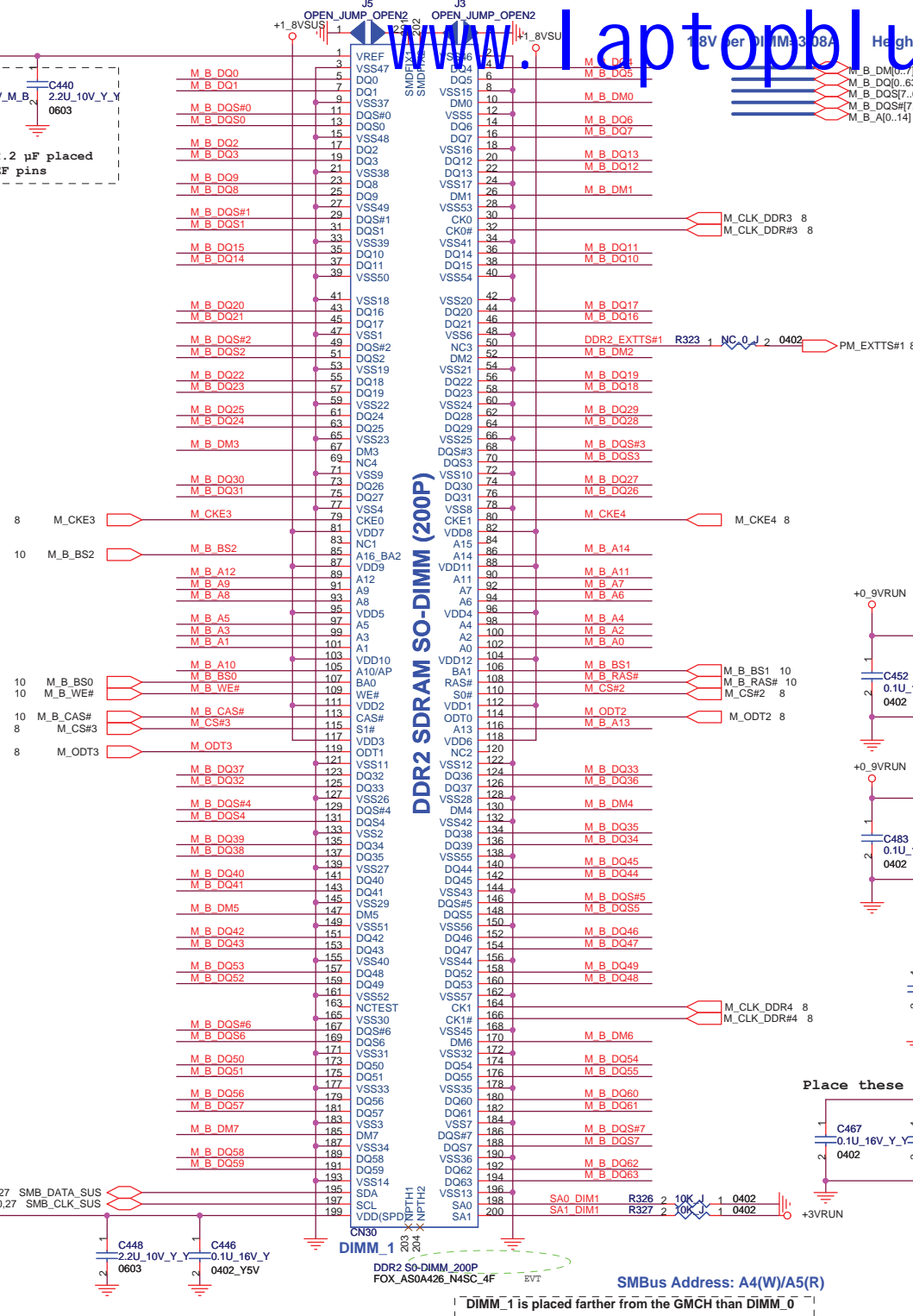
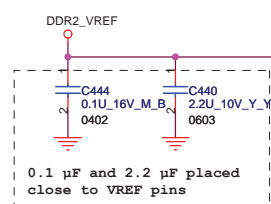


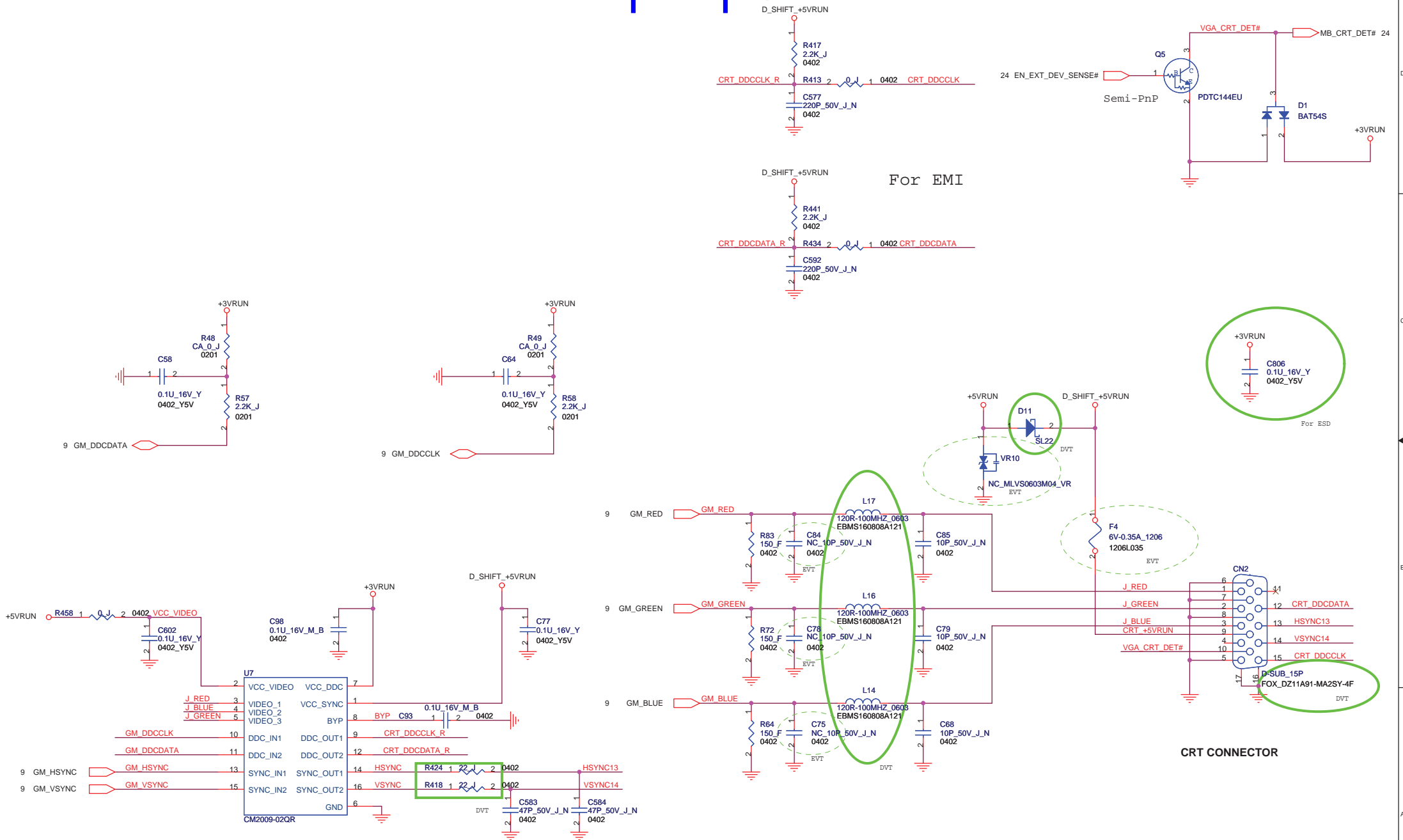


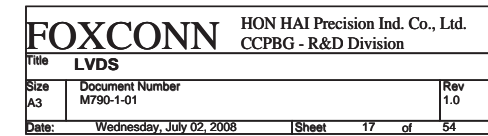


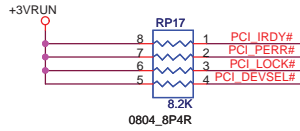
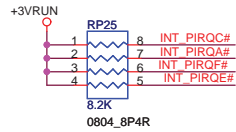
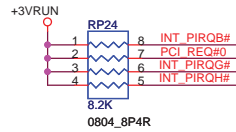
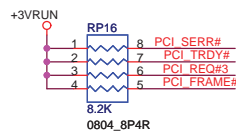
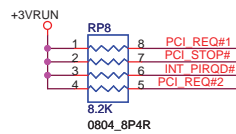
2
DDR2 SDRAM SO-DIMM (200P)











PCI Pullups

35 INT_PIRQA#
35 INT_PIRQB#

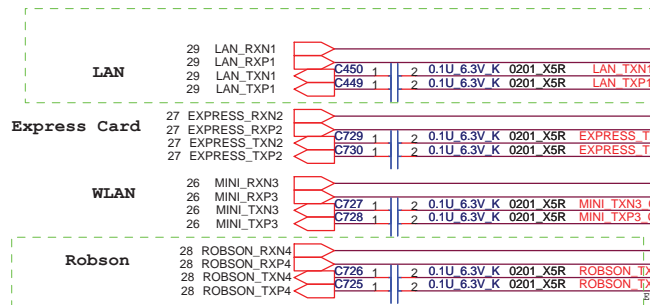
INT_PIRQA#
INT_PIRQB#
INT_PIROC#
INT_PIRQD#

Interrupt I/F
PIROA#
PIROB#
PIROC#
PIROD#

PIRQE#/GPIO2
PIROF#/GPIO3
PIROG#/GPIO4
PIROH#/GPIO5

ICH9M

null



U39D

PERN1
PERP1
PETN1
PETP1

PERN2
PERP2
PETN2
PETP2

PERN3
PERP3
PETN3
PETP3

PERN4
PERP4
PETN4
PETP4

PERN5
PERP5
PETN5
PETP5

PERN6/GLAN_RXN
PERP6/GLAN_RXP
PETN6/GLAN_TXN
PETP6/GLAN_TXP

SPI_CLK
SPI_CS0#
SPI_CS1#/GPIO58/CLGPI06

SPI_MOSI
SPI_MISO

OC0#/GPIO59
OC1#/GPIO40
OC2#/GPIO41
OC3#/GPIO42
OC4#/GPIO43
OC5#/GPIO29
OC6#/GPIO30
OC7#/GPIO31
OC8#/GPIO44
OC9#/GPIO45
OC10#/GPIO46
OC11#/GPIO47

USBRBIAS
USBRBIAS#

ICH9M
null

PCI-Express

PCI-Express

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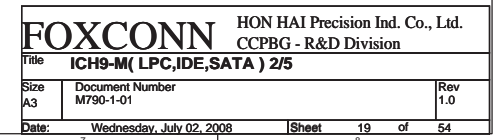
USB

USB

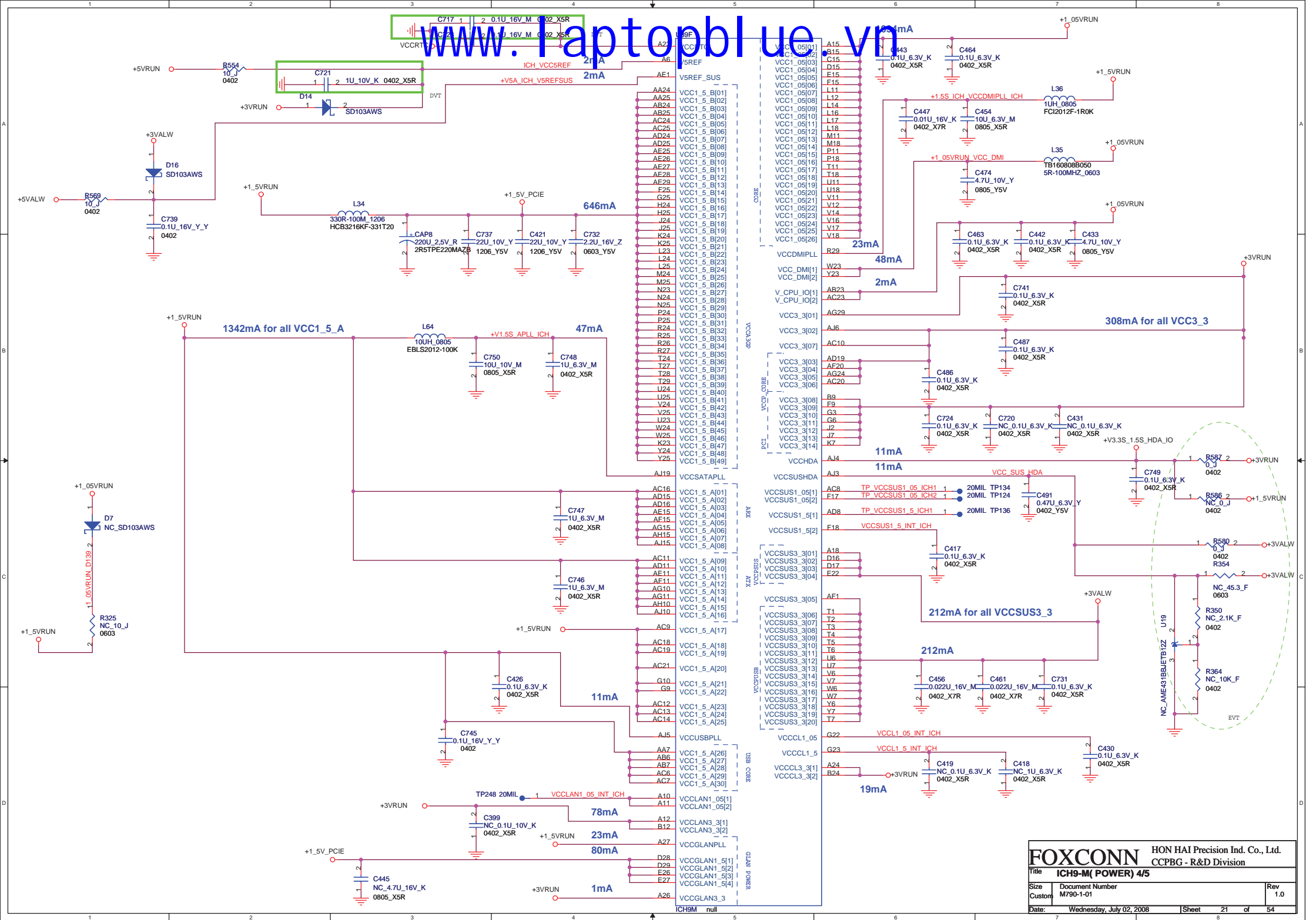
USB

USB

USB

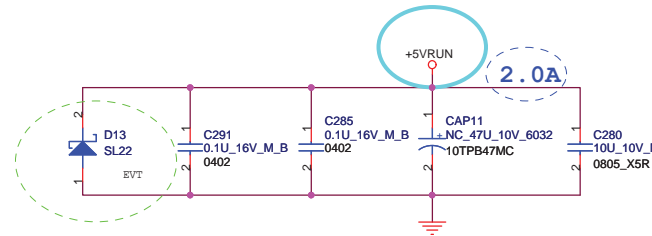
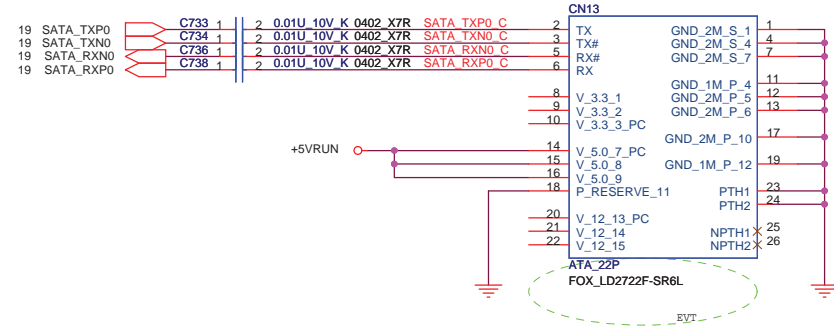
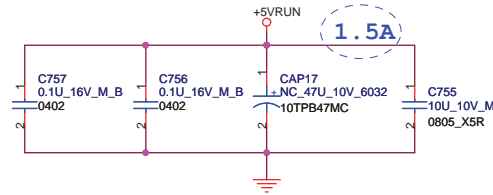




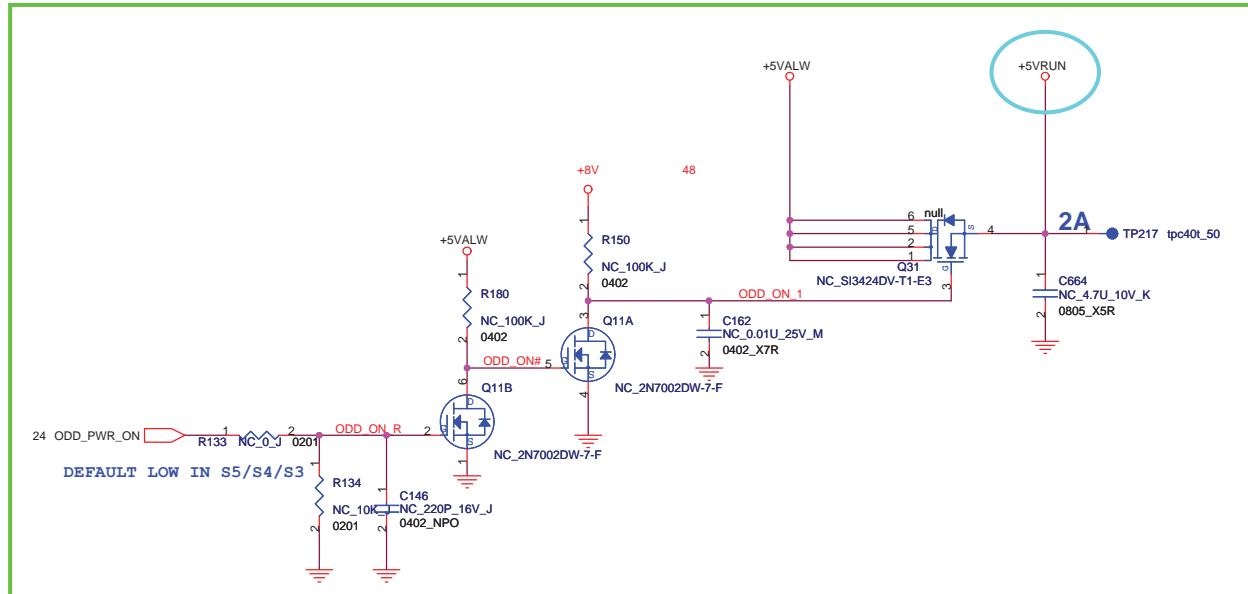
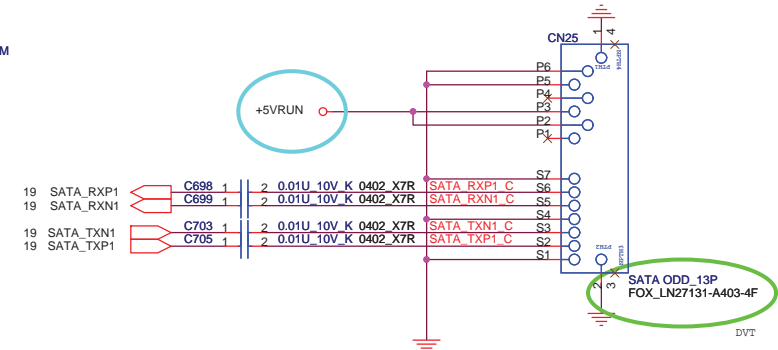


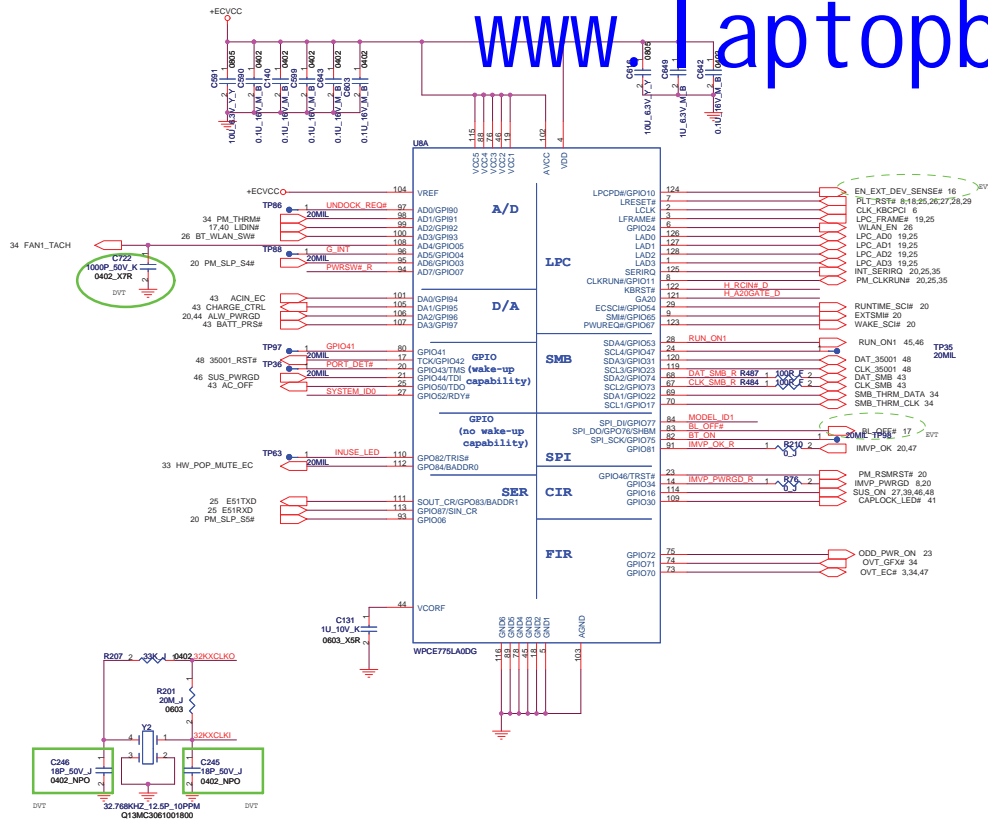
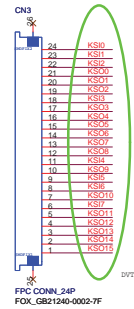
	U985	
A26	VSS[001]	V5[107]
A27	VSS[002]	V5[108]
A3	VSS[010]	V5[109]
AA6	VSS[004]	VSS[110]
AB1	VSS[005]	VSS[111]
AA23	VSS[006]	VSS[112]
AB28	VSS[007]	VSS[113]
AB23	VSS[008]	VSS[114]
AB4	VSS[009]	VSS[115]
AB5	VSS[010]	VSS[116]
AC17	VSS[011]	VSS[117]
AC26	VSS[012]	VSS[118]
AC27	VSS[013]	VSS[119]
AC3	VSS[014]	VSS[120]
AD1	VSS[015]	VSS[121]
AD10	VSS[016]	VSS[122]
AD12	VSS[017]	VSS[123]
AD13	VSS[018]	VSS[124]
AD14	VSS[019]	VSS[125]
AD17	VSS[020]	VSS[126]
AD18	VSS[021]	VSS[127]
AD21	VSS[022]	VSS[128]
AD28	VSS[023]	VSS[129]
AD29	VSS[024]	VSS[130]
AD4	VSS[025]	VSS[131]
AD5	VSS[026]	VSS[132]
AD6	VSS[027]	VSS[133]
AD7	VSS[028]	VSS[134]
AD9	VSS[029]	VSS[135]
AE12	VSS[030]	VSS[136]
AE13	VSS[031]	VSS[137]
AE14	VSS[032]	VSS[138]
AE16	VSS[033]	VSS[139]
AE17	VSS[034]	VSS[140]
AE2	VSS[035]	VSS[141]
AE20	VSS[036]	VSS[142]
AE24	VSS[037]	VSS[143]
AE3	VSS[038]	VSS[144]
AE4	VSS[039]	VSS[145]
AE6	VSS[040]	VSS[146]
AE9	VSS[041]	VSS[147]
AF13	VSS[042]	VSS[148]
AF16	VSS[043]	VSS[149]
AF18	VSS[044]	VSS[150]
AF22	VSS[045]	VSS[151]
AH26	VSS[046]	VSS[152]
AF26	VSS[047]	VSS[153]
AF27	VSS[048]	VSS[154]
AF5	VSS[049]	VSS[155]
AF7	VSS[050]	VSS[156]
AF9	VSS[051]	VSS[157]
AG13	VSS[052]	VSS[158]
AG16	VSS[053]	VSS[159]
AG18	VSS[054]	VSS[160]
AG20	VSS[055]	VSS[161]
AG23	VSS[056]	VSS[162]
AG3	VSS[057]	VSS[163]
AG6	VSS[058]	VSS[164]
AG9	VSS[059]	VSS[165]
AH12	VSS[060]	VSS[166]
AH14	VSS[061]	VSS[167]
AH17	VSS[062]	VSS[168]
AH19	VSS[063]	VSS[169]
AH2	VSS[064]	VSS[170]
AH22	VSS[065]	VSS[171]
AH25	VSS[066]	VSS[172]
AH28	VSS[067]	VSS[173]
AH5	VSS[068]	VSS[174]
AH8	VSS[069]	VSS[175]
AJ12	VSS[070]	VSS[176]
AJ14	VSS[071]	VSS[177]
AJ17	VSS[072]	VSS[178]
AJ8	VSS[073]	VSS[179]
B11	VSS[074]	VSS[180]
B14	VSS[075]	VSS[181]
B17	VSS[076]	VSS[182]
B2	VSS[077]	VSS[183]
B20	VSS[078]	VSS[184]
B23	VSS[079]	VSS[185]
B5	VSS[080]	VSS[186]
B8	VSS[081]	VSS[187]
C26	VSS[082]	VSS[188]
C27	VSS[083]	VSS[189]
E11	VSS[084]	VSS[190]
E14	VSS[085]	VSS[191]
E18	VSS[086]	VSS[192]
E2	VSS[087]	VSS[193]
E21	VSS[088]	VSS[194]
E24	VSS[089]	VSS[195]
E5	VSS[090]	VSS[196]
E8	VSS[091]	VSS[197]
F16	VSS[092]	VSS[198]
F28	VSS[093]	
F29	VSS[094]	VSS_NCTF[01]
G12	VSS[095]	VSS_NCTF[02]
G14	VSS[096]	VSS_NCTF[03]
G18	VSS[097]	VSS_NCTF[04]
G21	VSS[098]	VSS_NCTF[05]
G24	VSS[099]	VSS_NCTF[06]
G26	VSS[100]	VSS_NCTF[07]
G27	VSS[101]	VSS_NCTF[08]
G8	VSS[102]	VSS_NCTF[09]
H2	VSS[103]	VSS_NCTF[10]
H23	VSS[104]	VSS_NCTF[11]
H28	VSS[105]	VSS_NCTF[12]
H29	VSS[106]	

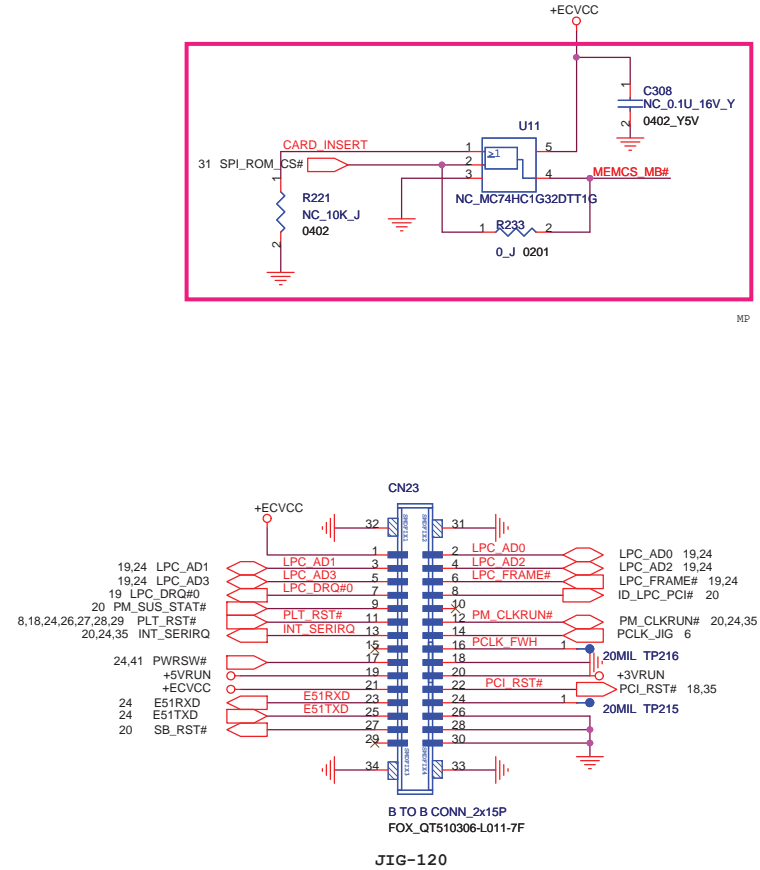
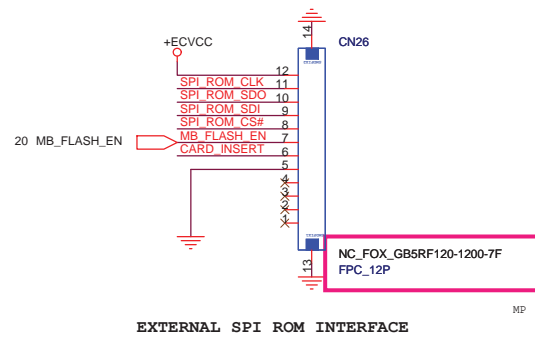
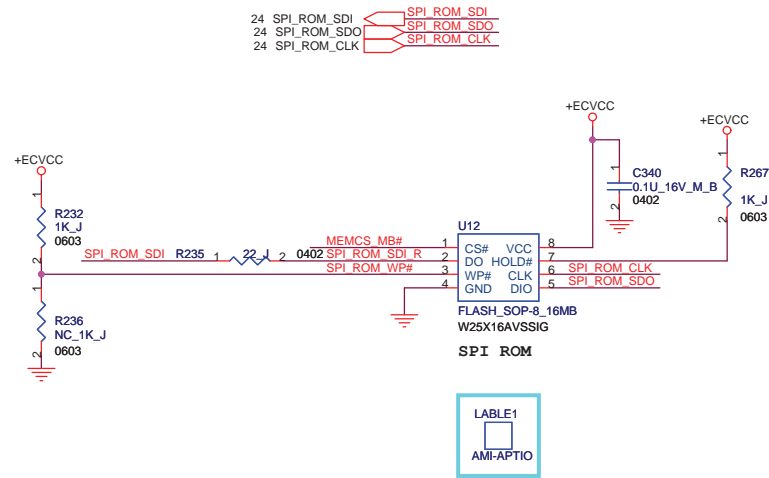
SATA HDD CONN



SATA ODD CONN



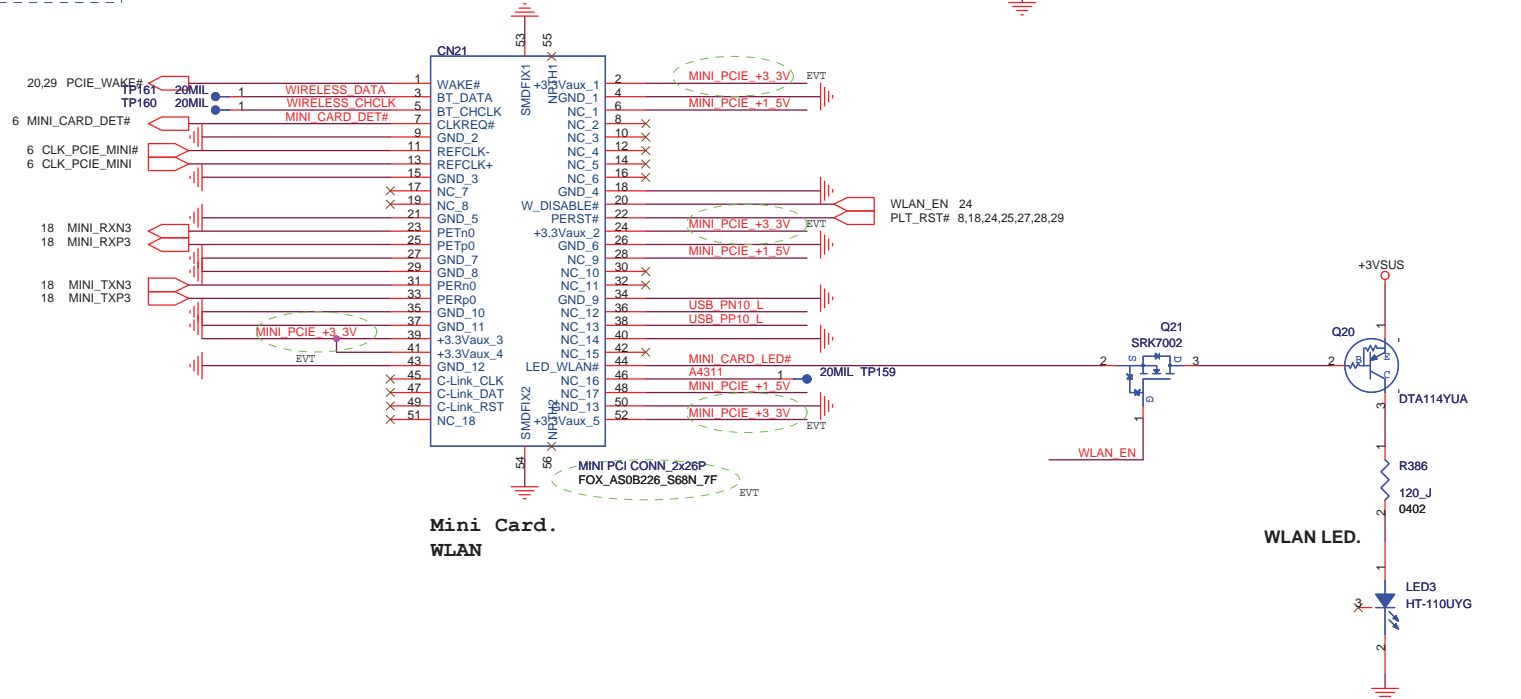
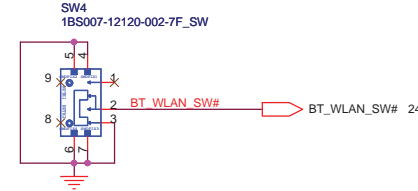






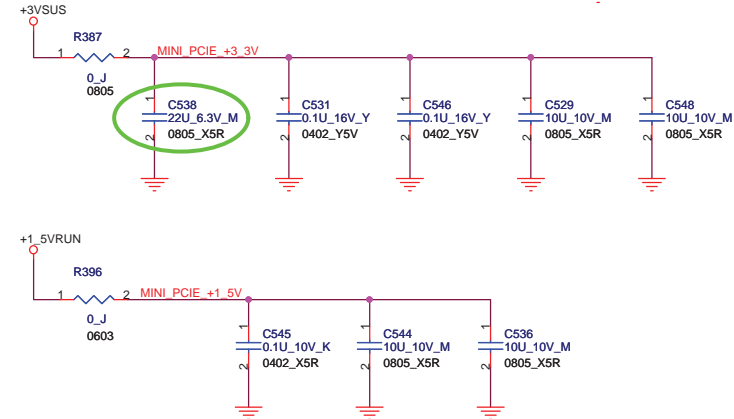
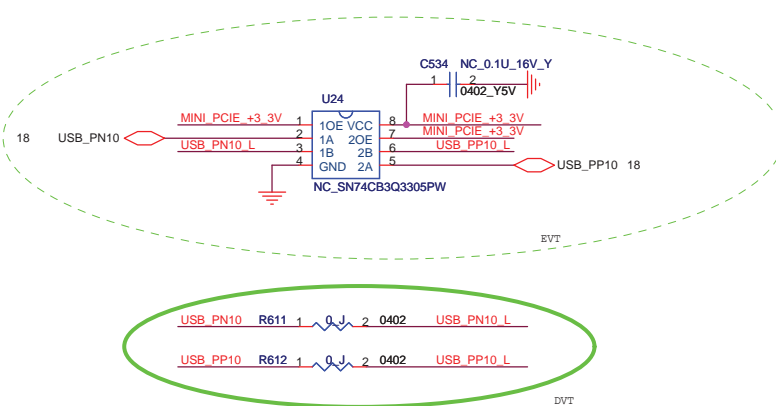
MINI CARD

+1_5V=>0.5A Peak/0.375A Normal
+3_3VAux=>2.75A Peak/1.1A Normal



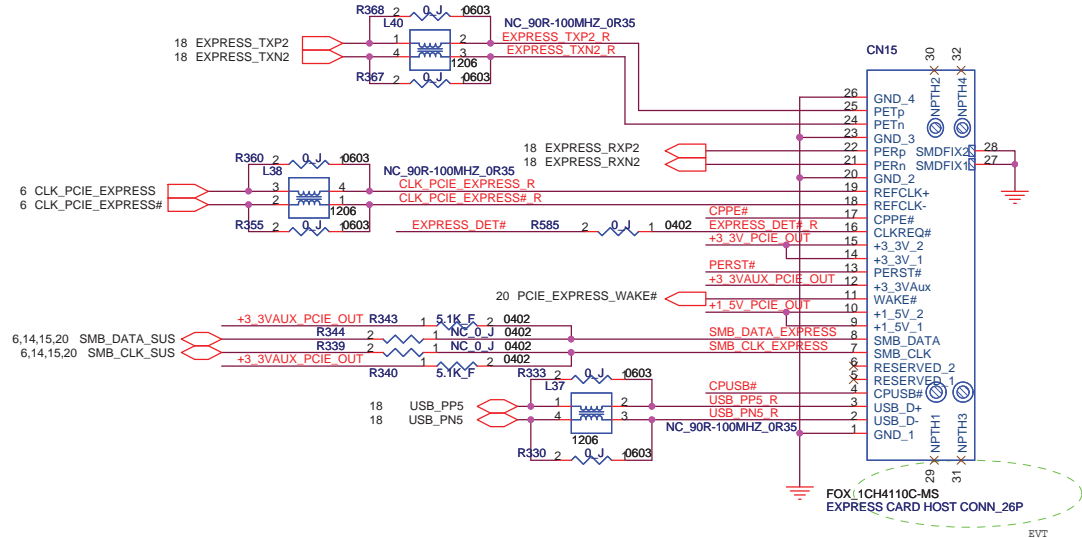
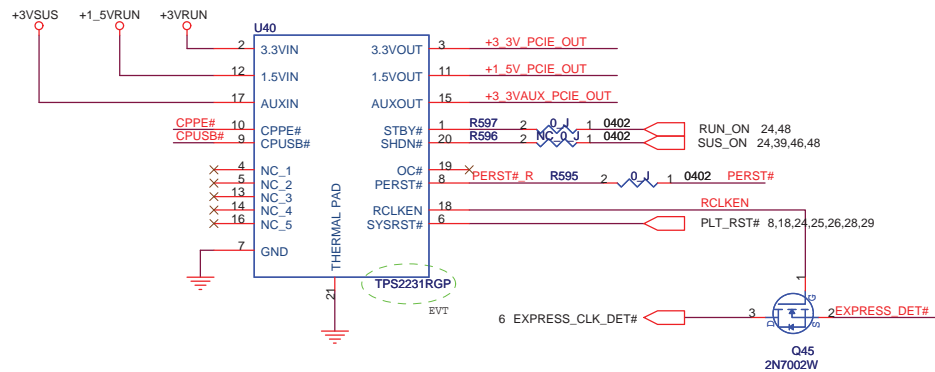
Mini Card.
WLAN

WLAN LED.

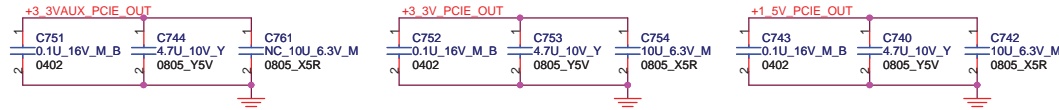
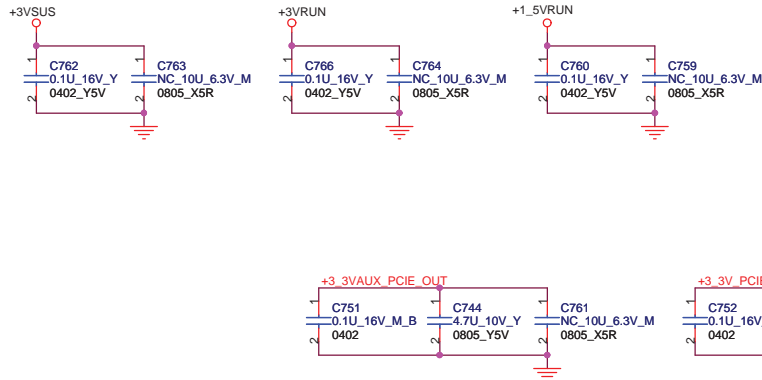
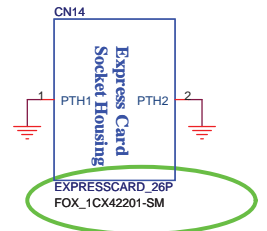


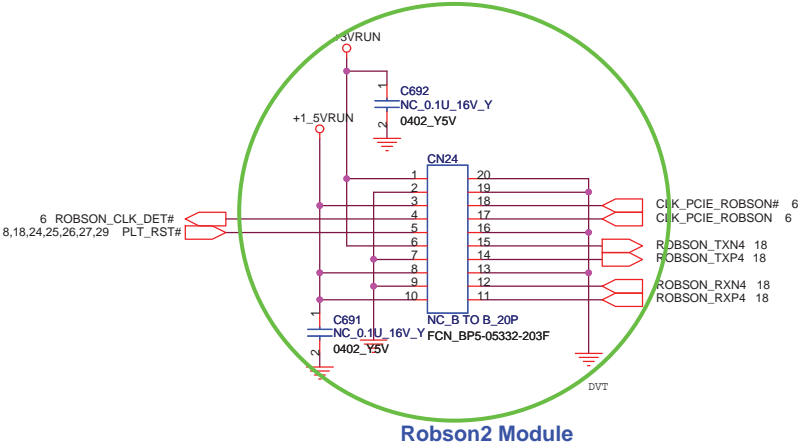
+1_5V=>1.3A
+3_3VAux=>0.6A
+3_3V=>2.5A

Express Card Power Switch

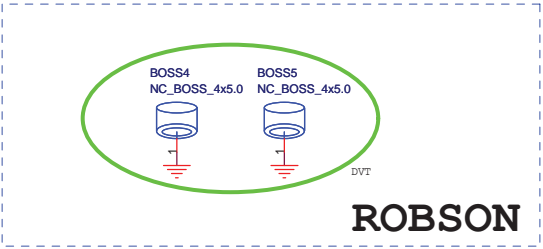


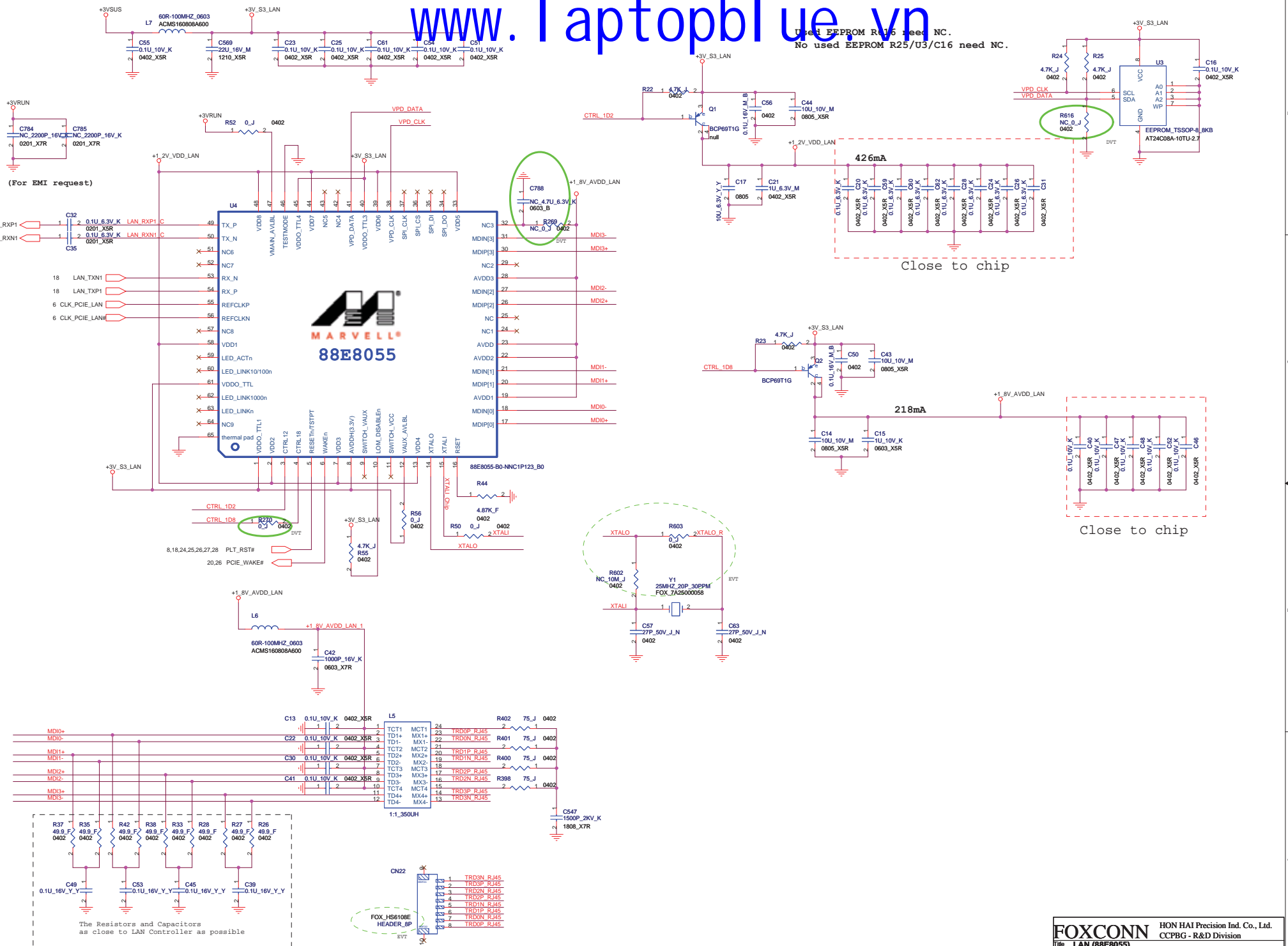
Express Card Slot.

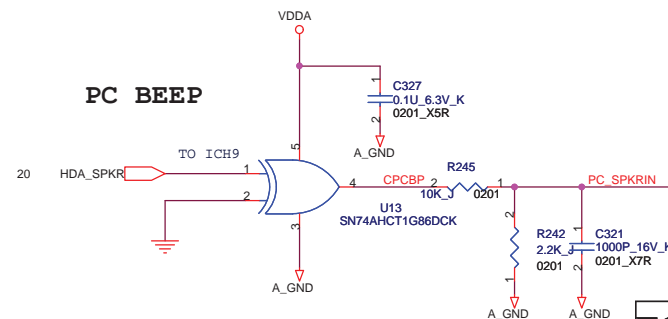
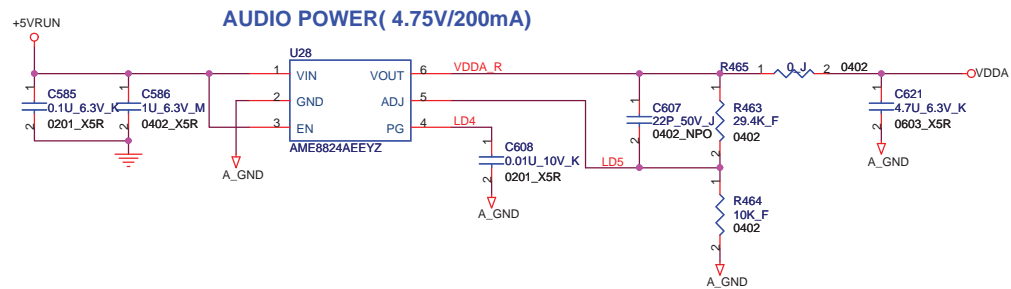
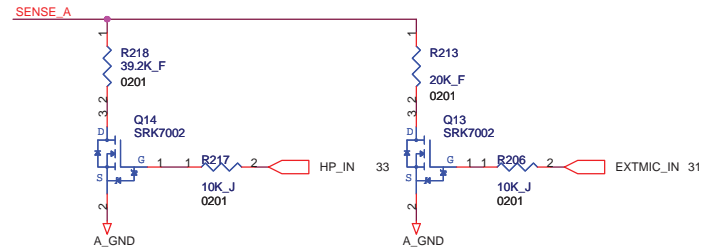
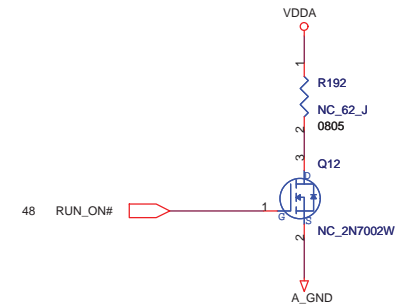
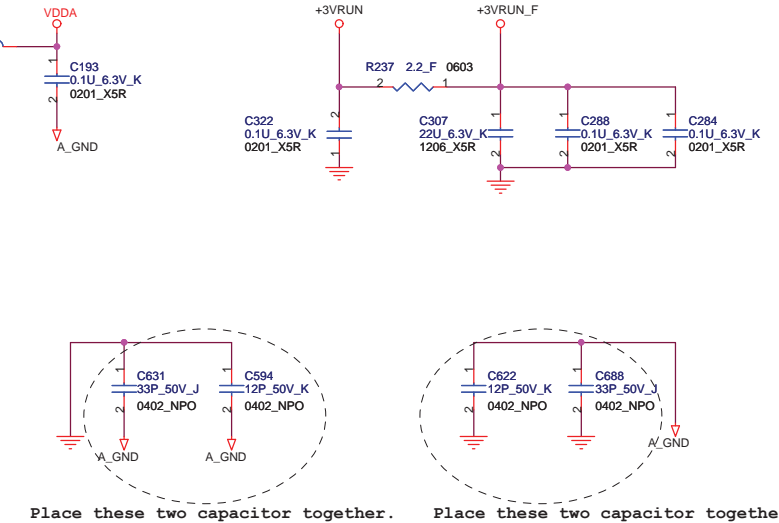
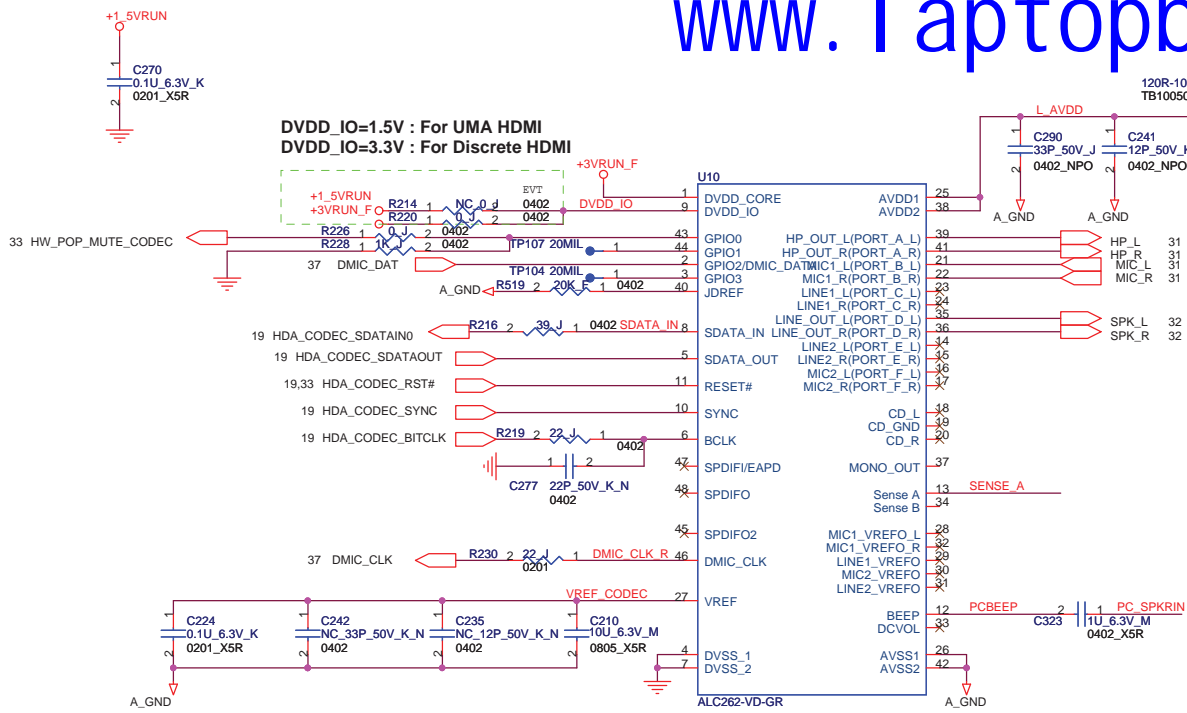


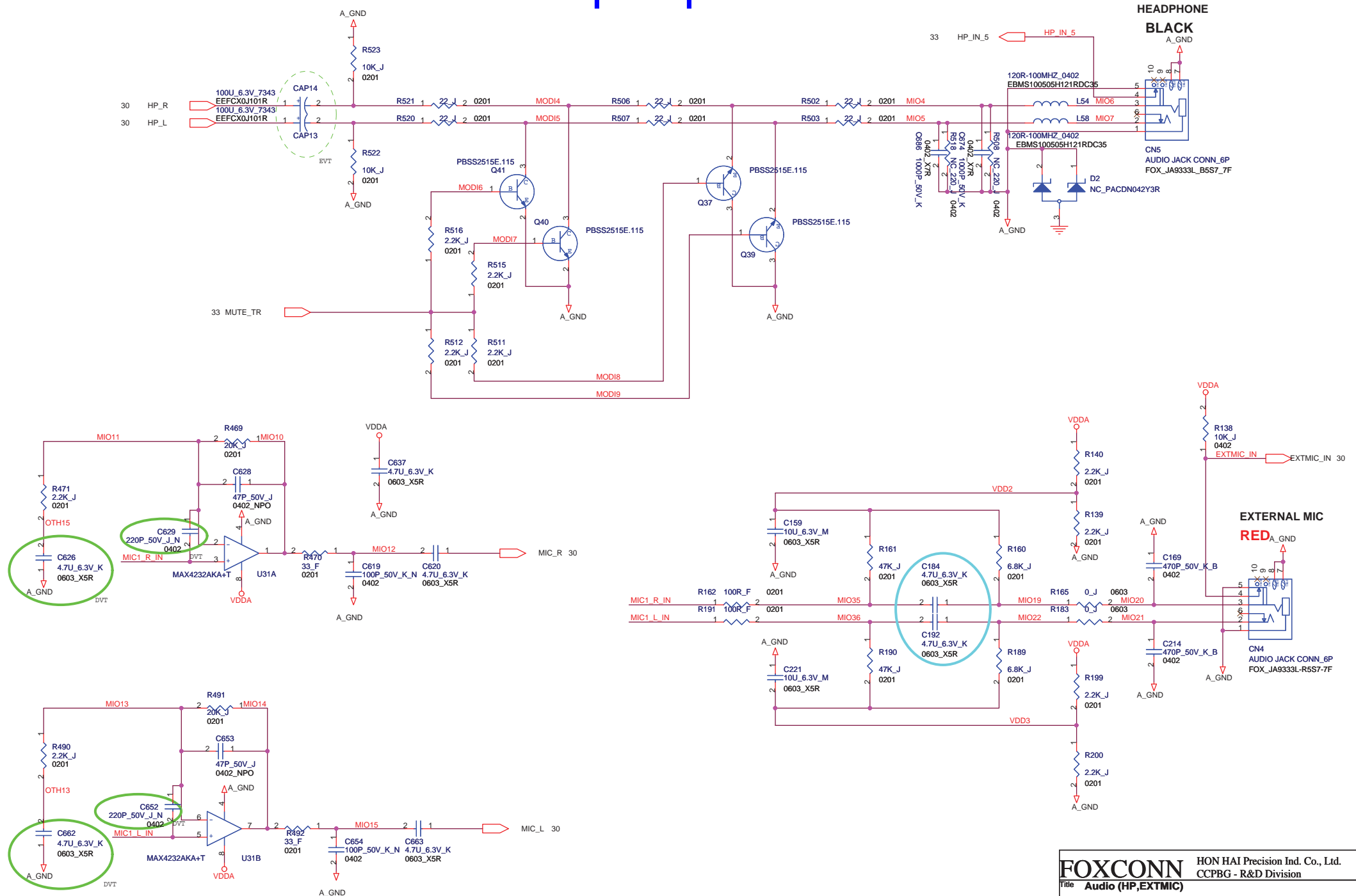


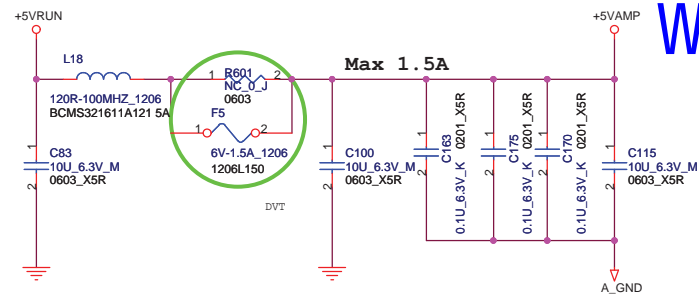
EVT2 11/6 Need to update new connector.



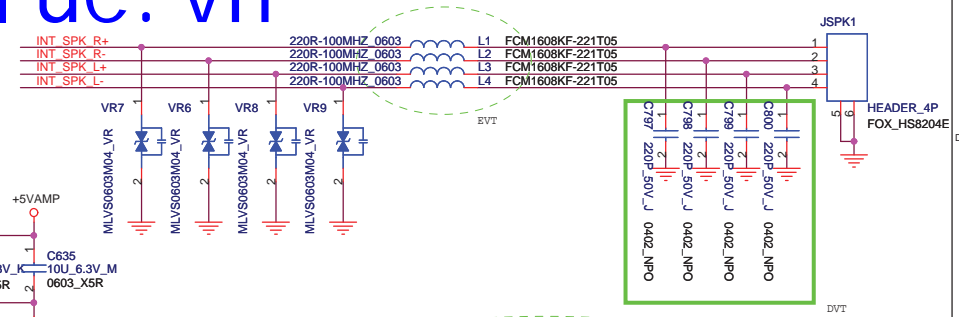
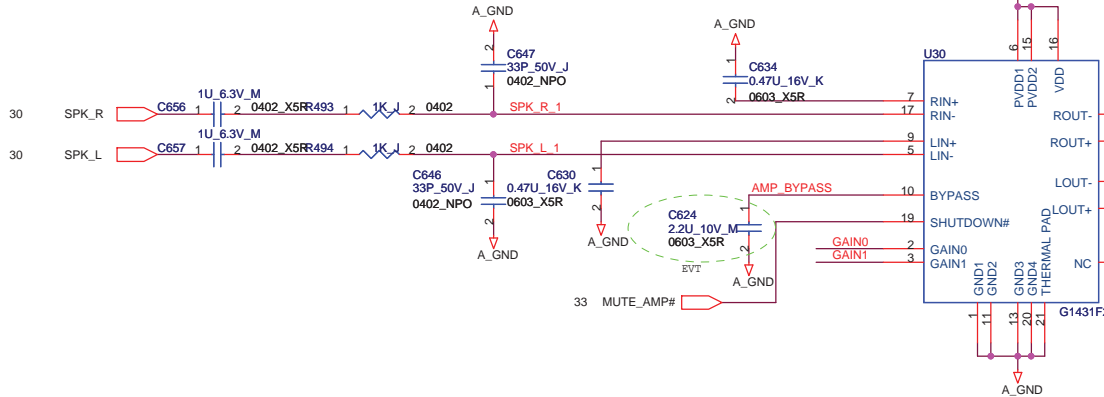






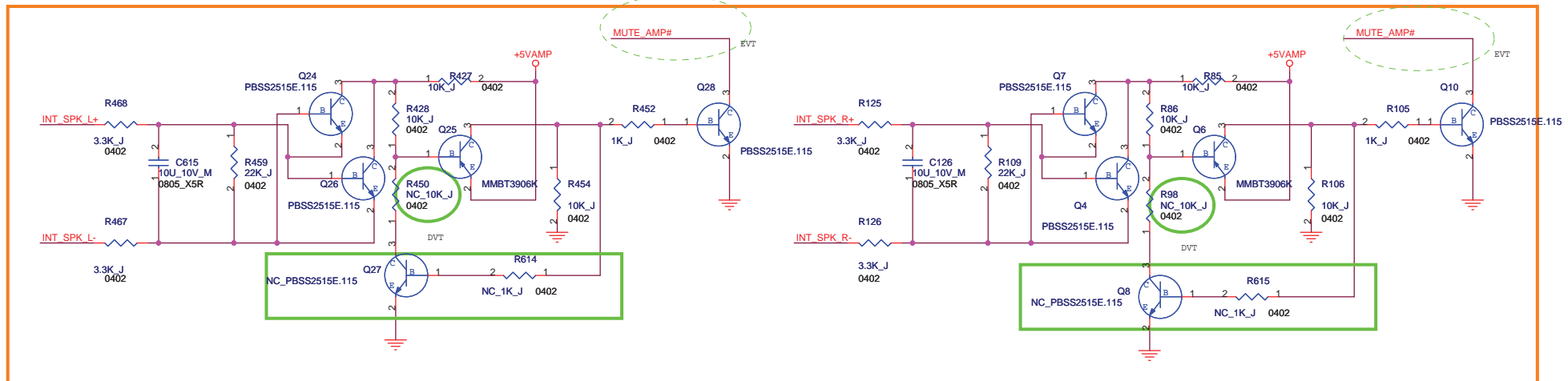


SPEAKER AMP

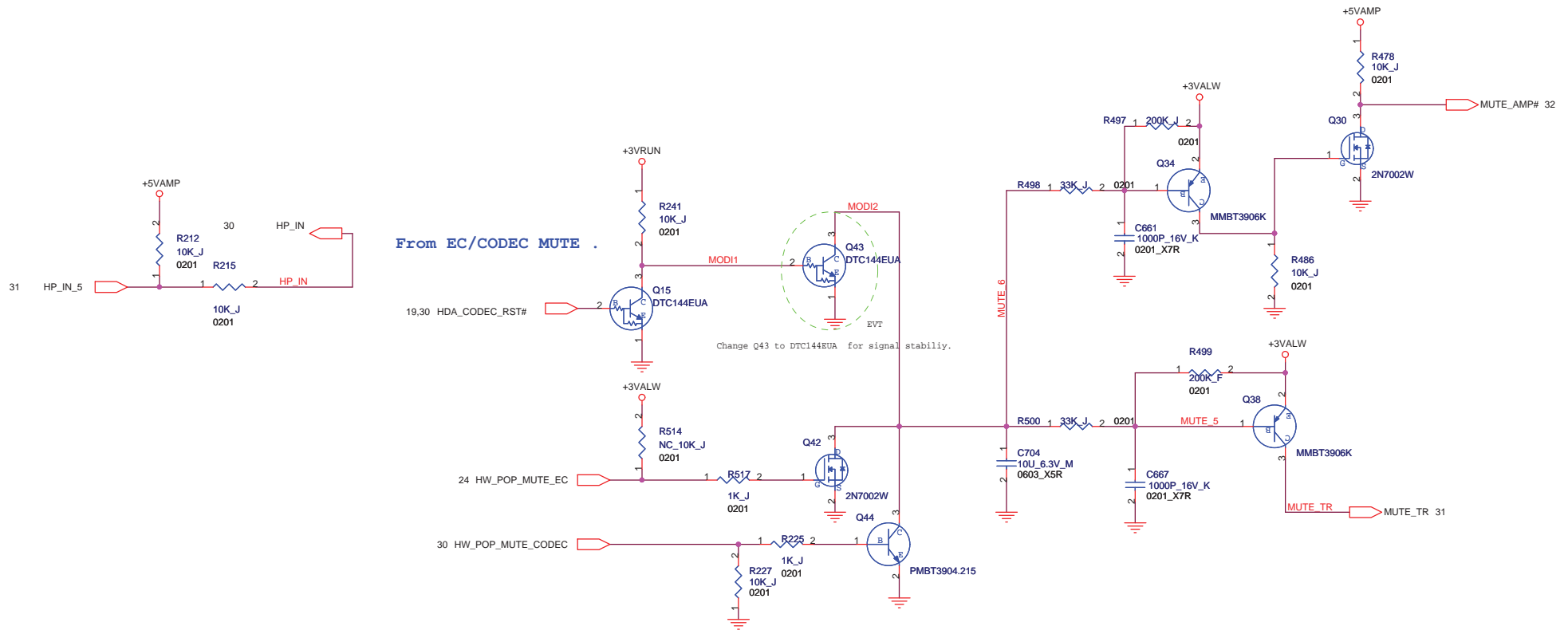


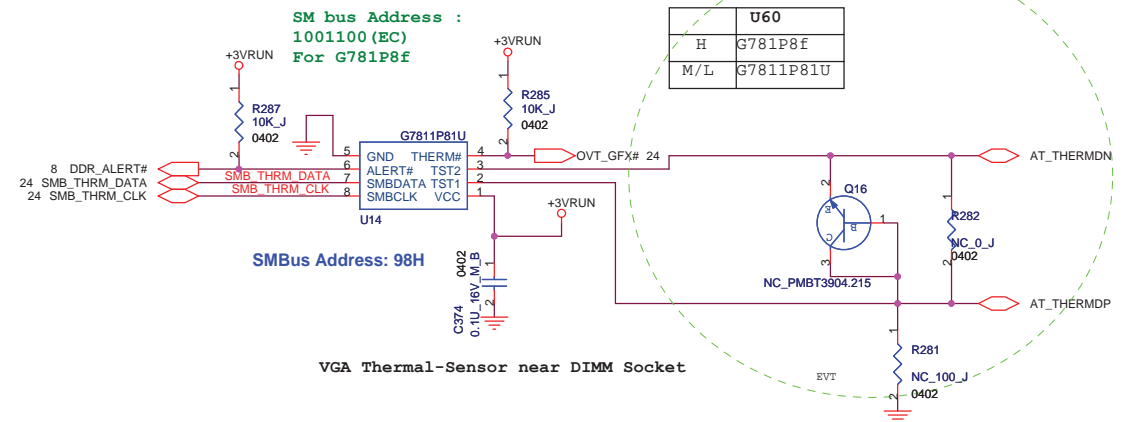
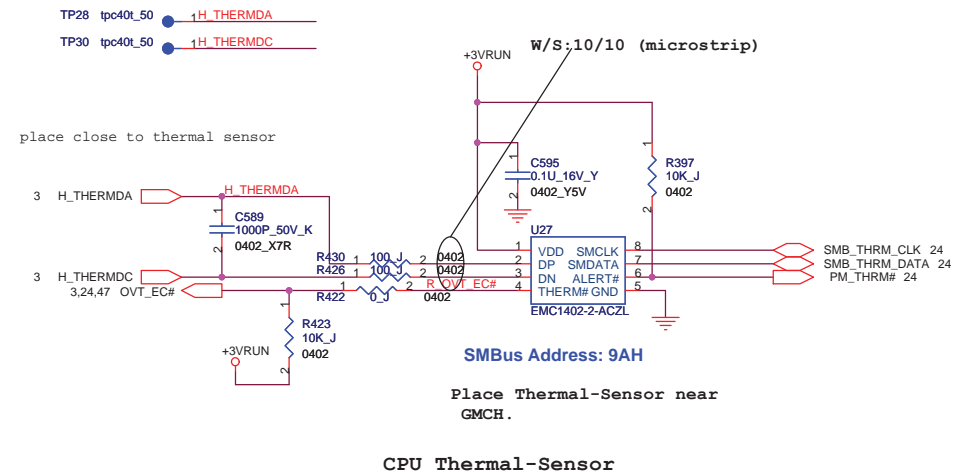
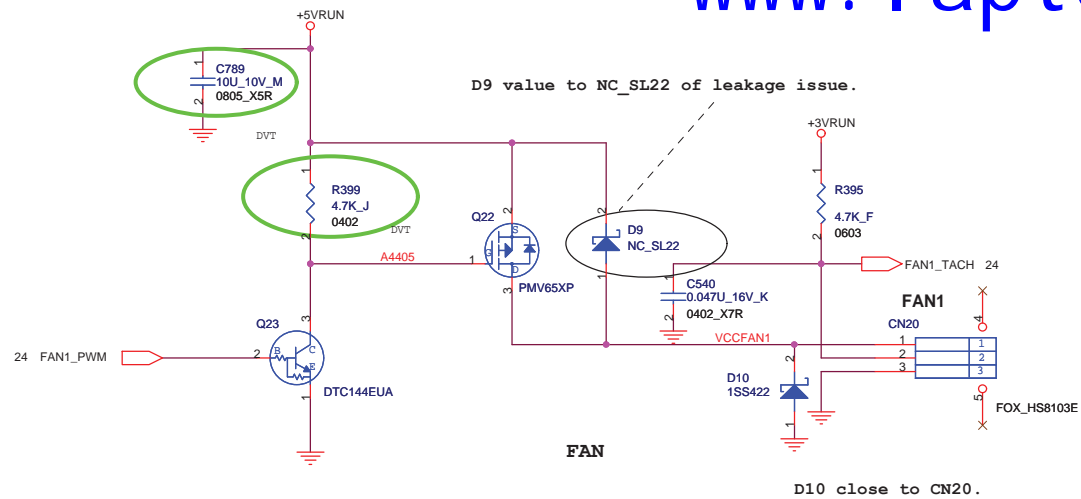
SPEAKER AMP

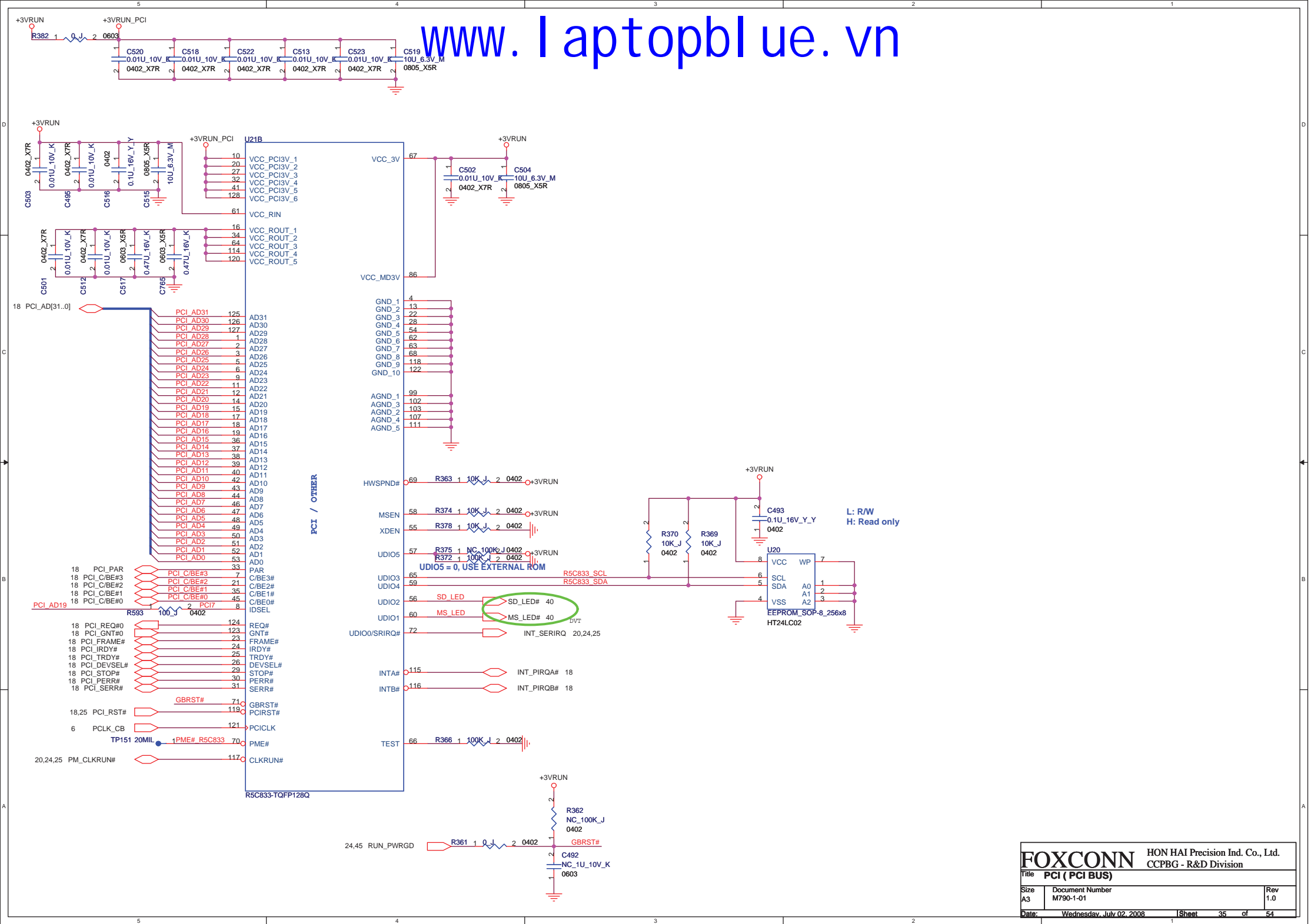
	GAIN0	GAIN1
6 dB	0	0
10 dB	0	1
15.6 dB	1	0
21.6 dB	1	1

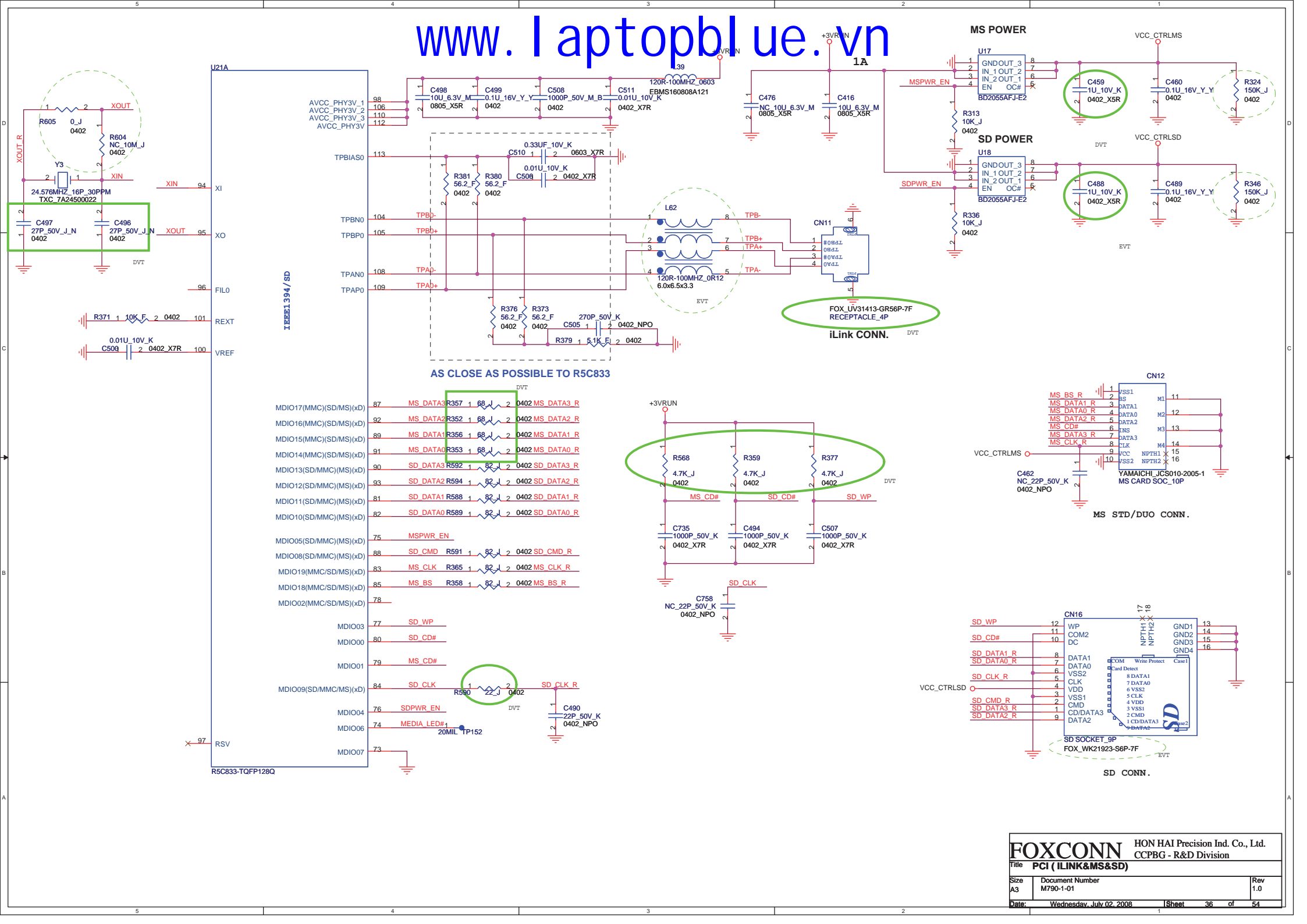


For Mor request, add the speaker cable short protection circuit

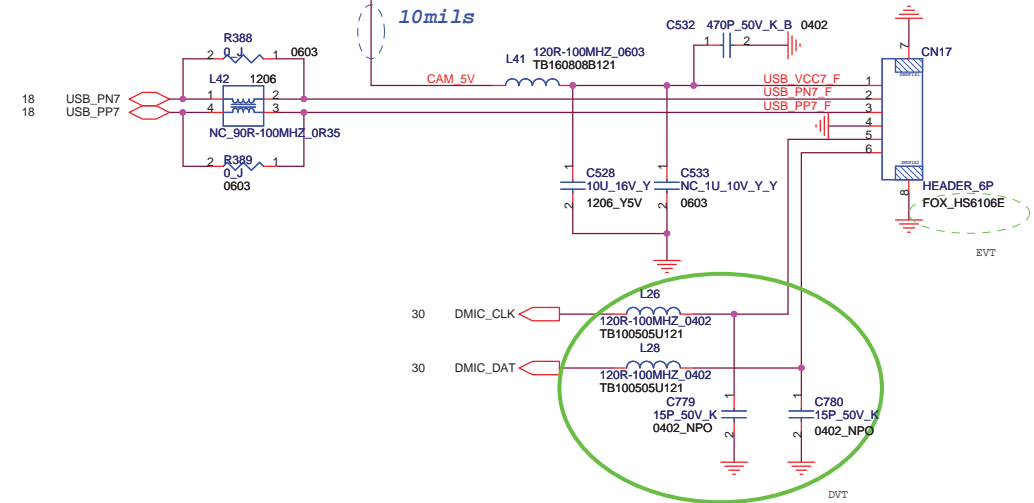
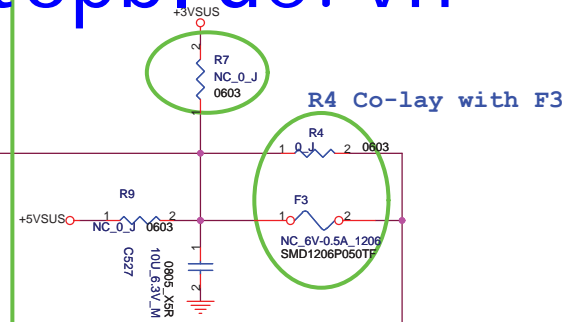
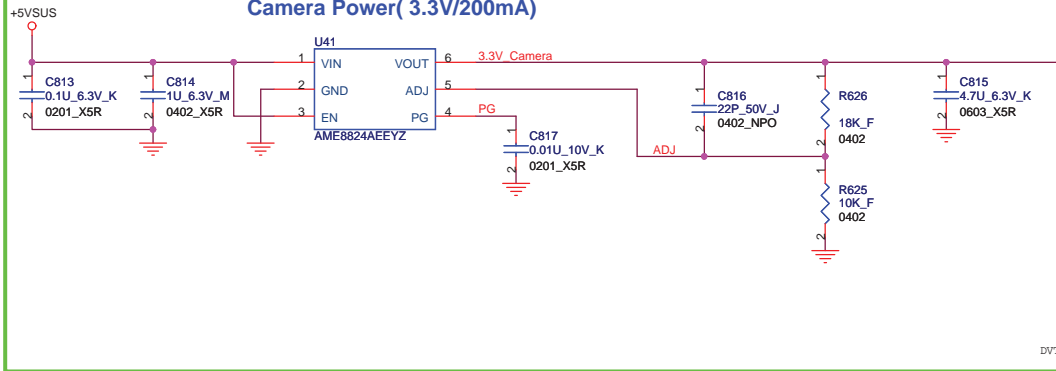




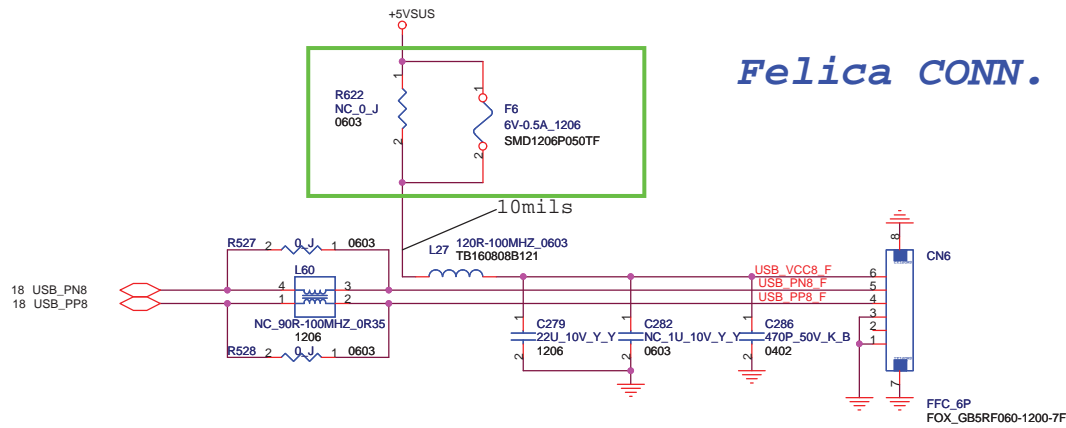




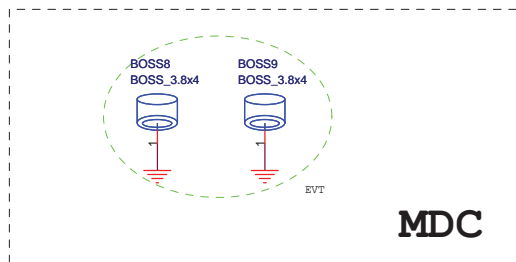
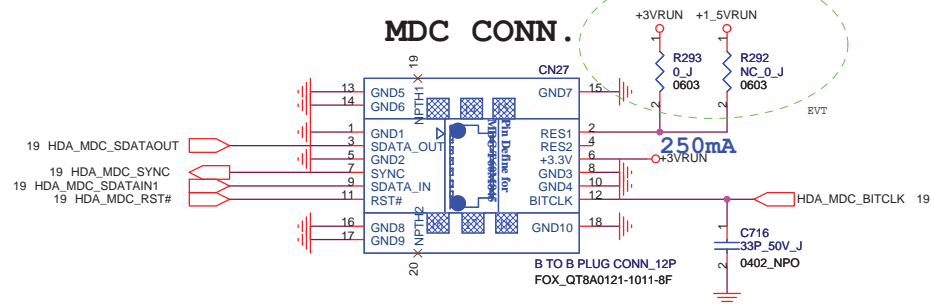
Camera Power(3.3V/200mA)

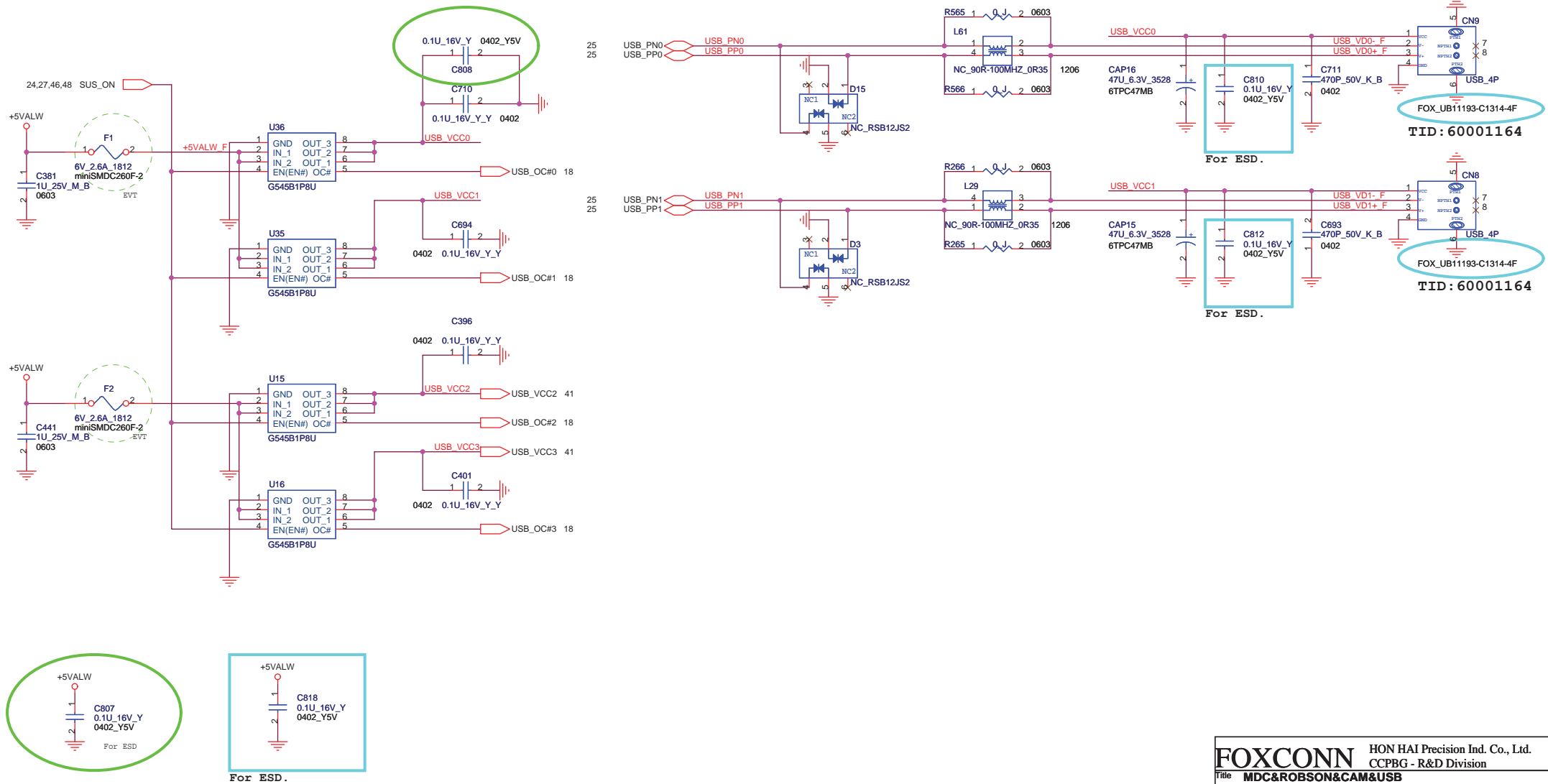


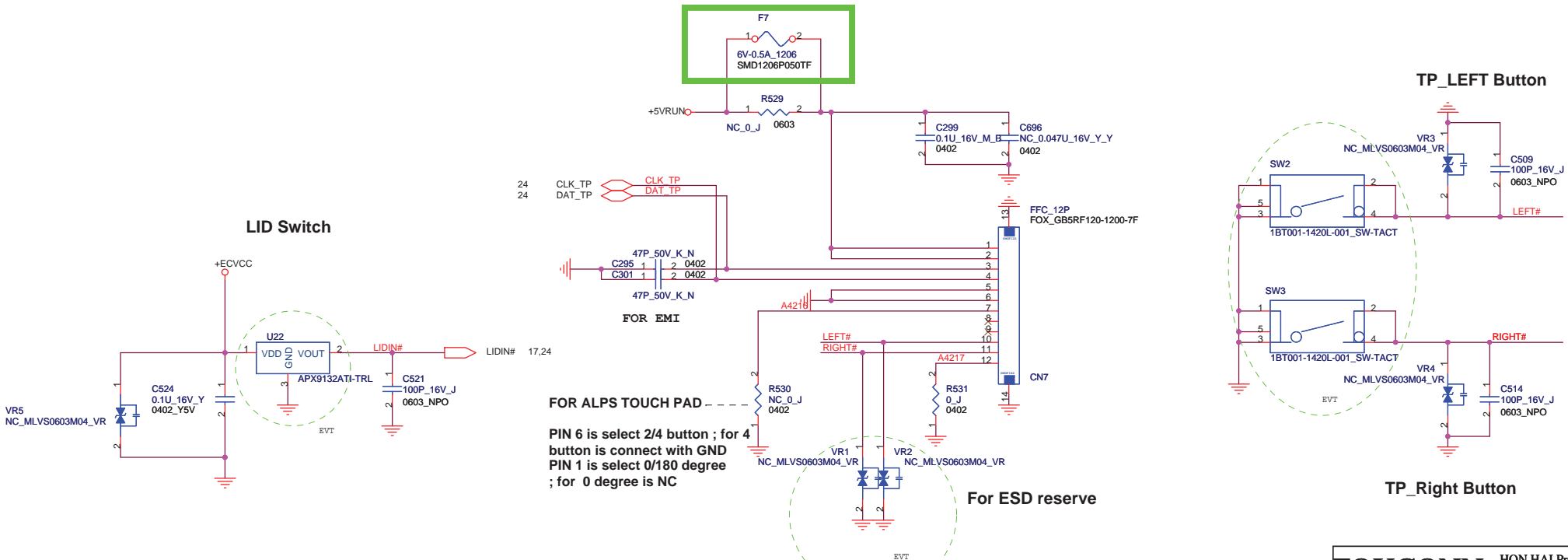
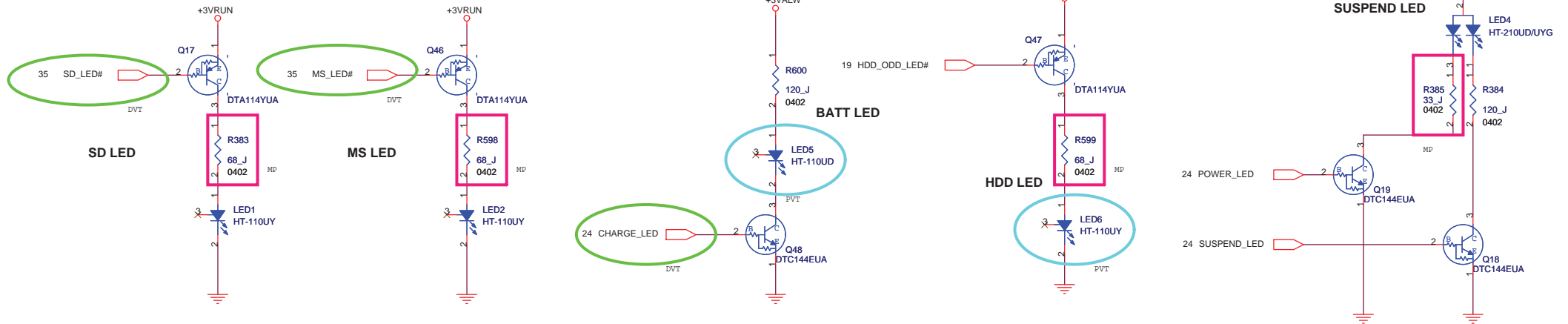
Felica CONN.



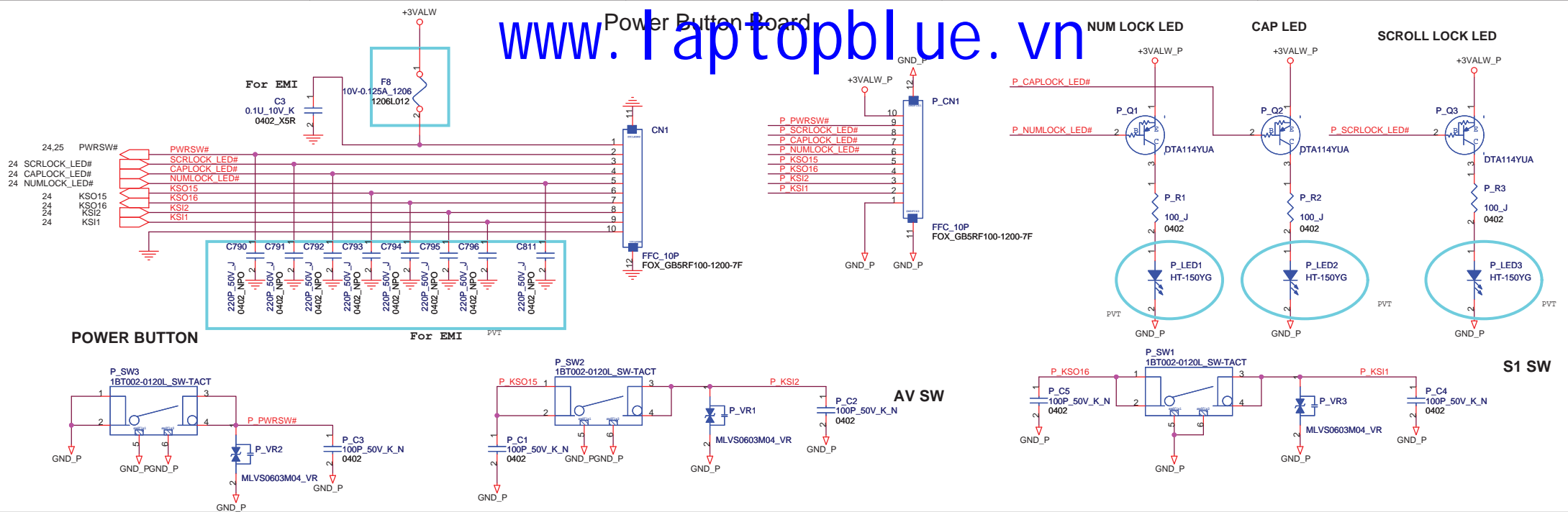
MDC CONN.



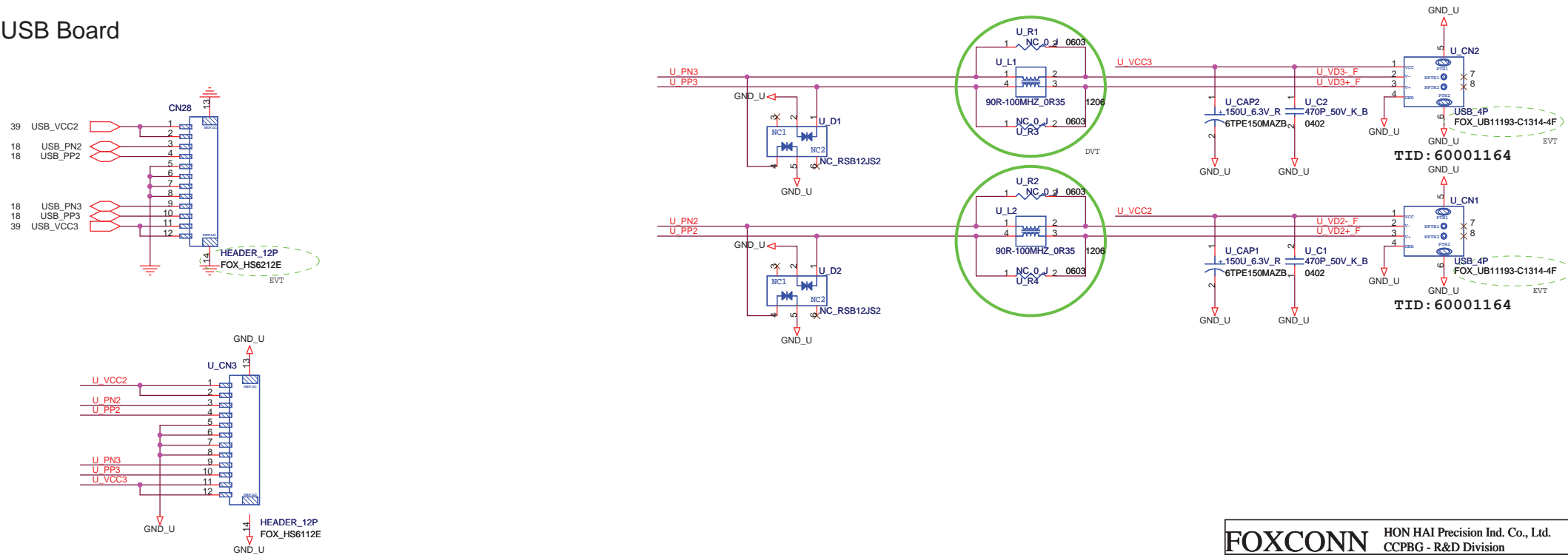


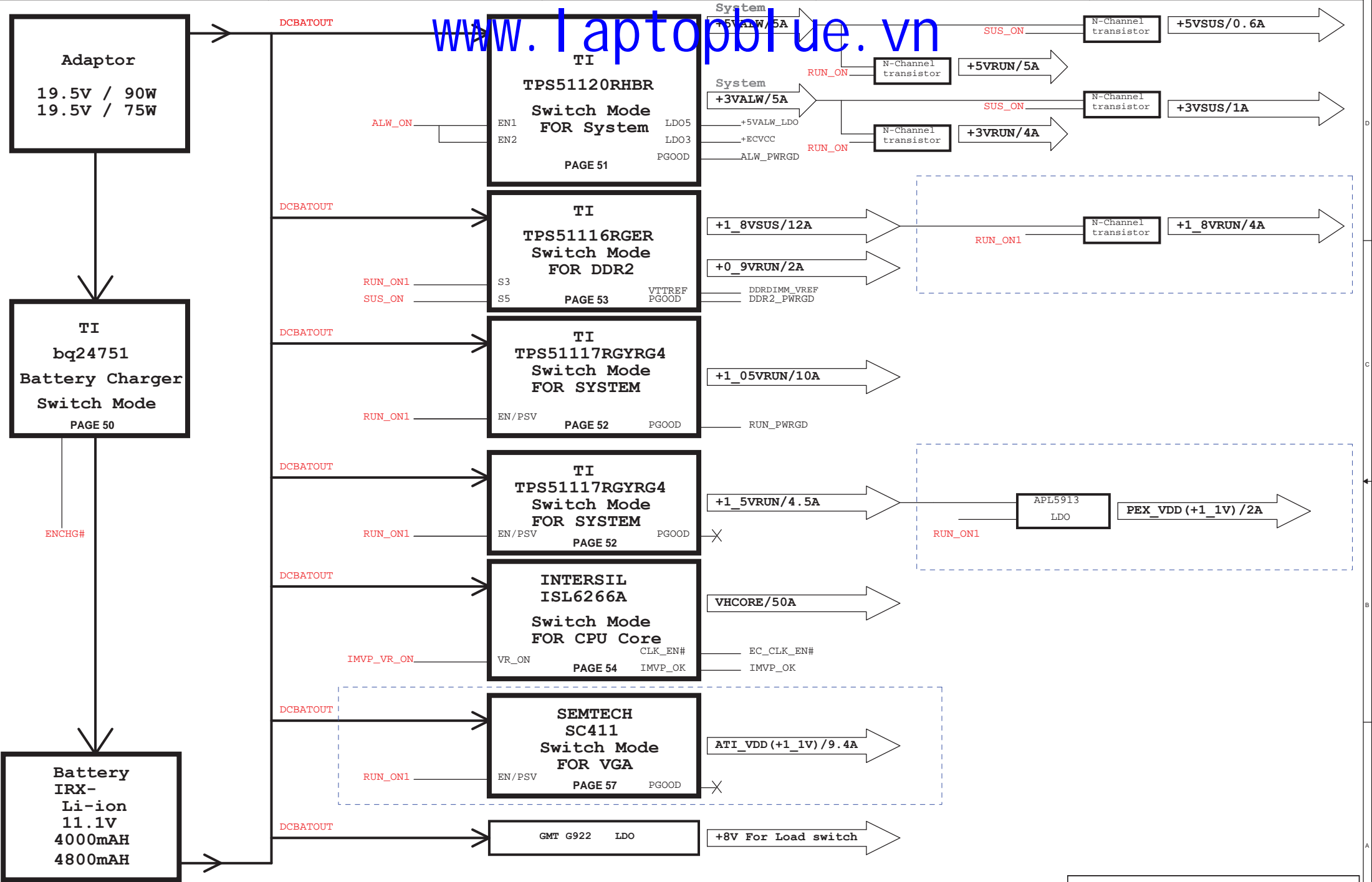


Power Button Board



USB Board







charge current set table:

charge current	CHARGE_CTRL D/A pin voltage setting	Required charge current control
1.5A	3.06V	High current
0.8A	1.6V	Middle current
350mA	0.72V	Low current
0A	0V	charge OFF

VREF=3.3V --->Vdac

Vbat=cell count*[4V+0.5*(Vadj/Vvdac)]=12.48V
(Vbat=4.2V when Vadj connected to REGN) 24

Icharge=(Vrsset/Vvdac)*(0.1/PR1)=1.5A 4

Iadapater=(Vacset/Vvdac)*(0.1/PR1)=4.1A 6

IADAPT=(Vacp - Vacn)*20

Input OCP: (VACP-VACN)/max/PR2=100mV/15mohm=6.6A

Input OVP : 22.2V

Input UVP : 17V

Battery OCP : Icharge*145%

Battery OVP : Vbat*104%

Pre-charge : <2.9V/cell ==> Icharge/8

Battery OTP : Tshut=155 degree

Fsw : 300KHz

Time that input current limit :

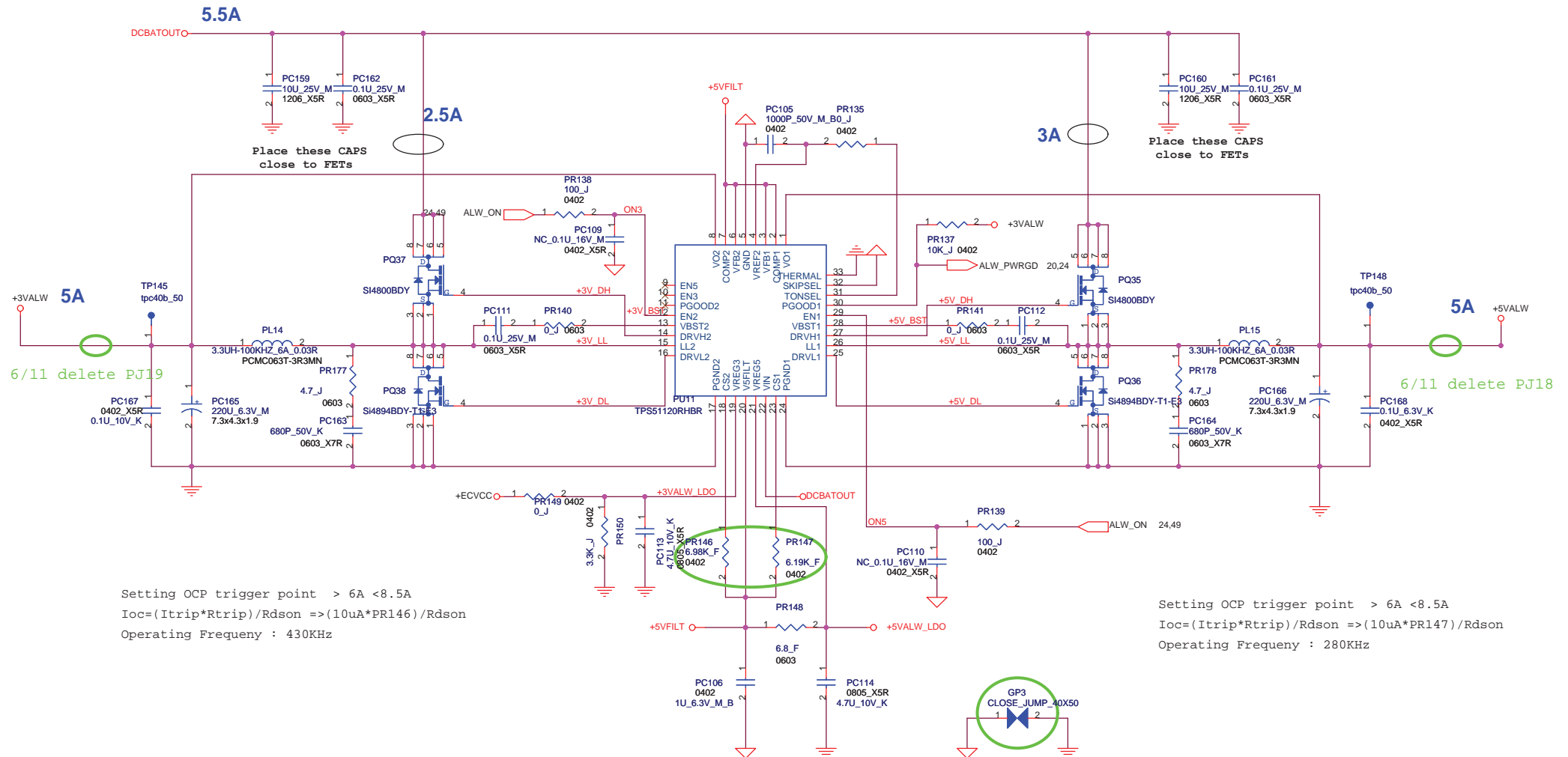
$t = (Cacop * 2) / (18uA / V * V(FVCC - ACP)) = 0.48s$

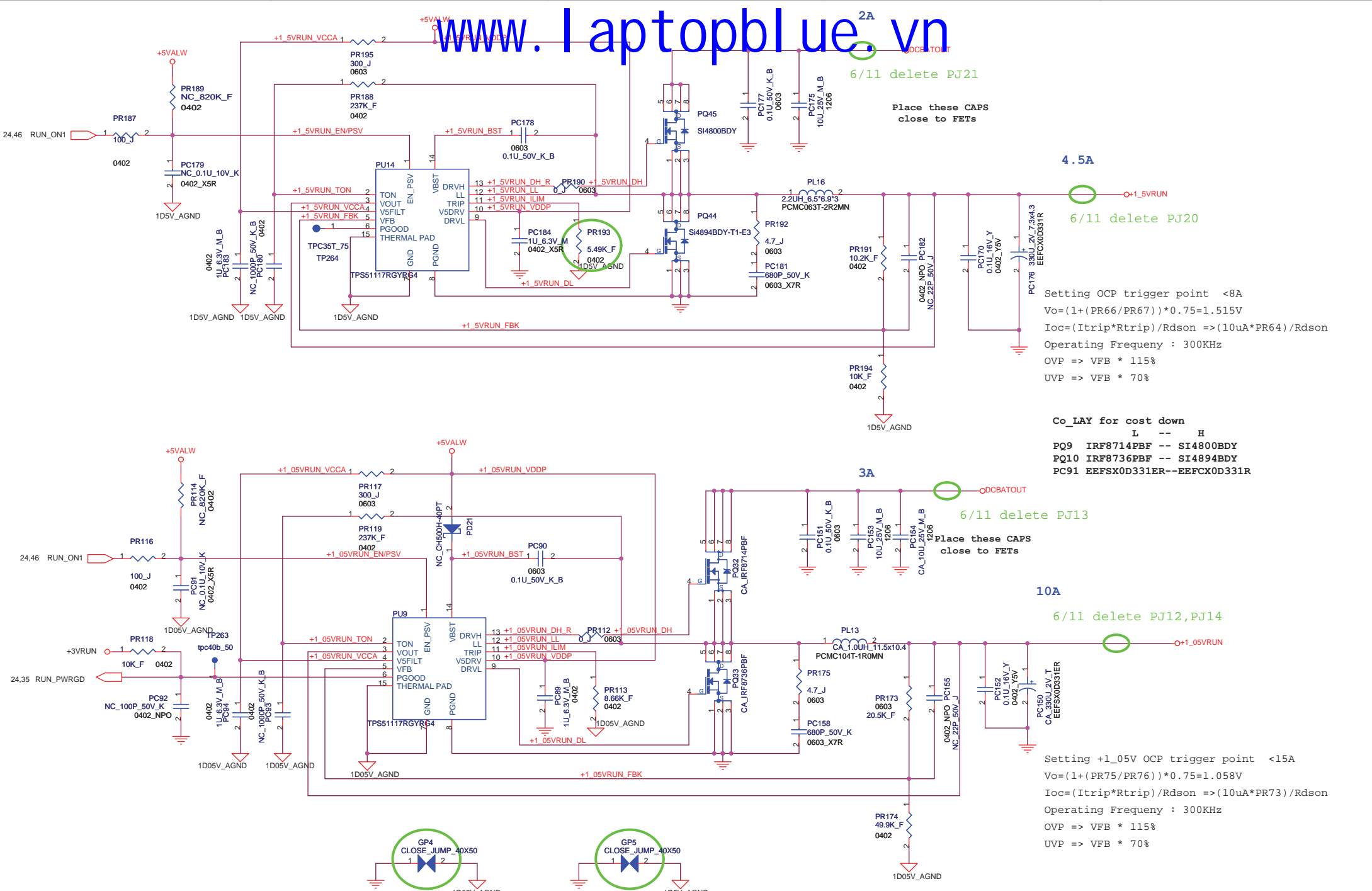
ENCHG# : Enable ==> L
 Disable ==> H

ACGOOD# : V_{acdet} > 2.4V ==> L
 V_{acdet} < 2.4V ==> H

CELLS	CELL COUNT
FLOAT	2
AGND	3
VREF	4

	CP	PWRLIMIT	Input OCP(Iadappter*1.5)
75W	3.3A/65W	3.7A/72W	4.2A/82.5W
90W	4.1A/80W	4.5A/87W	5A/97.5W





4.5A

6/11 delete PJ20

Setting OCP trigger point <8A
 $V_o = (1 + (PR66/PR67)) * 0.75 = 1.515V$
 $I_{oc} = (I_{trip} * R_{trip}) / R_{dson} \Rightarrow (10\mu A * PR64) / R_{dson}$
 Operating Frequency : 300KHz
 OVP => VFB * 115%
 UVP => VFB * 70%

Co_LAY for cost down

L -- H
 PQ9 IRF8714PBF -- SI4800BDY
 PQ10 IRF8736PBF -- SI4894BDY
 PC91 EEFSX0D331ER--EEFCX0D331R

3A

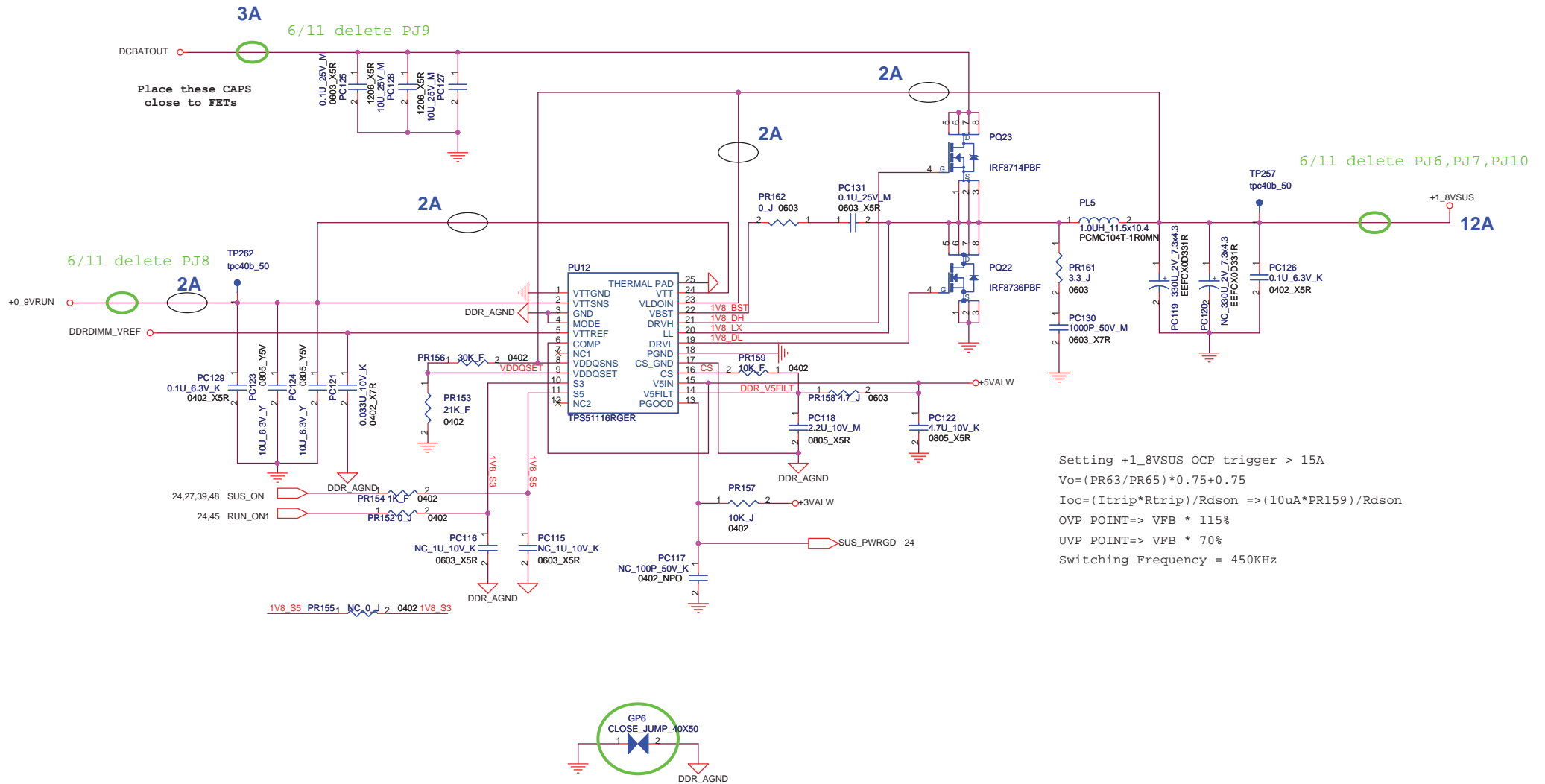
6/11 delete PJ13

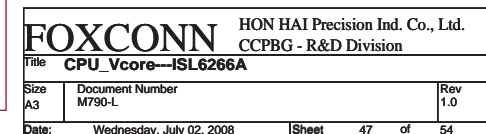
Place these CAPS close to FETs

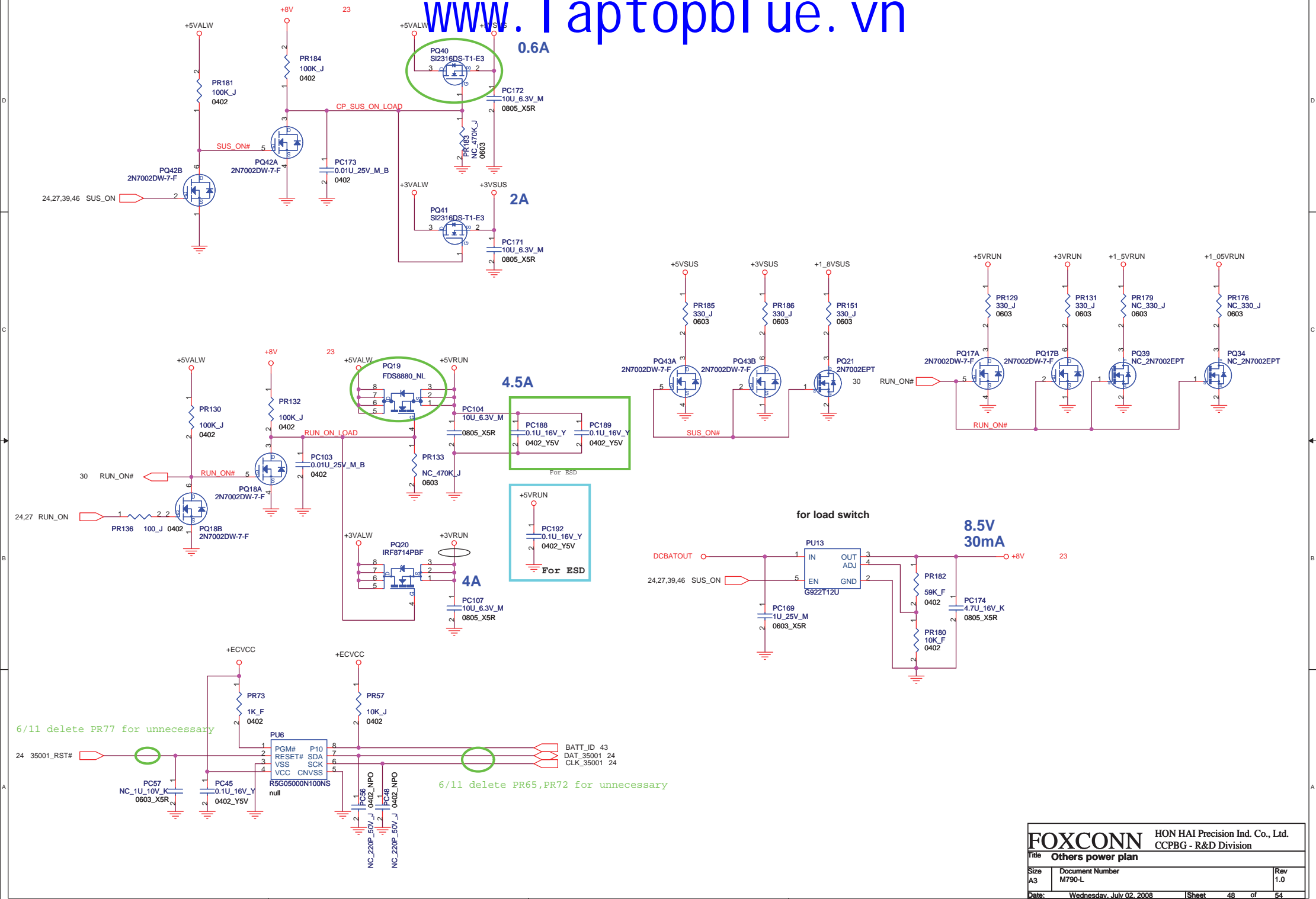
10A

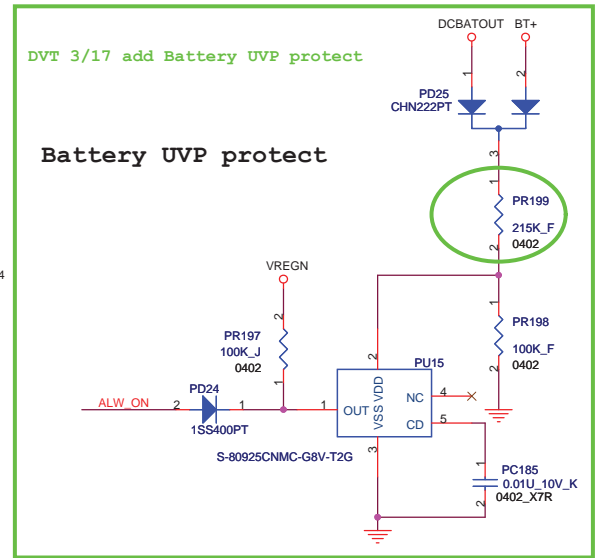
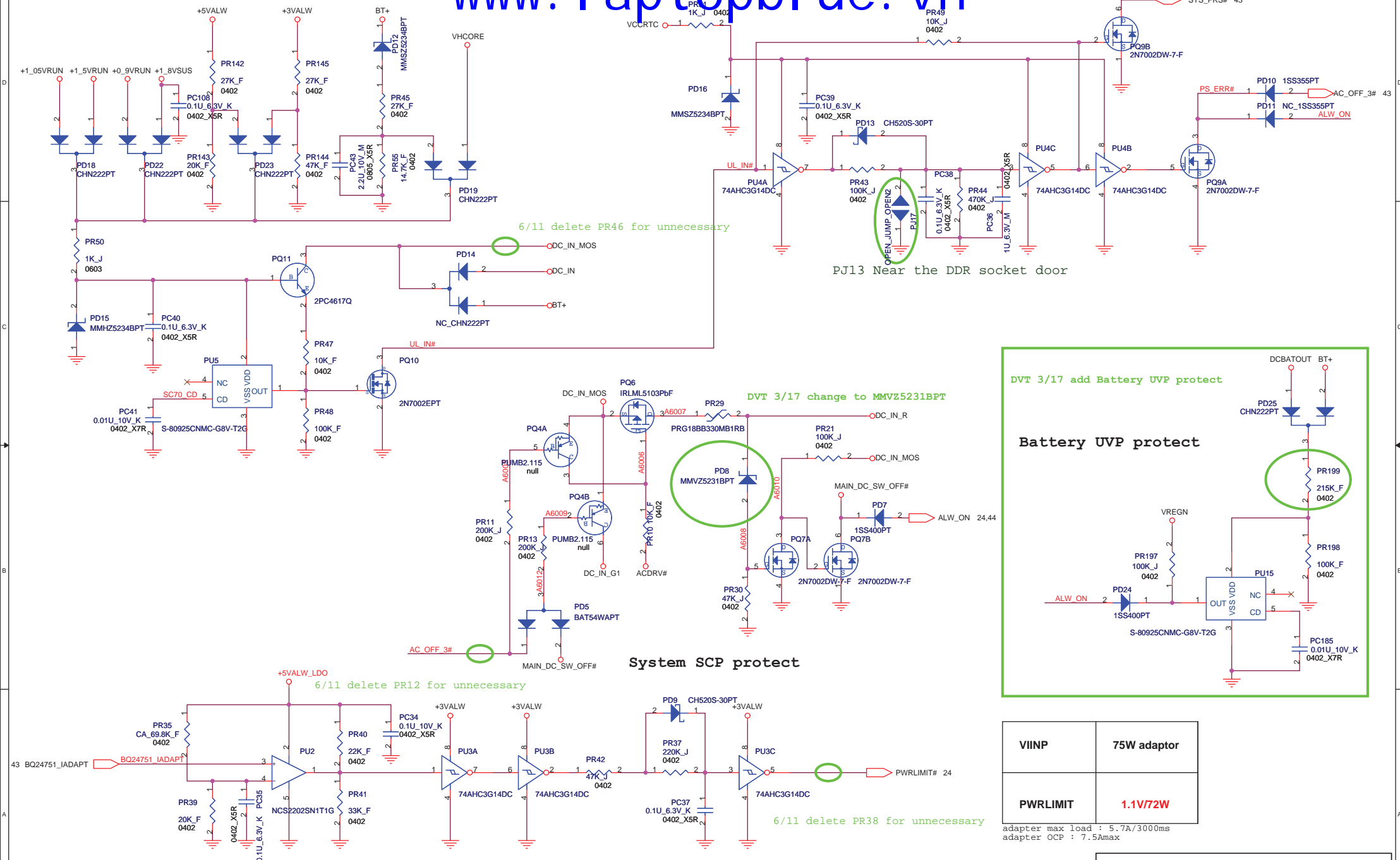
6/11 delete PJ12,PJ14

Setting +1.05V OCP trigger point <15A
 $V_o = (1 + (PR75/PR76)) * 0.75 = 1.058V$
 $I_{oc} = (I_{trip} * R_{trip}) / R_{dson} \Rightarrow (10\mu A * PR73) / R_{dson}$
 Operating Frequency : 300KHz
 OVP => VFB * 115%
 UVP => VFB * 70%







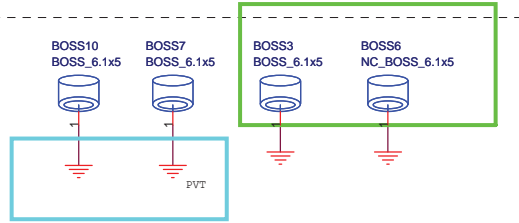


System SCP protect

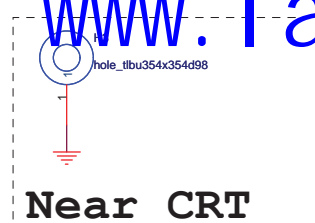
VIINP	75W adaptor
PWRLIMIT	1.1V/72W

adaptor max load : 5.7A/3000ms
adaptor OCP : 7.5Amax

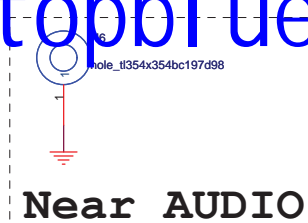
PWRLIMIT Protect



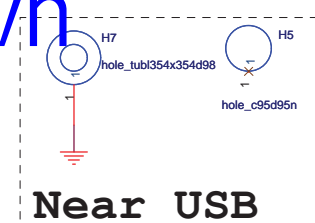
Thermal Module



Near CRT



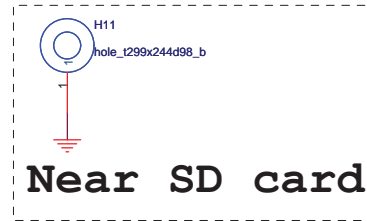
Near AUDIO



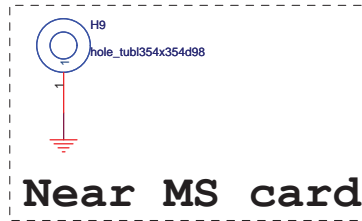
Near USB



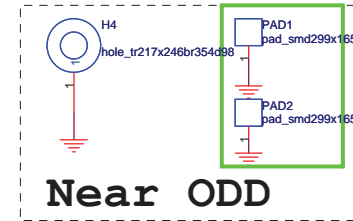
Near Express card



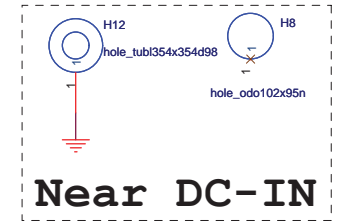
Near SD card



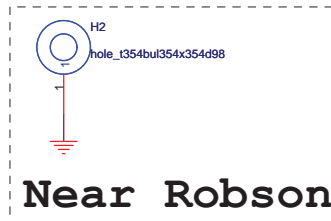
Near MS card



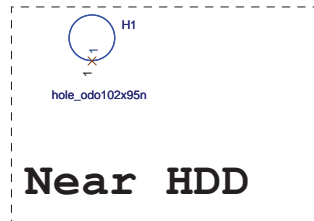
Near ODD



Near DC-IN



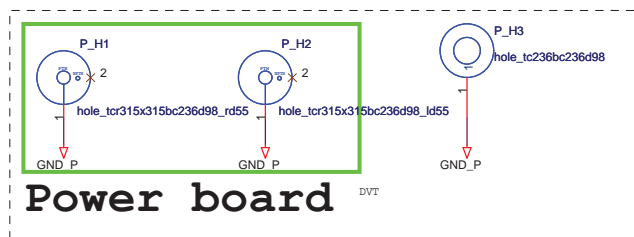
Near Robson



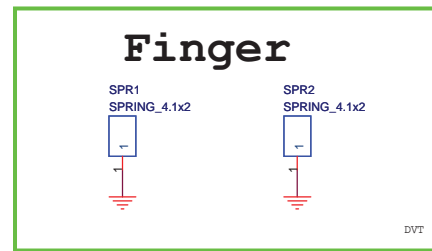
Near HDD



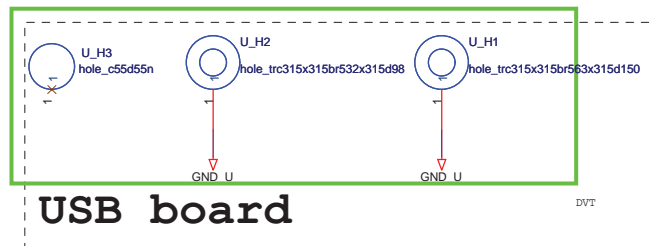
CPU



Power board



Finger



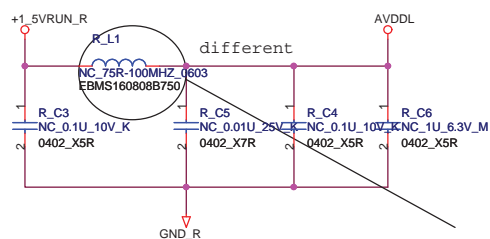
USB board

LAYOUT NOTE:
Place as close as possible
to AVDDL pins of Diamond Lake

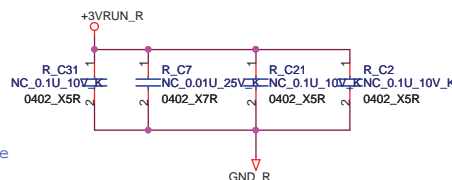
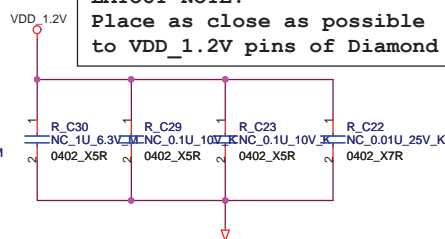
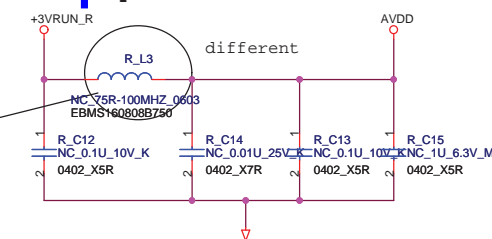
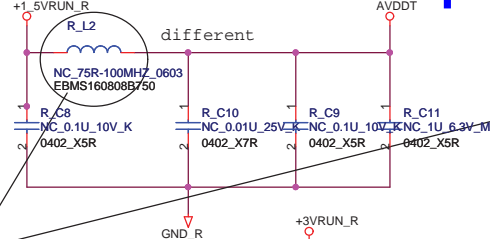
LAYOUT NOTE:
Place as close as possible
to AVDDT pins of Diamond Lake

LAYOUT NOTE:
Place as close as possible
to the top of the page.

LAYOUT NOTE:
Place as close as possible
to VDD 1.2V pins of Diamond Lake



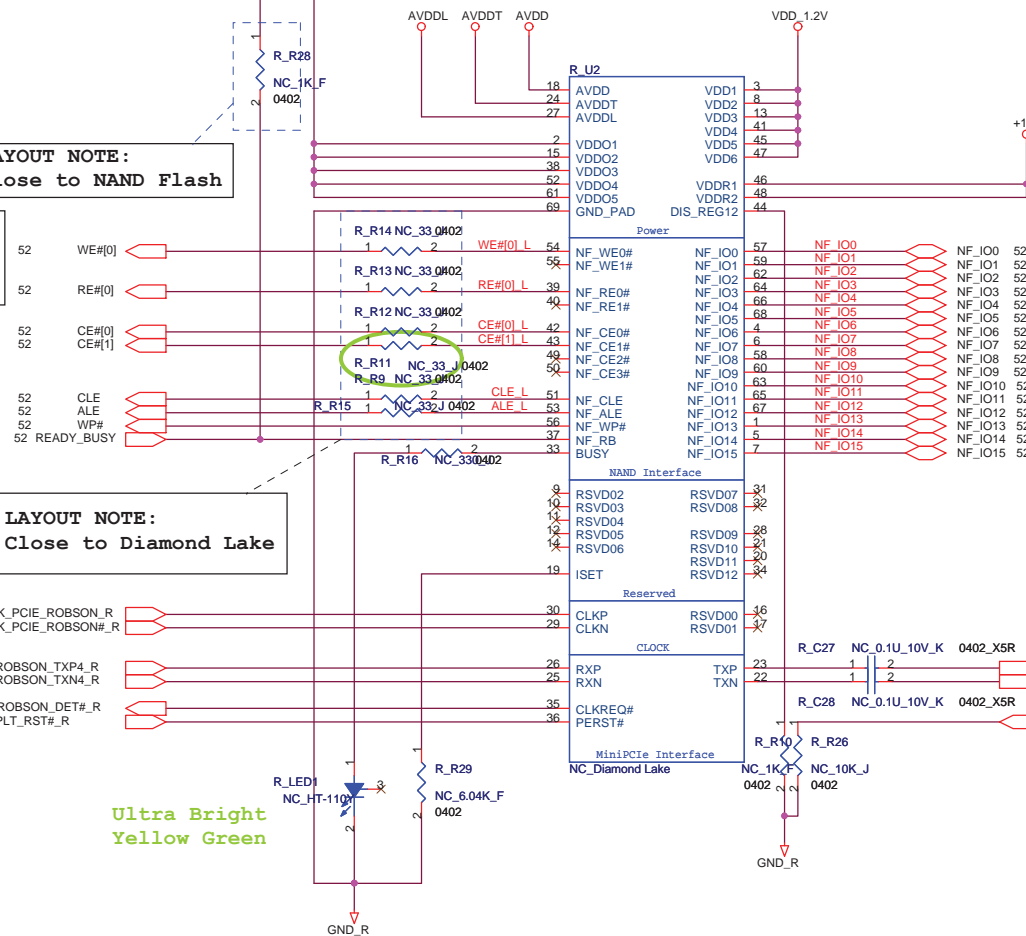
Intel sch use 70ohm /100MHZ



change

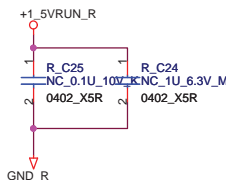
LAYOUT NOTE:
Place as close as possible
to +3VRUN pins of Diamond Lake

LAYOUT NOTE:
Close to NAND Flash

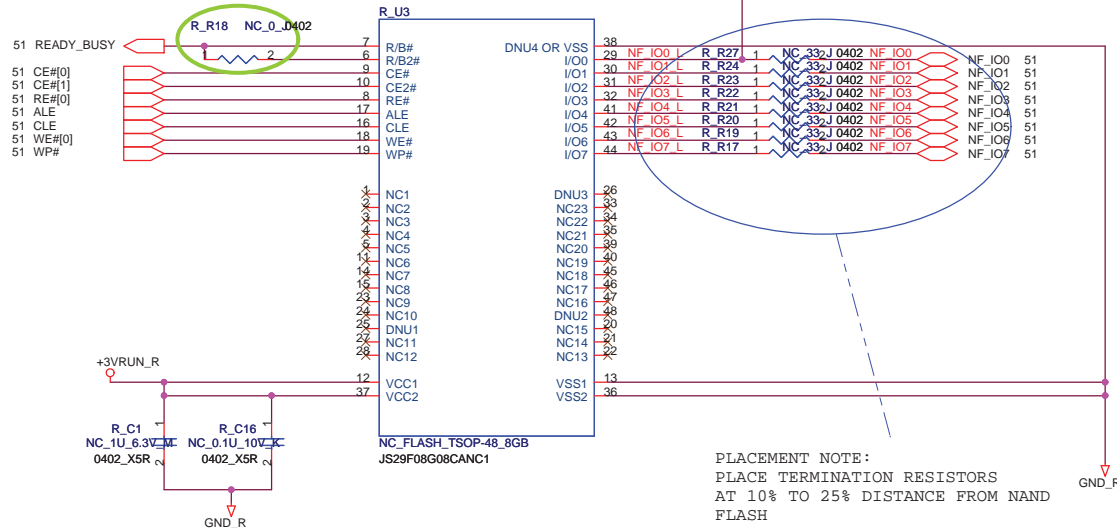


LAYOUT NOTE:
Close to Diamond Lake

LAYOUT NOTE:
Place as close as possible
to VDDR1 and VDDR2 pins
of Diamond Lake



Ultra Bright
Yellow Green



M790 EVT

(2008/01/23)
Update 8Layer H/L versio to 6Layer L version.

(2008/01/24)
Page 6. Change Clock to B version.
Page 25. Change SPI rom to new version.
Page 36. Change L62,delete L63 same as M760.

(2008/01/28)
Page 14/15. Add C767~C778 for EMI reserve.
Page 19. Add R610 damping resistor for adjustment for MOR request.
Page 20. Change GPIO18,20 to GPIO36,37 and delete R269,R270.
Page 23. Change D13 to mount.
Page 24. Change RP39,RP41 to +3VALW.Add D17,D18 for EC leakage current issue.
Page 29. Add R602,R603 damping resistor for adjustment for MOR request.
Page 32. Add R601 for MOR request.
Page 36. Add R604,R605 damping resistor for adjustment for MOR request.
Page 37. Add R611,R612,C779,C780 for EMI reserve.
Page 48. Change PQ40 from SI2304BDS-T1-E3 to SI2316DS-T1-E3 and Change PQ19 from IRF8714PBF to FDS8880_NL for MOR request.

(2008/01/29)
Page 50. Add H13~H16,P_H3.

(2008/01/30)
Page 50. Update H4,U_H1,U_H2.

(2008/01/31)
Page 20. Not AMT support,so delete R556, R557 and C722.
Page 32. Change GP2,GP3 to R556,R557.
Page 33. Change R486 connect to D_GND.
Page 50. Add Boss10.

(2008/02/01)
Page 49. Change PJ17 from PD13 pin1 to pin2.

(2008/02/02)
Page 17/24. Add FAN error detect function in panel switch pin4.
Page 32. Change C624 to 2.2uF for pop noise issue.

(2008/02/14)
Update some error discription for MOR request.

(2008/02/18)
Page 32. Mount Audio cable short components for EVT test.

(2008/02/20)
Page 32. Change U30 from TI(TPA6017) to GMT(G1431F2U) for pop noise issue.

(2008/02/21)
Page 17. Update Panel ID table.
Page 24. Update R447 CA to Mount,R437 Mount to CA.
Page 46. Add PJ10.
Page 14/15.Add J2/J3 for EMI request.
Page 6. Add reserverve C781,C782,C783 for EMI request.
Page 29. Add reserverve C784,C785 for EMI request.

(2008/02/22)
Page 14/15.Add J4/J5 for EMI request.
Page 6. Add reserverve C786,C787 for EMI request.

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(2008/02/26)
Page 5. Change C75 from 0.3V to 10V for PUR request.
Page 31. Change C201,C205,C609,C681,C143,C168,C596,C601,C605 from 10V to 16V for PUR request.
Page 18/29. Change C32,C35,C449,C450,C725,C726,C727,C728,C729,C730 to 0.1uF,6.3V,10% for PUR request.
Page 31. Change R202 for PUR request.
Page 31. Change Q37,Q39,Q40,Q41 to PBSS2515E.115 for PUR request.
Page 32/33. Change Q6,Q25,Q34,Q38 to MMBT3906K for PUR request.
Page 33. Change Q33/Q44 to MMBT3904 for PUR request.

M790 DVT

(2008/02/28)
Page 33. Change Q33/Q44 to PMBT3904.215 for PUR request.

(2008/03/03)
Page 52. Change R_U1,R_U3 to 8Gbit size and change R_R11,R_R18,R_R25 to mount.

(2008/03/05)
Page 24. Mirror CN3 for Int. keyboard issue.

(2008/03/06)
Page 38. Mirror CN6 for easy A'ssy.
Page 44. Change PR146 to 6.8K,PR147 to 5.49K for OCP current adjust.
Page 45. Change PR193 to 5.49K for OCP current adjust.

(2008/03/24)
For PUR request change C624 2.2uF(1.25mm) to (0.8mm),C510/PC42 0.33uF(X5R) to (X7R).
Page 38. SWAP L60 for CN6 mirror issue.
Page 24/40. Change CHARGE_LED# low enable to H enabe same as M750/M760.

(2008/03/28)
Page 24. Add C722(1000pF) for FAN speed stable.

(2008/04/02)
Page 28/51/52. Change Robson function to no mount.

(2008/04/07)
Page 23. Del PJ4 Change PJ3 type for JIG.
Page 43. Add for charger ocp improve.
Page 43~46,48. A_GND and P_GND change to GP2~6.
Page 47. Add PR134,PR115 for Power Solution- C4 Hang up issue.
Page 47. tpc40b_50 Change to tpc40t_75 power point part for JIG.
Page 49. Add Battery UVP protect for power issue.

(2008/04/08)
Page 29. Add C733,R269,R270/R616(no mount),Change R44 to 4.99Kohm and NC CTRL_LD8 circuit for 88E8057.
Page 32. Change R98,R450,Q8,Q27 to NC and Add R614,R615 no mount for speaker cable short protection circuit.
Page 35/40. Change MS/SD LED to Low active.

(2008/04/11)
Page 31. Change C182,C194 to 0.1uF for MIC THD+N issue
Page 49.Change PR199 to 215Kohm for Battery UVP adjust.

(2008/04/14)
Page 31. Change C626,C662 to 4.7uF for MIC FSIV issue.
Page 29. Design for 88E8055.

Title		History (1)	
Size A3		Document Number M790-1-01	
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HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division			

(2008/04/14)

Page 16. Change L14,L16,L17 to 120ohm/100Mhz bead for EMI EMI issue.

Page 32. Add C797~C800(220pF)for Speaker EMI issue.

Page 37. Change R611,R612 to L26,L28 120ohm/100Mhz bead and C779,C780 to 15pF for Camera EMI issue.

Page 41. Add C790~C79(2200pF)for power board EMI issue.

(2008/04/15)

Page 50. Change Boss7 pin2 connect to RC for EMI reserve and Add Finger SPR1,SPR2 for EMI issue.

Page 32. Add C802~C805(220pF)for Speaker EMI issue.

(2008/04/18)

Page 23. Update ODD connector for MOR request.

Page 26. Add R611,R612 and Change U24,C534 to no mount for MOR request.

(2008/04/21)

Page 19. Change C388,C389 to 15pF for Y6 test fail.

Page 24. Change C245,C246 to 18pF for Y2 test fail.

Page 31. Change C629,C652 to 220pF same as M760.

Page 36. Change C496,C497 to 27pF for Y3 test fail.Change R352,R353,R356,R357 to 100ohm,R590 to 33ohm for MS/SD fail items.

(2008/04/23)

Page 16. Update CN2 VGA connector type.

(2008/04/24)

Page 41. Change U_R1~R4 to NC,U_L1,L2 to mount.

Page 43. Add PC187,and Change PC23,PC33 to 680P for EMI request.

(2008/04/25)

Page 16. Update D11 to SL22 for CRT ripple noise.

(2008/04/28)

Add C806~C808,PC188,PC189(0.1uF) for ESD solution.

Page 27. Change CN14 to FOX_1CX42201-SM for ME request.

Page 36. Change CN11 to Gray color,C459,C488 to X5R and R568,R359,R377 to mount for MOR request.

(2008/04/29)

Page 21. Change C717,C723 to X5R.

(2008/04/30)

Page 26. Change C538 to mount for ripple noise.

Page 37. Change R7/F3 to mount,R9/R4 to NC for 3.3V Camera.

Page 50. Change BOSS3,6,7,10 to 1M-1F50M20-5000 and NC BOSS6.

(2008/05/02)

For MOR request,add F5,F6,F7.

Page 6. Add R618~R621 475ohm for CR# issue.

Page 21. Change C721 to 1uF for Intel design change.

(2008/05/06)

Page 16. Change R418,R424 to 30 ohm for Graphic test fail item.

Page 50. Update PAD1,2 size.

(2008/05/07)

Page 36. Change R590 to 22ohm for SD card clock issue.

Page 37. Add resver regulater IC for Camera power.

Page 41. Add C811 for ESD issue.

Page 50. Add resver C812,R623 for EMI issue.

Page 43. Change PQ5 from AO4433 to SI4825DY-T1-E3 for EMI issue.

Page 47. Add PC190 for EMI issue.

(2008/05/08)

Page 6. Add TP153,TP154 for USB easy measure.

BOM Change

Change CN25 to LN27131-A403-4F.

Change R352,R353,R356,R357 to 68ohm.

Change R601 to mount,F5 to NC.

Change U41,C813~C817,R626,R625,R4 to mount,R7,F3 to NC.

(2008/05/14)

Page 16. Change R418,R424 to 22ohm for CRT issue.

Page 43. Change PL6,PL8 to no mount,Change PL7 to 1000R-100MHZ_0.015R for EMI issue.

Page 47. Change PR115 to no mount.

M790 PVT

(2008/06/03)

Page 25. Add lable1 for BIOS.

Page 39. Change CN8,CN9 to grey color.

(2008/06/11)

Page 23. Delete PJ3 for unnecessary

Page 43. Change PL7 to 1L-FPWC090-7H01 for EMI request.

Page 43. Change PQ1,PQ2 from17-S134240-VT00 to 17-S13424B-DV00 for PUR request.

Page 43. Delete PR20 for unnecessary

Page 44. Delete PJ18,PJ19 for unnecessary

Page 45. Delete PJ12,PJ13,PJ14,PJ20 ,PJ21for unnecessary

Page 46. Delete PJ6,PJ7,PJ8,PJ9 ,PJ10 for unnecessary

Page 47. Delete PJ11,PR97,PR89,PR105,PR108 for unnecessary

Page 48. Delete PR77,PR72,PR65 for unnecessary

Page 49. Delete PR12,PR38,PR46 for unnecessary

(2008/06/16)

Page 32. Change F5 to 1.5A and to mount for MOR request.

(2008/06/23)

Page 41. Change C790~C796,C811 to 220pF for EMI and Hotkey issue.

Page 41. Add Fuse(F8) in power source on power board for short test.

Page 43. Mount PC156,PC157 and Add PC191 for EMI issue.

Page 50. Change BOSS7,10 connect to GND.

(2008/06/24)

Page 40. Change LED1,LED2,LED6 to HT-110UY,LED5 to HT-110UD.R383,R598 to 120ohm for MOR request.

Page 41. Change P_LED1~P_LED3 to HT-150YG.P_R1~P_R3 to 100 ohm for MOR request.

(2008/07/02)

Page 31. Change C184,C192 to 4.7uf 0603 for MIC THD+N issue this change same as M761.

Add ESD solution C810,C812,C818,PC192(0.1uF).

M790 MP

(2008/07/24)

Page 25. Change CN26,U11,C308,R221 to no mount,R233 to mount.

Page 40. Change R385 to 33ohm,R383,R598,R599 to 68ohm for LED bright issue.

FOXCONN			HON HAI Precision Ind. Co., Ltd.	
History (2)			CCPBG - R&D Division	
Title				
Size	Document Number			Rev
A3	M790-1-01			1.0
Date:	Friday, July 25, 2008	Sheet	54	of 54