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19	LAN TRANSFORMER	1.0	07/28	54		
20	CLOCK GEN	1.0	07/28	55		
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27	EC+KBC	1.0	07/28			
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32	OIDE	1.0	07/28			
33	AUDIO(CODEC & POWER)	1.0	07/28			
34	AUDIO(AMP & HP & SPK)	1.0	07/28			
35	AUDIO(EXTMIC)	1.0	07/28			

P. Leader	Check by	Design by

Project Code & Schematics Subject: MS70 Main Board

PCB P/N: (FUBAI) 1P-0067100-6010
(NAN YA) 1P-0067200-6010
(HANSTAR) 1P-0067500-6010

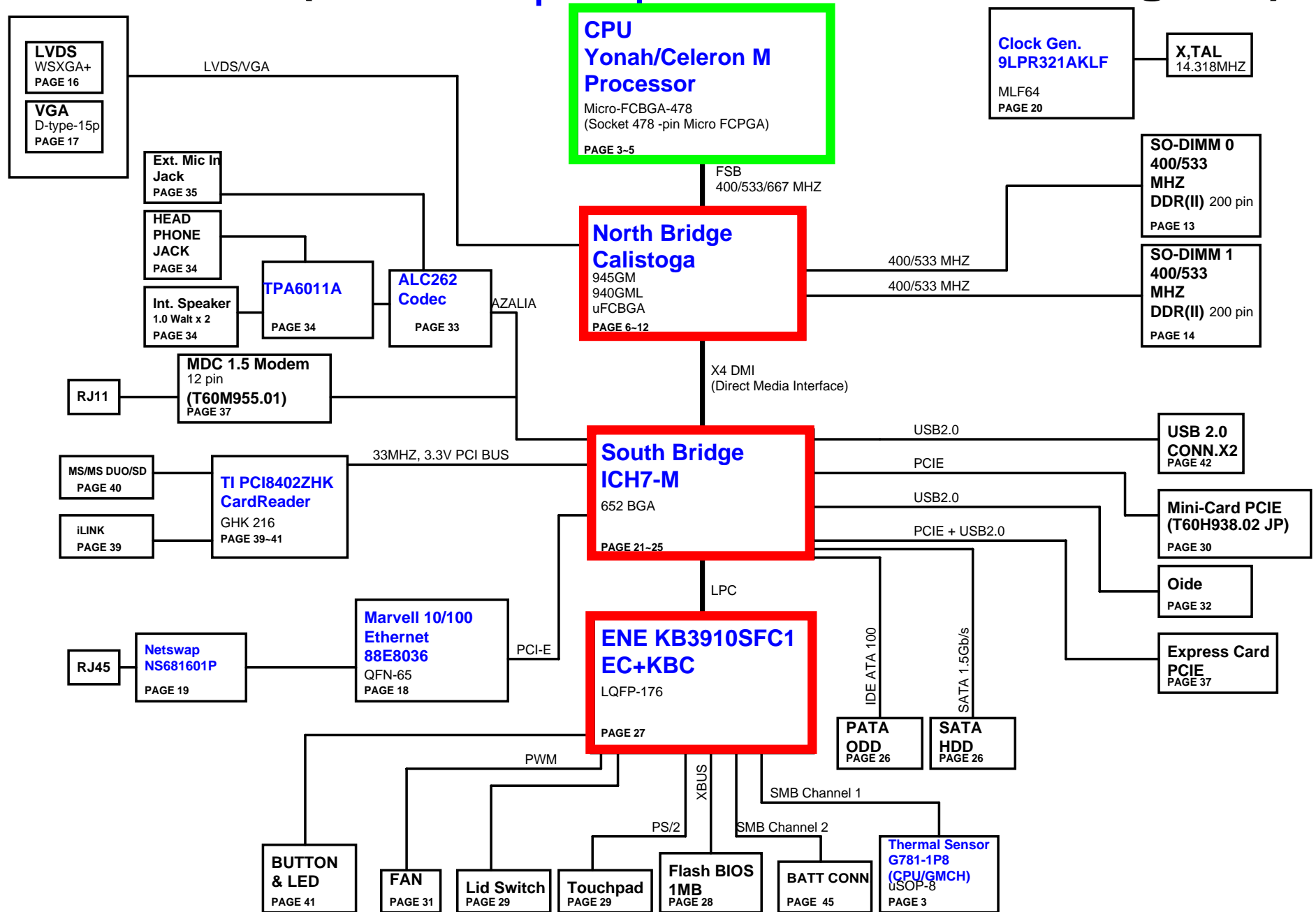
FOXCONN HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

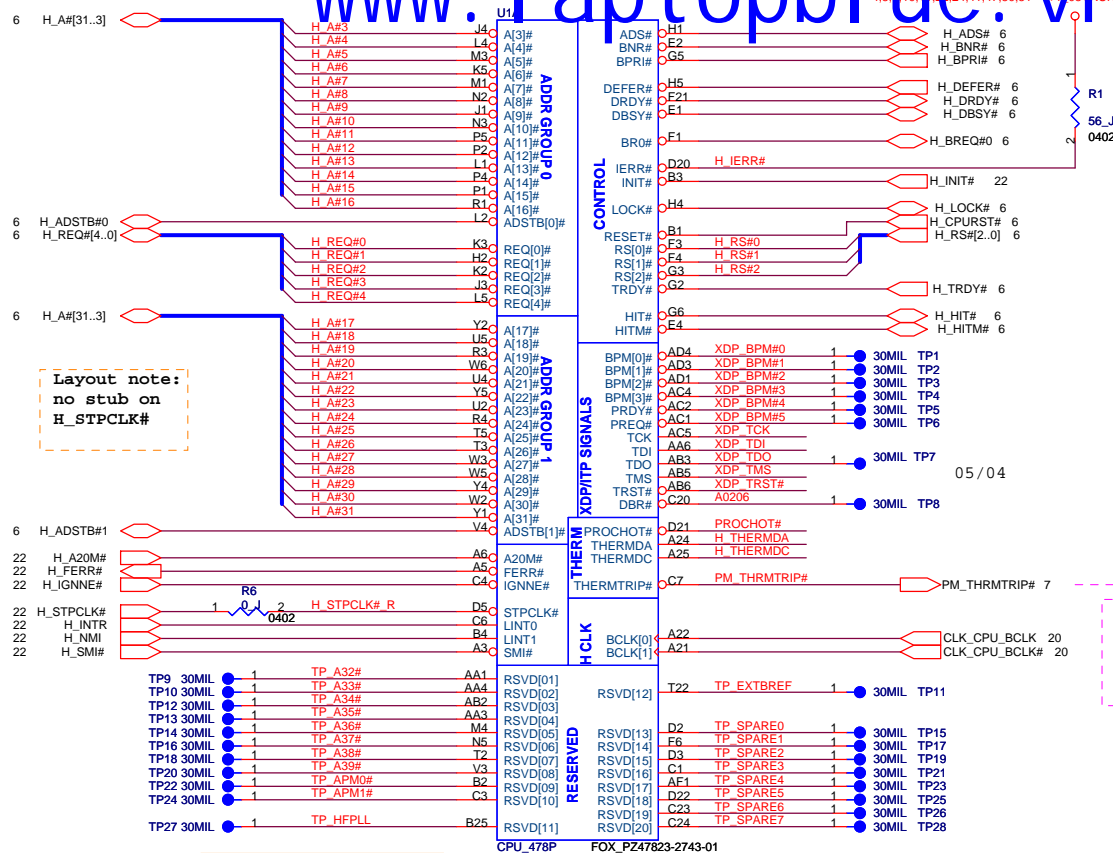
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RAPTOR/MS70(CALISTO GML) Block Diagram

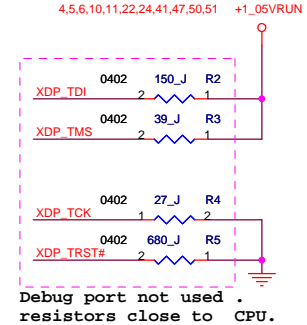




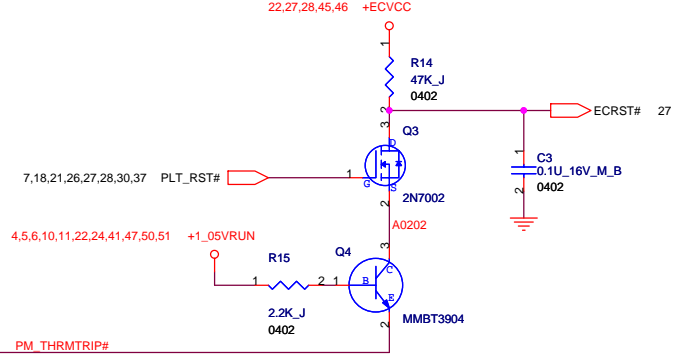
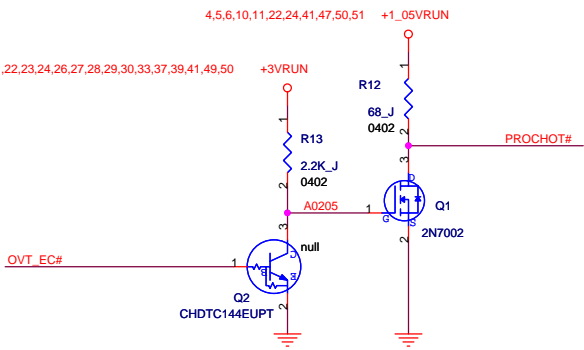
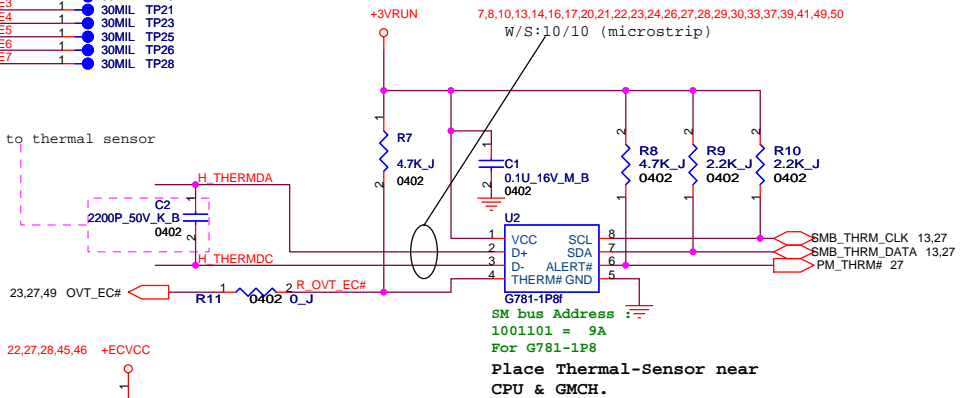
Layout note:
no stub on
H_STPCLK#

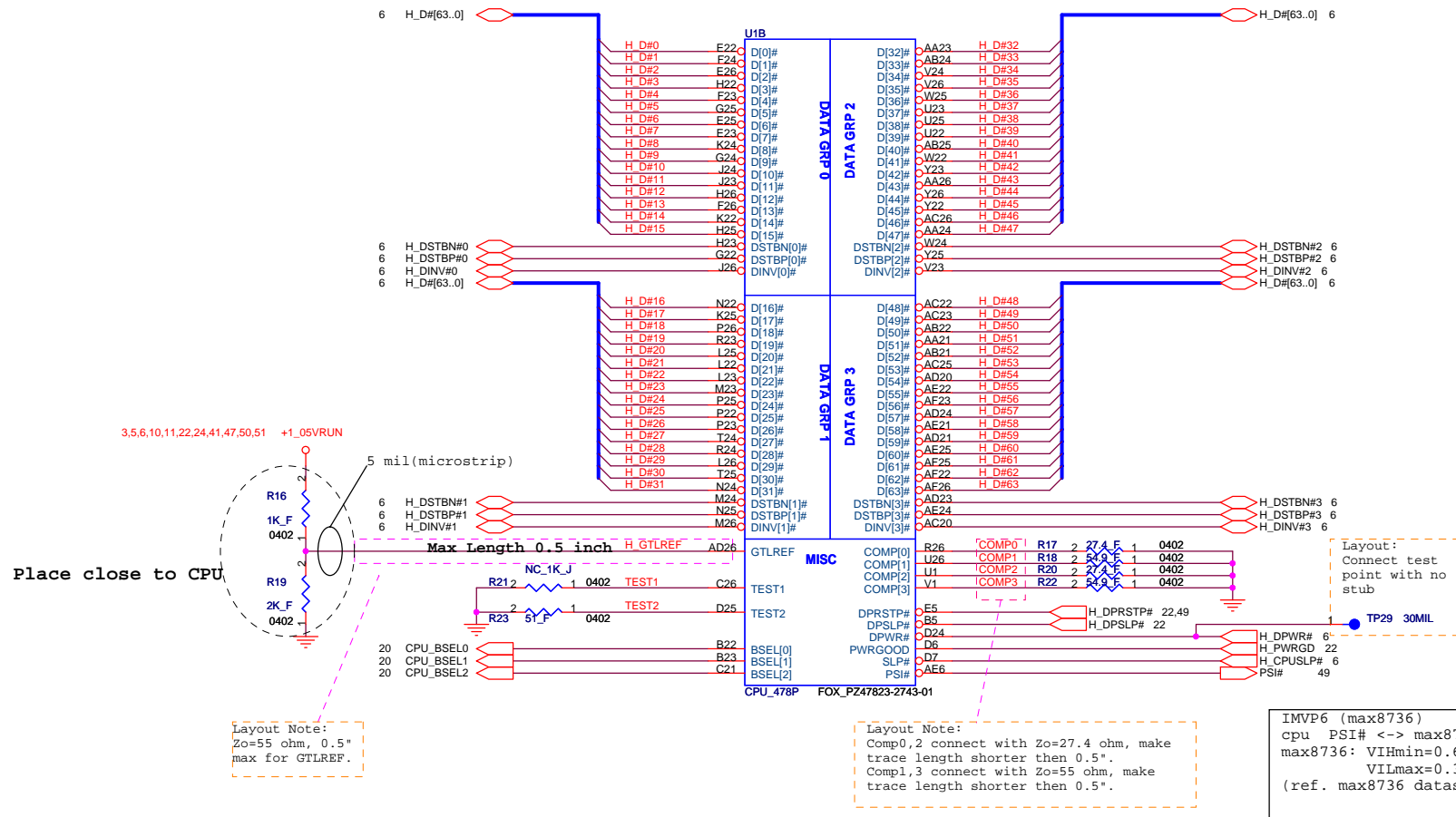
A#[32-39], APM#[0-1]:
Leave escape routing
on for future
functionality

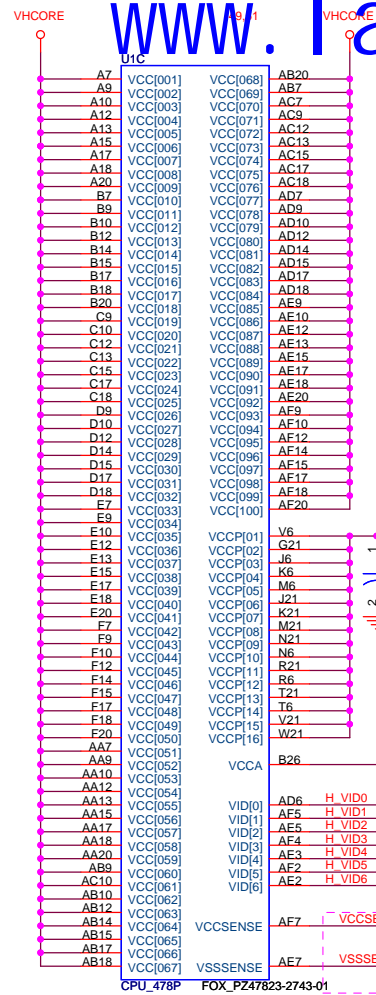
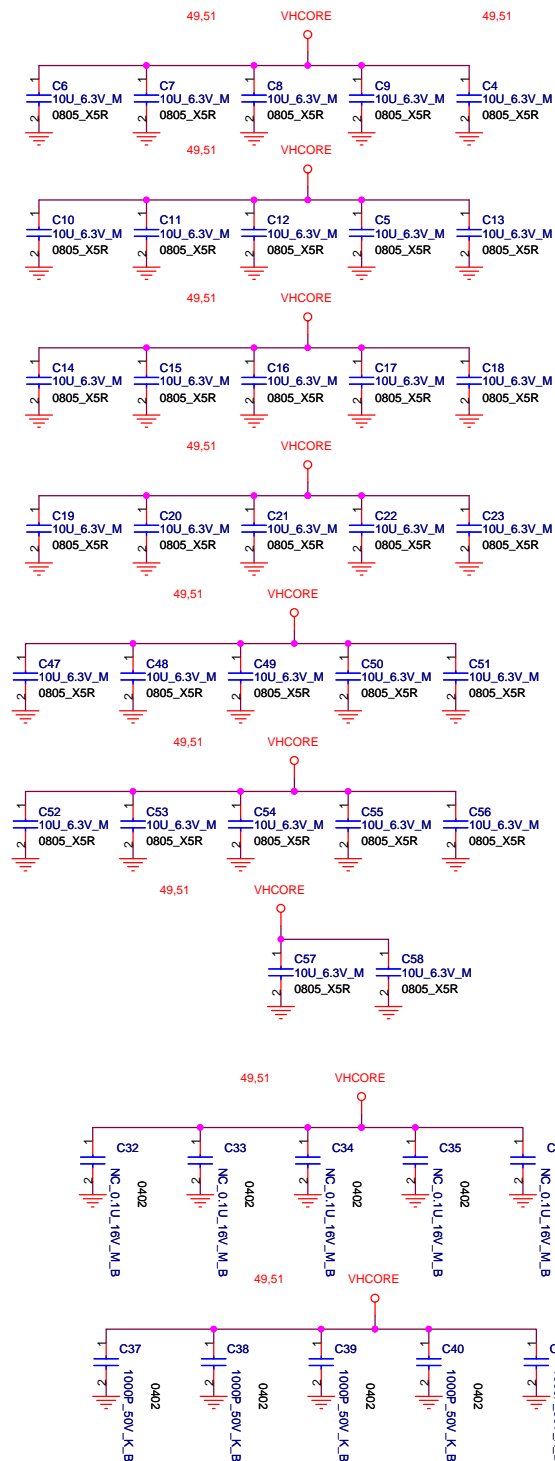
ICH7M's GPIO12: VIL----> -0.5V ~ 0.8V
VIH----> 2.0V ~ 3.3+0.5V
YONAH's PROCHOT#: VIL----> -0.1V ~ 0.3*VCCP
VIH----> 0.7*VCCP ~ VCCP+0.1



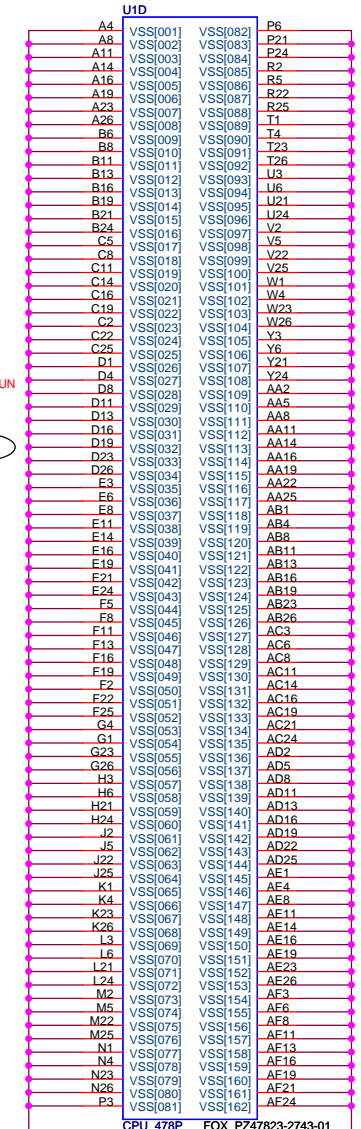
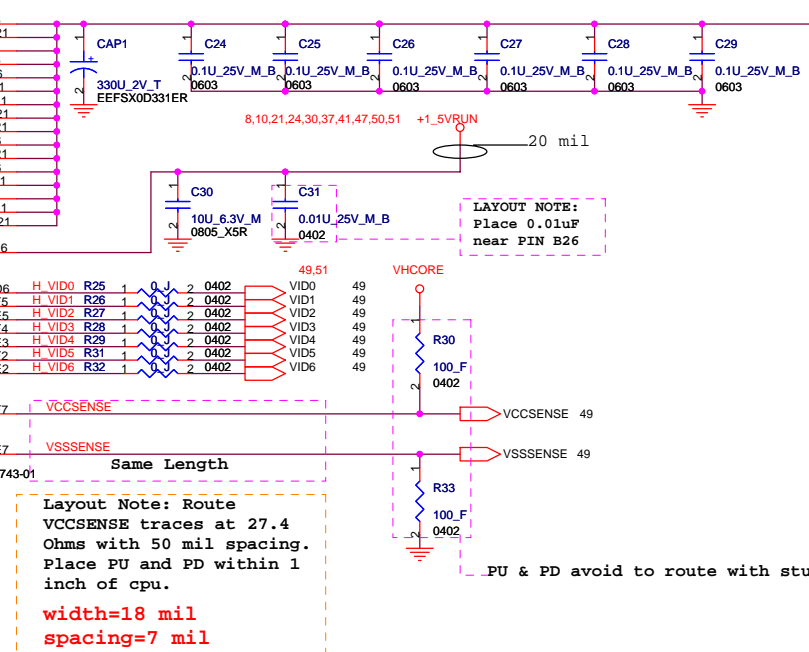
PM_THRMTRIP#
should connect to
ICH7-M and GMCH
without T-ing (No
stub)

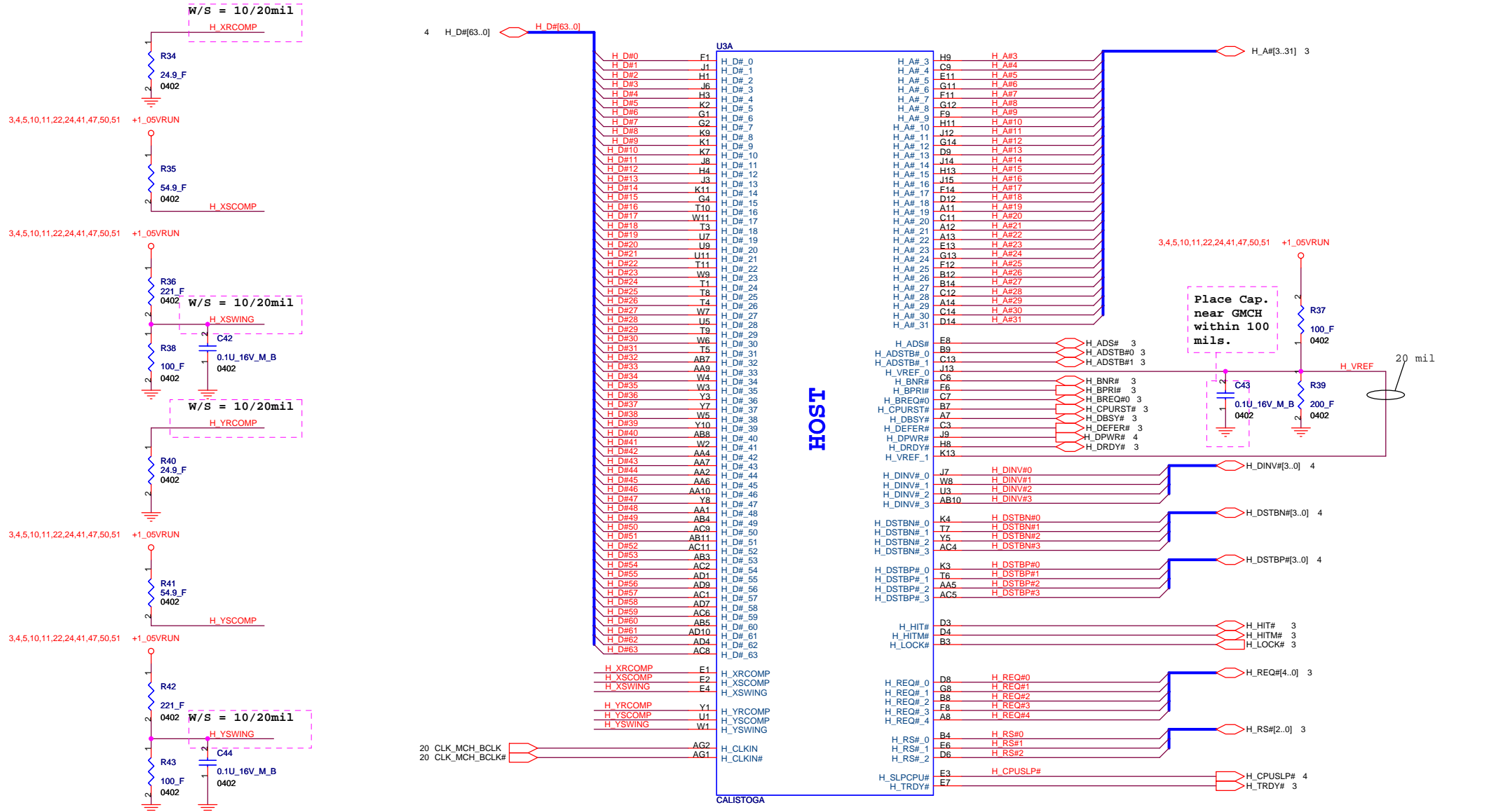


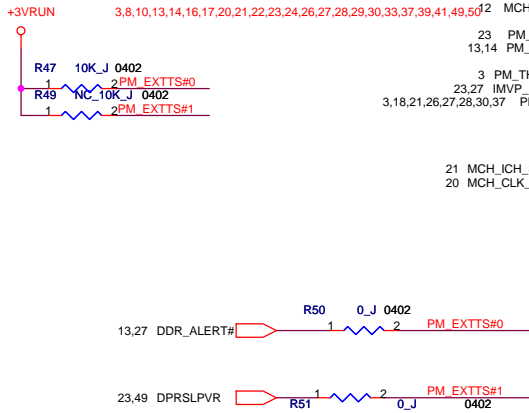
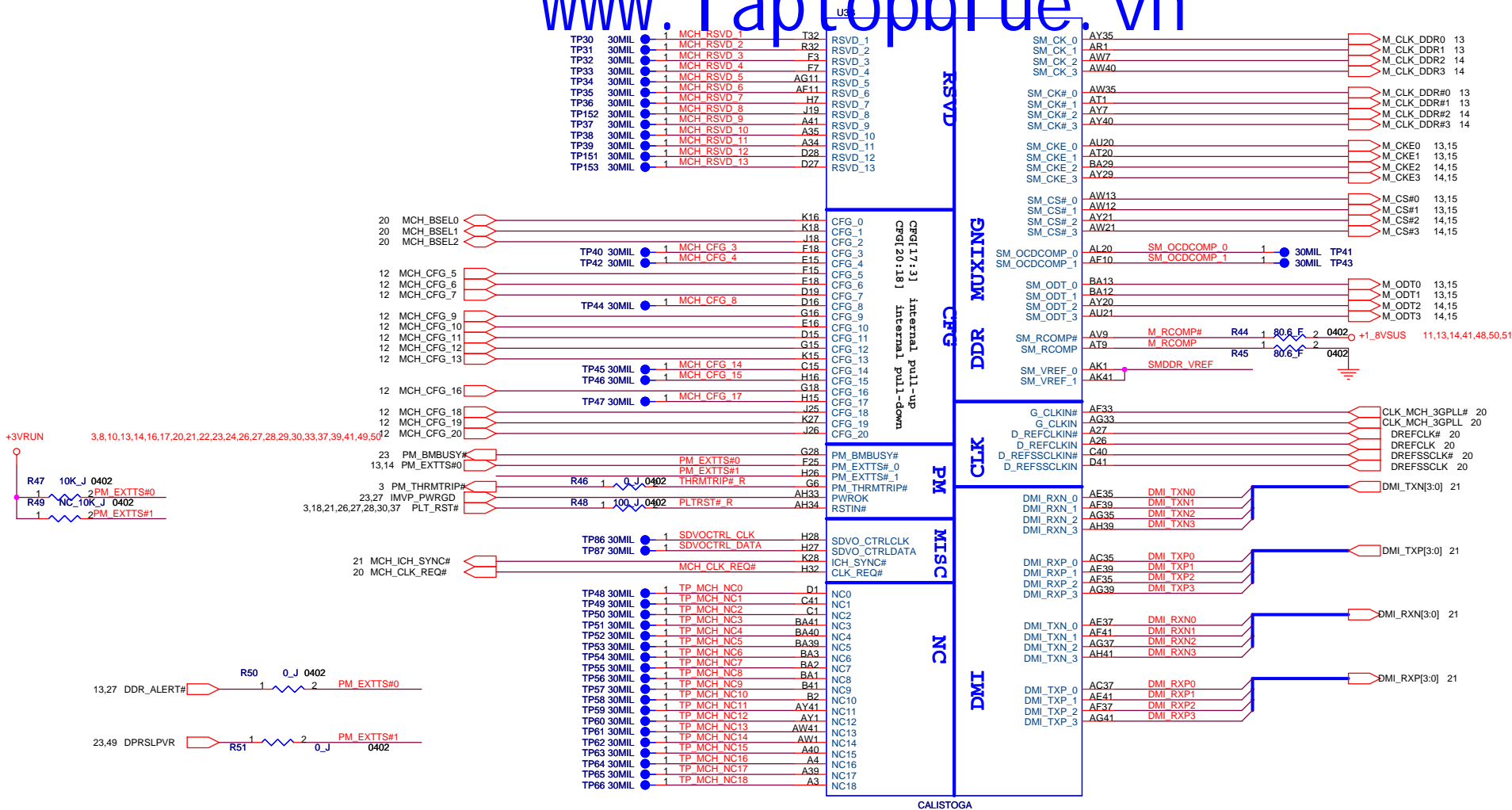




CPU_VCCA---->120mA
CPU_VCCP----->2.5A
CPU_VCC----->36A

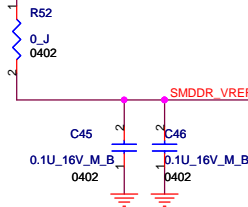


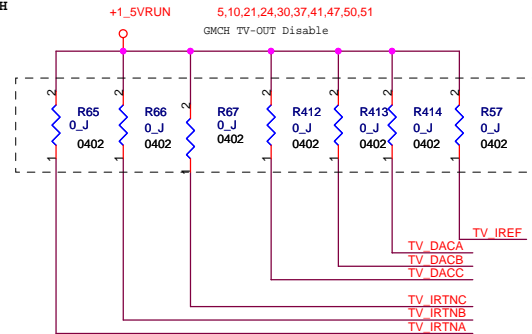
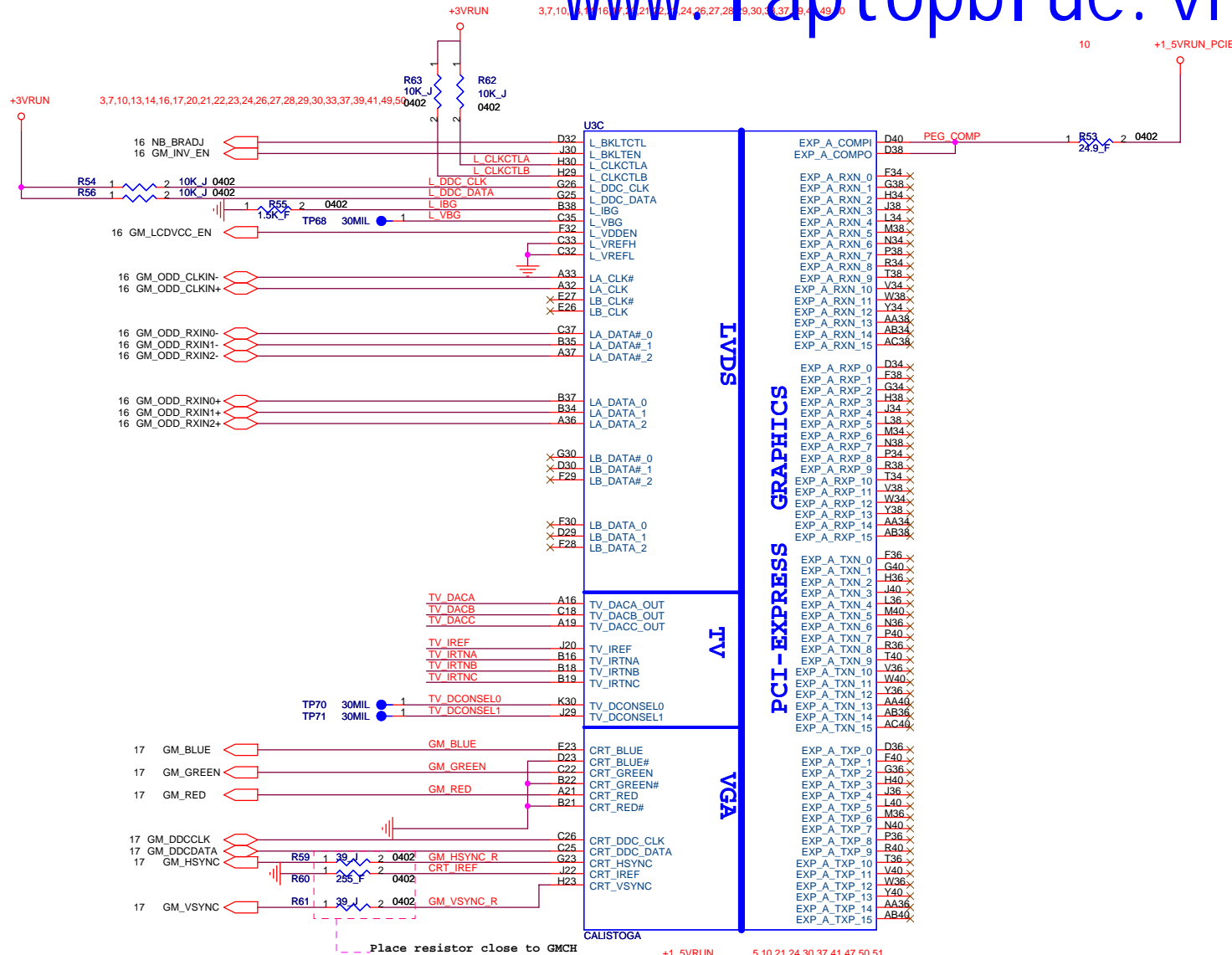


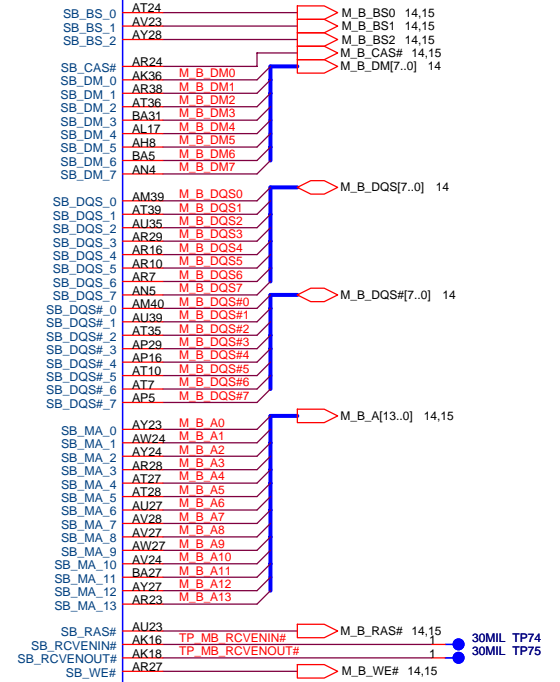
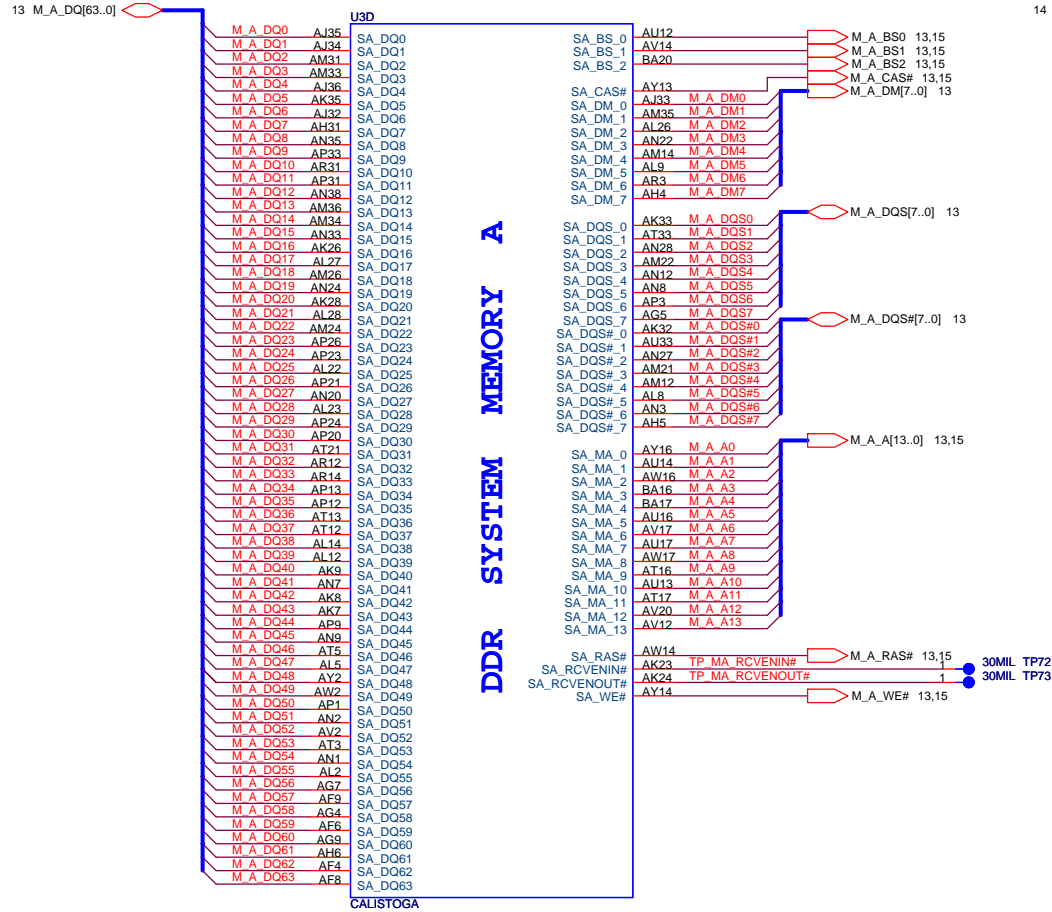


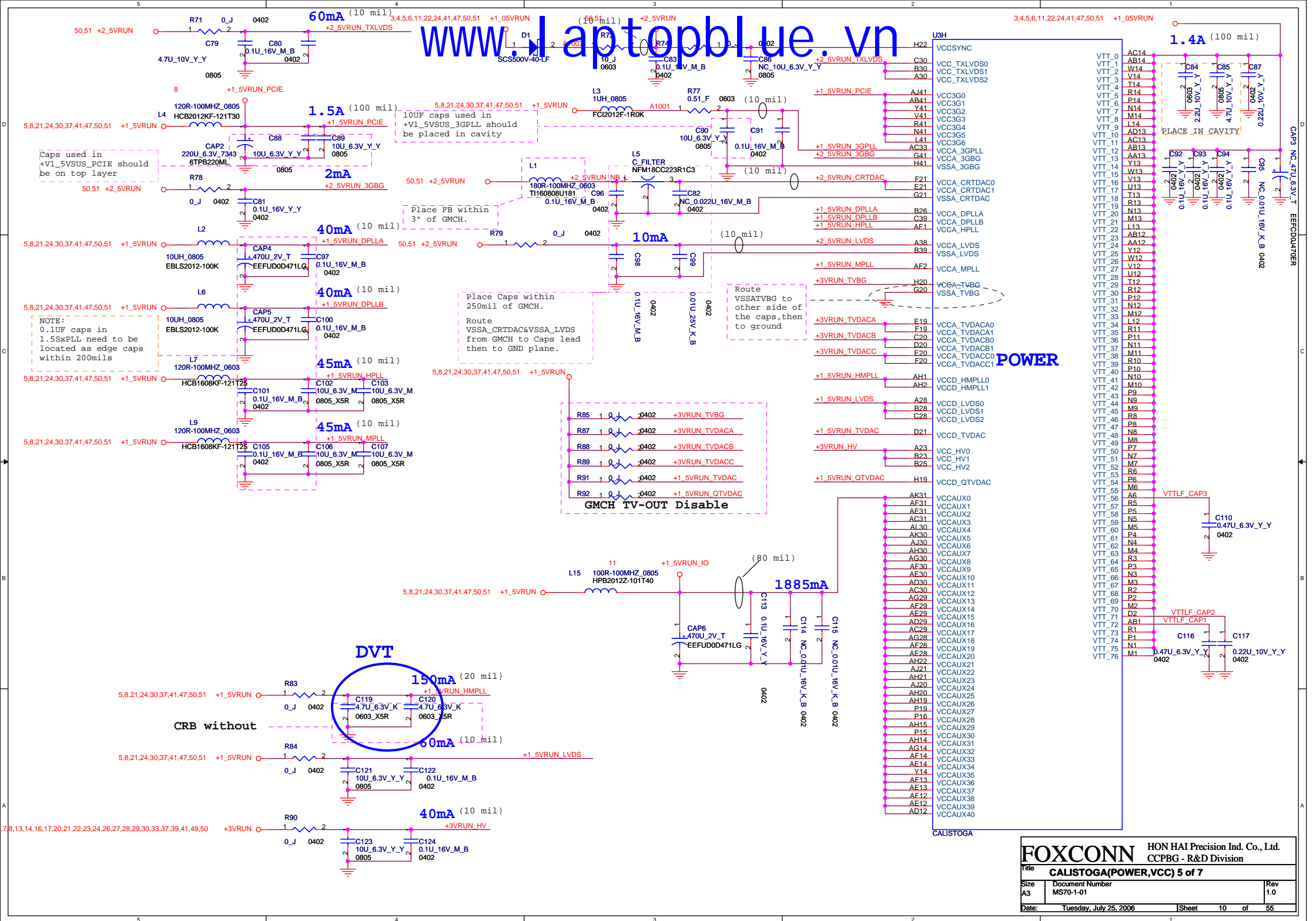
13,48 DDRDIMM_VREF

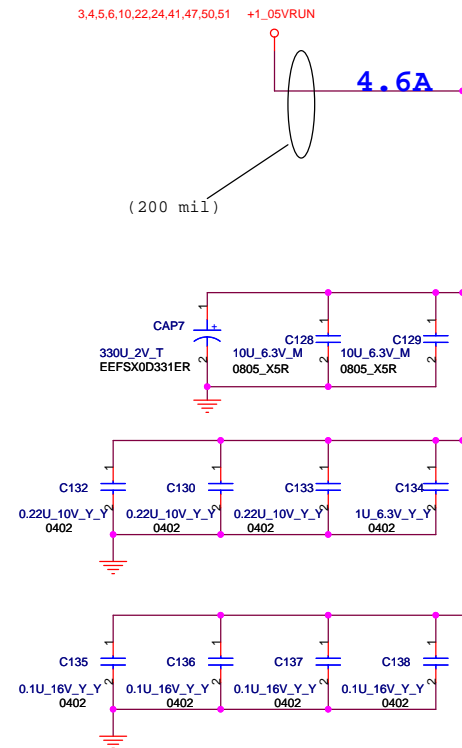
Place close to chipset





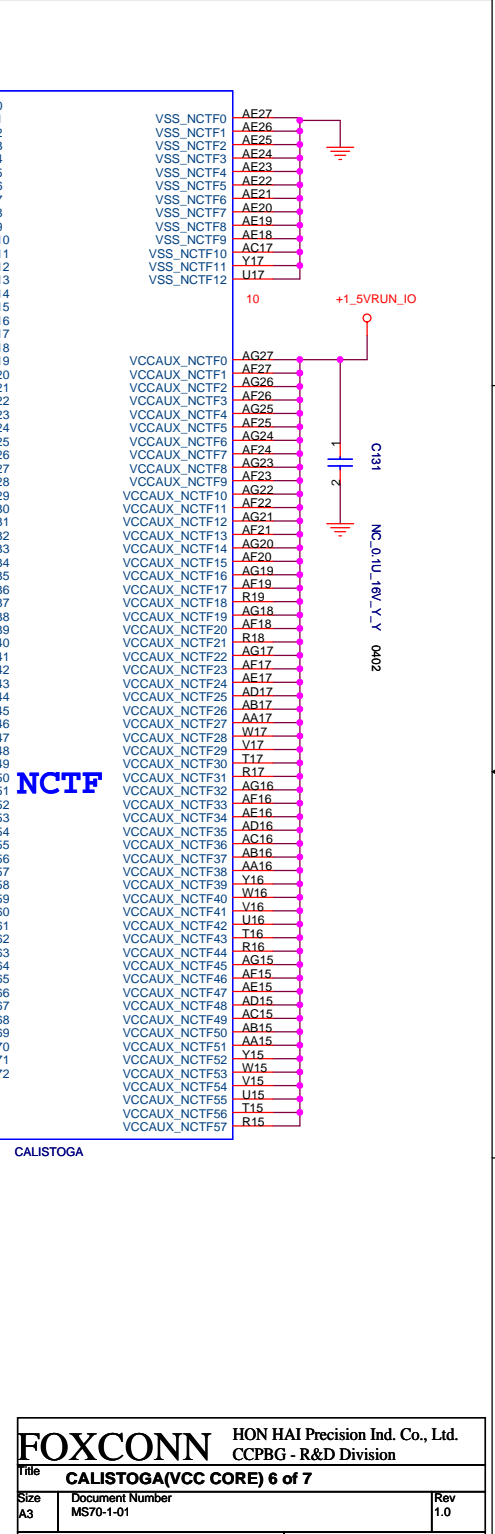
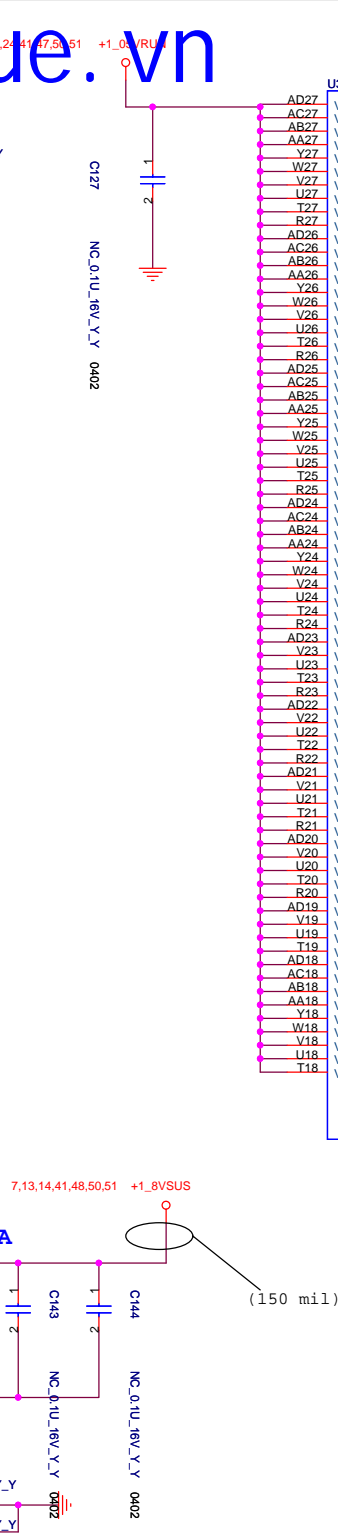
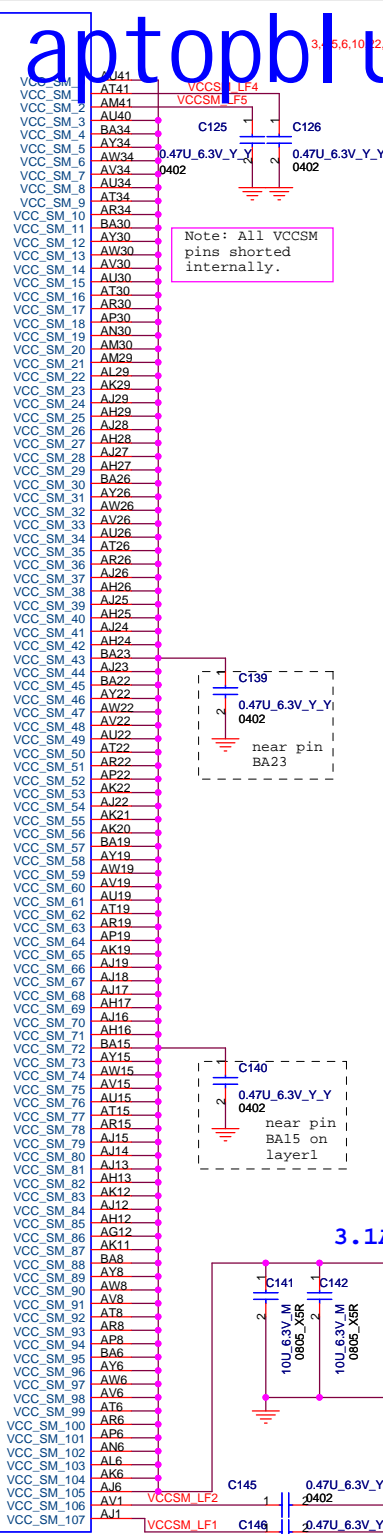






- CALISTOGA
- AA33 VCC_0
 - W33 VCC_1
 - P33 VCC_2
 - N33 VCC_3
 - L33 VCC_4
 - J33 VCC_5
 - AA32 VCC_6
 - Y32 VCC_7
 - W32 VCC_8
 - V32 VCC_9
 - P32 VCC_10
 - N32 VCC_11
 - M32 VCC_12
 - L32 VCC_13
 - J32 VCC_14
 - AA31 VCC_15
 - W31 VCC_16
 - P31 VCC_17
 - N31 VCC_18
 - M31 VCC_19
 - W30 VCC_20
 - V30 VCC_21
 - T30 VCC_22
 - R30 VCC_23
 - P30 VCC_24
 - N30 VCC_25
 - M30 VCC_26
 - L30 VCC_27
 - AA29 VCC_28
 - Y29 VCC_29
 - W29 VCC_30
 - V29 VCC_31
 - T29 VCC_32
 - R29 VCC_33
 - P29 VCC_34
 - N29 VCC_35
 - M29 VCC_36
 - L29 VCC_37
 - AA28 VCC_38
 - Y28 VCC_39
 - W28 VCC_40
 - V28 VCC_41
 - T28 VCC_42
 - R28 VCC_43
 - P28 VCC_44
 - N28 VCC_45
 - M28 VCC_46
 - L28 VCC_47
 - AA27 VCC_48
 - Y27 VCC_49
 - W27 VCC_50
 - V27 VCC_51
 - T27 VCC_52
 - R27 VCC_53
 - P27 VCC_54
 - N27 VCC_55
 - M27 VCC_56
 - L27 VCC_57
 - AA26 VCC_58
 - Y26 VCC_59
 - W26 VCC_60
 - V26 VCC_61
 - T26 VCC_62
 - R26 VCC_63
 - P26 VCC_64
 - N26 VCC_65
 - M26 VCC_66
 - L26 VCC_67
 - AA25 VCC_68
 - Y25 VCC_69
 - W25 VCC_70
 - V25 VCC_71
 - T25 VCC_72
 - R25 VCC_73
 - P25 VCC_74
 - N25 VCC_75
 - M25 VCC_76
 - L25 VCC_77
 - AA24 VCC_78
 - Y24 VCC_79
 - W24 VCC_80
 - V24 VCC_81
 - T24 VCC_82
 - R24 VCC_83
 - P24 VCC_84
 - N24 VCC_85
 - M24 VCC_86
 - L24 VCC_87
 - AA23 VCC_88
 - Y23 VCC_89
 - W23 VCC_90
 - V23 VCC_91
 - T23 VCC_92
 - R23 VCC_93
 - P23 VCC_94
 - N23 VCC_95
 - M23 VCC_96
 - L23 VCC_97
 - AA22 VCC_98
 - Y22 VCC_99
 - W22 VCC_100
 - V22 VCC_101
 - T22 VCC_102
 - R22 VCC_103
 - P22 VCC_104
 - N22 VCC_105
 - M22 VCC_106
 - L22 VCC_107
 - AA21 VCC_108
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 - W21 VCC_110
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 - R21 VCC_113
 - P21 VCC_114
 - N21 VCC_115
 - M21 VCC_116
 - L21 VCC_117
 - AA20 VCC_118
 - Y20 VCC_119
 - W20 VCC_120
 - V20 VCC_121
 - T20 VCC_122
 - R20 VCC_123
 - P20 VCC_124
 - N20 VCC_125
 - M20 VCC_126
 - L20 VCC_127
 - AA19 VCC_128
 - Y19 VCC_129
 - W19 VCC_130
 - V19 VCC_131
 - T19 VCC_132
 - R19 VCC_133
 - P19 VCC_134
 - N19 VCC_135
 - M19 VCC_136
 - L19 VCC_137
 - AA18 VCC_138
 - Y18 VCC_139
 - W18 VCC_140
 - V18 VCC_141
 - T18 VCC_142
 - R18 VCC_143
 - P18 VCC_144
 - N18 VCC_145
 - M18 VCC_146
 - L18 VCC_147
 - AA17 VCC_148
 - Y17 VCC_149
 - W17 VCC_150
 - V17 VCC_151
 - T17 VCC_152
 - R17 VCC_153
 - P17 VCC_154
 - N17 VCC_155
 - M17 VCC_156
 - L17 VCC_157
 - AA16 VCC_158
 - Y16 VCC_159
 - W16 VCC_160
 - V16 VCC_161
 - T16 VCC_162
 - R16 VCC_163
 - P16 VCC_164
 - N16 VCC_165
 - M16 VCC_166
 - L16 VCC_167

VCC



7 MCH_CFG_5 1 30MIL TP76

MCH_CFG_5
Low = DMIX2
High = DMIX4

MCH_CFG_18
(VCC_CORE Select)
Low = 1.05V(default)
High = 1.5V

7 MCH_CFG_18 1 30MIL TP79

7 MCH_CFG_6 1 30MIL TP77

MCH_CFG_6
Low = Moby Dick
High = Calistoga
DDR2 select (default high)

MCH_CFG_19
(DMI LANE REVERSAL)
Low = Normal(default)
High = LANES REVERSED

7 MCH_CFG_19 1 30MIL TP80

7 MCH_CFG_7 1 30MIL TP78

MCH_CFG_7
(CPU Strap)
Low = RSVD
High = Mobile Yonah processor

MCH_CFG_20
(PCIe Backward Interoperability mode)
Low = Only SDVO or PCIE x1 is operational (defaults)
High = SDVO and PCIE x1 are operating simultaneously via the PEG port

7 MCH_CFG_20 1 30MIL TP83

7 MCH_CFG_9 1 30MIL TP81

MCH_CFG_9
(PCIe Graphics Lane)
Low = Reverse Lane
High = Normal operation

For layout convenience

7 MCH_CFG_10 1 30MIL TP82

MCH_CFG_10
(HOST PLL VCC SELECT)
Low = RESERVED
High = MOBILITY

Layout Noe:
Location of all MCH_CFG strap resistors needs to be close to trace to minimize stub

7 MCH_CFG_11 1 30MIL TP84

MCH_CFG_11
(PSB 4x CLK ENABLE)
Low = Calistoga
High = Reserved

R93
NC 2.2K_Ω
0402

7 MCH_CFG_12 1 30MIL TP84

7 MCH_CFG_13 1 30MIL TP85

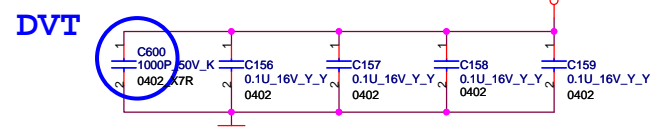
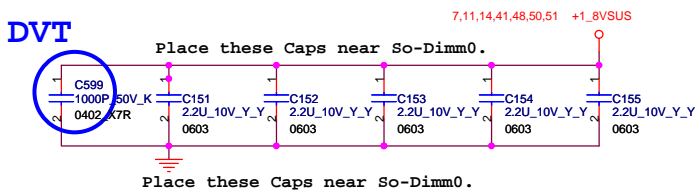
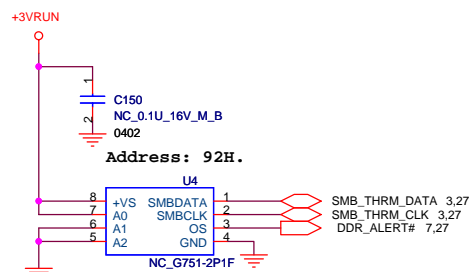
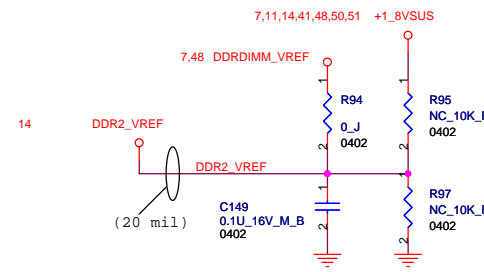
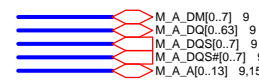
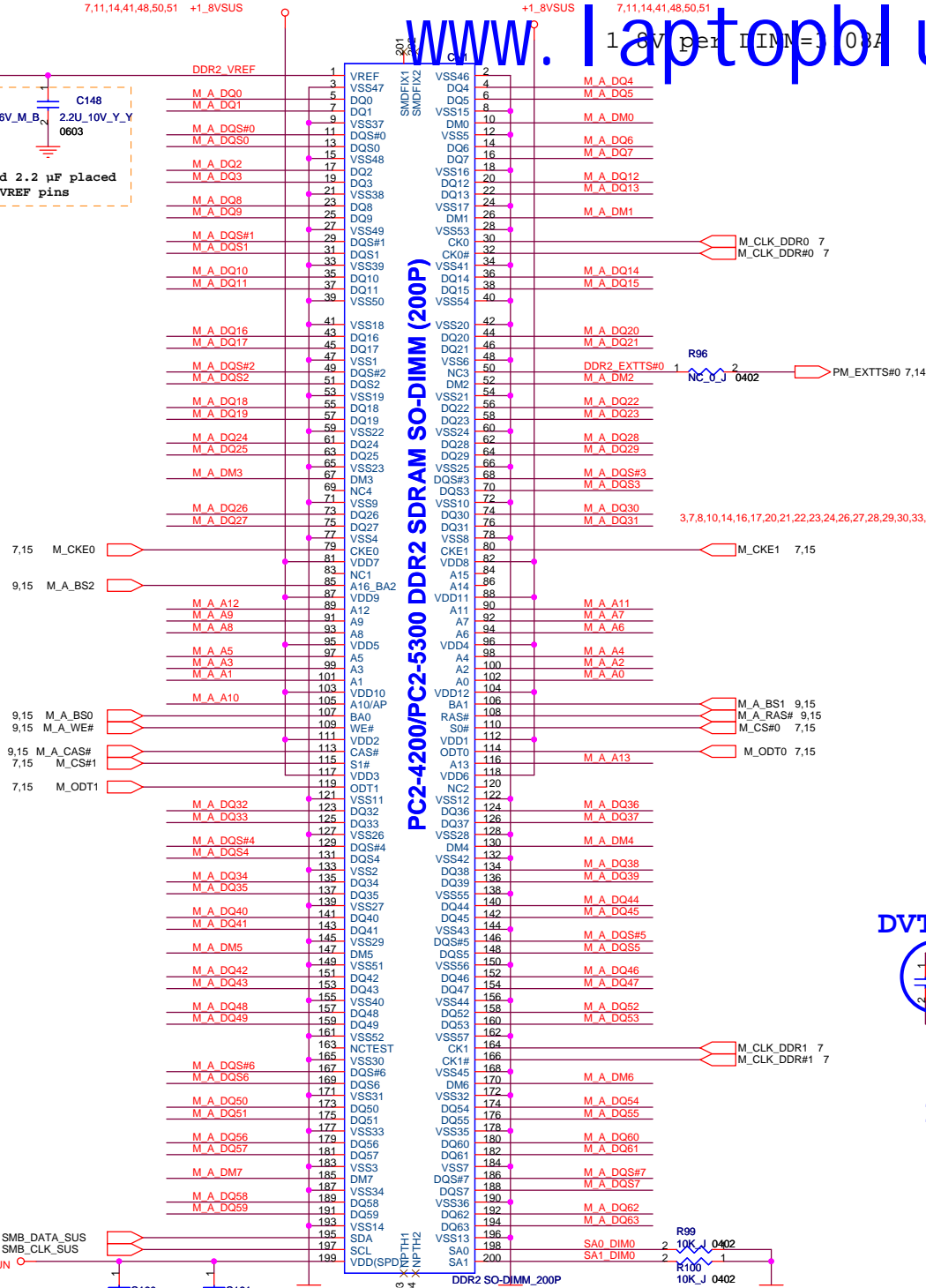
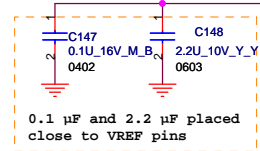
MCH_CFG [13:12]
(XOR/ALLZ)
00=Partial Clock Gating Disable
01=XOR Mode Enable
10=All-Z Mode Enable
11=Normal Operation(Default)

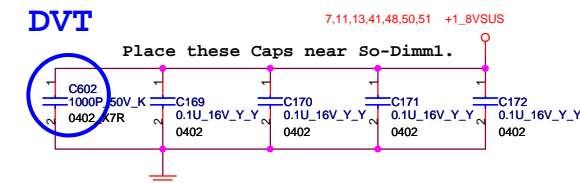
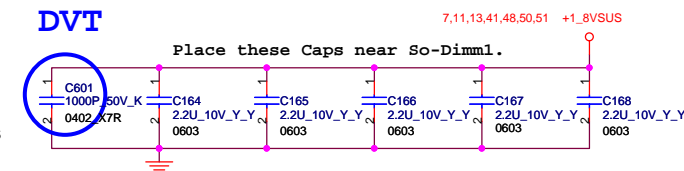
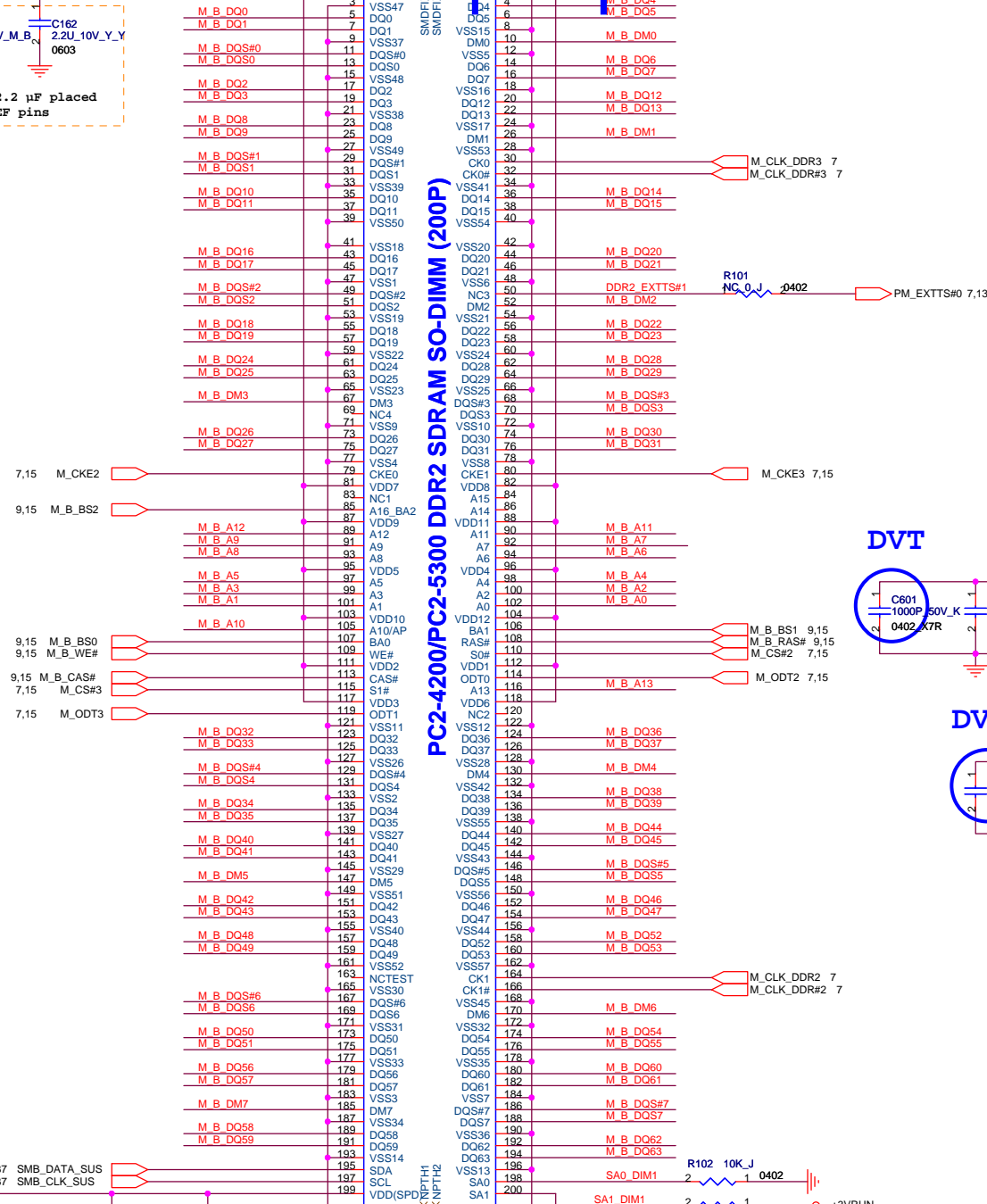
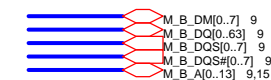
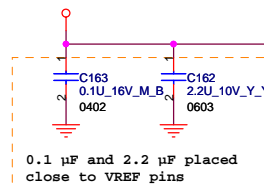
7 MCH_CFG_16 1 30MIL TP160

MCH_CFG_16
(FSB Dynamic ODT)
Low = Dynamic ODT Disabled
High = Dynamic ODT Enable

AC34	VSS_97	AK34
AA41	VSS_98	AG34
W41	VSS_99	AF34
T41	VSS_100	AE34
P41	VSS_101	AC34
M41	VSS_102	C34
J41	VSS_103	AW33
F41	VSS_104	AV33
AV40	VSS_105	AR33
AP40	VSS_106	AE33
AN40	VSS_107	AE33
AK40	VSS_108	Y33
W40	VSS_109	G22
T40	VSS_110	T33
P40	VSS_111	R33
M40	VSS_112	M33
J40	VSS_113	H33
F40	VSS_114	B421
AV39	VSS_115	F33
AP39	VSS_116	D33
AN39	VSS_117	B33
AK39	VSS_118	AH32
W39	VSS_119	AG32
T39	VSS_120	AF32
P39	VSS_121	AE32
M39	VSS_122	AC32
J39	VSS_123	AB32
F39	VSS_124	G32
AV38	VSS_125	B32
AP38	VSS_126	AY31
AN38	VSS_127	AR20
AK38	VSS_128	AN31
W38	VSS_129	AA20
T38	VSS_130	AA20
P38	VSS_131	AB31
M38	VSS_132	Y31
J38	VSS_133	AB30
F38	VSS_134	E30
AV37	VSS_135	AT29
AP37	VSS_136	AN29
AN37	VSS_137	AB29
AK37	VSS_138	T29
W37	VSS_139	N29
T37	VSS_140	K29
P37	VSS_141	G29
M37	VSS_142	E29
J37	VSS_143	C29
F37	VSS_144	B29
AV36	VSS_145	A29
AP36	VSS_146	BA28
AN36	VSS_147	AM28
AK36	VSS_148	AP28
W36	VSS_149	AM28
T36	VSS_150	AD28
P36	VSS_151	AC28
M36	VSS_152	W28
J36	VSS_153	J28
F36	VSS_154	E28
AV35	VSS_155	AP27
AP35	VSS_156	AM27
AN35	VSS_157	AK27
AK35	VSS_158	J27
W35	VSS_159	G27
T35	VSS_160	F27
P35	VSS_161	C27
M35	VSS_162	B27
J35	VSS_163	AN26
F35	VSS_164	M26
AV34	VSS_165	K26
AP34	VSS_166	AA14
AN34	VSS_167	D26
AK34	VSS_168	AK25
W34	VSS_169	P25
T34	VSS_170	K25
P34	VSS_171	H25
M34	VSS_172	AR13
J34	VSS_173	D25
F34	VSS_174	A25
AV33	VSS_175	BA24
AP33	VSS_176	AU24
AN33	VSS_177	P13
AK33	VSS_178	AL24
W33	VSS_179	AW23
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AT23	VSS_180	J11
AN23	VSS_181	D11
AM23	VSS_182	B11
AH23	VSS_183	AV10
AC23	VSS_184	AP10
W23	VSS_185	AL10
K23	VSS_186	AJ10
J23	VSS_187	AG10
F23	VSS_188	AC10
C23	VSS_189	W10
CA22	VSS_190	U10
G22	VSS_191	BA9
F22	VSS_192	AW9
E22	VSS_193	AR9
D22	VSS_194	AH9
A22	VSS_195	AB9
BA21	VSS_196	Y9
AR21	VSS_197	R9
AN21	VSS_198	G9
AL21	VSS_199	E9
AB21	VSS_200	A9
Y21	VSS_201	AG8
P21	VSS_202	AD8
K21	VSS_203	AA8
J21	VSS_204	UR
H21	VSS_205	K8
C21	VSS_206	C8
AW20	VSS_207	BA7
AR20	VSS_208	AV7
AN20	VSS_209	AP7
AA20	VSS_210	AL7
K20	VSS_211	AH7
B20	VSS_212	AF7
A20	VSS_213	AC7
AN19	VSS_214	R7
E20	VSS_215	G7
W19	VSS_216	D7
K19	VSS_217	VSS
G19	VSS_218	AG6
C19	VSS_219	AD6
AH18	VSS_220	AB6
K22	VSS_221	Y6
H18	VSS_222	U6
D18	VSS_223	N6
A18	VSS_224	H6
B29	VSS_225	B6
AR17	VSS_226	AV5
AP17	VSS_227	AE5
AM17	VSS_228	AD5
AK17	VSS_229	AY4
AV16	VSS_230	AR4
AN16	VSS_231	AP4
AL16	VSS_232	AL4
J16	VSS_233	AJ4
F16	VSS_234	Y4
C16	VSS_235	U4
CA15	VSS_236	R4

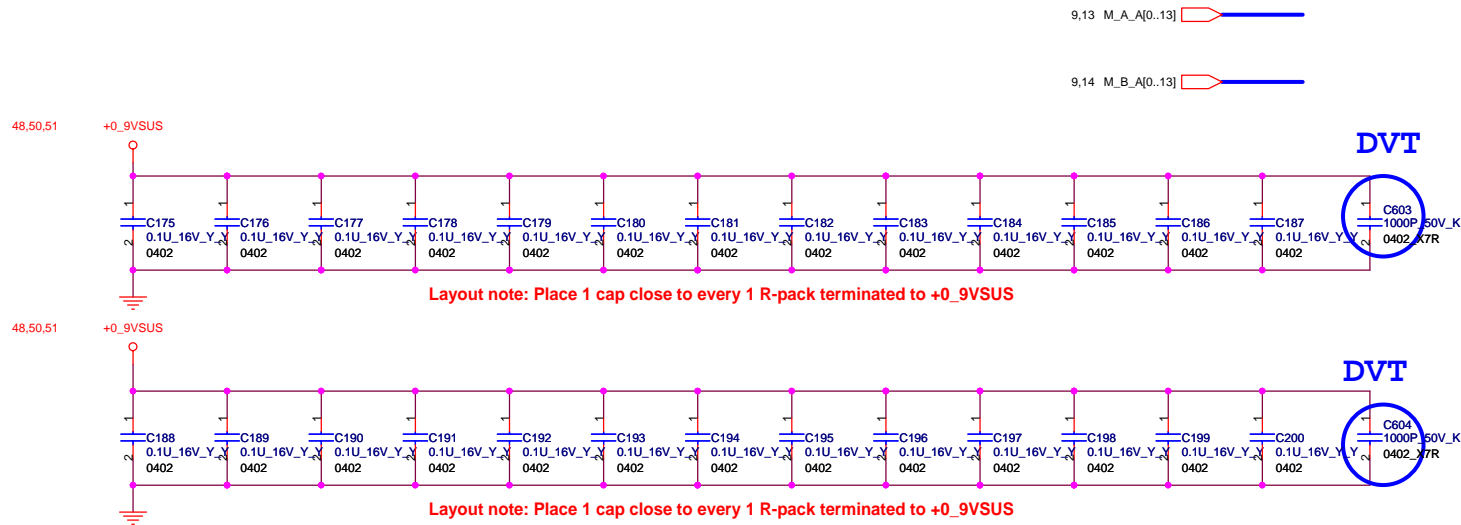




FOX_ASOA426_N4SC_4F

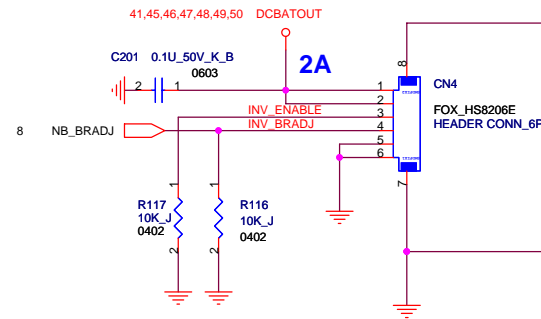
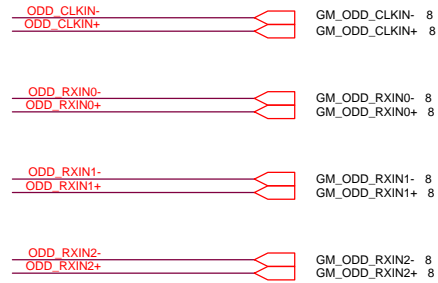
SMBus Address: A4(W)/A5(R)

DIMM 1 is placed farther from the GMCH than DIMM 0

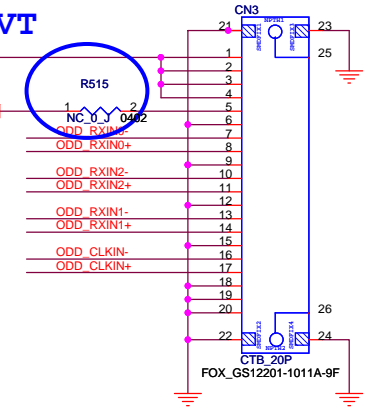


LVDS

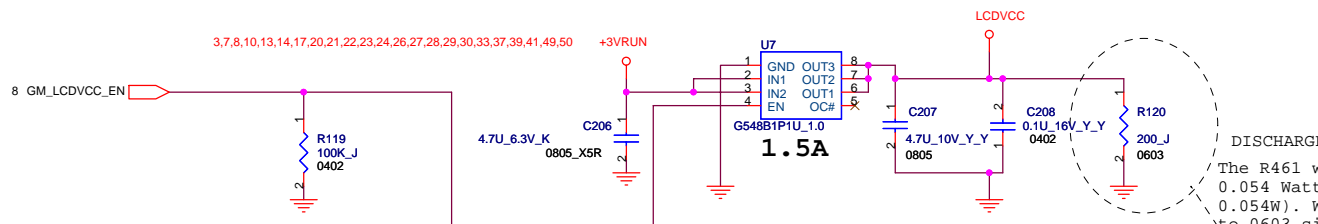
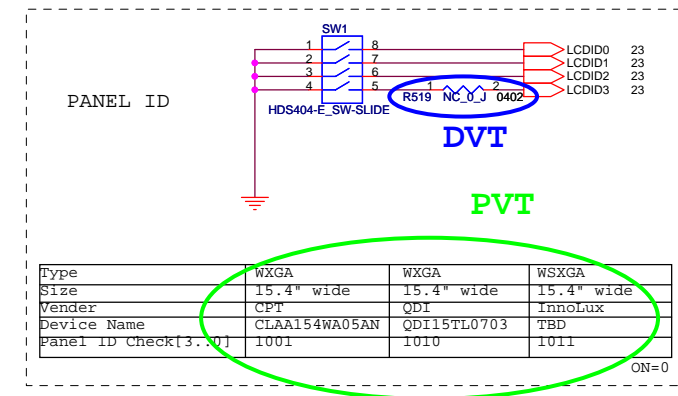
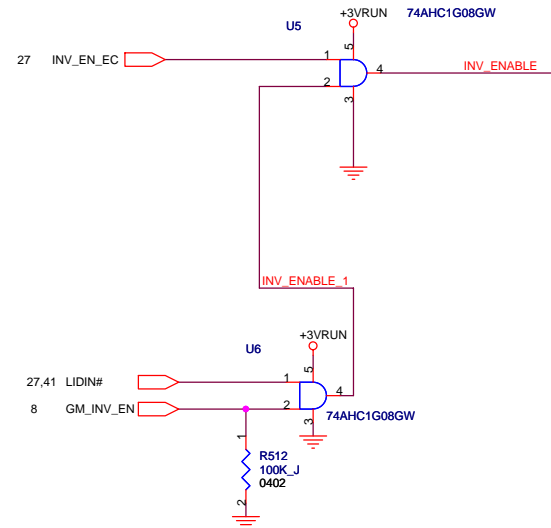
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LVDS CONNECTOR DVT

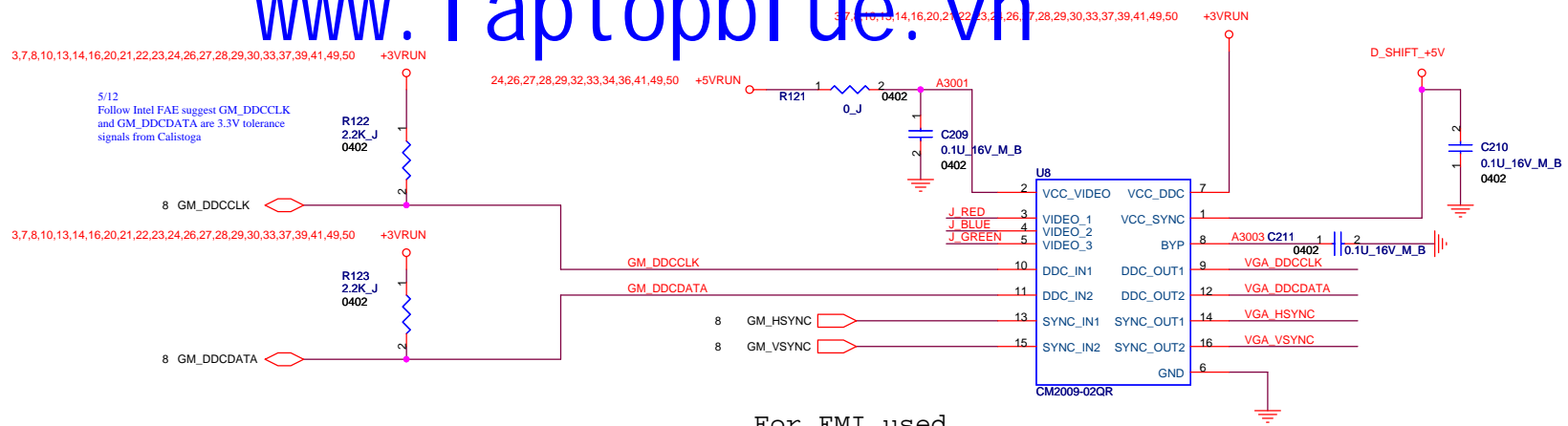


INVERTER CONNECTOR

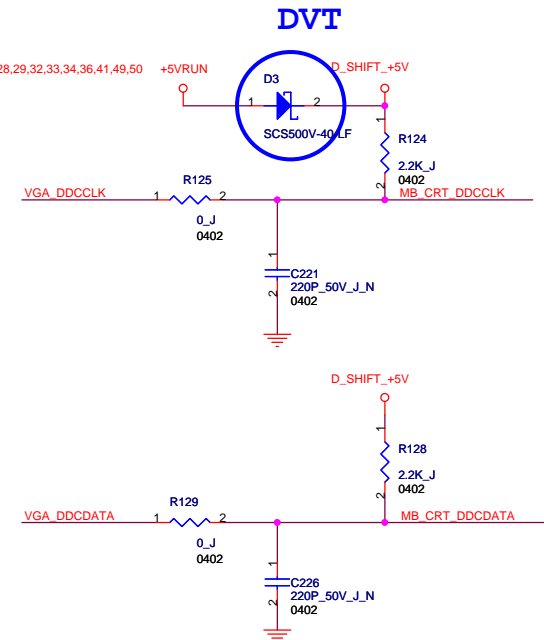
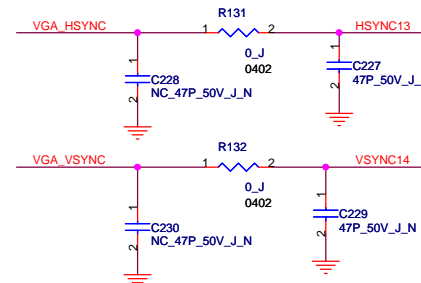
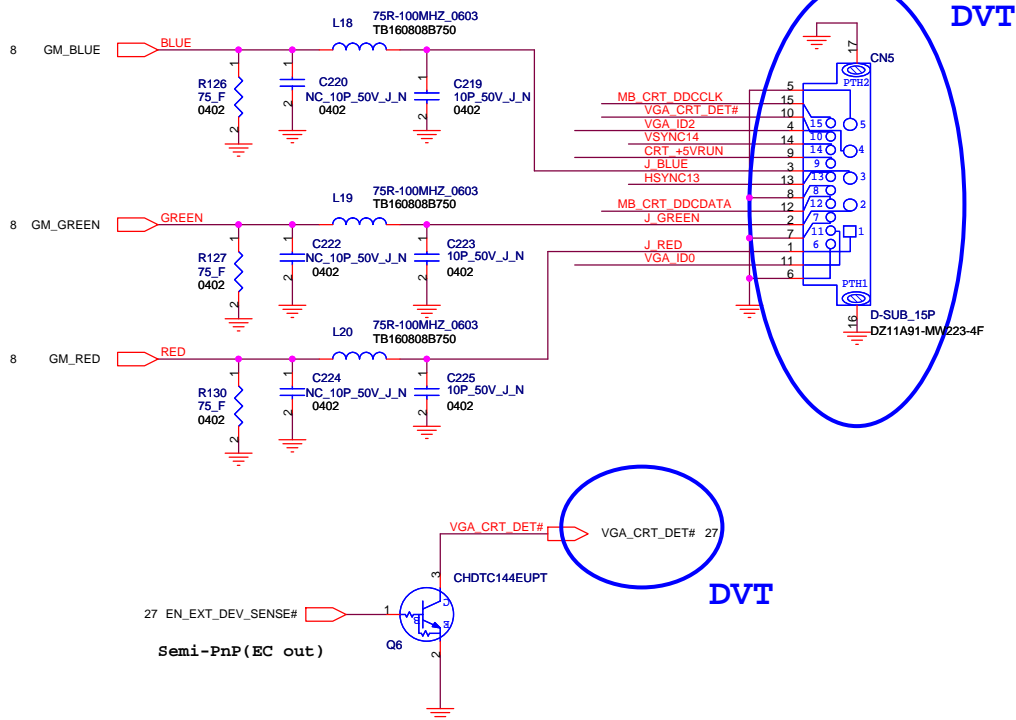


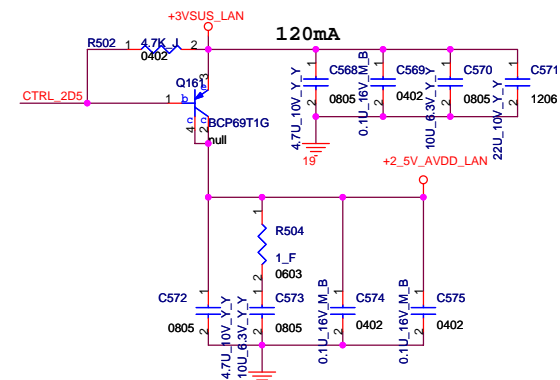
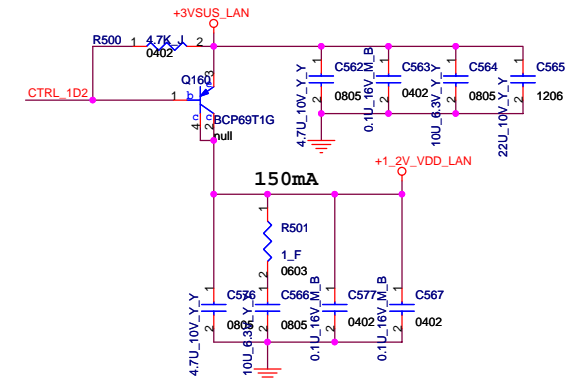
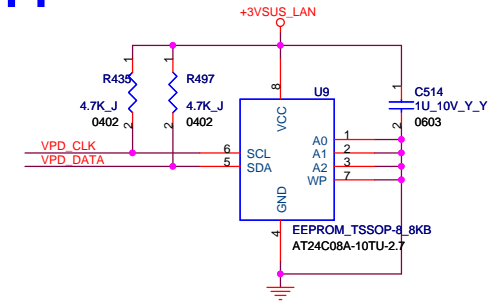
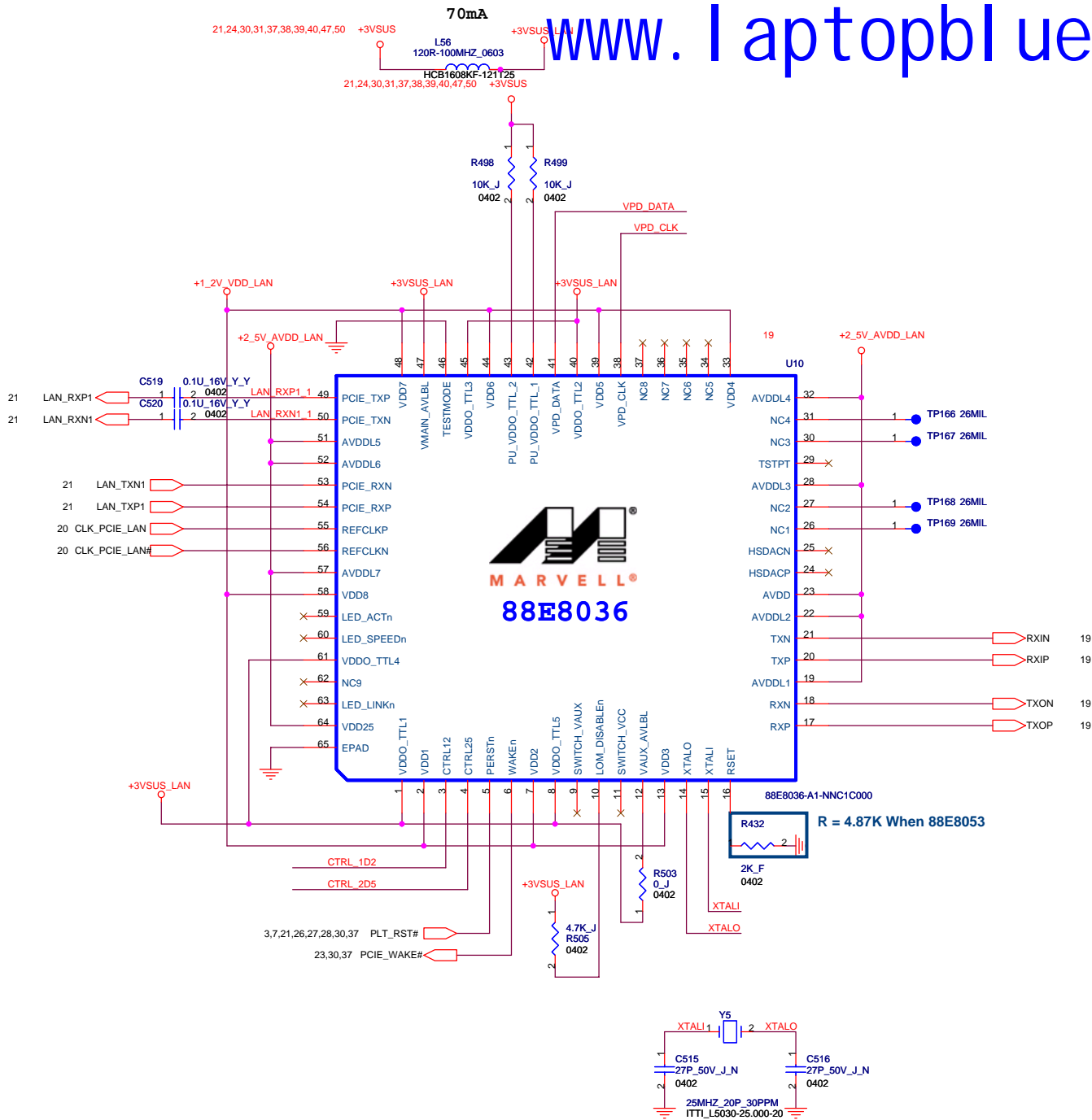
The R461 will consume about 0.054 Watt (3.3x3.3/200 = 0.054W). We changed resistor to 0603 size (1/8 Watt)

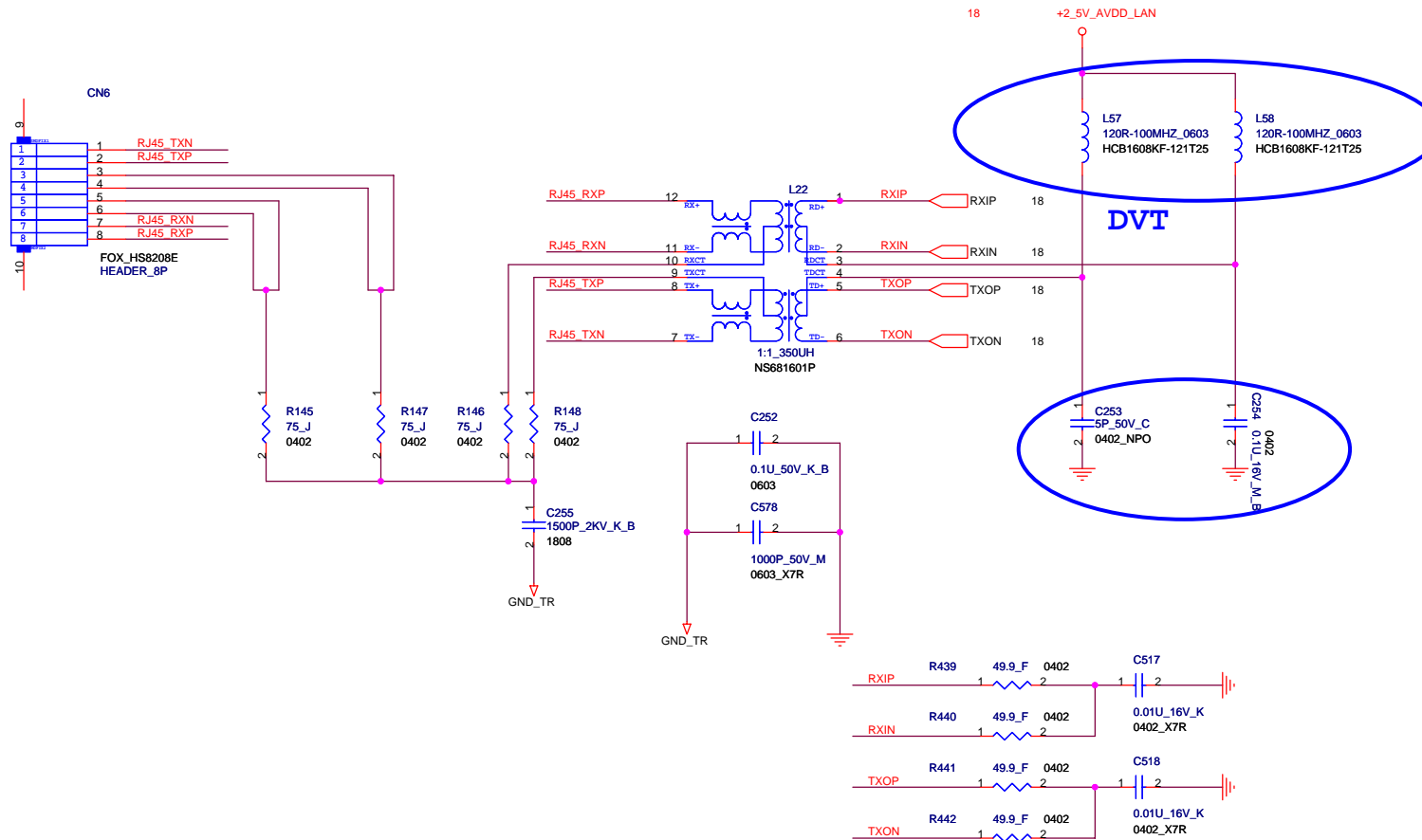
FOXCONN HON HAI Precision Ind. Co., Ltd.		
CCPBG - R&D Division		
Title LVDS		
Size A3	Document Number MS70-1-01	Rev 1.0
Date: Tuesday, July 25, 2006	Sheet 16	of 55

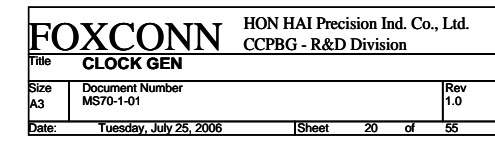


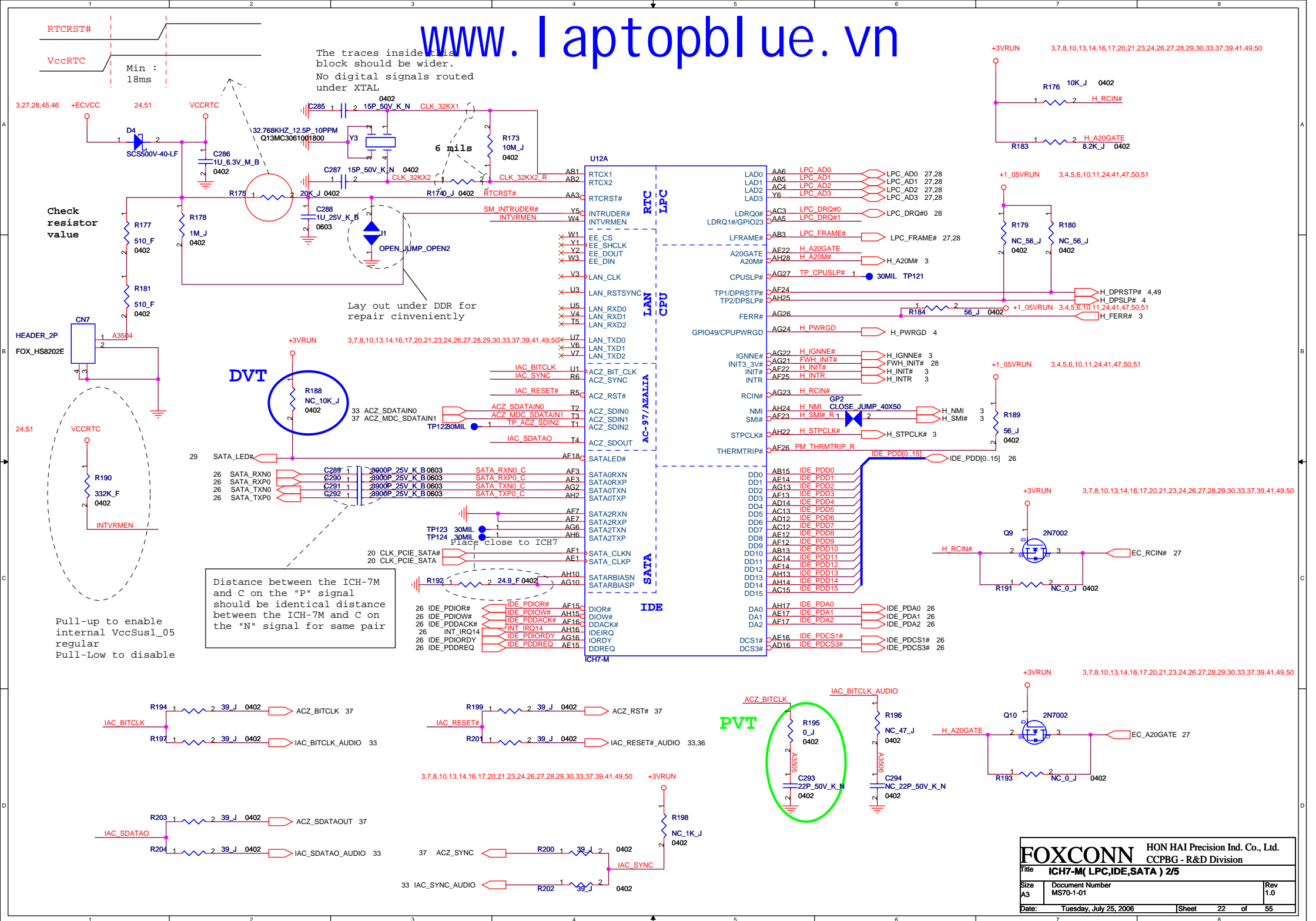
CRT CONNECTOR

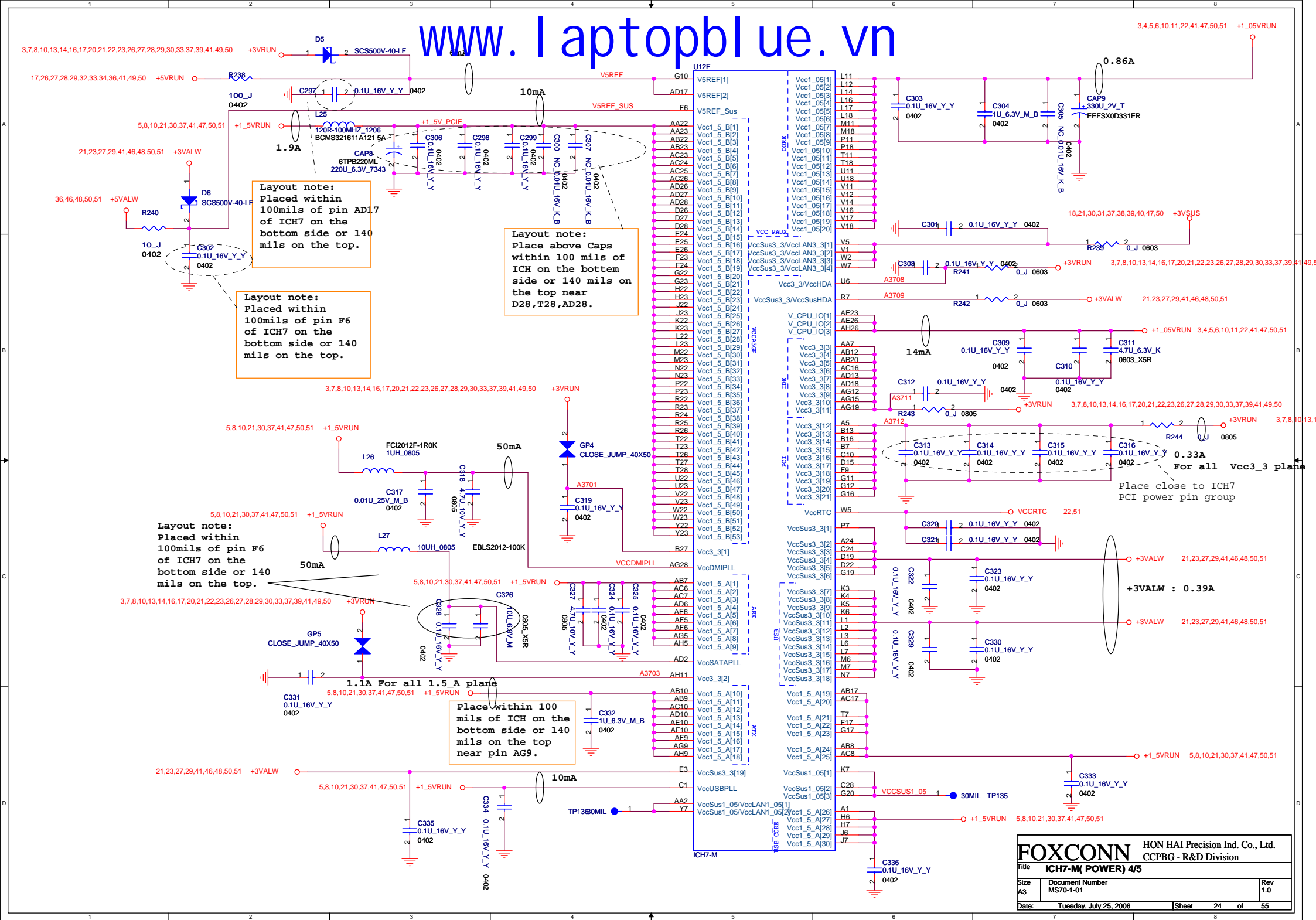


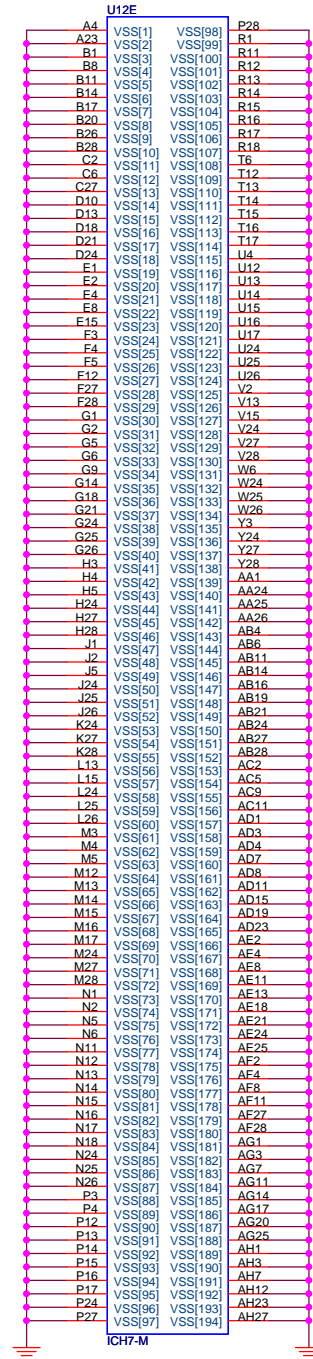




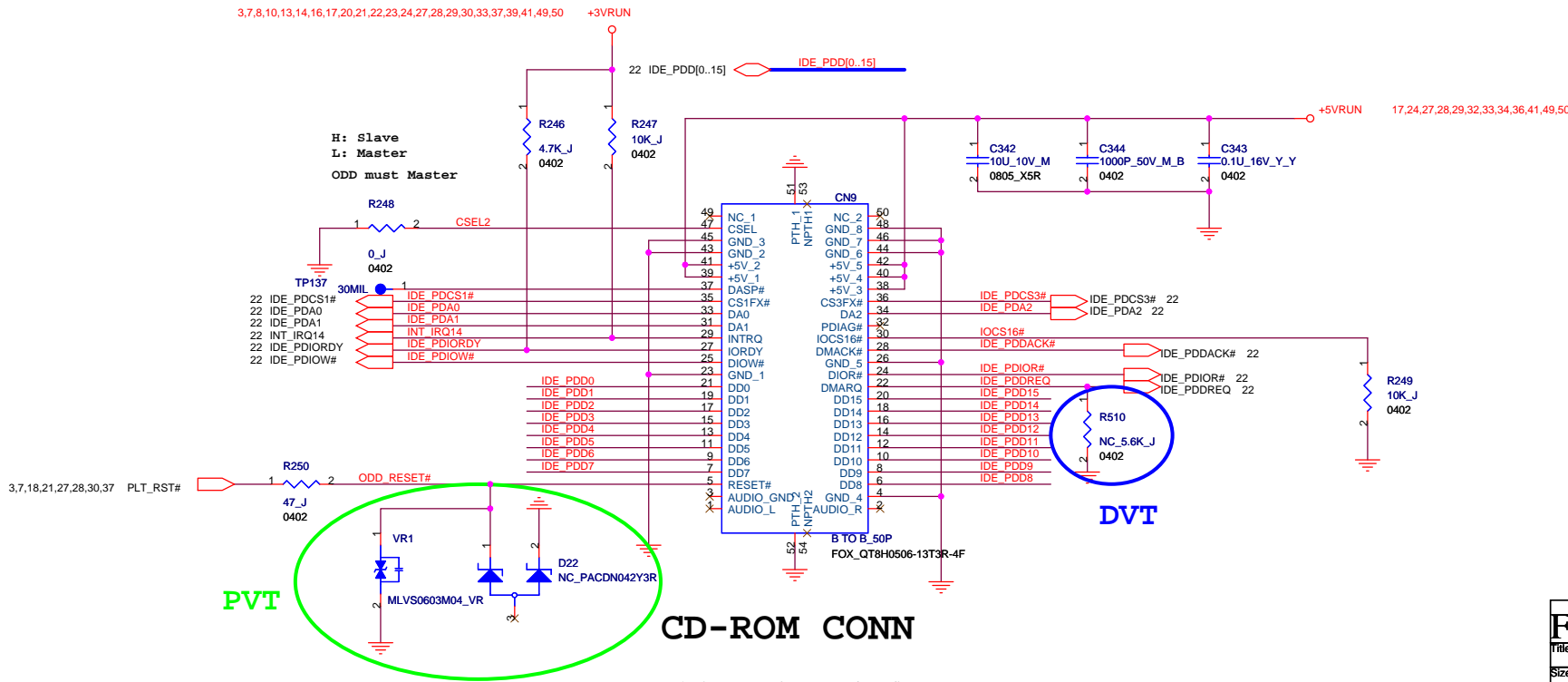
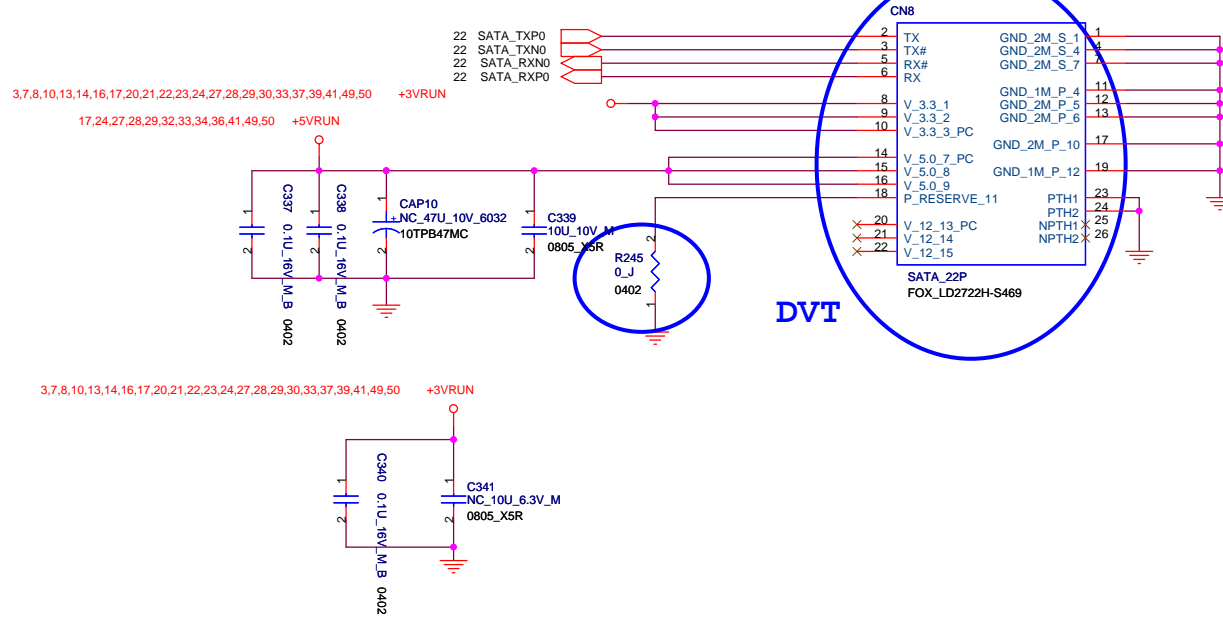






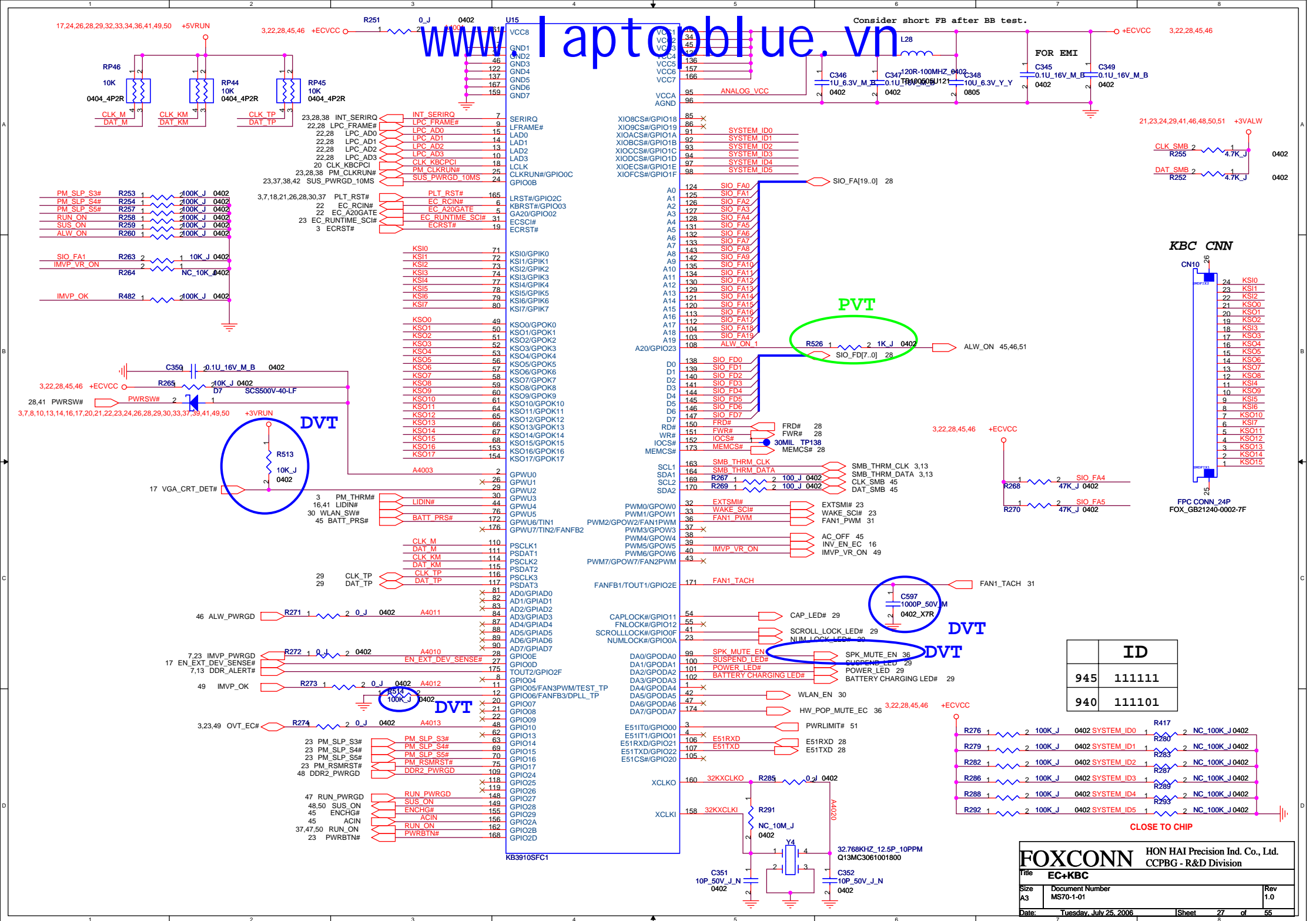


SATA HDD CONN

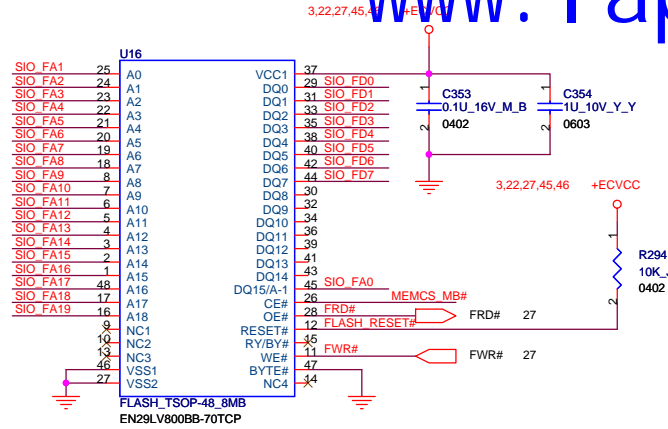


CD-ROM CONN

Follow Adoi san suggest ODD: Master/HDD:Slave

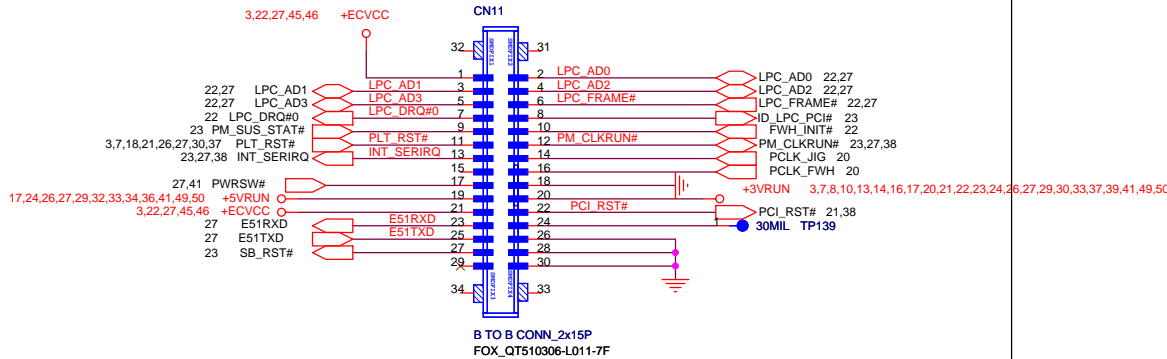


27 SIO_FA[19..0]
27 SIO_FD[7..0]

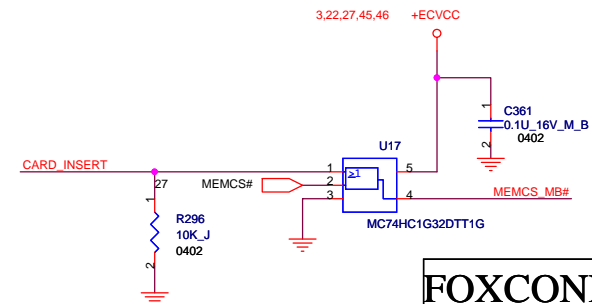
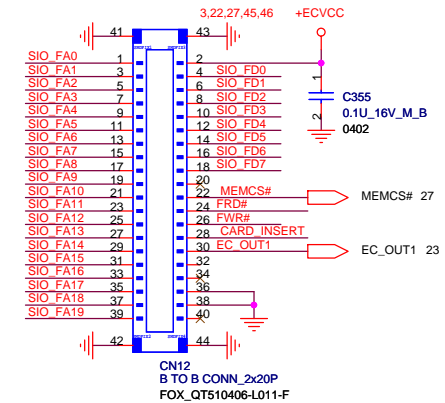


FLASH BIOS

JIG-120



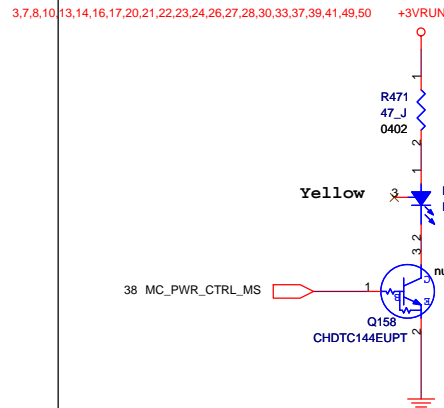
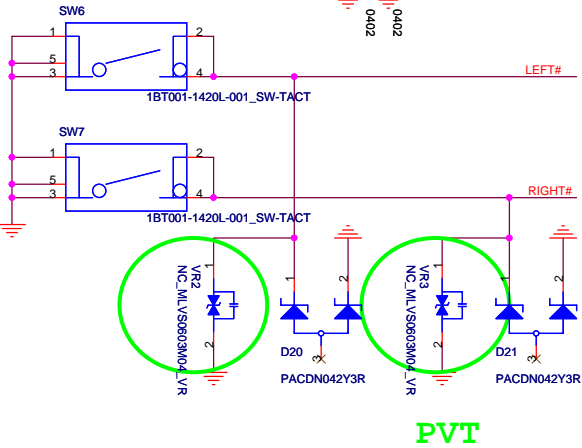
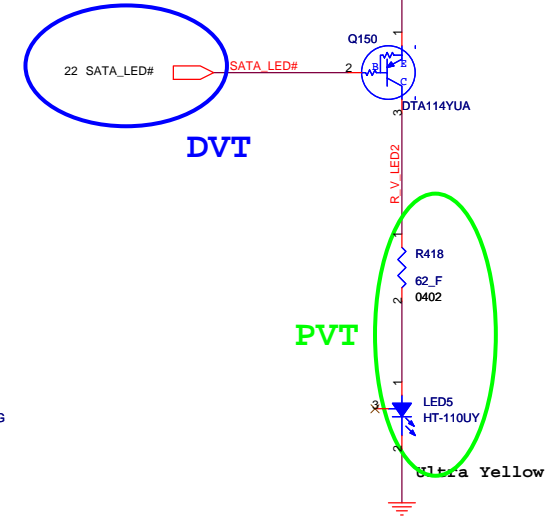
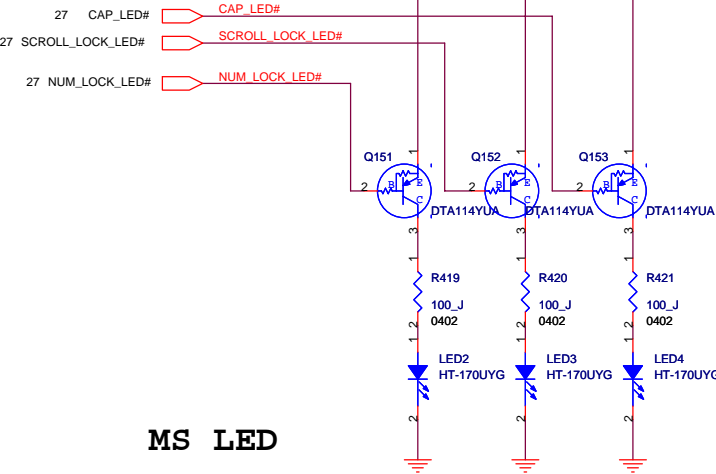
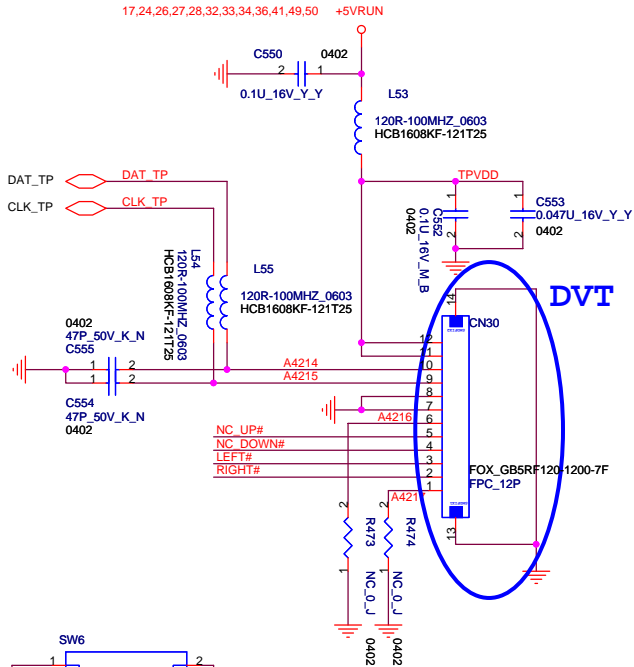
X-BUS



Touch Pad Board

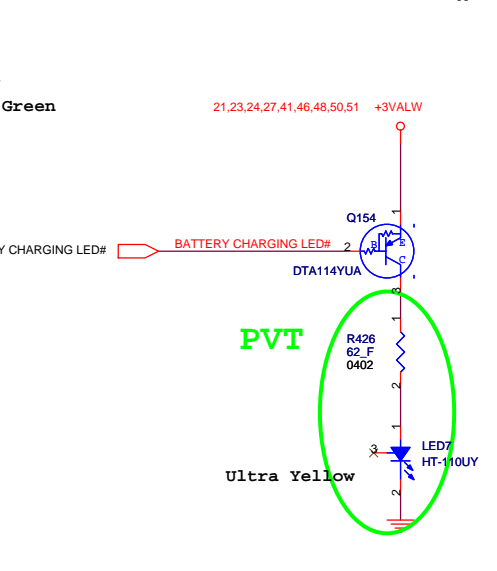
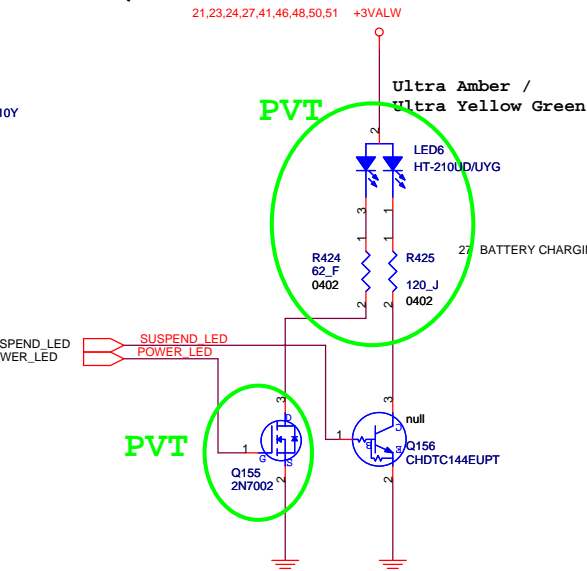
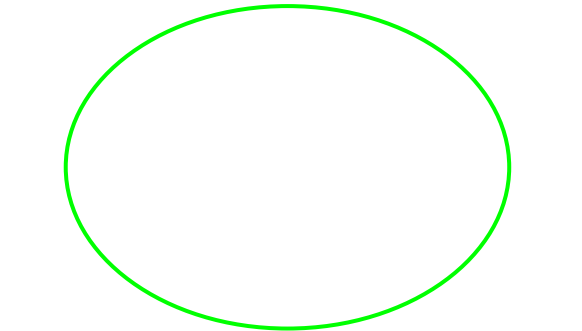
CAP_LED#
SCROLL_LOCK_LED#
NUM_LOCK_LED#

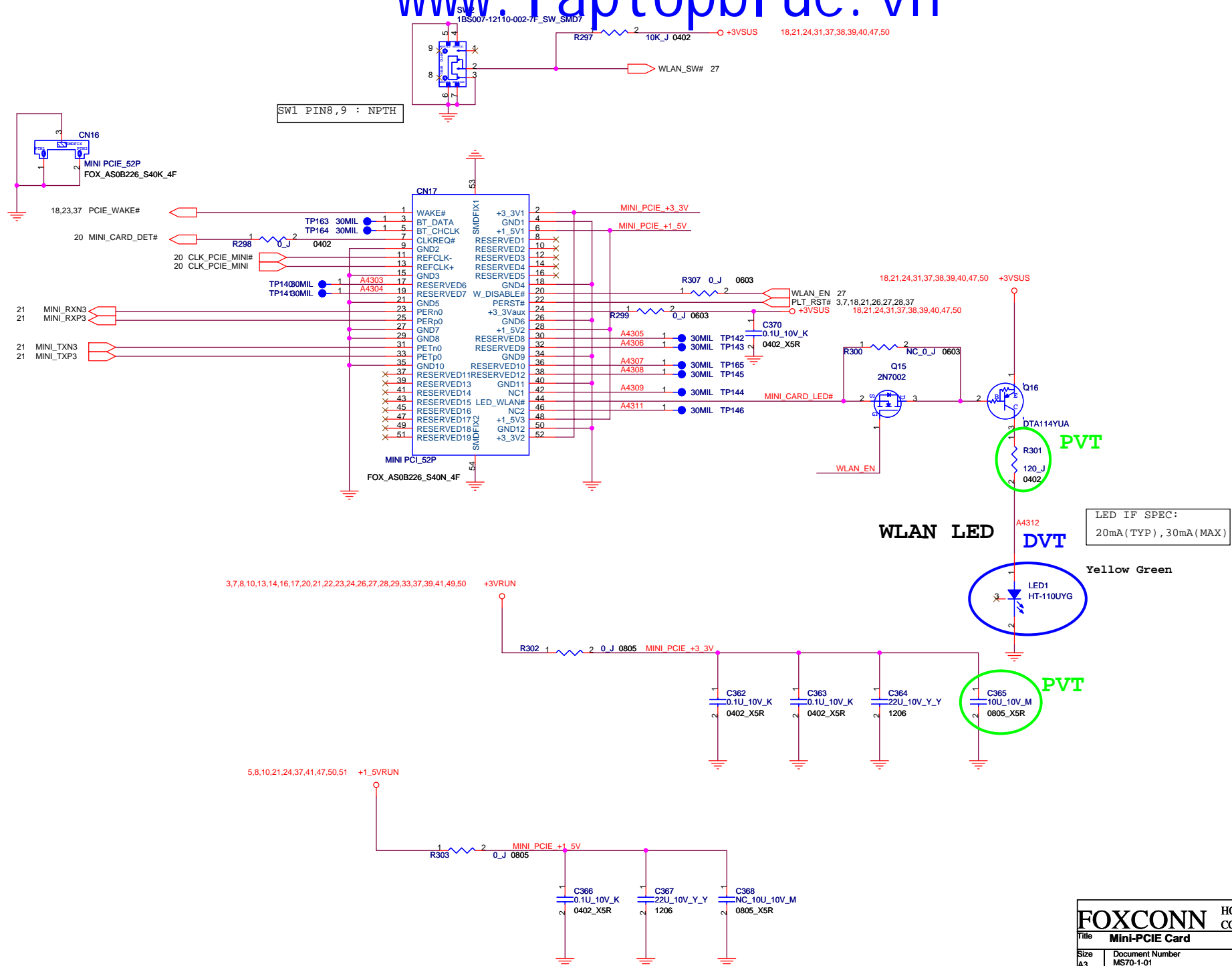
HDD_LED#



POWER/SUSPEND LED

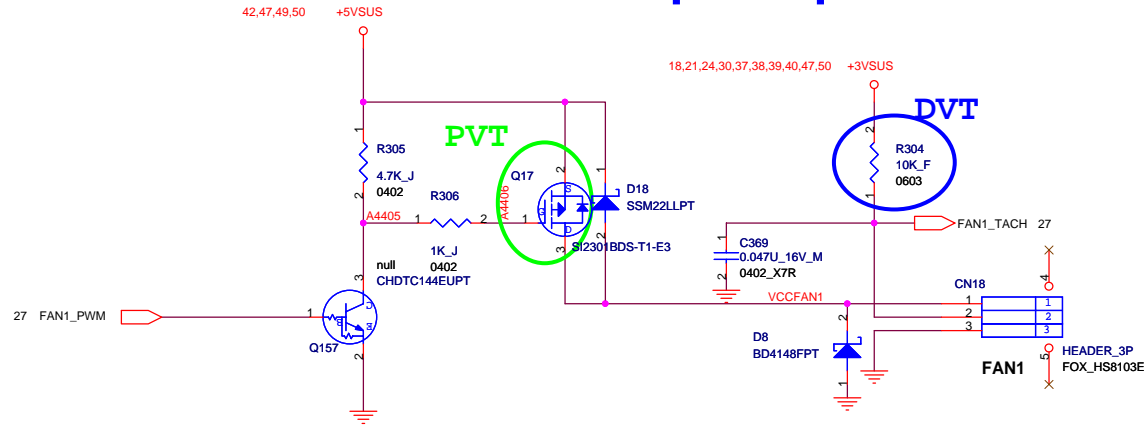
BATTERY CHARGING LED#

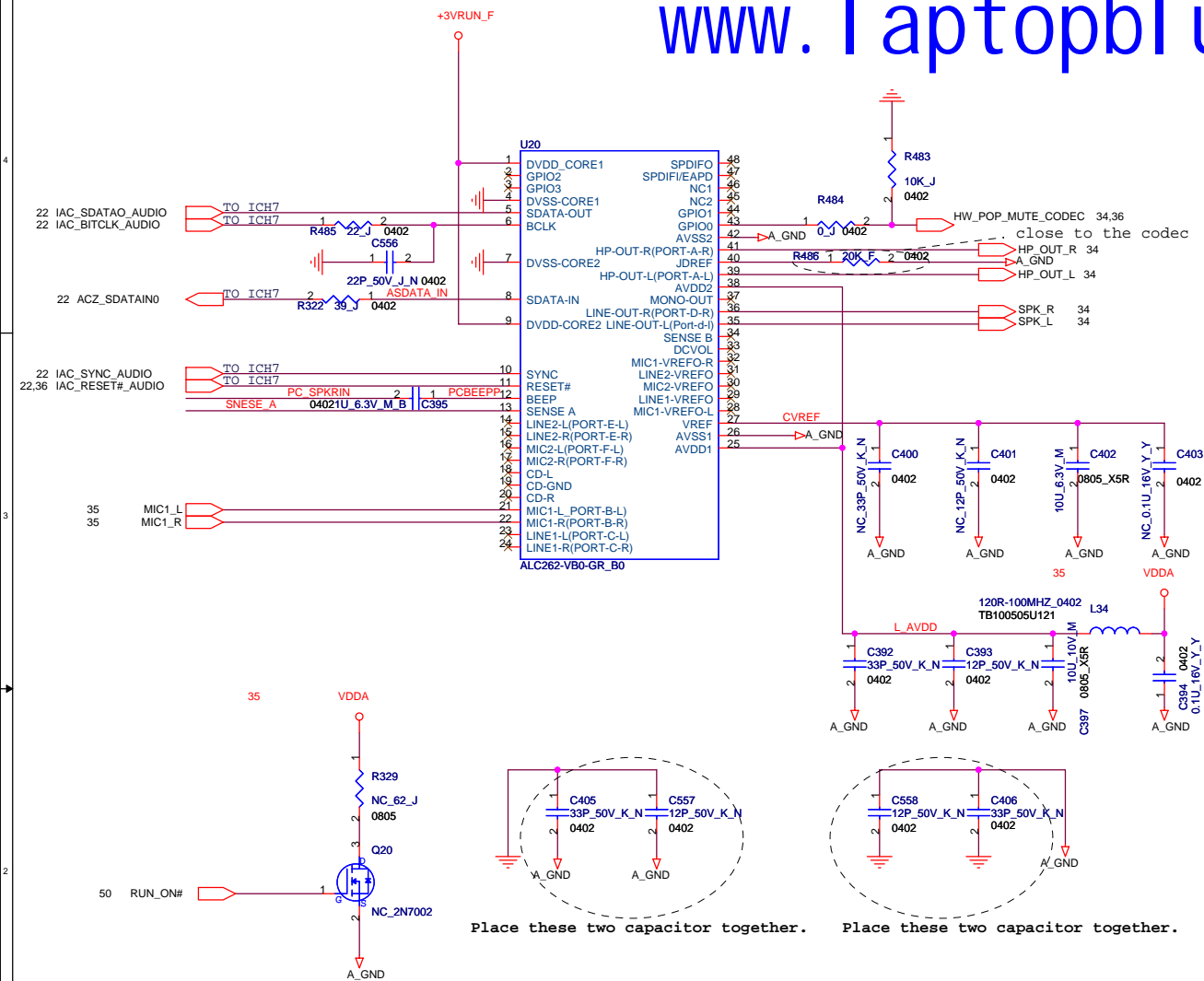




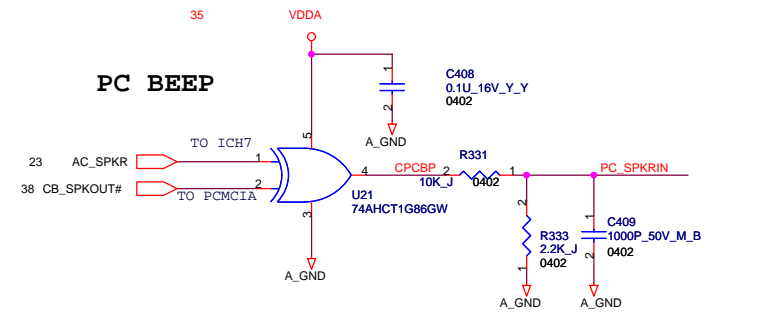
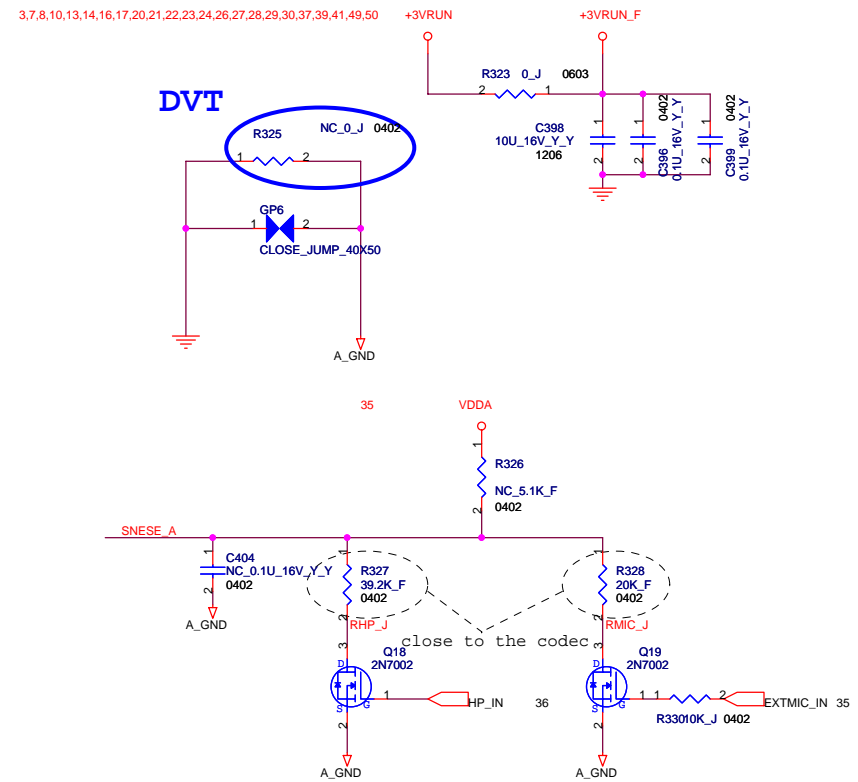
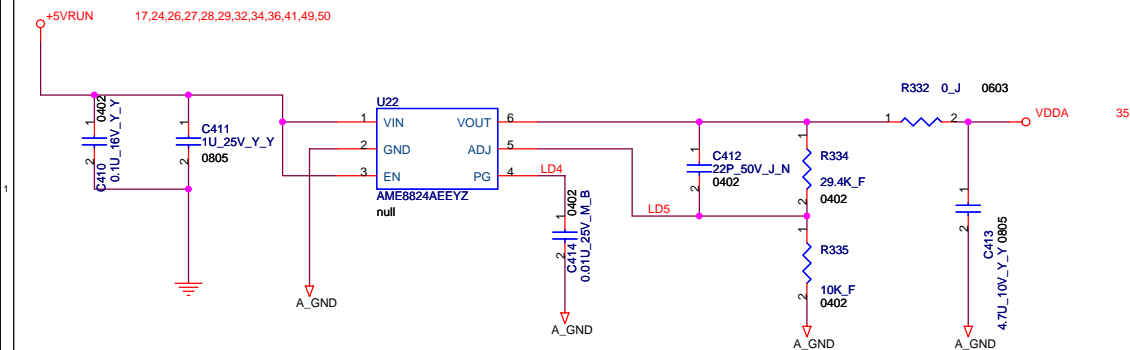
FAN1

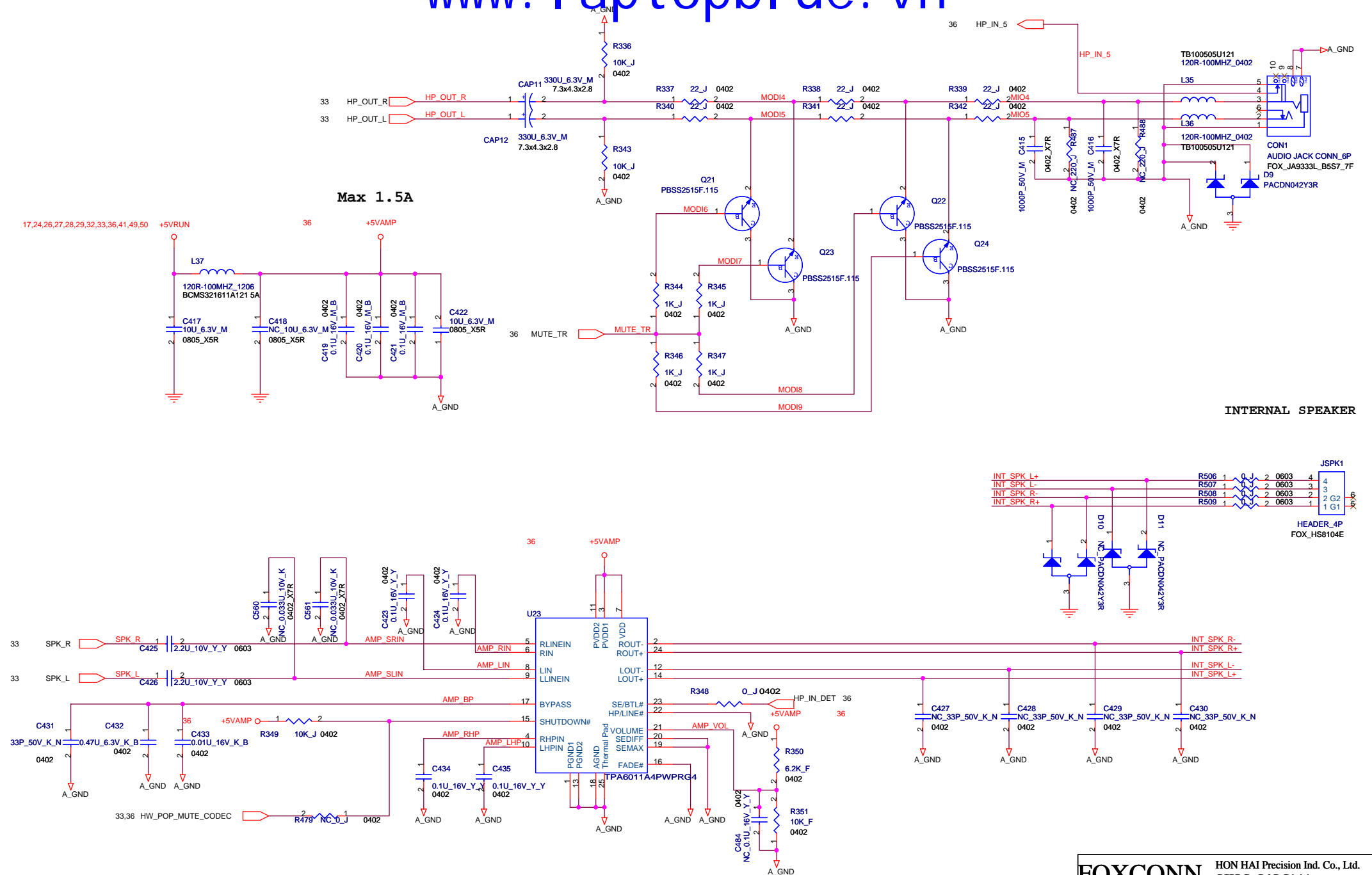
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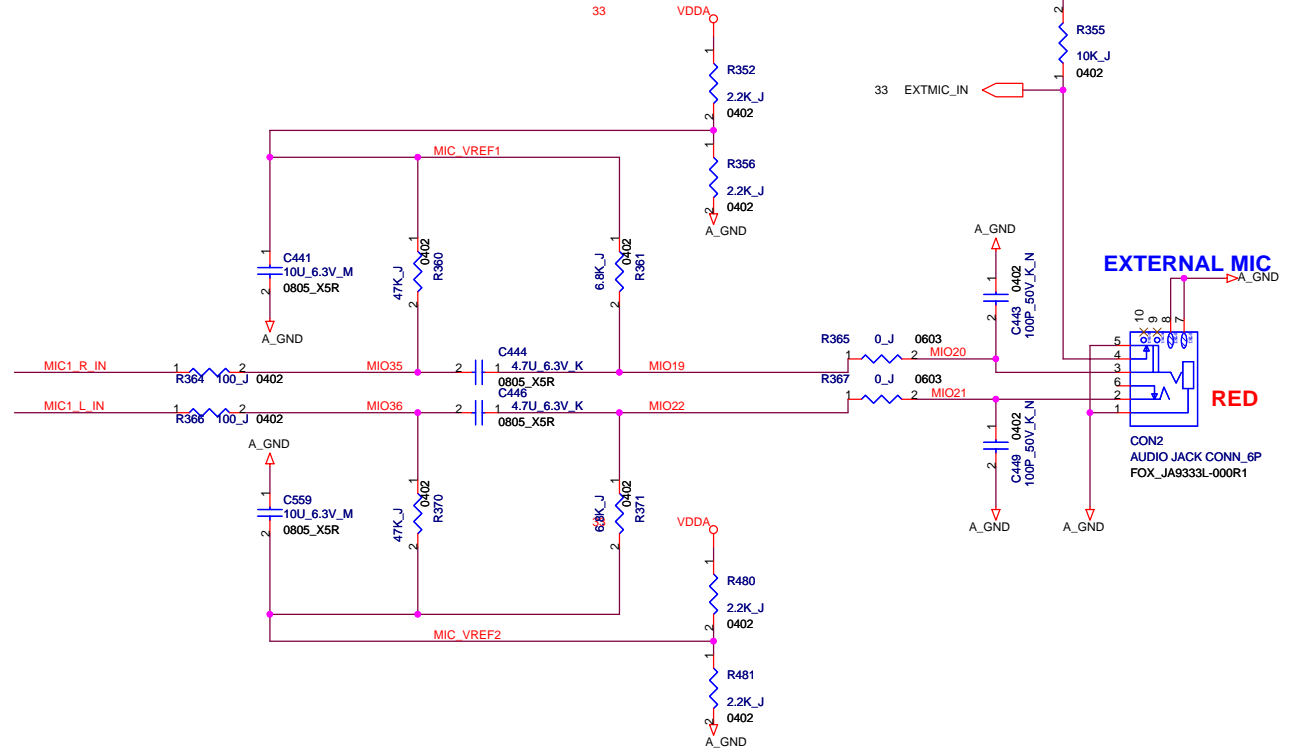
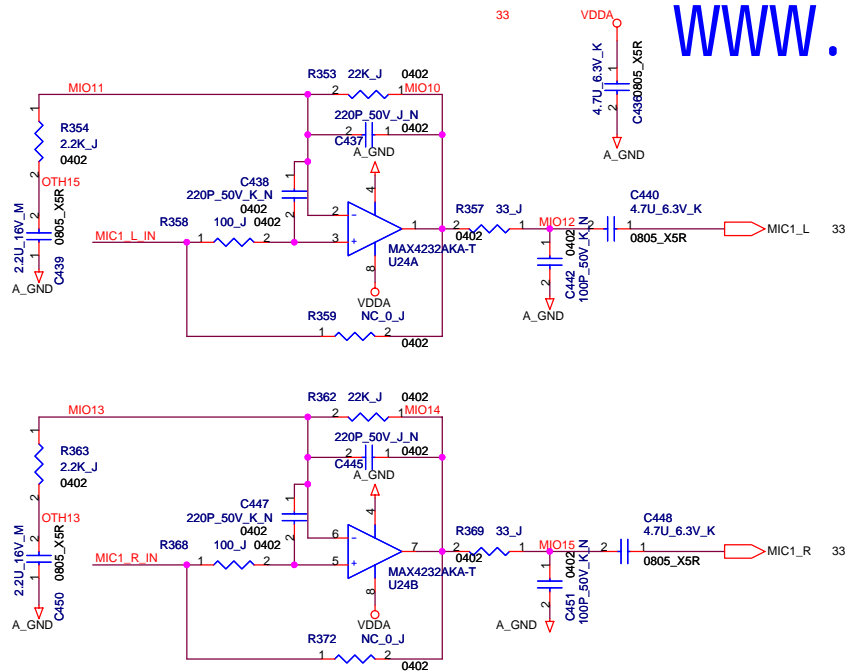


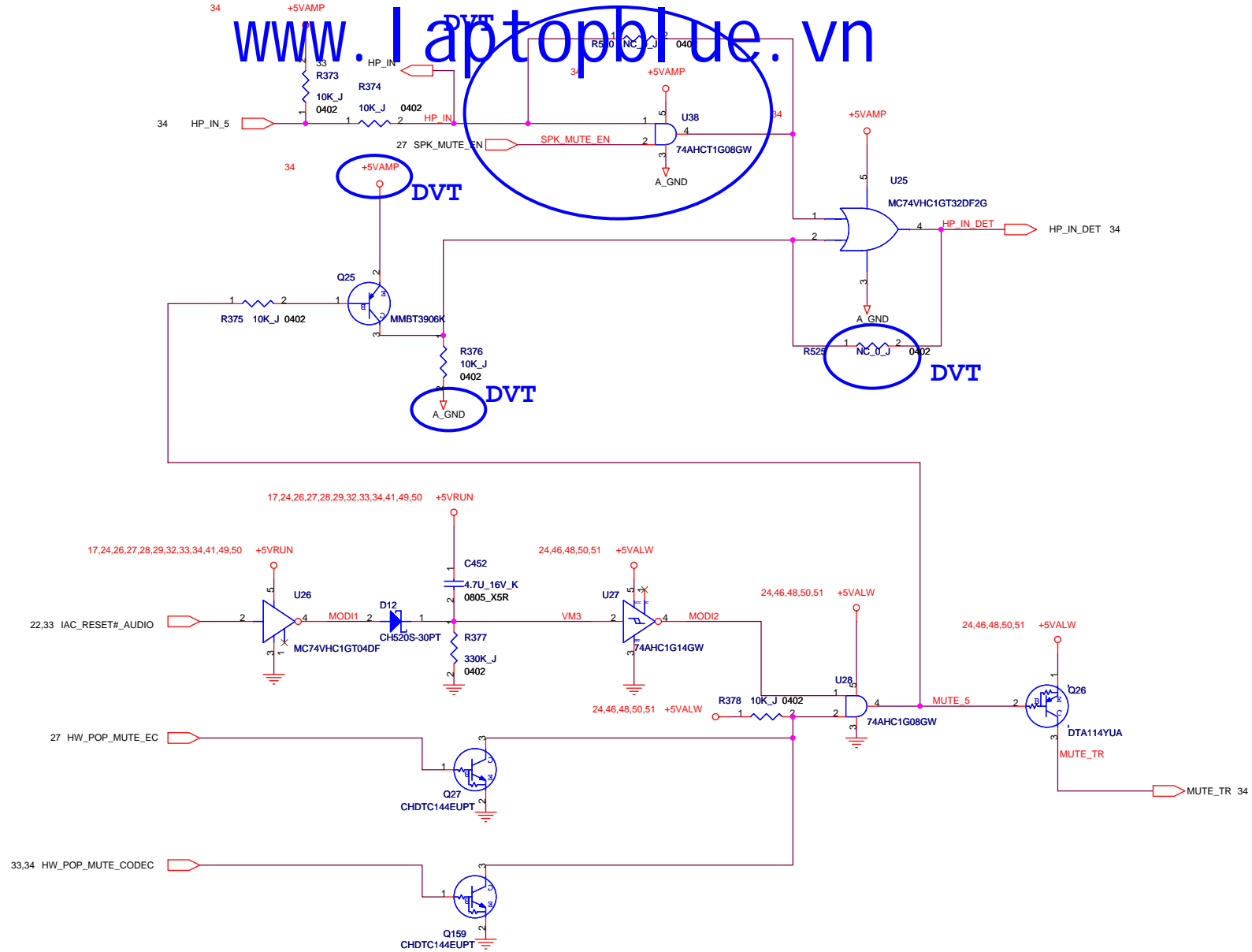


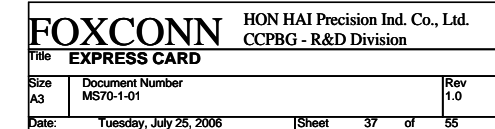
AUDIO POWER(Change to 4.75V/200mA)

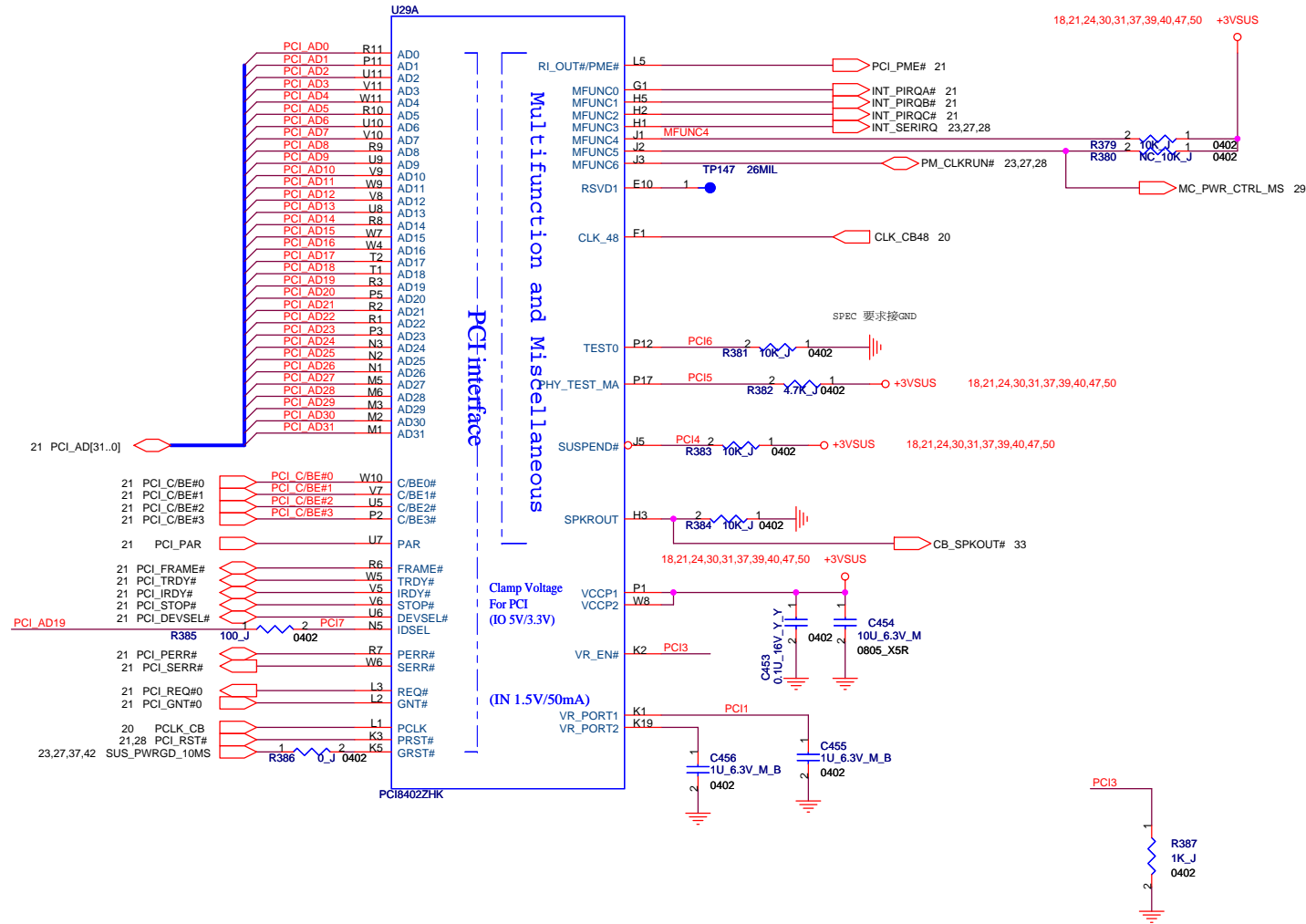




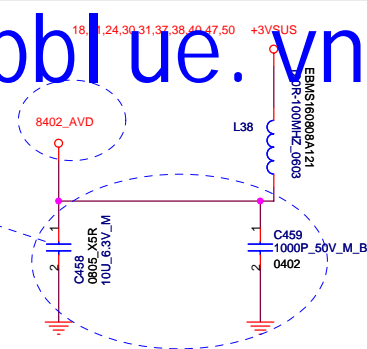






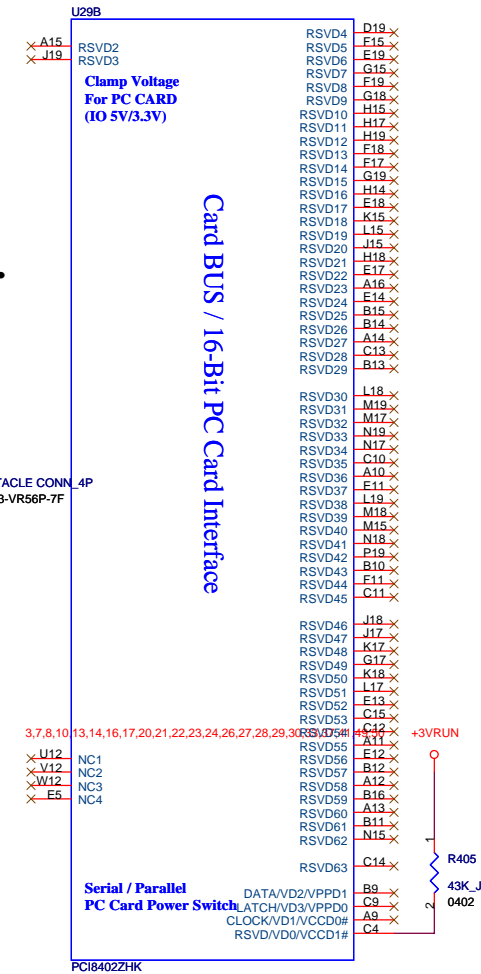


This array must be placed close to AVDD(Pin P13,P14,U15) They must be tied to a low-impedance GND.



iLink CONN.

Card BUS / 16-Bit PC Card Interface



**Serial / Parallel
PC Card Power Switch**

DATA/VD2/VPPD1	B
LATCH/VD3/VPPD0	C
CLOCK/VD1/VCCD0#	A
RSVD/VD0/VCCD1#	C

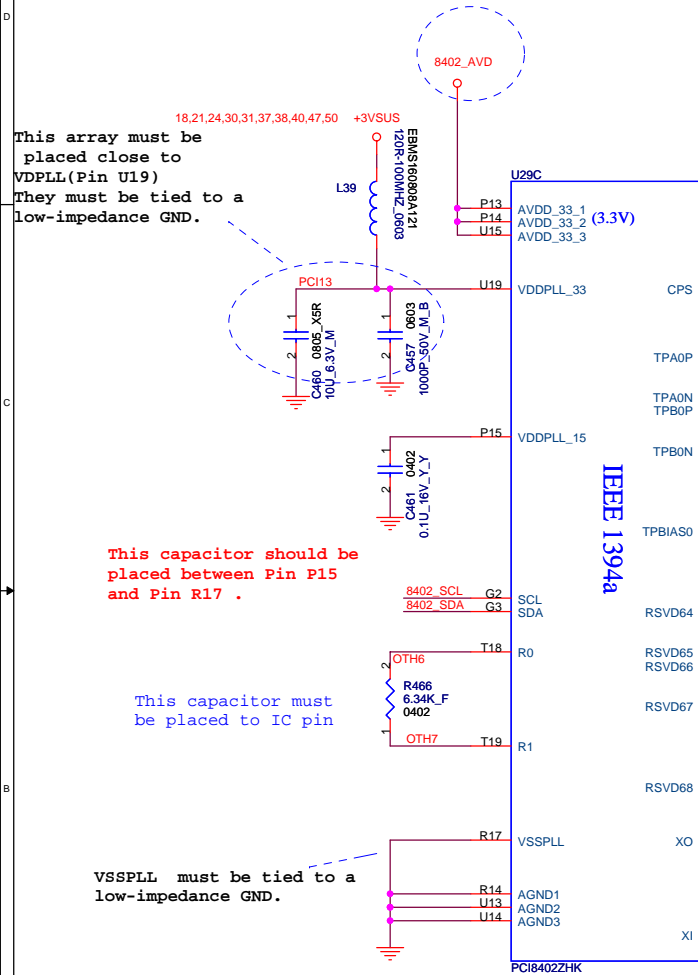
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

FOXCONN
Title **PCI (ILINK)2/3**

Size	Document Number
A3	MS70-1-01

Date: Tuesday, July 25, 2006

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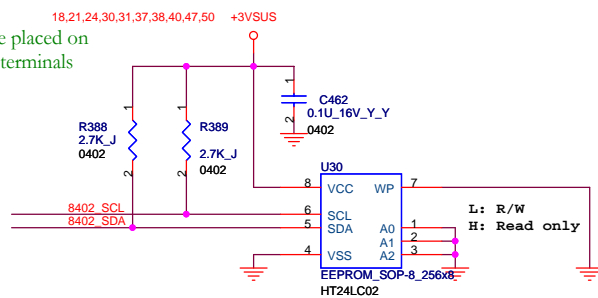


VSSPLL must be tied to a low-impedance GND.

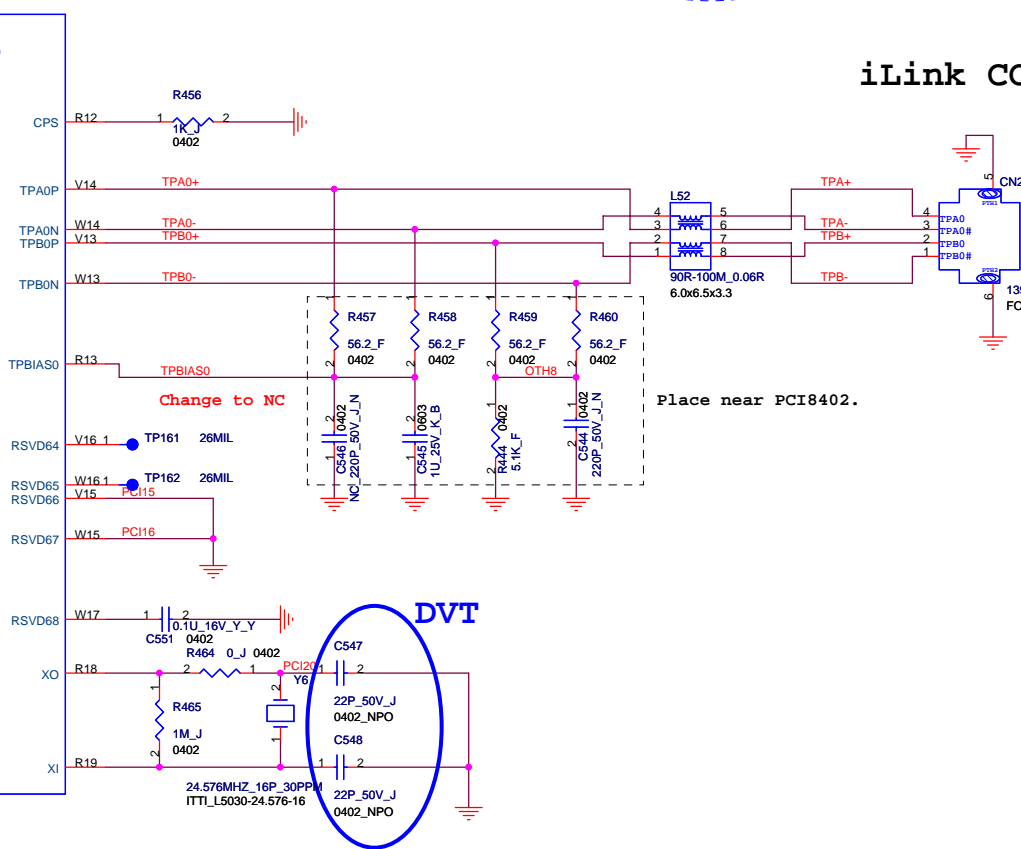
This capacitor must
be placed to IC pin

This capacitor should be placed between Pin P15 and Pin R17 .

18,21,24,30,3
y must be
close to
U19)
be tied to a
ance GND.



Resistors should be placed on the SCL and SDA terminals



Place near PCI8402.

DVT

Change to NC

IEEE 1394a

FOXCONN

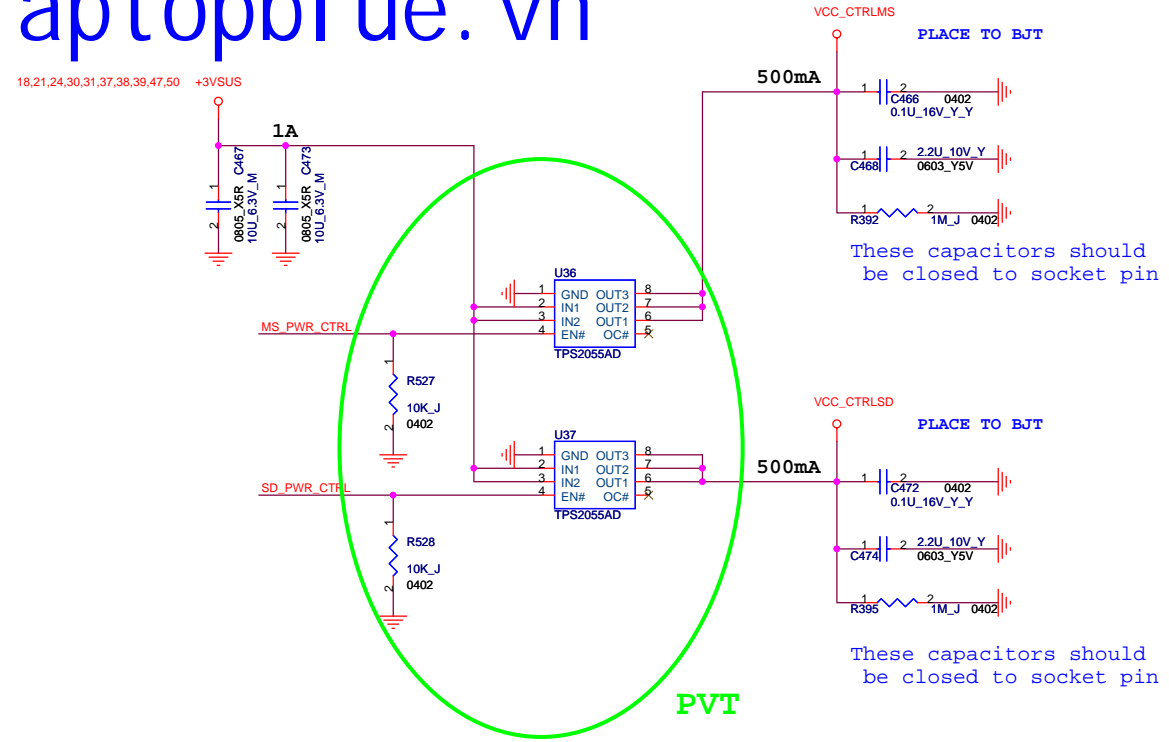
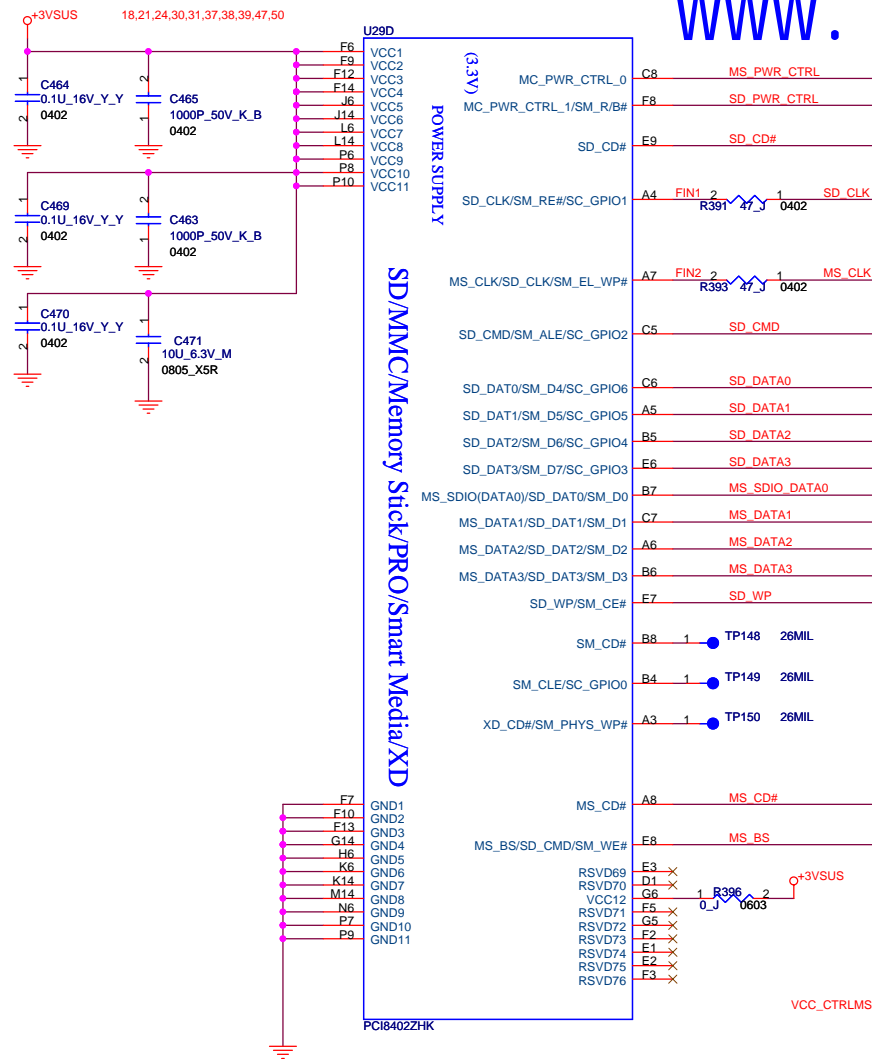
HON HAI Precision Ind. Co., Ltd.
CCPBG - R&D Division

Title	PCI (iLINK)2/3
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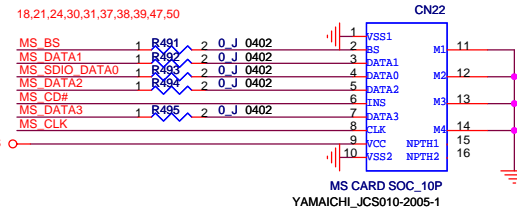
Size	Document Number
A3	MS70-1-01

Date: Tuesday, July 25, 2006

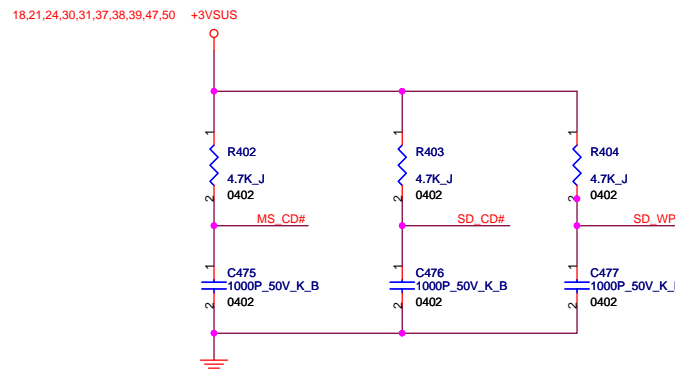
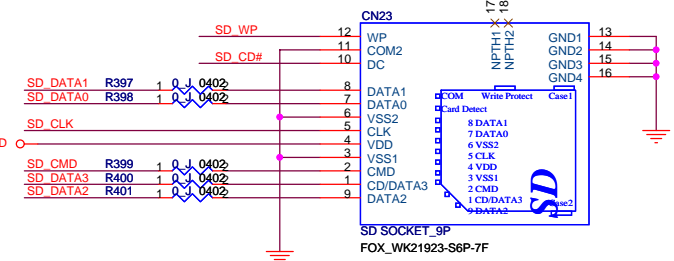
Sheet 39 of 55



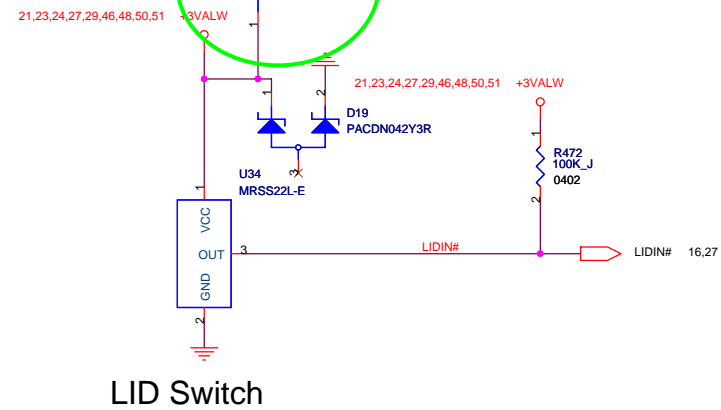
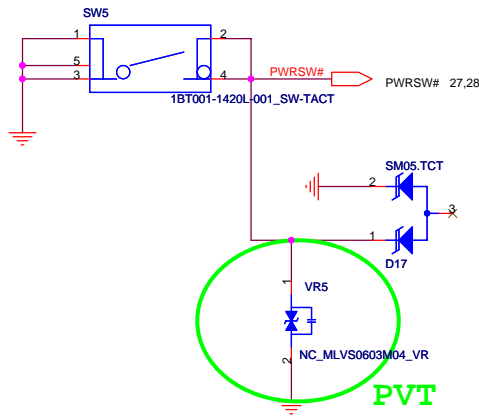
MS STD/DUO CONN.



SD CONN.

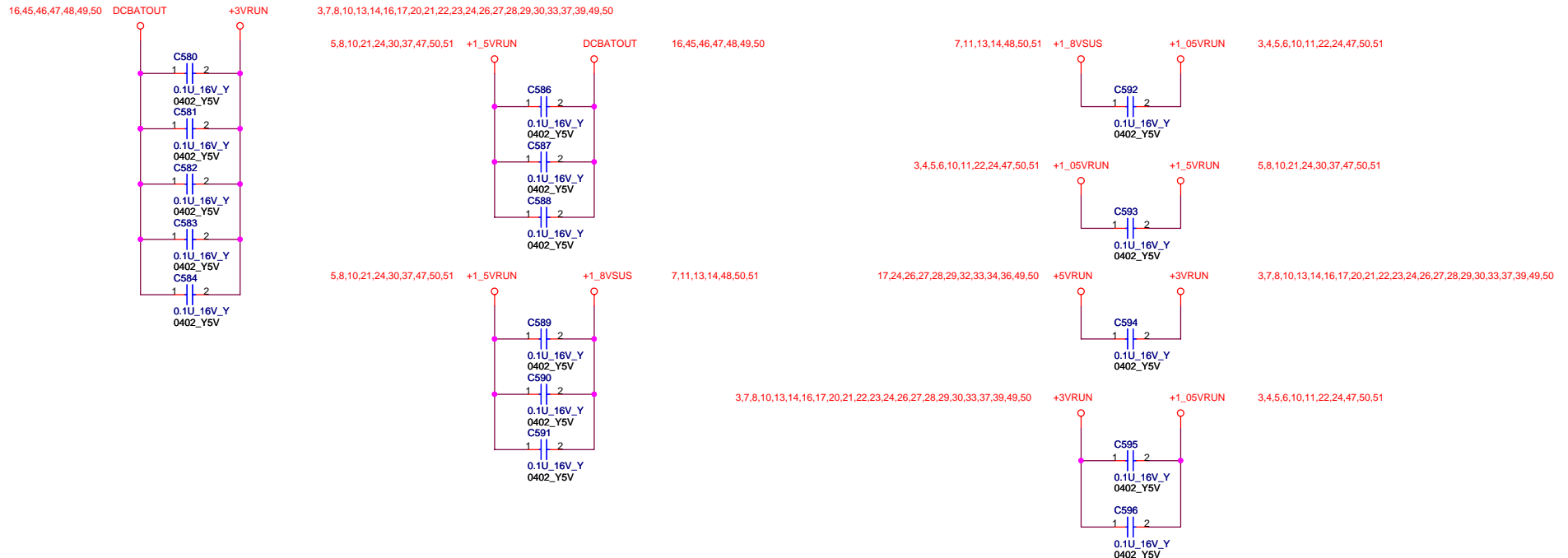


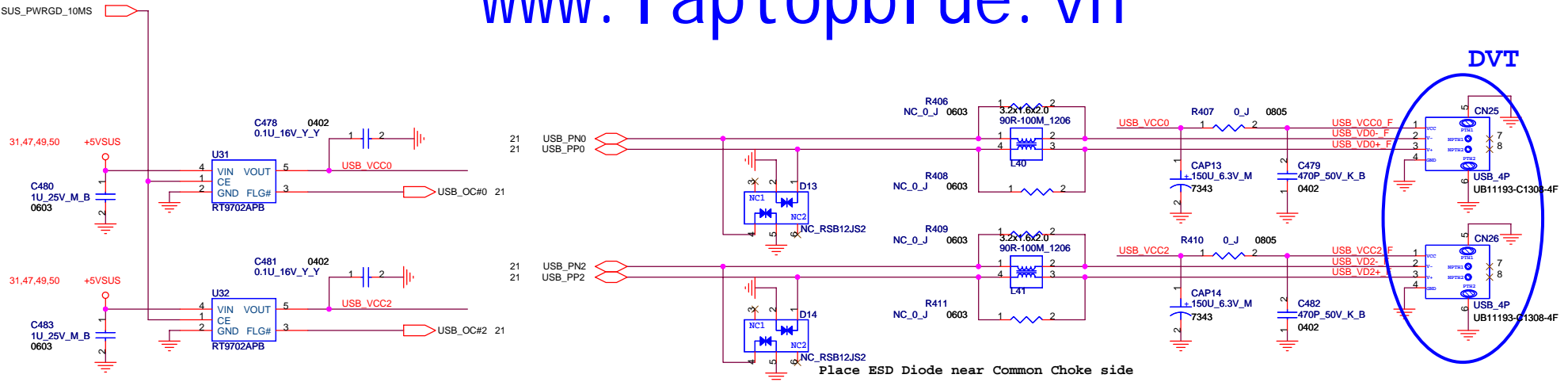
POWER BUTTON

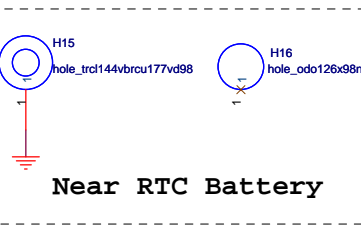
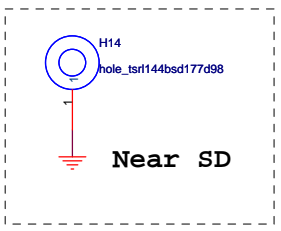
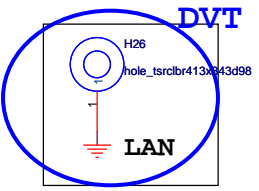
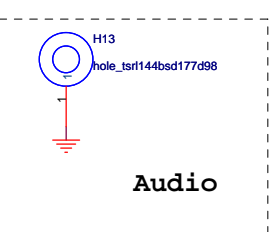
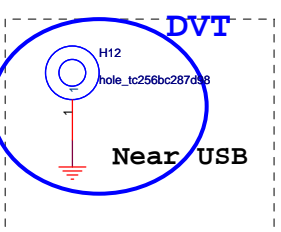
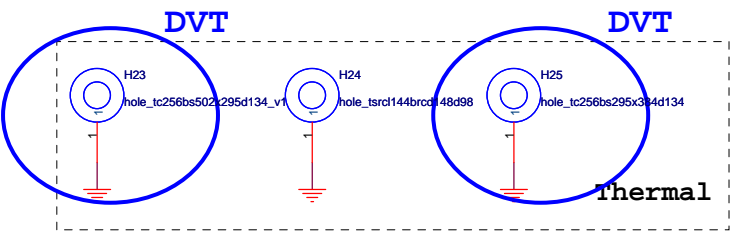
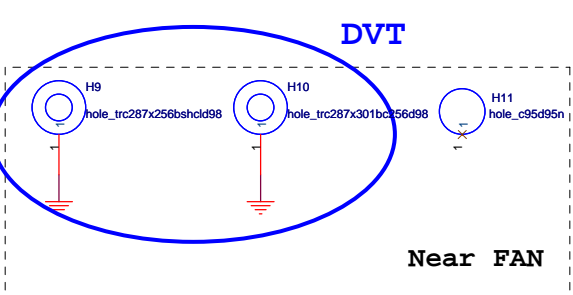
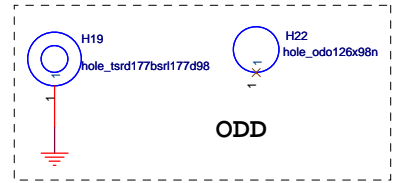
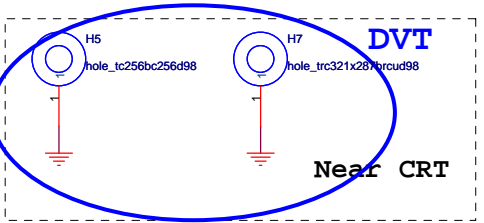
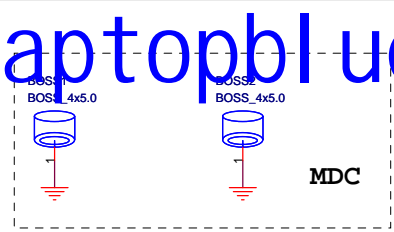
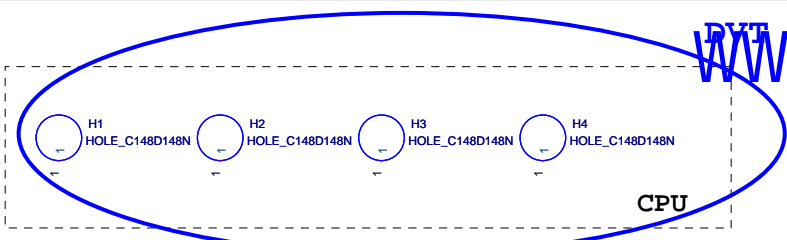


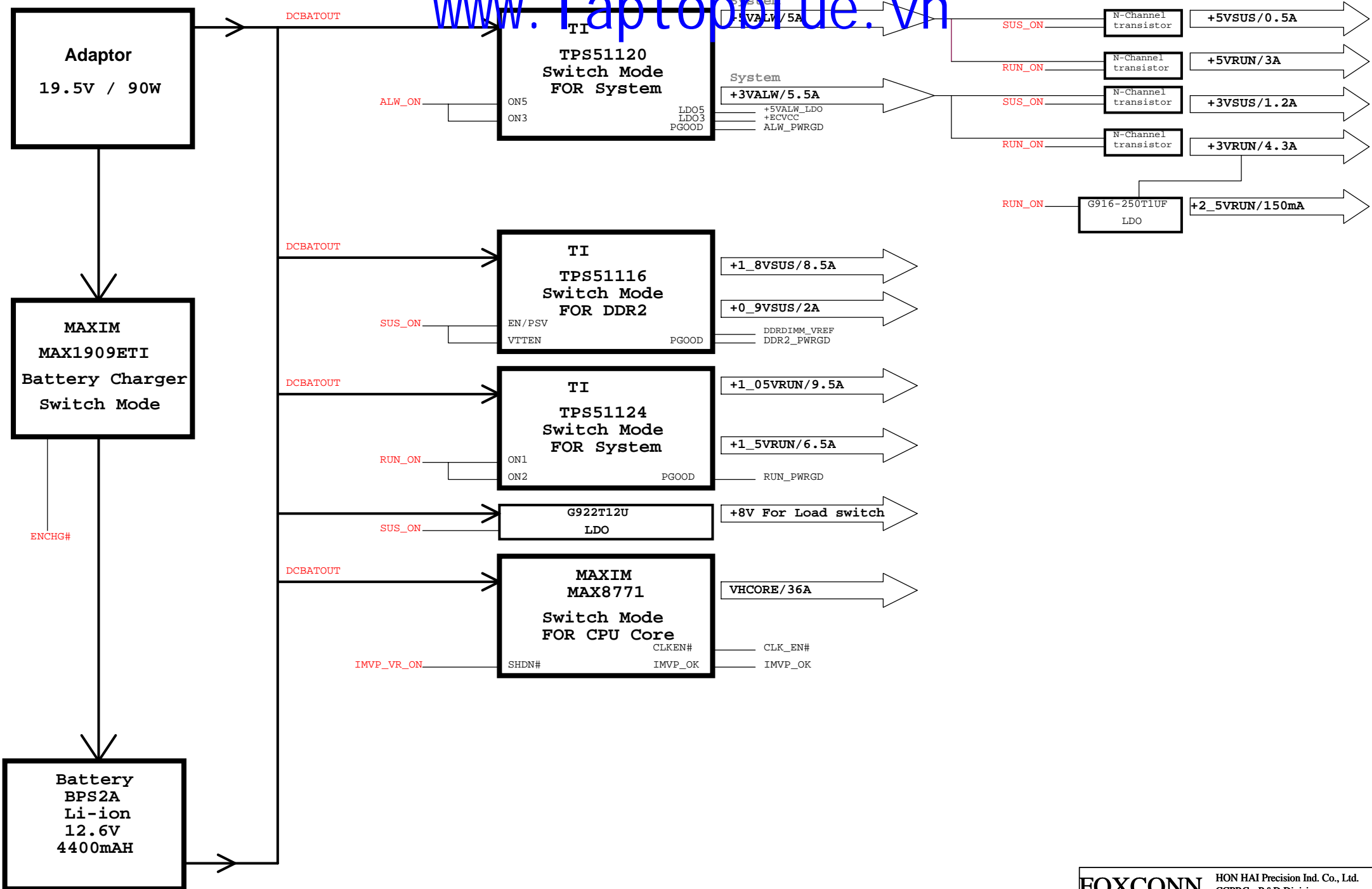
LID Switch

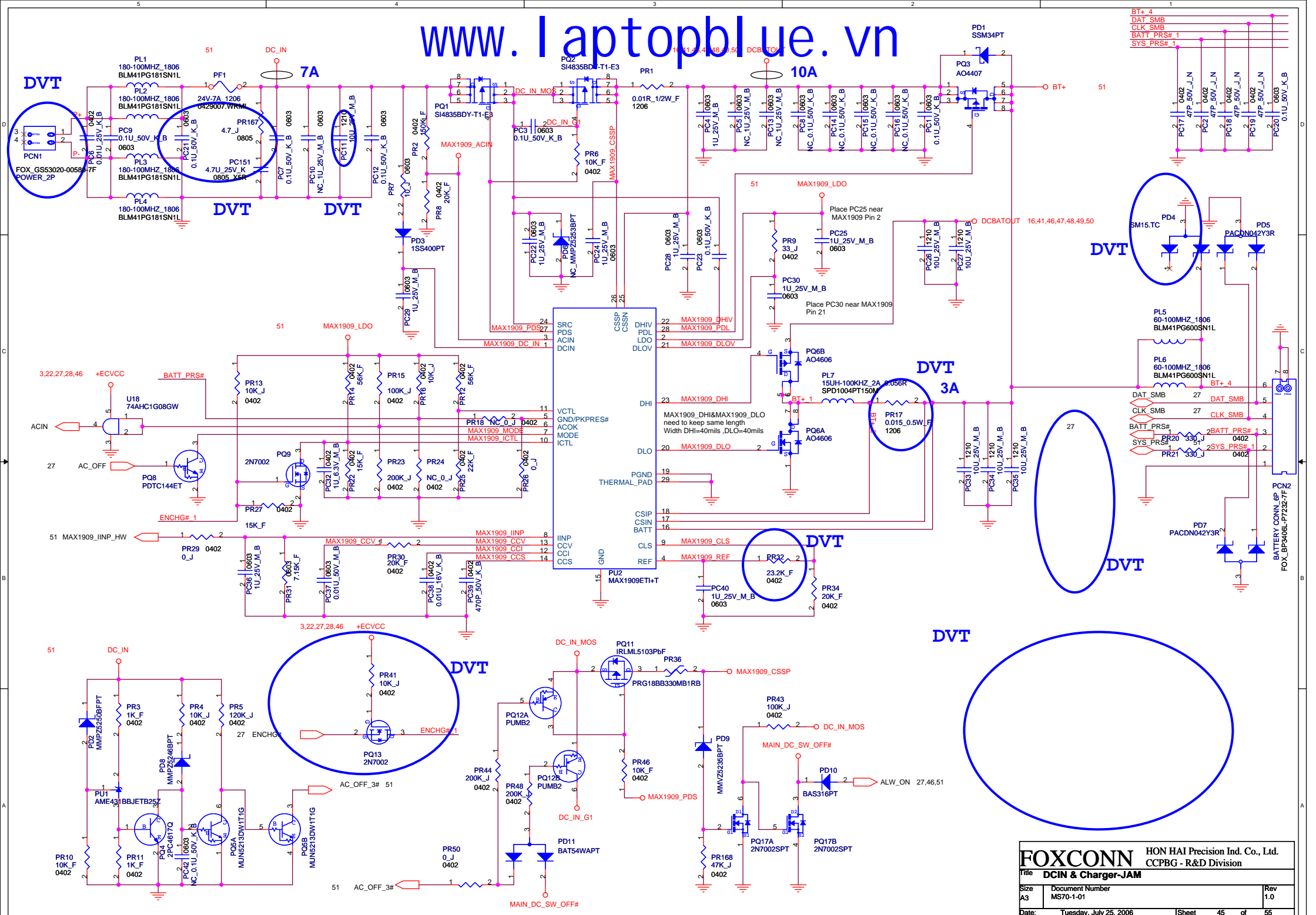
EMI CAP

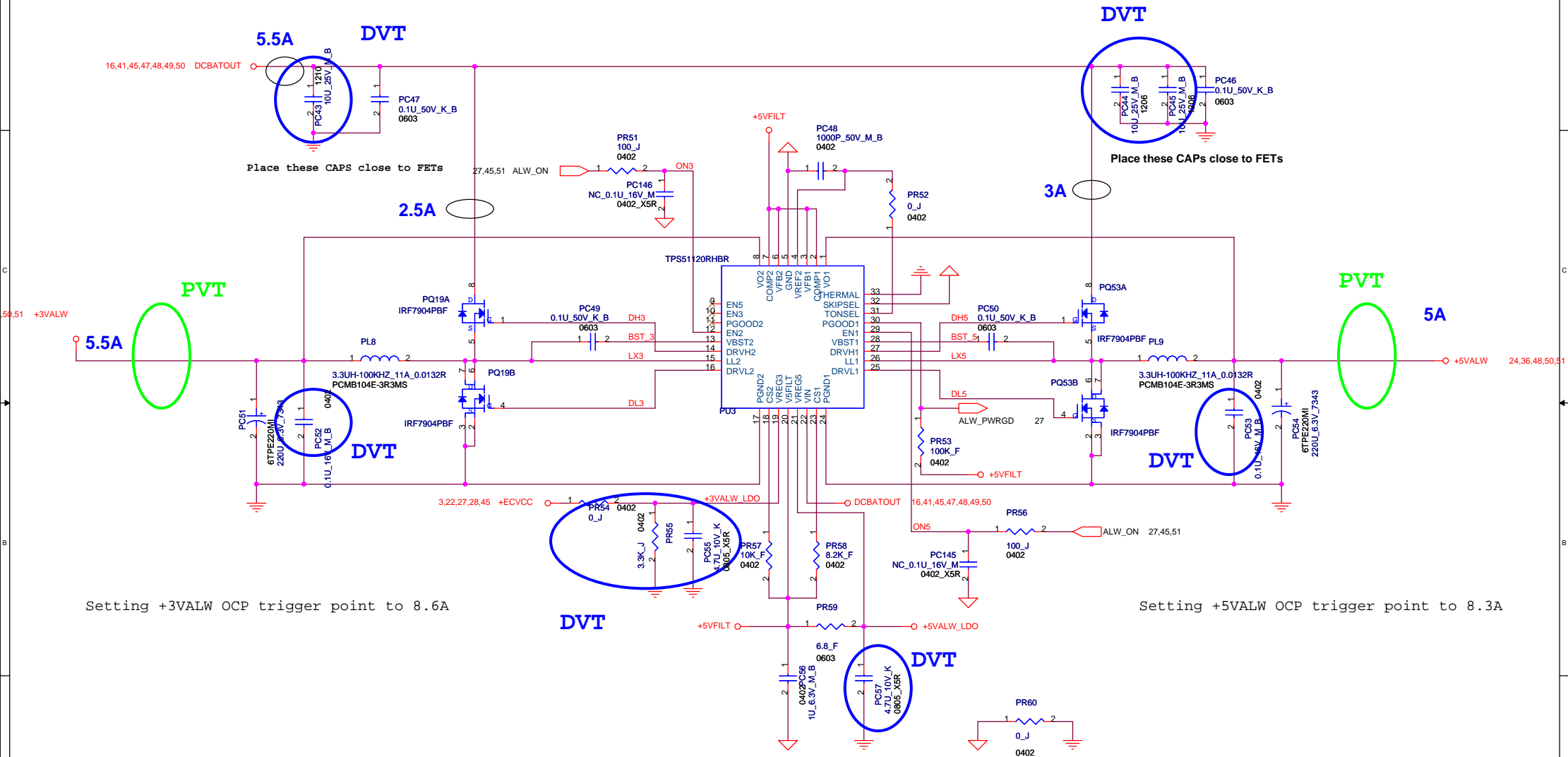


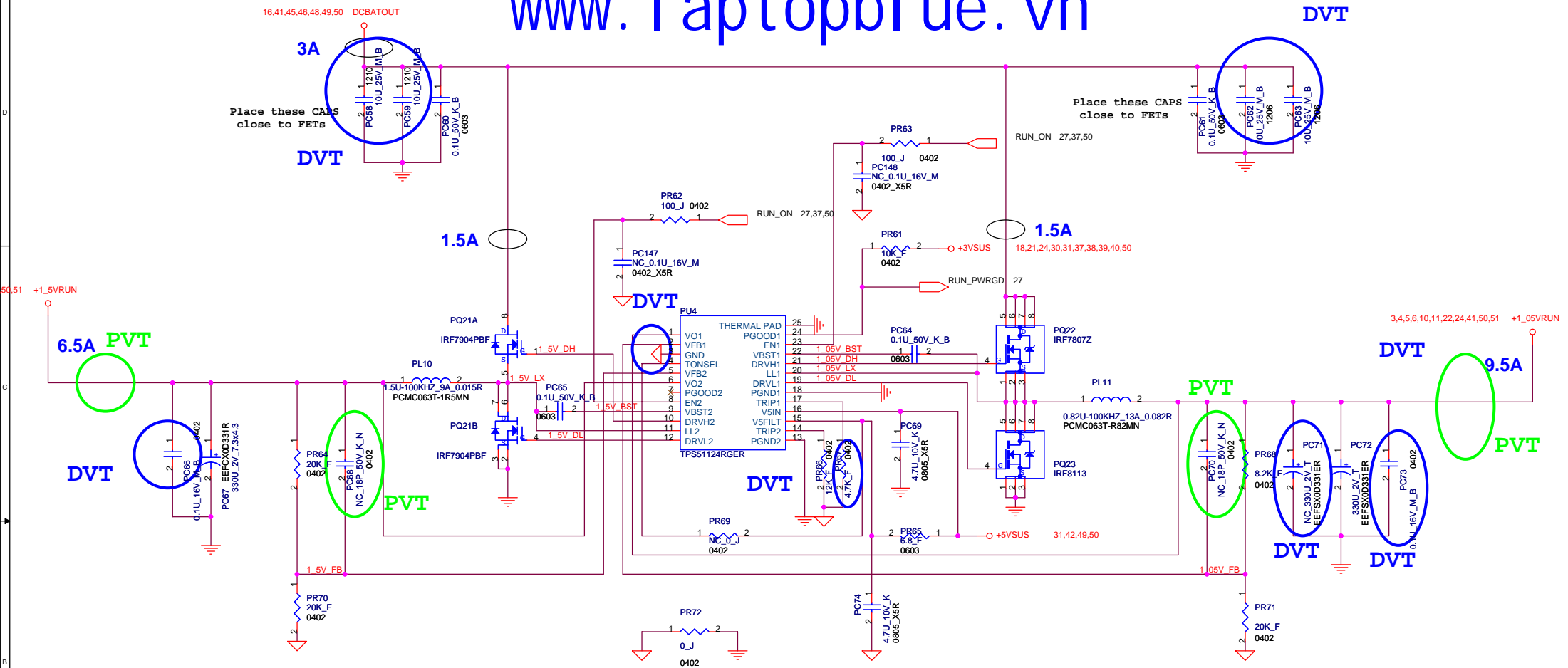






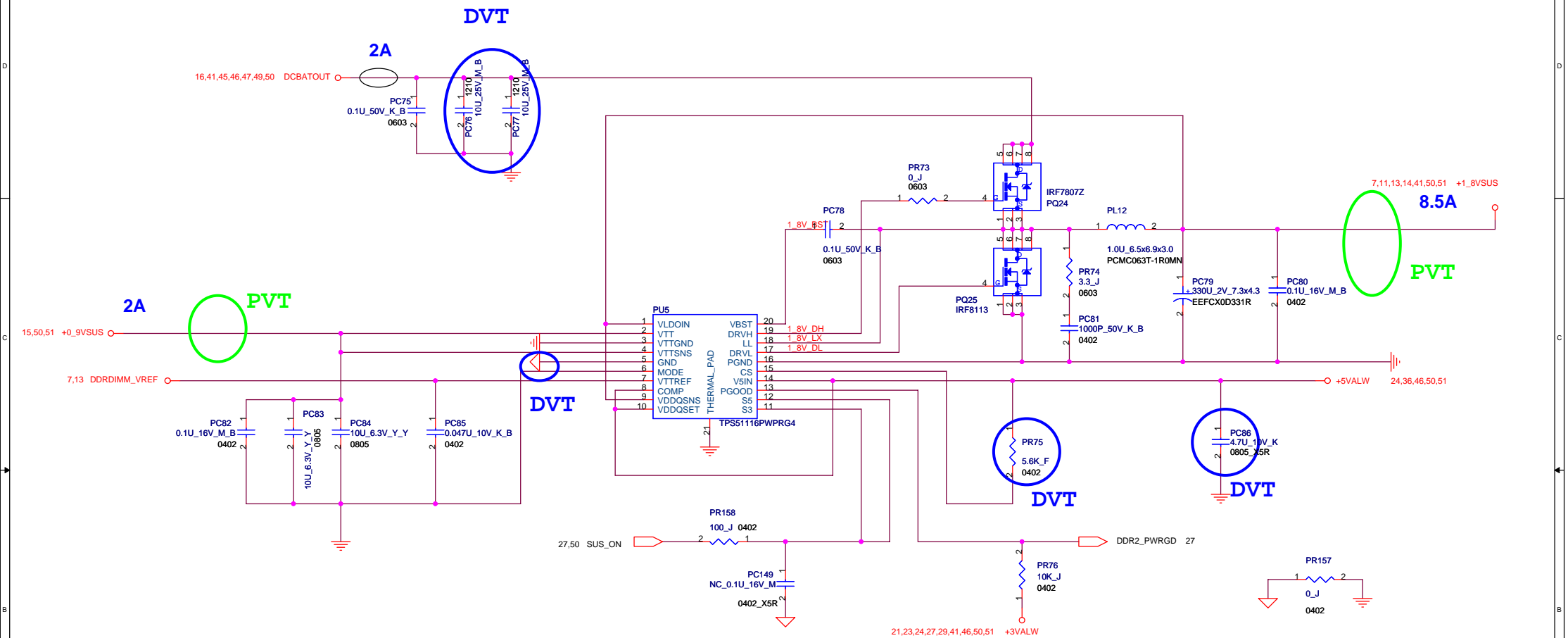




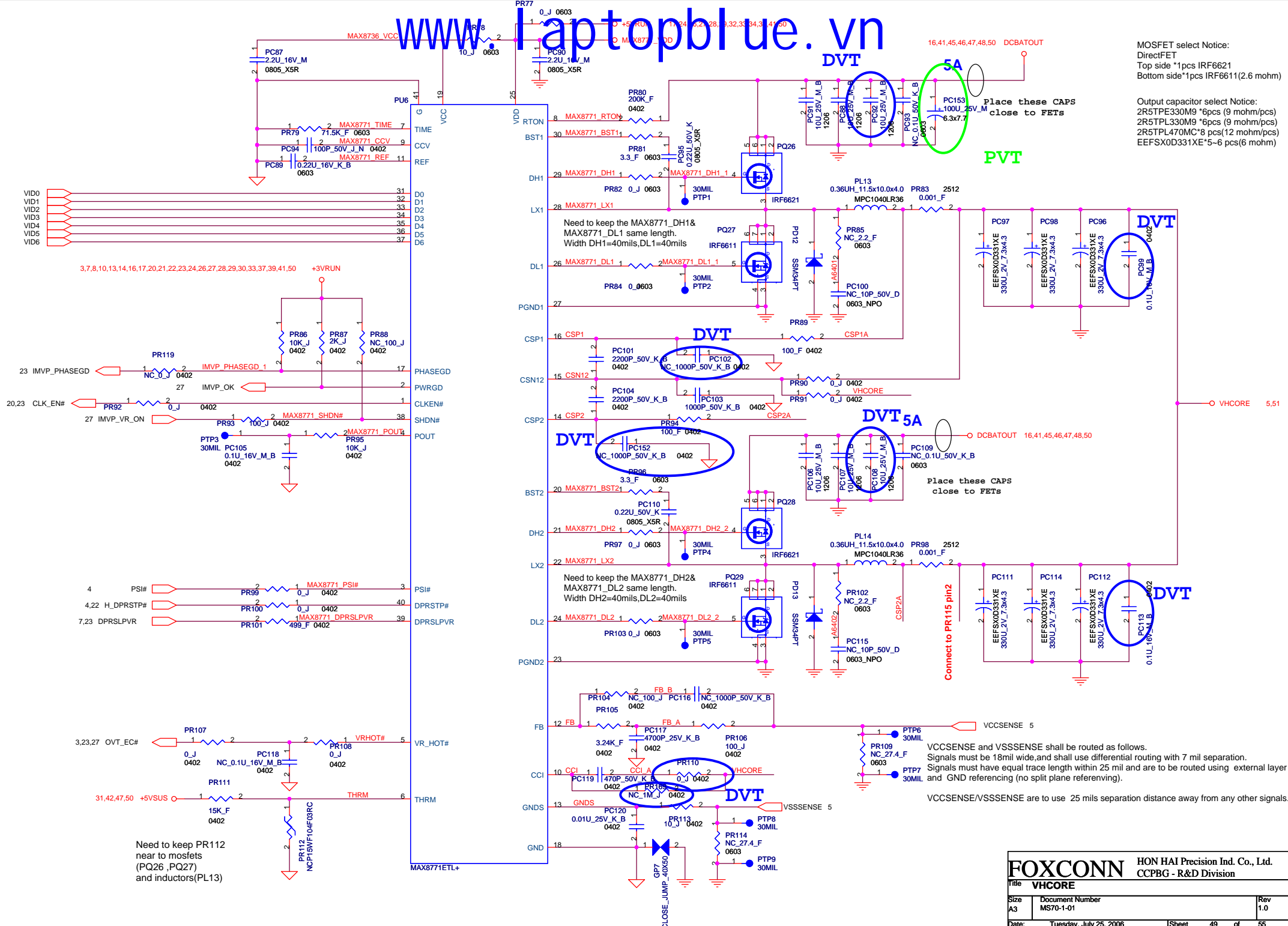


Setting +1.5VRUN OCP trigger point to 10.5A

Setting +1.05VRUN OCP trigger point to 12.8A

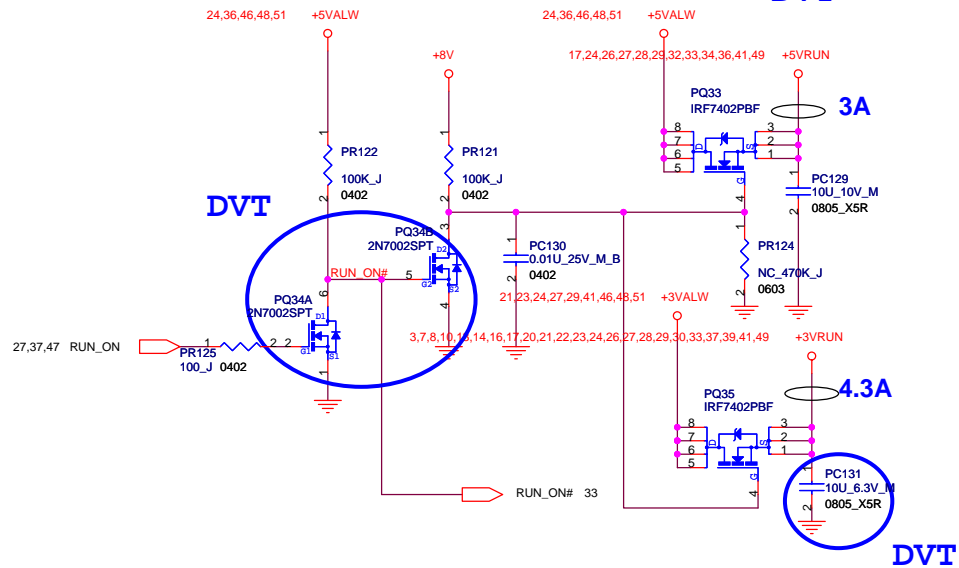
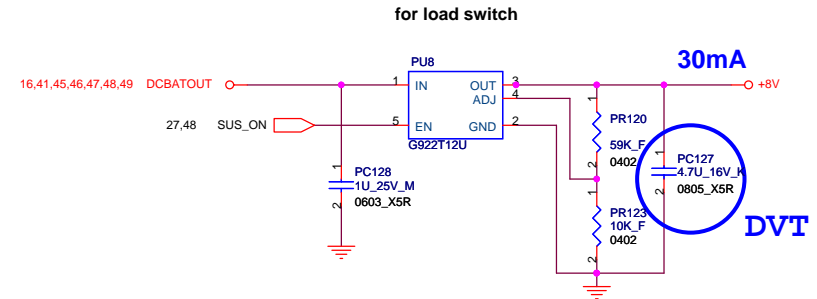
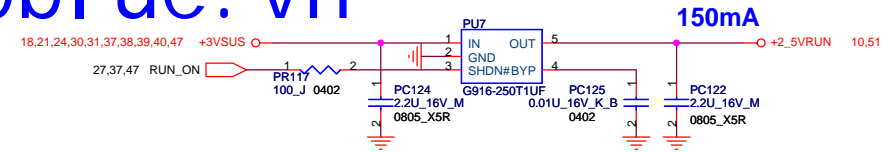
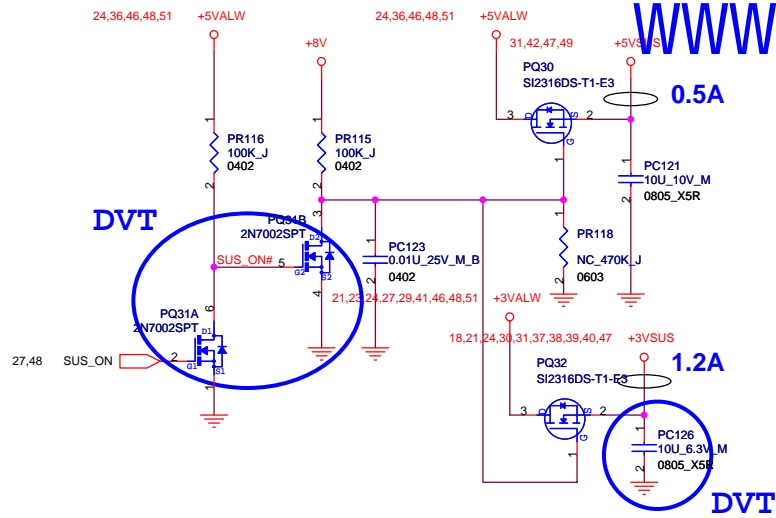


Setting +1_8VSUS OCP trigger point to 11.6A

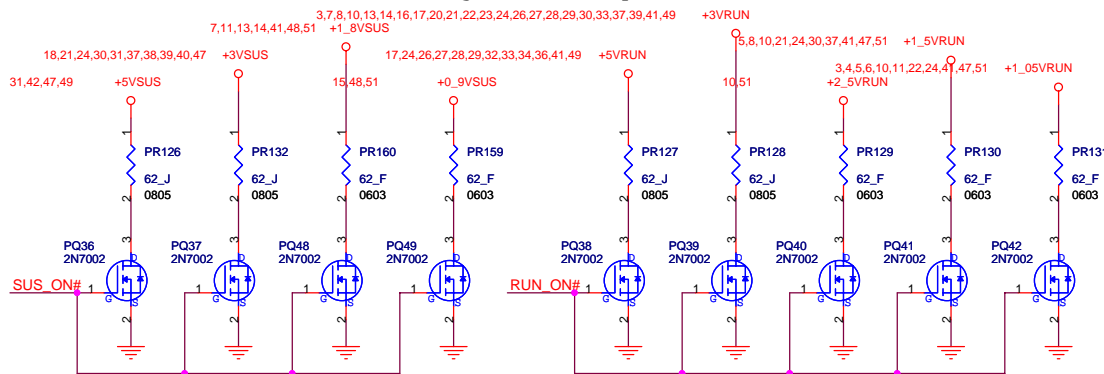


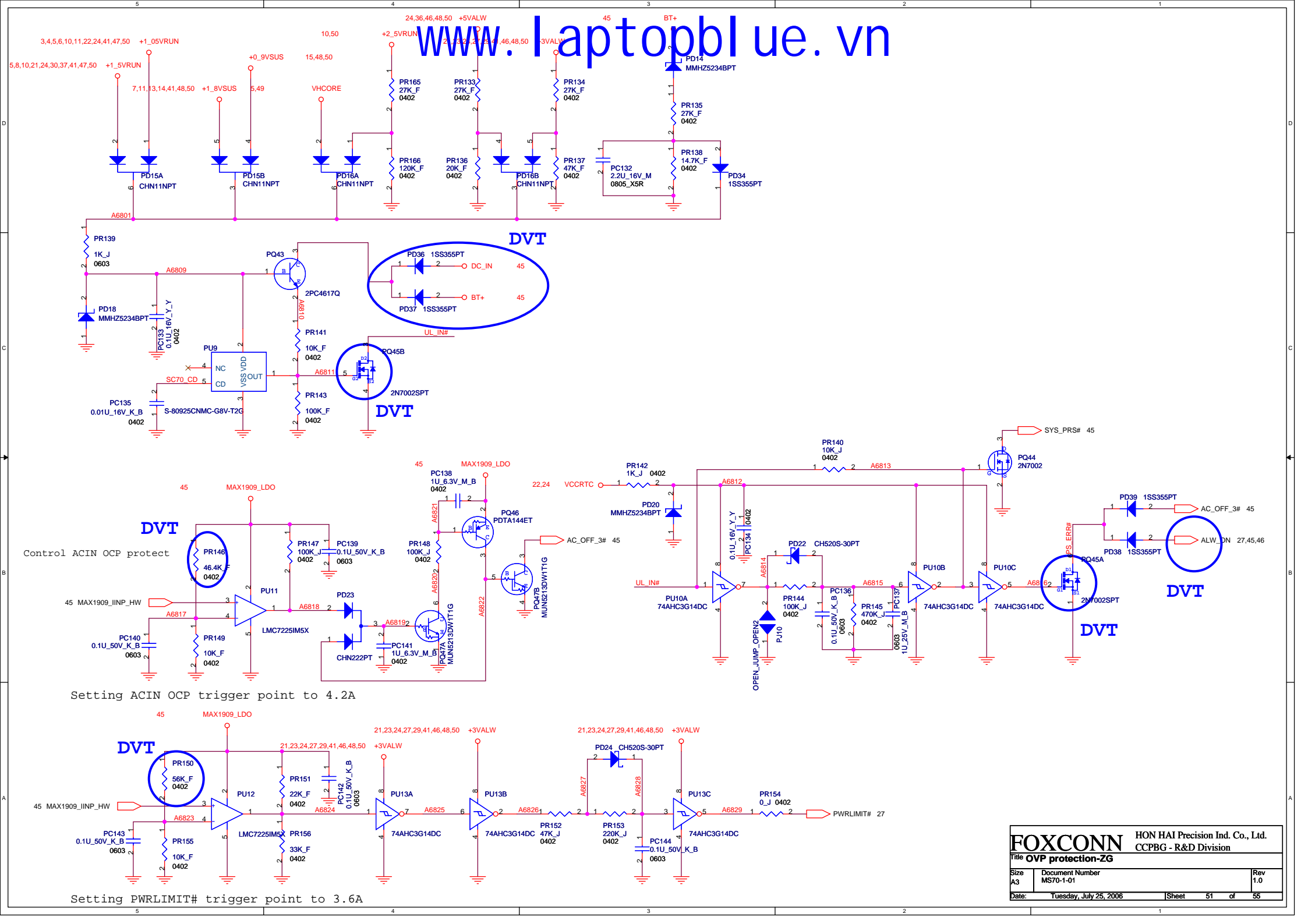
MOSFET select Notice:
DirectFET
Top side *1pcs IRF6621
Bottom side *1pcs IRF6611(2.6 mohm)

Output capacitor select Notice:
2R5TPE330M9 *6pcs (9 mohm/pcs)
2R5TPL330M9 *6pcs (9 mohm/pcs)
2R5TPL470MC*8 pcs(12 mohm/pcs)
EEFSX0D331XE*5~6pcs(6 mohm)



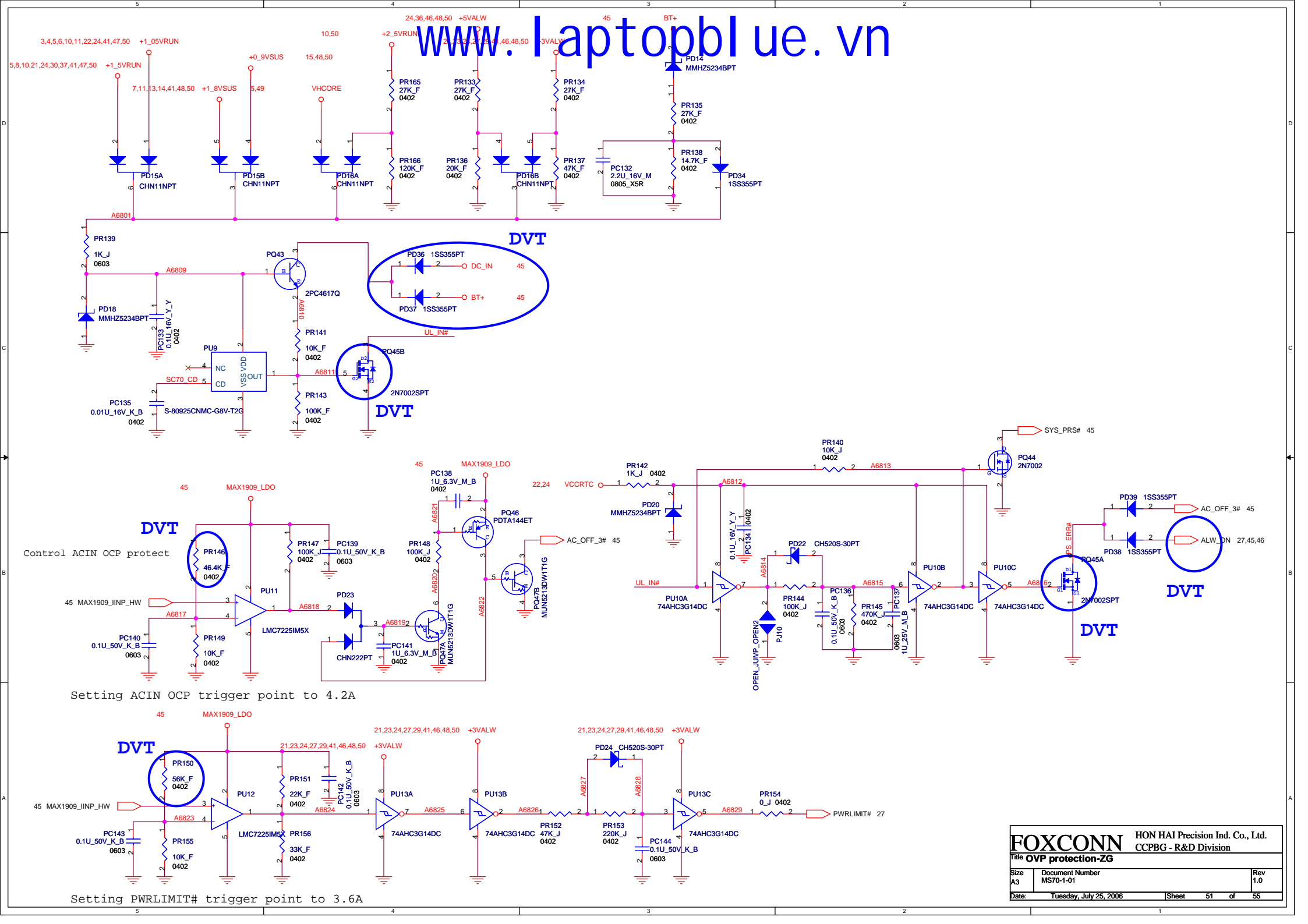
Discharge circuit for power-off





Control ACIN OCP protect

Setting ACIN OCP trigger point to 4.2A



Setting PWRLIMIT# trigger point to 3.6A

HISTORY (1)

(2006/04/25)

P.16 CN3 pin5 Add R515 0ohm for QDI LCD Panel doesn't support gamma correction issue.
P.17 Add VGA_CRT_DET# connect to U15 pin29 for Semi-PnP function fail issue.
P.20 U11 pin57 add R516 10Kohm pull down for LAN can't be recognized issue.
P.27 U15 pin29 add R513 10Kohm pull up for Semi-PnP function fail issue.
P.27 U15 pin12 add R514 100Kohm pull down for EC hardware strap pin.
P.27 U15 pin171 add C597 1000pF to ground for FAN can't be controlled issue.
P.31 R304 change from 4.7k to 10k for FAN can't be controlled issue.

(2006/05/03)

P.49 Add netname(IMVP_PHASEGD_1) on the right side of PR119 for application modification.
P.45 PC11, PC21, PC151, PR167 change from DNI to mount for DC_IN spike issue
P.45 PD4 pin1 and pin3 exchange for application modification
P.46 PR54 change from 100 ohm to 0 ohm for PU3 output abnormal issue
P.46 PR55 change from DNI to mount for can't boot up issue
P.47 PU4 pin3 change from GND to GND_SIGNAL_1D5V for application modification
P.48 PU5 pin5 change from GND to GND_SIGNAL_1D8V for application modification
P.49 Add PR169 NC_1M ohm for MAX8771 CCI issue
P.49 PC96, PC97, PC98, PC111, PC112, PC114 change from SANYO 2R5TPL330M9 to Panasonic EEFSX0D331XE for purchase difficult

(2006/05/04)

P.37 Add Q162(NC), R517, R518(NC) for Express card power sequence issue

(2006/05/15)

P.45 PR32 change from 22K_F to 23.2K_F for ACIN Vcls function trigger point correct to 3.4A
P.47 PR67 change from 8.2K_F to 4.7K_F for +1_05VRUN OCP trigger point correct to 12.8A
P.48 PR75 change from 6.8K_F to 5.6K_F for +1_8VSUS OCP trigger point correct to 11.6A
P.51 PR146 change from 51K_F to 46.4K_F for DCBATOUT OCP trigger point correct to 4.2A
P.51 PR150 change from 62K_F to 56K for PWRLIMIT# function trigger point correct to 3.6A
P.46 Delete PJ1, PJ2
P.47 Delete PJ3, PJ4, PJ5, PJ6
P.48 Delete PJ7, PJ8, PJ9

(2006/05/17)

P.45 Add PR41 10K_J_0402 and PQ13 2N7002 for preventing leakage current
P.45 PR17 change from 0.015_J 0805 to 0.015_F 1206 for application modification
P.46 PR55 change from 1K_J to 3.3K_J and PC55, PC57 change from 10u_25V X5R 1206 to 4.7u_10V X5R 0805 for reducing +ECVCC static current
P.47 PC71 change from mount to DNI for application modification
P.49 PR110 change from 20K_J to 0_J and PC102 change from DNI to mount for MAX8771 CCI issue
P.45 Delete PR19, PR28, PR33, PR35, PR37, PR38, PR39, PR40, PC31, PQ7, PQ10, PQ14, PQ16 For +ECVCC needed to work in battery only mode
P.51 Delete PD36, PD37 for +ECVCC needed to work in battery only mode
P.51 The net of VSOURCE (PQ43 pin3) change to DCBATOUT for +ECVCC needed to work in battery only mode
P.51 The net of BATT_EN (PD38 pin2) change to ALW_ON for +ECVCC needed to work in battery only mode

(2006/05/19)

P.13 C155 change from 2.2U_10V_Y_Y to 1000P_16V_K ; C159 change from 0.1U_16V_Y_Y to 1000P_50V_K for EMC DDR2 solution
P.14 C168 change from 2.2U_10V_Y_Y to 1000P_16V_K ; C172 change from 0.1U_16V_Y_Y to 1000P_50V_K for EMC DDR2 solution
P.15 C177,C179,C181,C191,C192,C196 change from 0.1U_16V_Y_Y to 1000P_50V_K for EMC DDR2 solution
P.39 C547,C548 change from 18P_50V_J_N to 22P_50V_J for PC18402's Crystal issue
P.30 LED1 change from HT-110Y to HT-110UYG for LED color requirement
P.45 PCN1 change from MOLEX_53259-0229 to FOX_GS53020-00580-7F
P.45 PC11 change from 10U_25V_M_1206 to 10U_25V_M_B_1210 for purchase convenient
P.46 PC43,PC44,PC45 change from 10U_25V_M_B_1206 to 10U_25V_M_B_1210 for purchase convenient
P.47 PC58,PC59,PC62,PC63 change from 10U_25V_M_B_1206 to 10U_25V_M_B_1210 for purchase convenient
P.48 PC76,PC77 change from 10U_25V_M_B_1206 to 10U_25V_M_B_1210 for purchase convenient
P.48 PC86 change from 4.7U_10V_K_B_1206 to 4.7U_10V_K_0805 for purchase convenient
P.50 PC126,PC131 change from 10U_10V_M to 10U_6.3V_M for purchase convenient
P.50 PC127 change from 4.7U_25V_K_B_1206 to 4.7U_16V_K_0805 for purchase convenient

(2006/05/22)

P.32 CN21 change from FOXCONN_GB11060_0221_7F to FOXCONN_GB5RF060_1200_7F for ME's requirement
P.29 CN30,CN31 change from foxconn_gb11120_0221_7F to FOXCONN_GB5RF120_1200_7F for ME's requirement
P.29 CN31 change from mount to DNI for ME's requirement
P.50 PQ31,PQ34 change from DIODES,2N7002DW-7-F to CHENMKO,2N7002SPT for purchase convenient
P.51 PQ45 change from DIODES,2N7002DW-7-F to CHENMKO,2N7002SPT for purchase convenient
P.27 Q149 change from DIODES,2N7002DW-7-F to CHENMKO,2N7002SPT for purchase convenient

(2006/05/23)

P.47 PQ43,PC47 change from 10U_25V_M_1210 to 10U_25V_M_B_1206 for ME limit of height
P.46 PQ44,PC45 change from 10U_25V_M_B_1210 to 10U_25V_M_B_1206 for ME limit of height
P.46 Add PJ1,PJ2 for test request
P.47 Add PJ3,PJ4 for test request
P.48 Add PJ7,PJ9 for test request

(2006/05/24)

P.42 CN25,CN26(USB CONN) change from FOX_UB11193_C1301_4F to UB11193-C1308-4F for ME's requirement
P.17 CN5(VGA CONN) change from FOX_DZ11A91_MB221_4F to DZ11A91-MW223-4F for ME's requirement

(2006/05/25)

P.47 Add PJ6 for test request
P.26 CN8 footprint change from FOXCONN_LD2722H_S469 to FOXCONN_LD2722H_S469_MS70 for ME PAD request
P.32 C389 change from 22U_10V_Y_Y_1206 to 10U_10V_M_0805 and add C598 10U_10V_M_0805 for limit of ME
P.43 H1,H2,H3,H4 change from hole_c158d158n to HOLE_C148D148N for ME request
P.43 H7 change from hole_tsru144bsru177d98 to hole_trc321x287brud98 for ME request
P.43 H26 change from hole_c120d100 to hole_tsrlbr413x343d98 for ME request
P.43 H23 change from hole_tc256brclD295d98_v1 to hole_tc256bs502x295d134_v1 for ME request
P.43 H25 change from hole_tc256brcul48d98 to hole_tc256bs295x384d134 for ME request
P.43 H5 change from hole_tc256bc315d98 to hole_tc256bc256d98 for ME request
P.43 H10 change from hole_tshrd144bc315d98 to hole_trc287x301bc256d98 for ME request
P.43 H9 change from hole_trcd144brcl177d98 to hole_trc287x321brcd98 for ME request
P.43 H12 change from hole_tc256bsrcul44d98 to hole_tc256bc287d98 for ME request

(2006/05/26)

P.42 CN25,CN26 change from FOXCONN_UB11193_C1308_4F to FOXCONN_UB11193_C1308_4F_HM for solder issue
P.43 H9 change from hole_trc287x321brcd98 to hole_trc287x256bshclD98 for ME request
P.17 D3 change from 16-CH500H4-0P00 to 16-SCS500V-4000 for purchase convenient
P.10 C119 change from 1C-2B30105-K000 to 1C-2B30475-K100 ;
C120 change from 1C-2B20103-K001(0402) to 1C-2B30475-K100(0603) for +1_5VRUN_HMPLL noise issue
P.48 Add PJ8 for test request

(2006/06/01)

P.16 Add R519(0ohm 0402) for desinger set "LCDID3" to "0" by mistake.
P.49 PC102 change from mount to NC for application modification
P.46 PC52,PC53 change from 0.1U_16V_Y_Y(Y5V) to 0.1U_16V_M_B(X5R) for application modification
P.47 PC66,PC73 change from 0.1U_16V_Y_Y(Y5V) to 0.1U_16V_M_B(X5R) for application modification
P.49 PC99,PC113 change from 0.1U_16V_Y_Y(Y5V) to 0.1U_16V_M_B(X5R) for application modification
P.36 Q25 pin2 netname change from +5VRUN to +5VAMP
P.36 R376 pin2 netname change from GND to A_GND
P.36 Add U38,R520(NC) for SPK_MUTE_EN for Vista requirement
P.36 Add NET "SPK_MUTE_EN" from U38 pin2 to U15 pin99 for Vista requirement
P.40 Change U36,U37 from RT9702 to RT9703, Add R521-R524.
P.27 Delete Q149.

(2006/06/02)

P.16 R515,R519 change from mount to NC
P.36 Add R525(NC) for Audio mute option
P.49 Add PC152 for application modification
P.26 R510 change from mount to NC for application modification
P.22 R188 change from mount to NC for application modification

(2006/06/05)

P.49 PC92,PC108 change from NC to mount for design rating
P.40 U36,U37 change from RT9703 to RT9702, Del R521-R524 for RT9703 phase out issue
P.49 Add PC153 for solving audible noise
P.19 Add R526 for LAN application modification

(2006/06/06)

P.13 Add C599,C600(1000P_50V_K) ; C155 change from 1000P_16V_K to 2.2U_10V_Y_Y ; C159 change from 1000P_50V_K to 0.1U_16V_Y_Y for EMC solution
P.14 Add C601,C602(1000P_50V_K) ; C168 change from 1000P_16V_K to 2.2U_10V_Y_Y ; C172 change from 1000P_50V_K to 0.1U_16V_Y_Y for EMC solution
P.15 Add C603,C604(1000P_50V_K) ; C177,C179,C181,C191,C192,C196 change from 1000P_50V_K to 0.1U_16V_Y_Y for EMC solution
P.51 Add PD36,PD37 for application modification
P.19 C253 change form 0.1U_16V_M_B to 5P_50V_C ; C254 change from 5P_50V_C to 0.1U_16V_M_B ; Add L58 ; Del R526 for LAN application modification

FOXCONN		HON HAI Precision Ind. Co., Ltd.	
History (1)		CCPBG - R&D Division	
File	Document Number		Rev
Size	MST0-1-01		1.0
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Date:	Tuesday, July 25, 2006	Sheet	52 of 55

HISTORY (2)

(2006/06/07)
P.26 R245 change from NC to mount for application modification

(2006/06/13)
P.33 R325 change from mount to NC for acoustic noise.
P.36 U38 change from 74AHC1G08GW to 74AHCT1G08GW for Vih can't meet EC spec.

PVT

(2006/07/12)
P.47 Change PC68, PC70 from mount to NC for TPS51124 OVP issue.
P.27 Add R526 (1K ohm) series on ALW_ON net to prevent EC damage issue.
P.46 Delete PJ1, PJ2
P.47 Delete PJ3, PJ4, PJ6
P.48 Delete PJ7, PJ8, PJ9
P.32 F1,R320,R321,L32,C598,C389,C390,C391,CN21 change from mount to NC for cancel Oide function
P.37 U33 change from 15-TPS2231-0000(24pin) to 15-TPS2231-0002(20pin) for purchase convenient
P.40 U36,U37 change from 15-RT9702A-0000 to 15-TPS2055-0000 ;
Add R527,R528 for MS_PWR_CTRL and SD_PWR_CTRL are recognized to be high level by accident

(2006/07/20)
P.26 CN9 vendor part number change from QT8H0506-13T3R-7F to QT8H0506-13T3R-4F
for packing type change.
P.49 PC153 change from 1C-10X0107-M403 to 1C-1XX0107-M400 for purchase convenient.
P.29 Delete CN31 for touch pad application modification.
P.22 R195 change from 47ohm to 0ohm ; C293 change from NC to mount for EMI solution.
P.29 LED6 change from 16-HT210DY-G000 to 16-HT210UD-UY00 for ME brightness issue.
LED5,LED7 change from 16-HT110Y0-0000 to 16-HT110UY-0000 for ME brightness issue.

(2006/07/21)
P.26 Add D22(NC) for ESD solution
P.30 R301 change from 120ohm to 47ohm for ME brightness issue

(2006/07/25)
P.30 C365 change from NC to mount for WLAN power ripple noise issue
P.26 Add VR1 for ESD solution
P.29 Add VR2,VR3(NC) for ESD solution
P.41 Add VR4,VR5(NC) for ESD solution

(2006/07/28)
P.29 Q155 change from 17-CHDTC14-4E01 to 17-2N70020-0000 for ME brightness issue.

(2006/07/31)
P.30 R301 change from 47ohm to 120ohm (LED1 is 10mA) for ME brightness issue
P.29 R418 change from 47ohm to 62ohm (LED5 is 18.75mA) for ME brightness issue
P.29 R424 change from 47ohm to 62ohm (LED6 is 19.5mA)
R425 change from 47ohm to 120ohm (LED6 is 10mA)for ME brightness issue
P.29 R426 change from 47ohm to 62ohm (LED7 is 18.9mA)

(2006/08/01)
P.31 Q17 change from 17-ME2301T-1000 to 17-S12301B-DS00 for Fan rotational speed issue

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