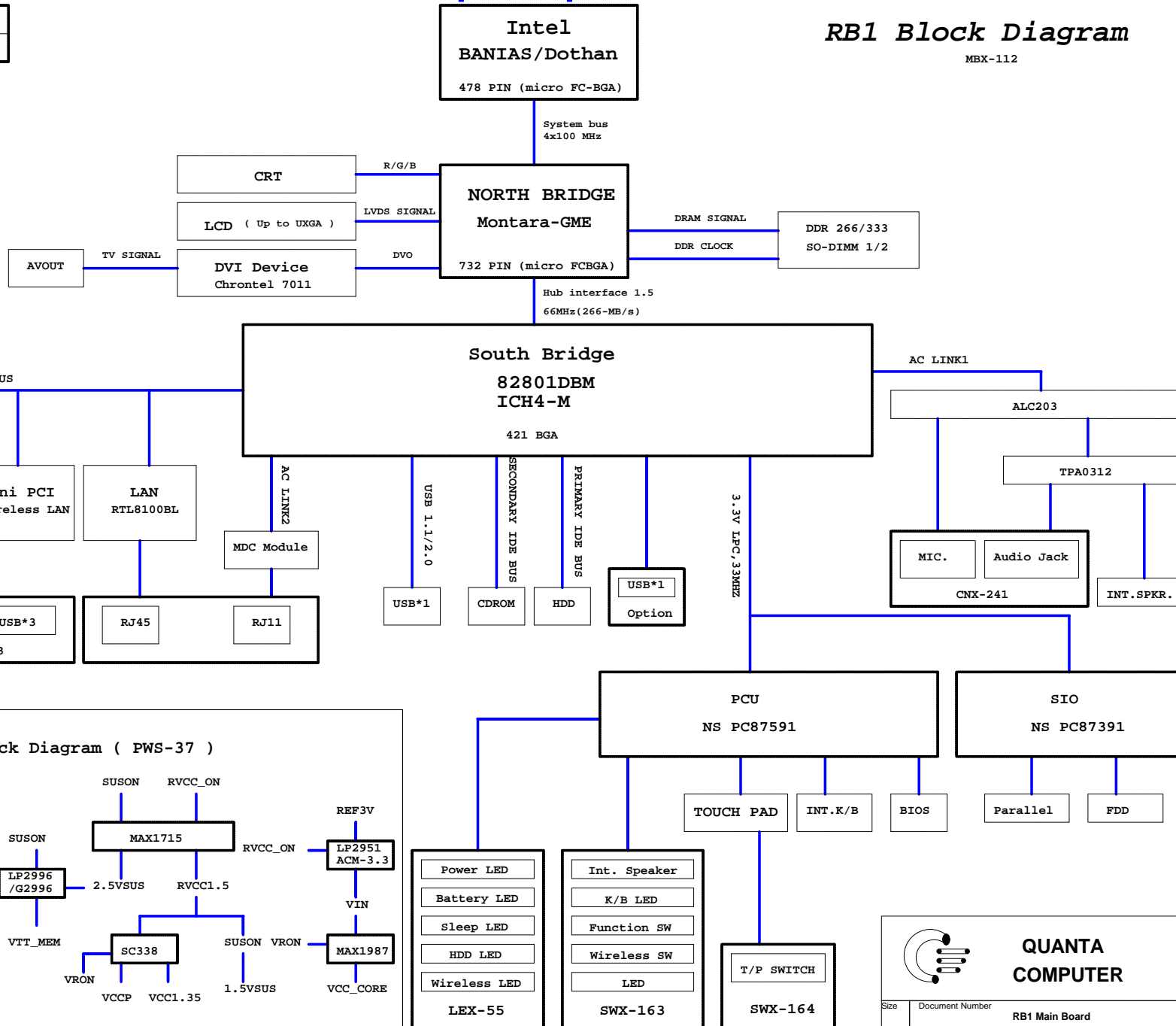


\* GM GME

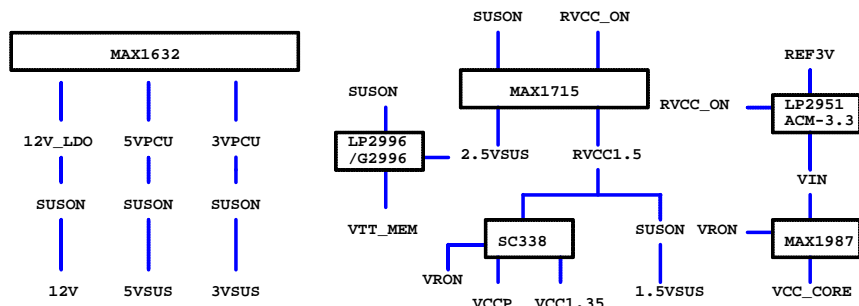
1. VCC PR57:	1.2V 17.4K/F	1.35V 14K/F
2. HLRCOMP R91:	27.4/F	37.4/F

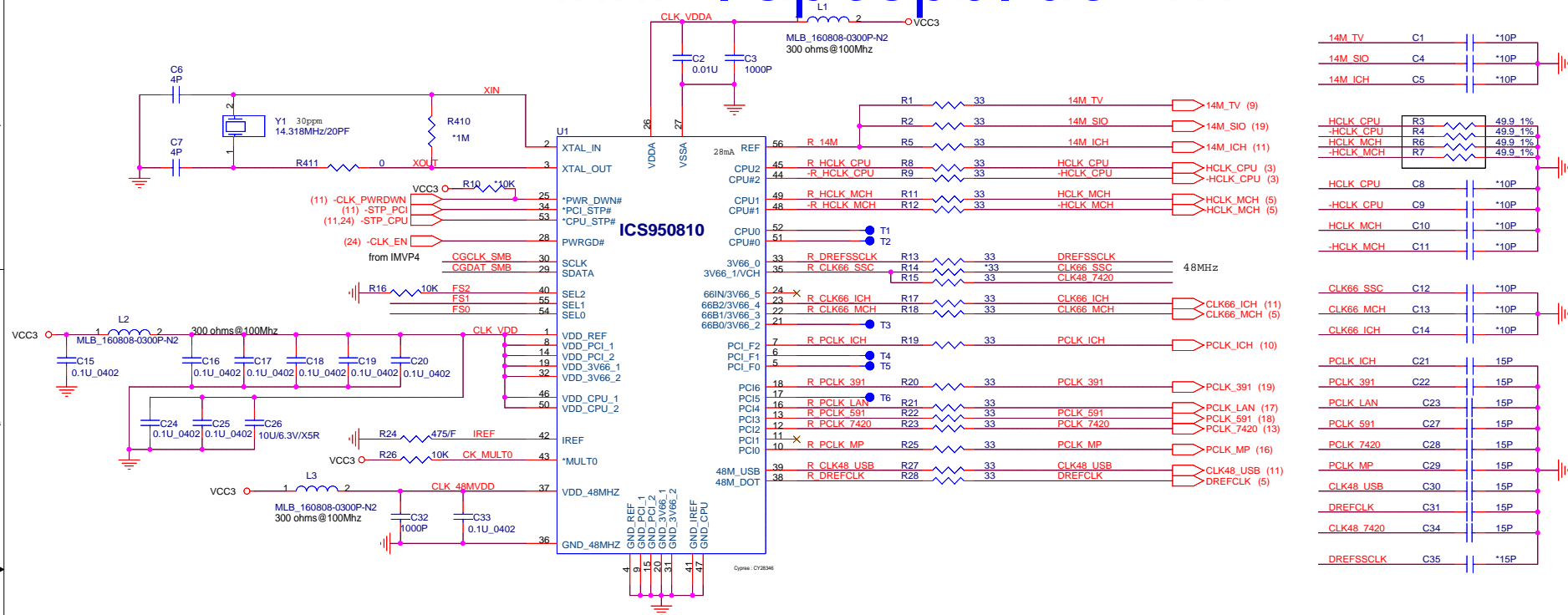
## RB1 Block Diagram

MBX-112



### POWER Block Diagram ( PWS-37 )





X : Pull high or pull low  
Mid : Pin float

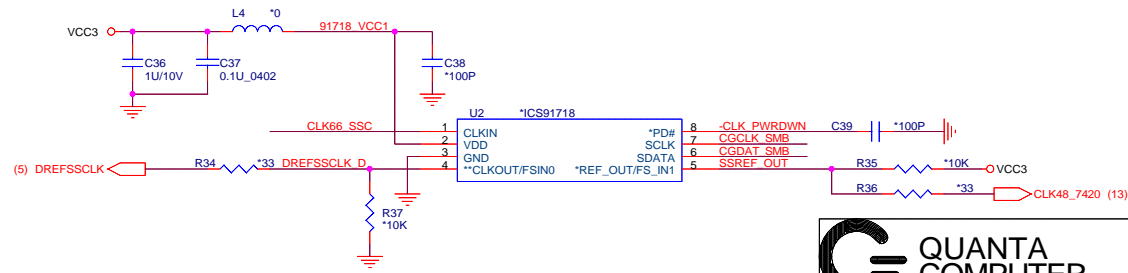
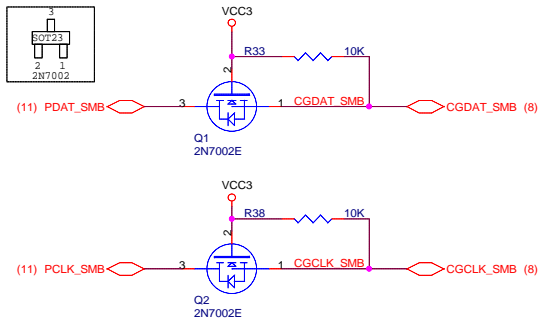
FS2	FS1	FS0	CPU	3V66[5:0]	PCI_F[2:0]
X	0	0	166.66	66.66	33.33
* X	0	1	100.00	66.66	33.33
X	1	0	200.00	66.66	33.33
X	1	1	133.33	66.66	33.33
Mid	0	0	Tristate	Tristate	Tristate
Mid	0	1	TCLK/2	TCLK/4	TCLK/8
Mid	1	0	Reserved	Reserved	Reserved
Mid	1	1	Reserved	Reserved	Reserved

1. Use ICS 91718  
Mount : U2,R35,R36,R37,R34,L4,R14  
No mount : R13,R15
2. Don't use ICS91718  
Mount : R13,R15  
No mount : U2,R35,R36,R37,R34,L4,R14

\* : Internal pull-up resistor  
\*\* : Internal pull-down resistor

CPUCLK IREF SETUP:

CK_MULT0	IREF	Output Current
0	221 1% Iref=5mA	Ioh = 4*IREF
1	475 1% Iref=2.32mA	Ioh = 6*IREF



FS IN1	FS IN0	MHz	SPREAD
0	0	14.318 In 48 Out	-1.0% down Spnd
0	1	14.318 In 66 Out	-1.0% down Spnd
1	0	48 In/Out, 66 In/Out	-1.0% down Spnd
1	1	148 In/Out, 66 In/Out	-1.0% down Spnd

THIS PART SHOULD NOT CONTAIN ANY SUBSTANCES WHICH ARE SPECIFIED IN SS-00259-1



Title: **CLOCK GENERATOR**

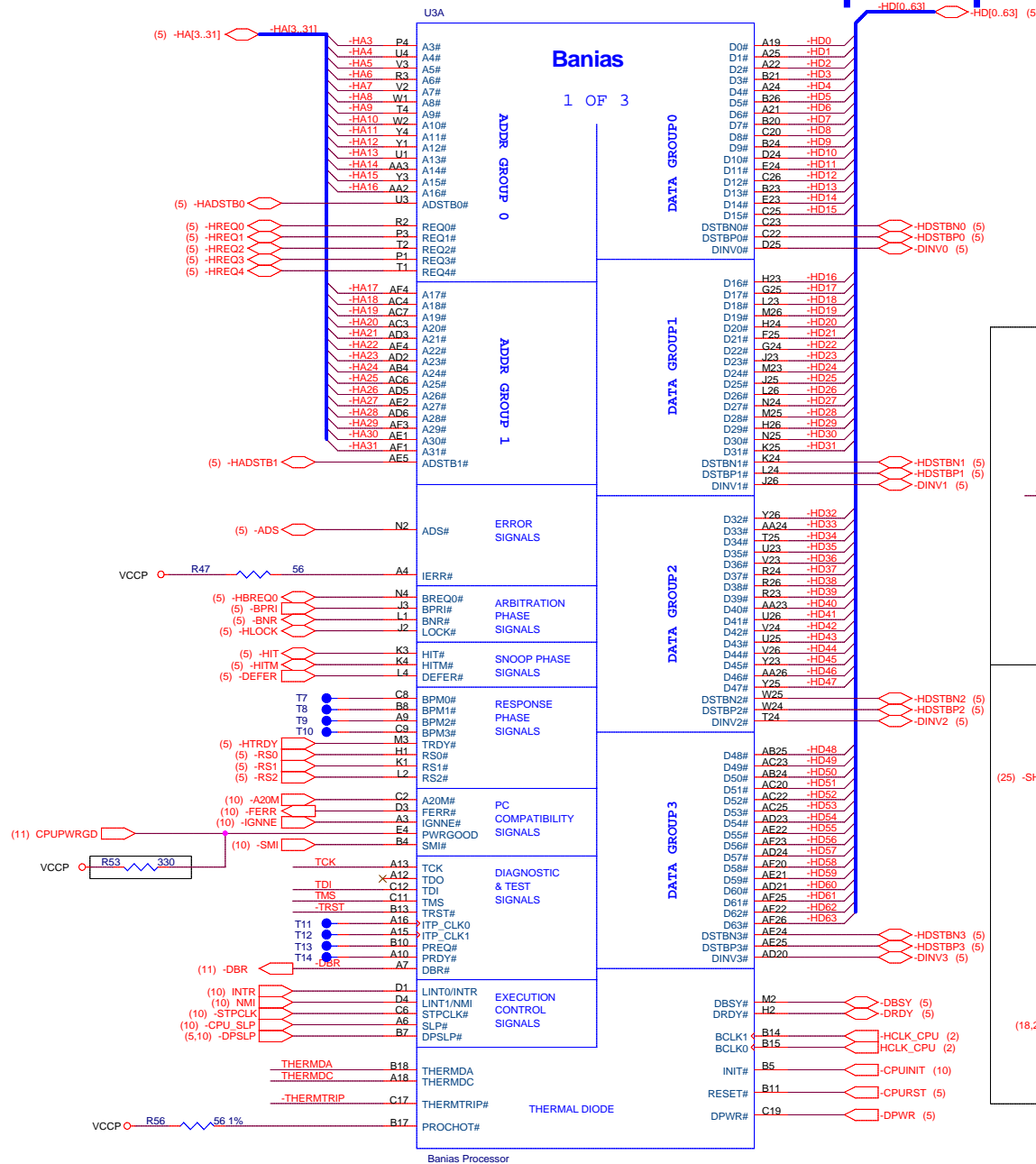
Size: **Document Number**

Custpm: **RB1 Main Board**

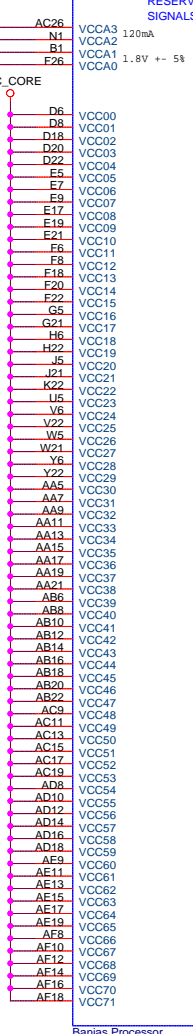
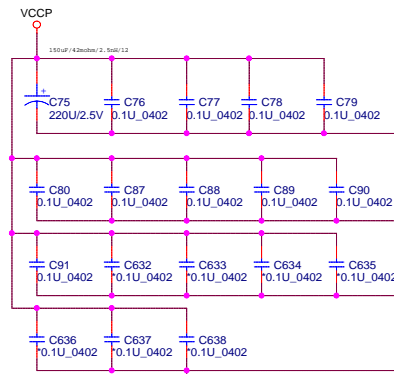
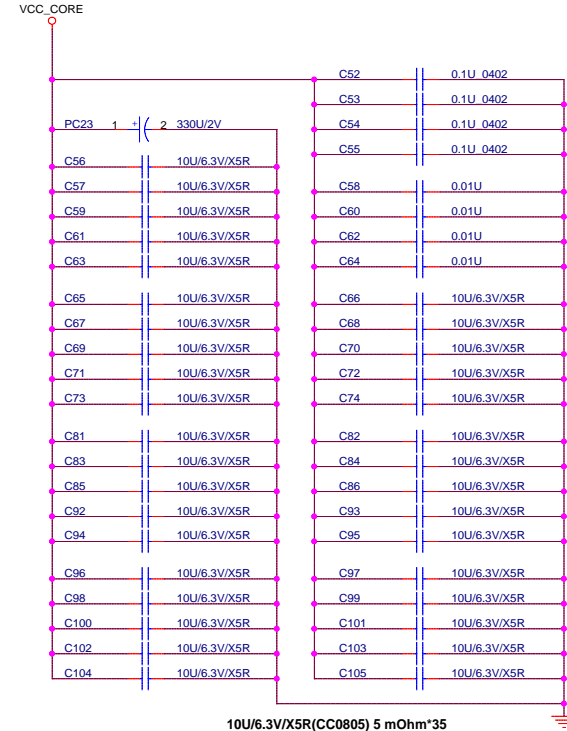
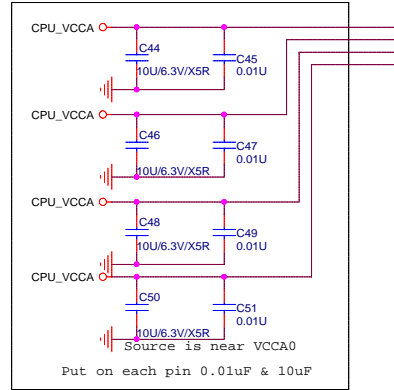
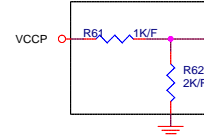
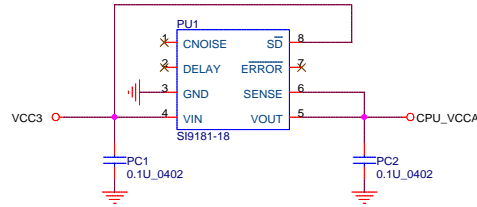
Date: **星期二, 九月 02, 2003**

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Rev: **1A**



VCCA:  
VCC1.5 : Intel Dothan Processor only.  
VCC1.8 : Intel Dothan/Pentium M Processor only.



Banias  
2 OF 3

POWER,  
GROUND,  
RESERVED  
SIGNALS

Banias Processor

Banias  
3 OF 3

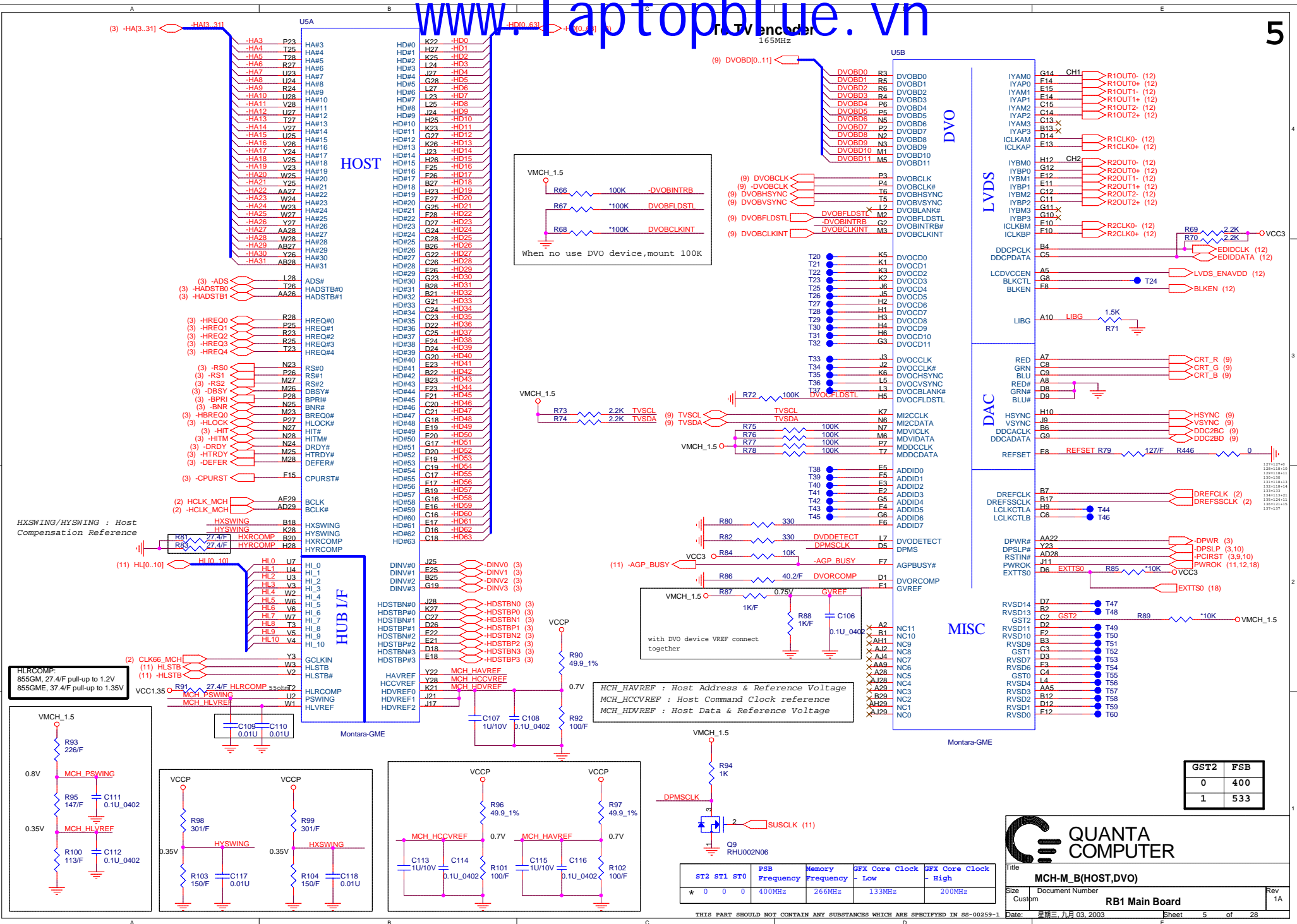
POWER, GROUND AND NC

VID  
1.05V OUTPUT

Banias Processor

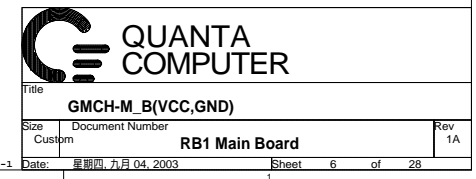


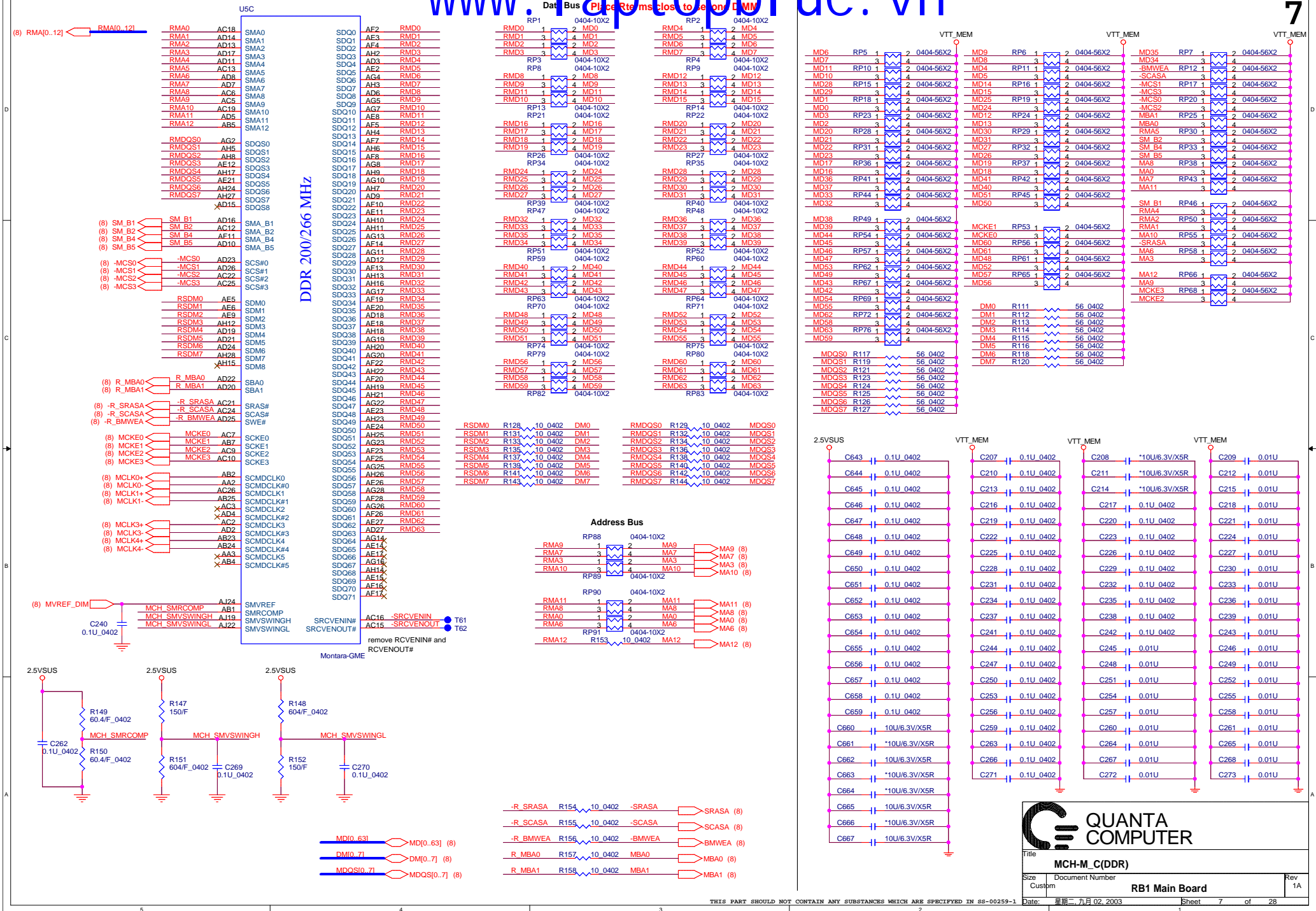
Title			Rev
BANIAS CPU-B			1A
Size	Document Number		
Custom	RB1 Main Board		
Date:	星期二, 九月 03, 2003	Sheet	4 of 28



Title		MCH-M B (HOST,DVO)	
Size	Document Number	Rev	1A
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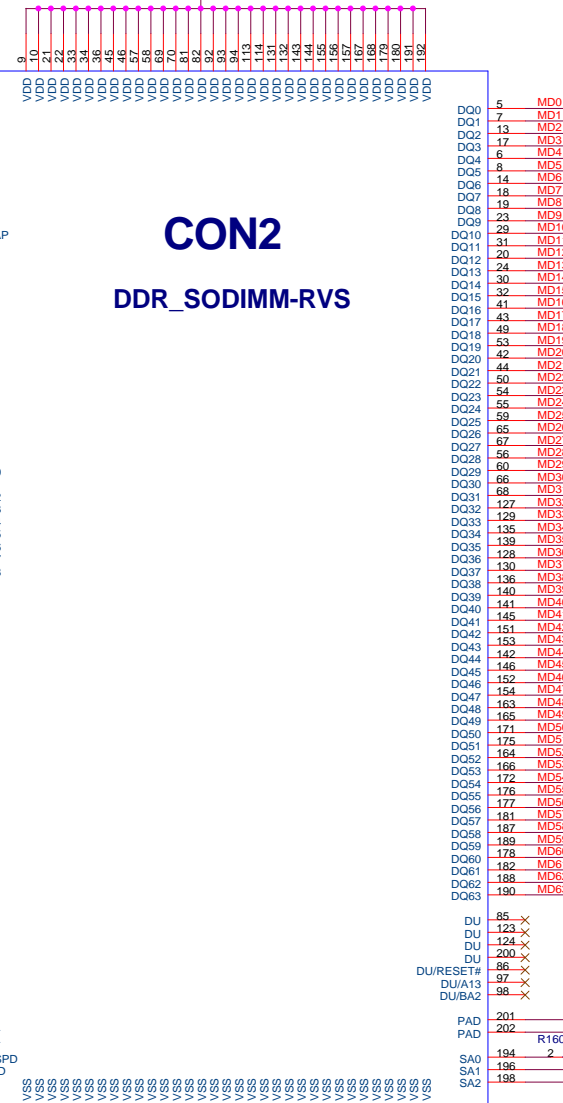
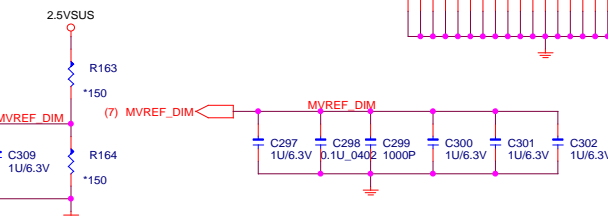
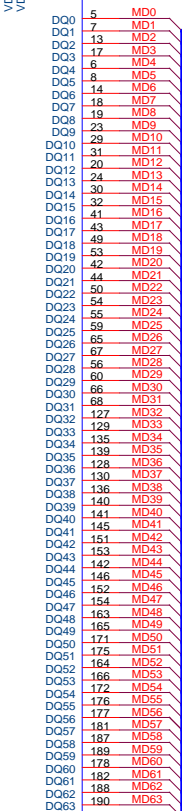
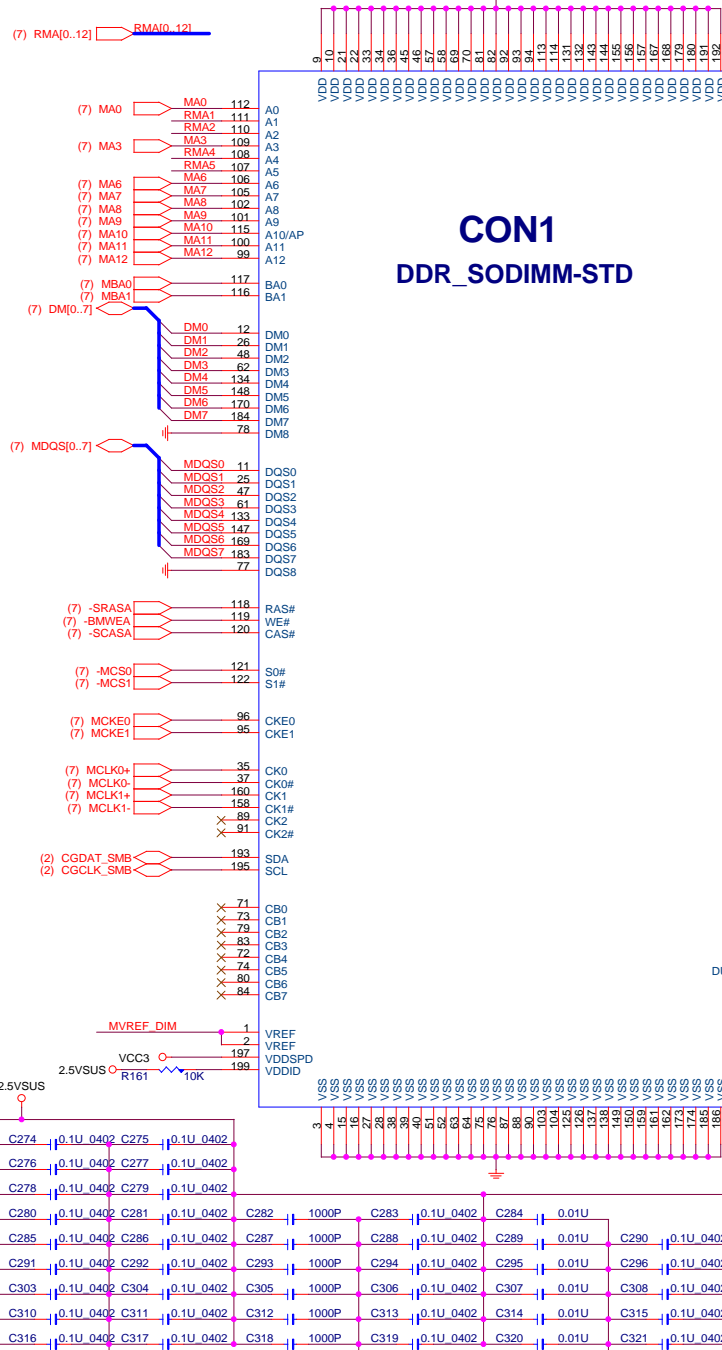




## DDR - Modules

2.5VSUS

2.5VSUS



Title			Rev
Memory SODIMM			
Size	Document Number	Sheet 8 of 28	
Custom	RB1 Main Board		
Date:	星期五, 九月 05, 2003	Rev 1A	



(5) TVSDA

(5) TVSCL

R169 2.2K

R170 2.2K

Q10 FDV301N

Q11 FDV301N

VCC1.5

VCC3

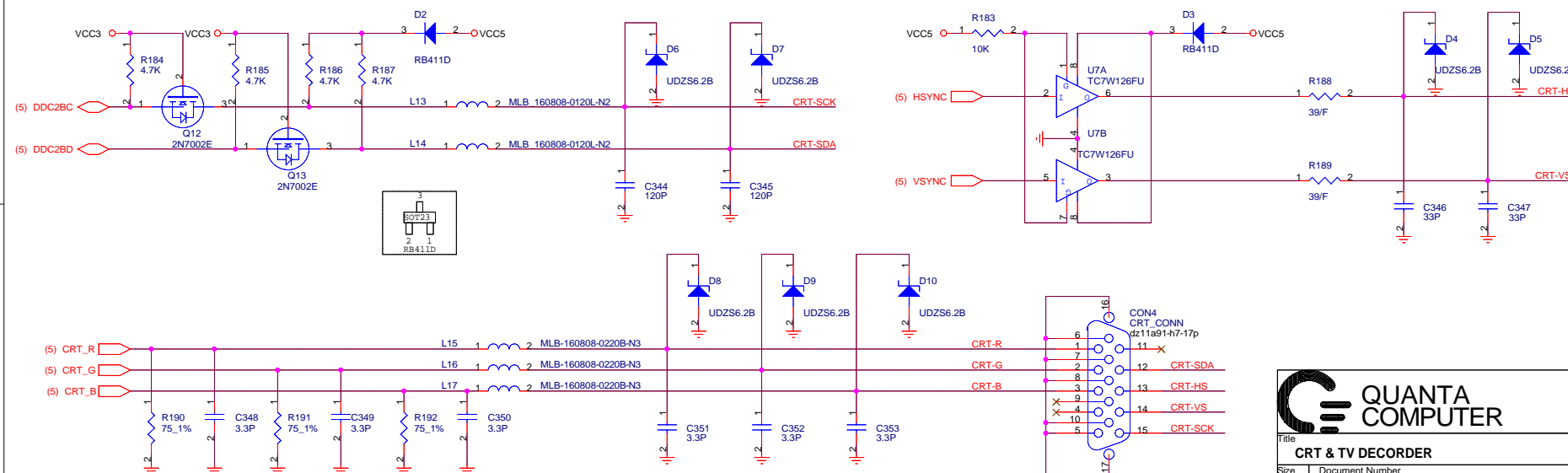
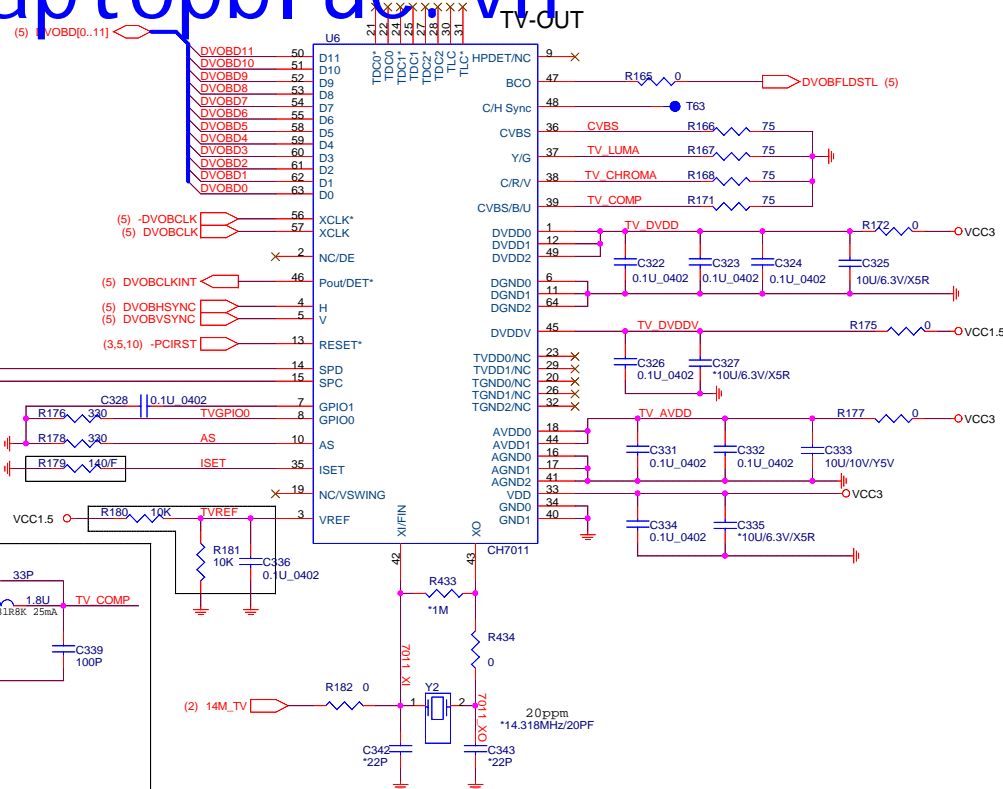
R173 2.2K

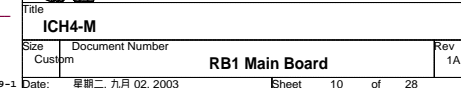
R174 2.2K

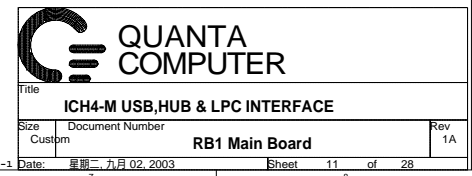
C329 22P

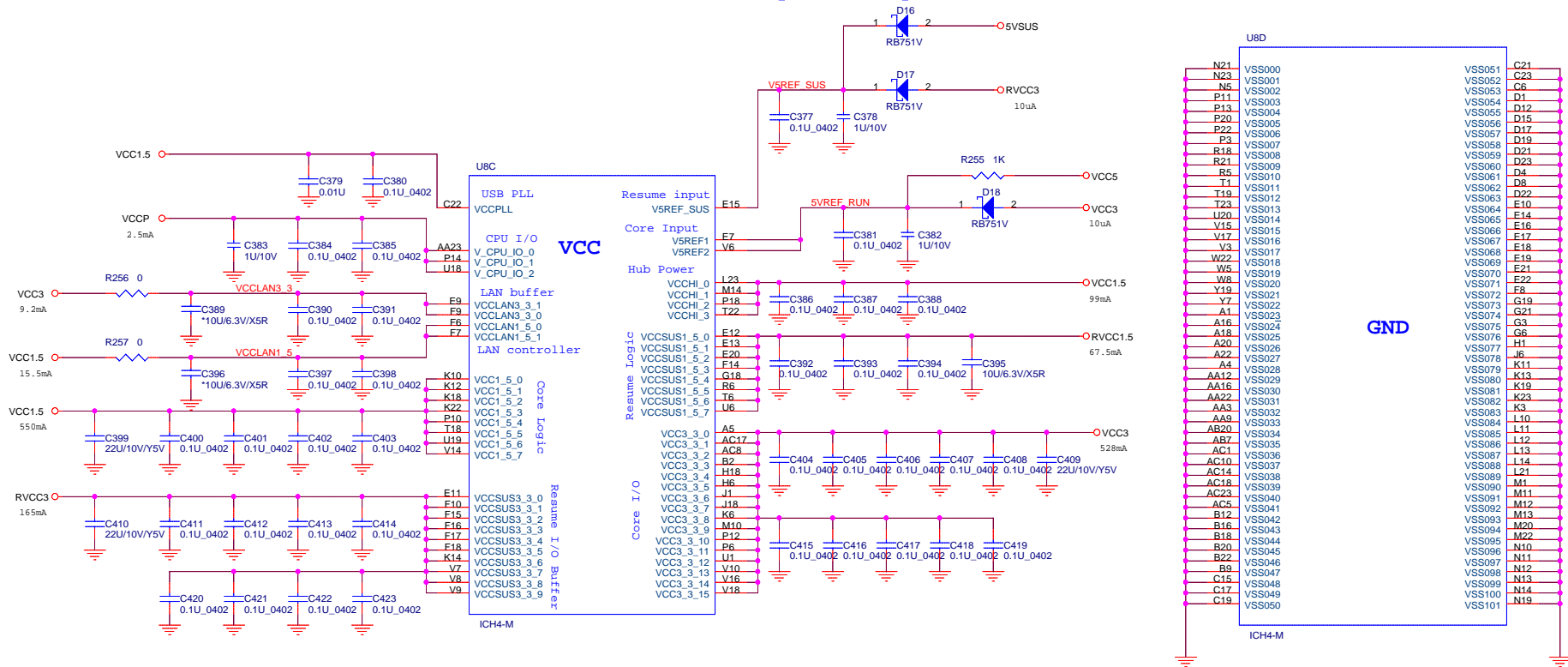
C330 22P

140/F put on between pin35 & 34

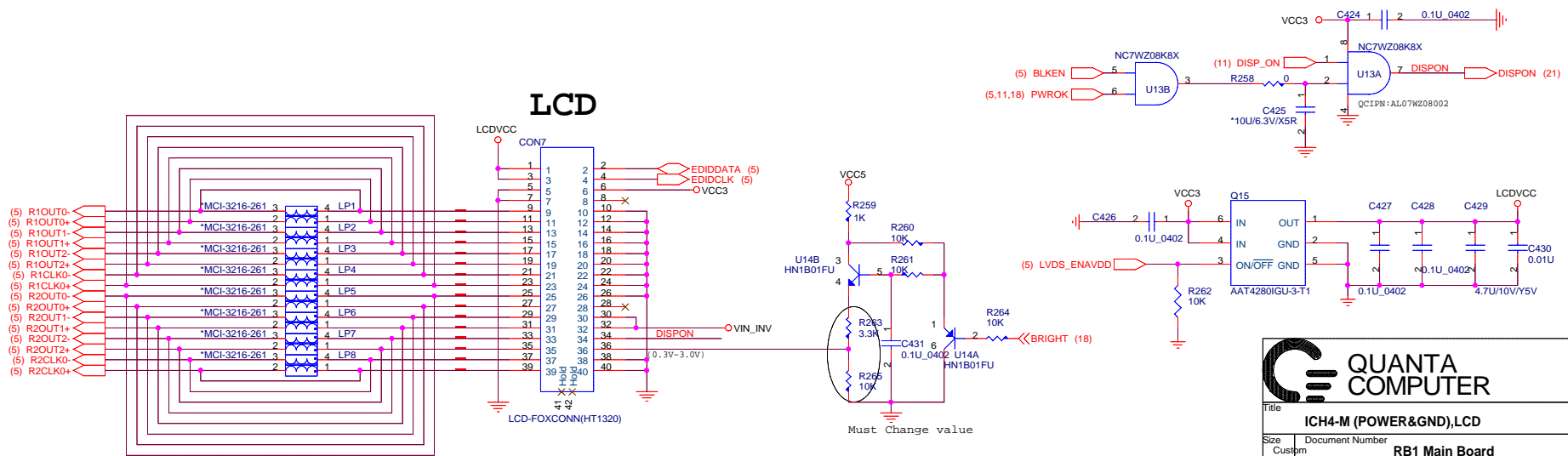


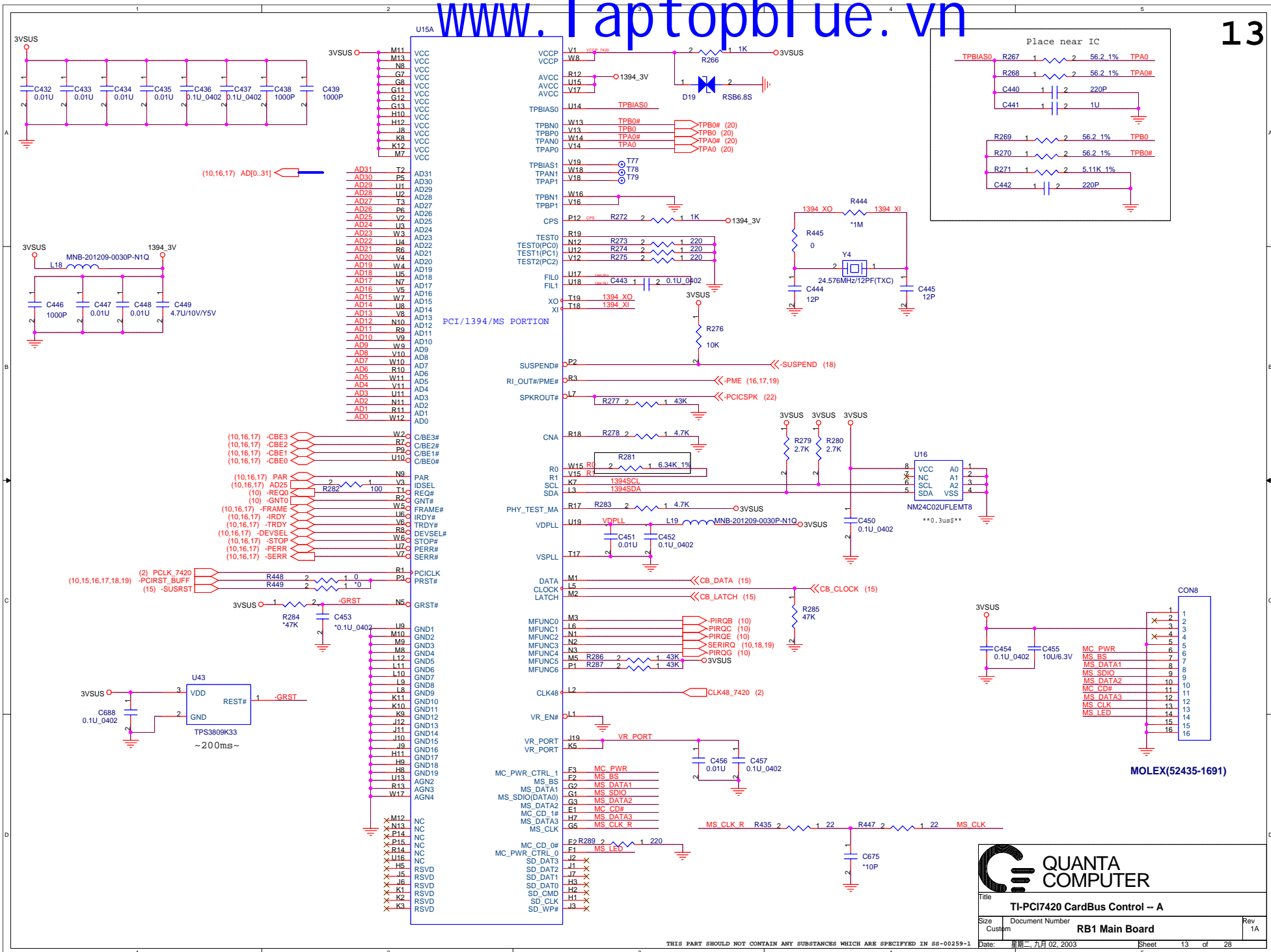






## LCD





**QUANTA COMPUTER**

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Size: Custom

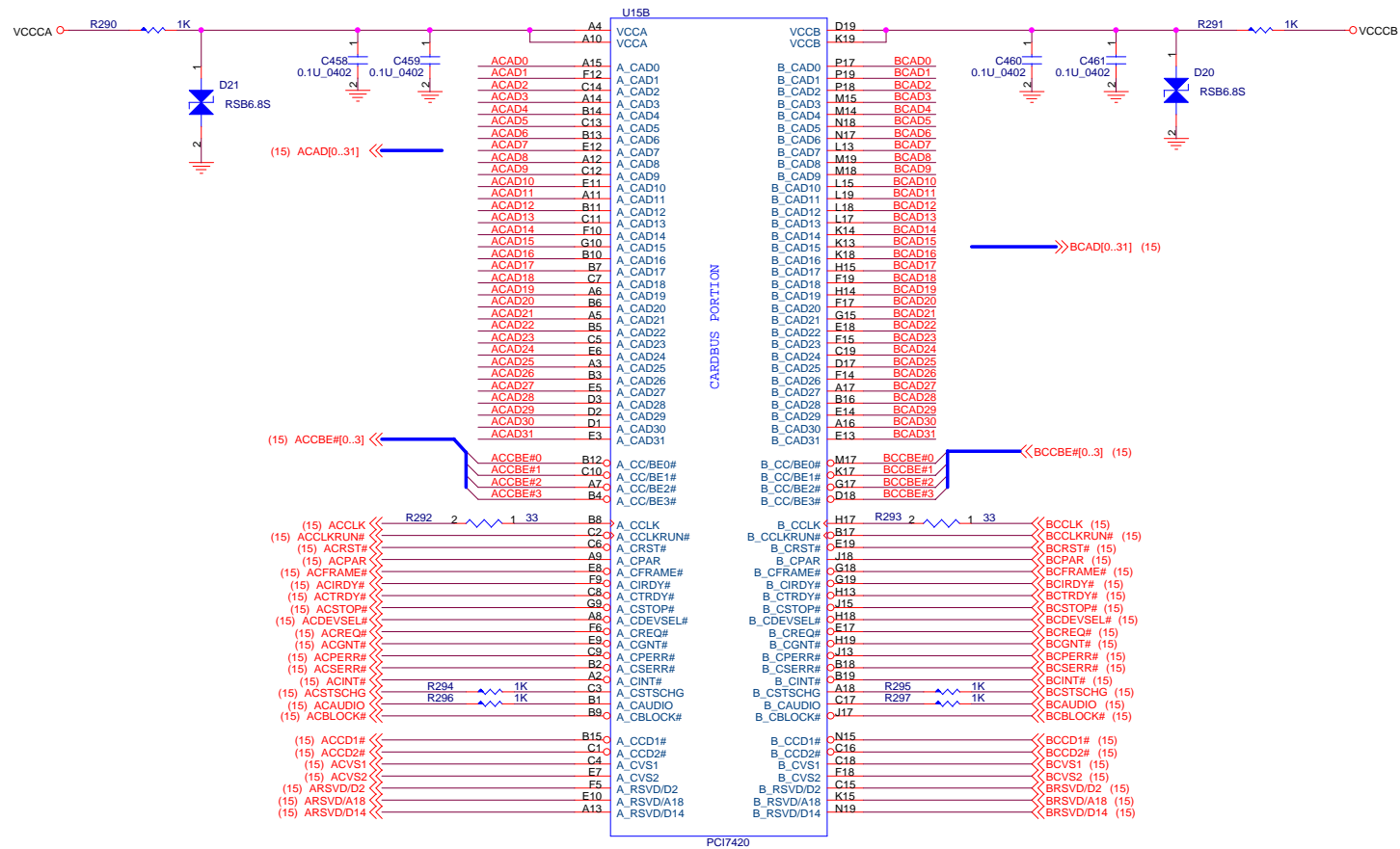
Document Number: RB1 Main Board

Date: 星期二, 九月 02, 2003

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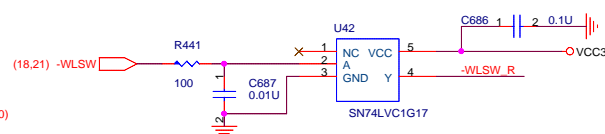
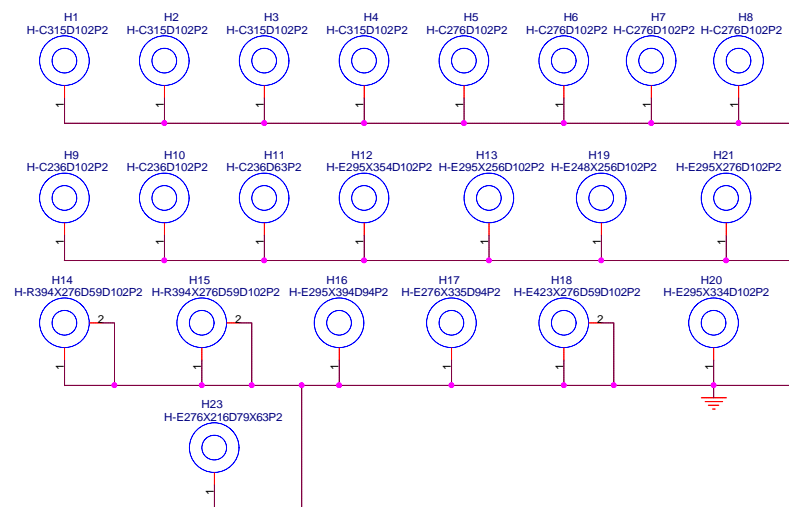
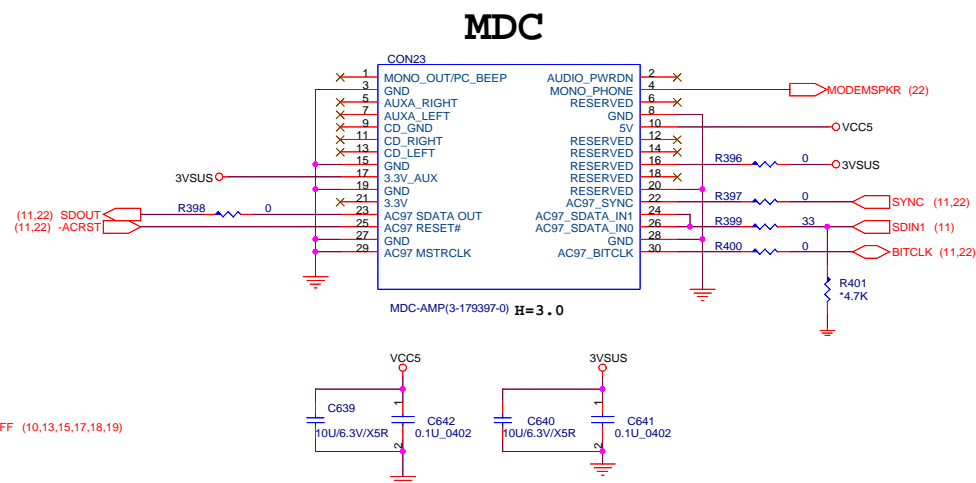
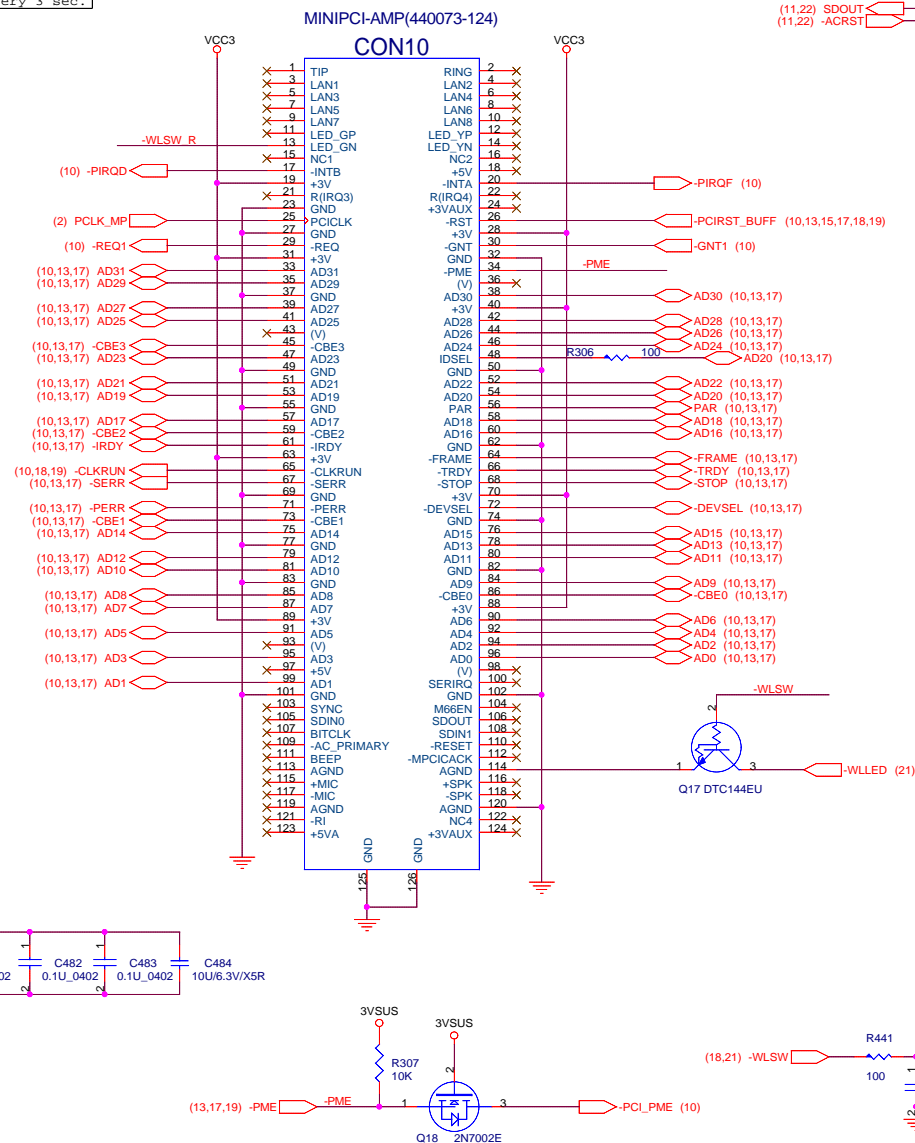
Rev: 1A







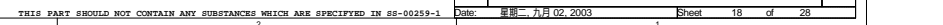
```
WLSW:
Low : disable the radio.
High : Enable the radio.
```



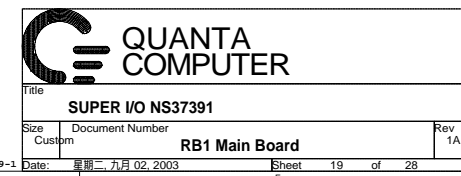
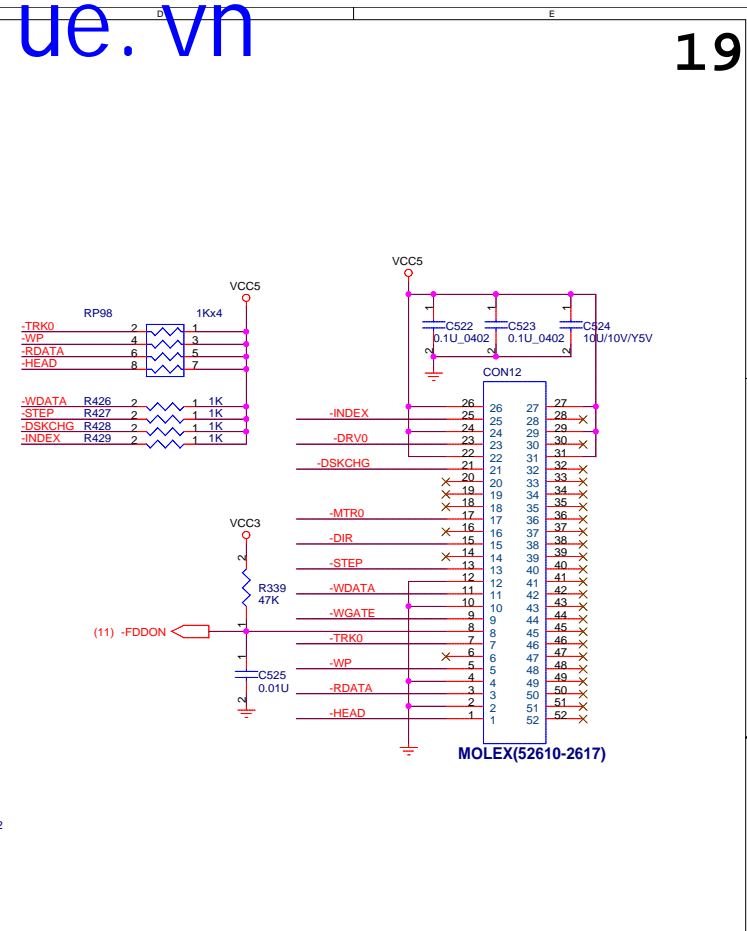
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Size	Document Number	Rev	
Custom	RB1 Main Board	1A	
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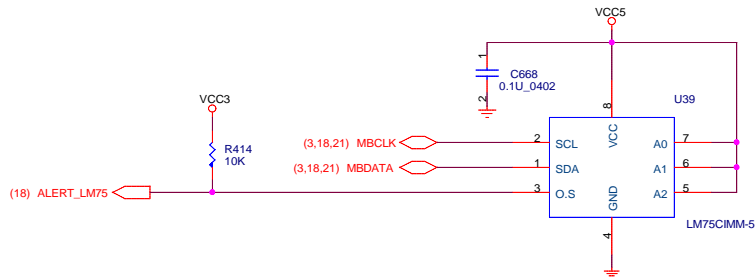
Rev	
1A	



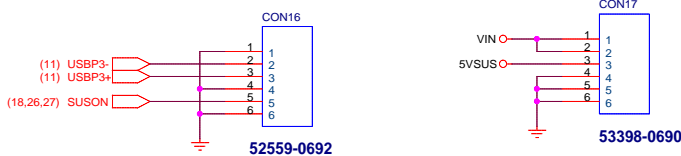




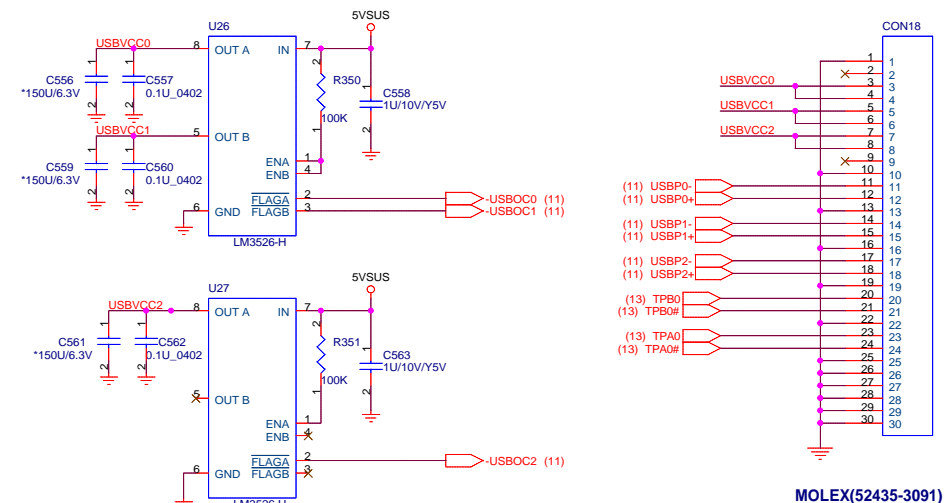
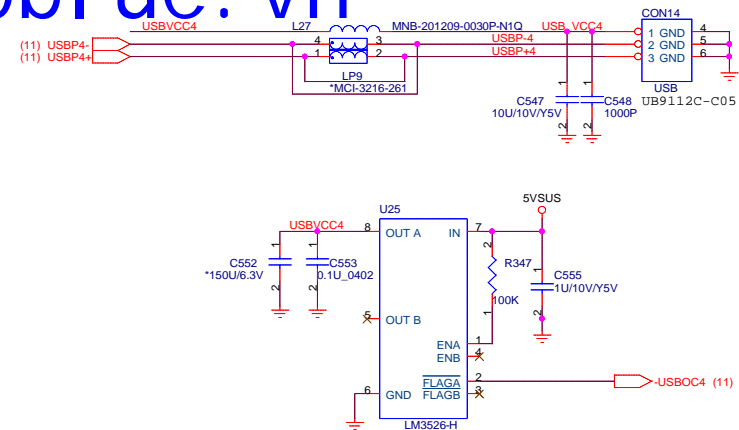
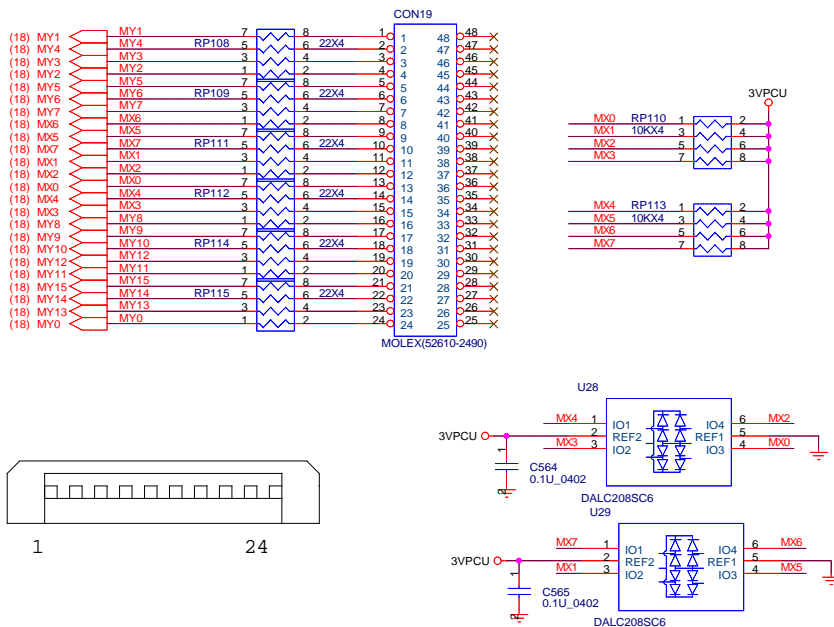
### DRAM Thermal



### USB Option

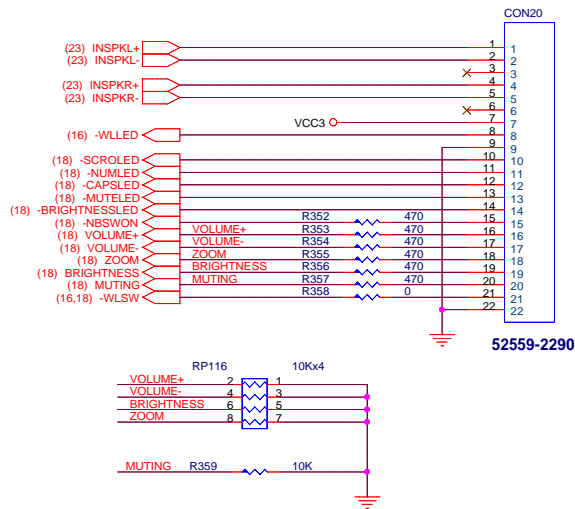


### KEYBOARD

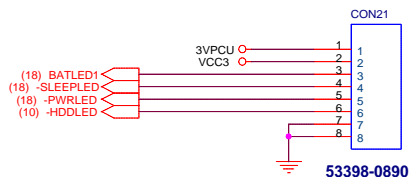


Title			K/B ,USB
Size	Document Number	RB1 Main Board	
Custom		Sheet	20 of 28
Date:	星期二 九月 02, 2003	Rev	1A

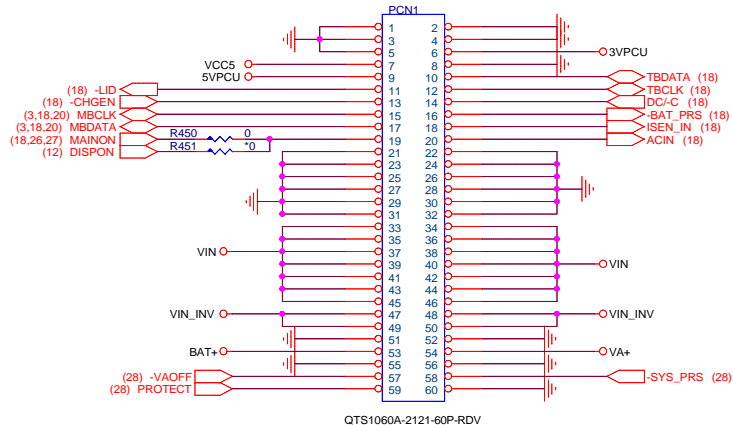
# POWER SWITCH /LED BOARD CONNECTOR(SWX-163)



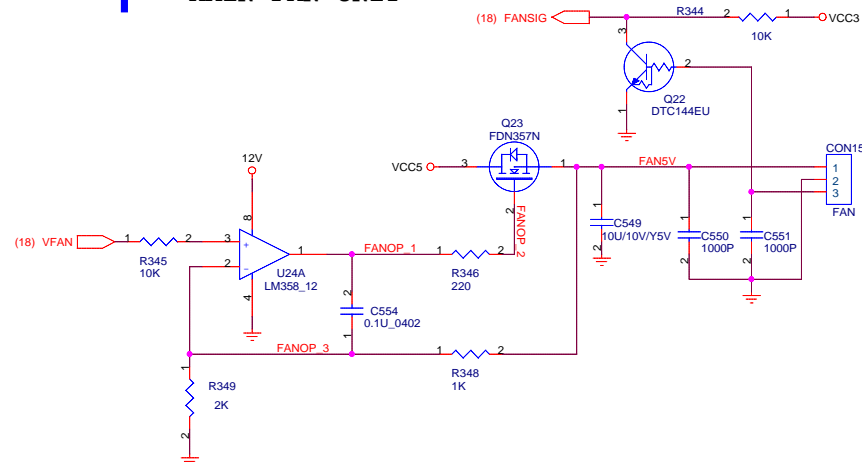
## LED BOARD CONNECTOR(LEX-55)



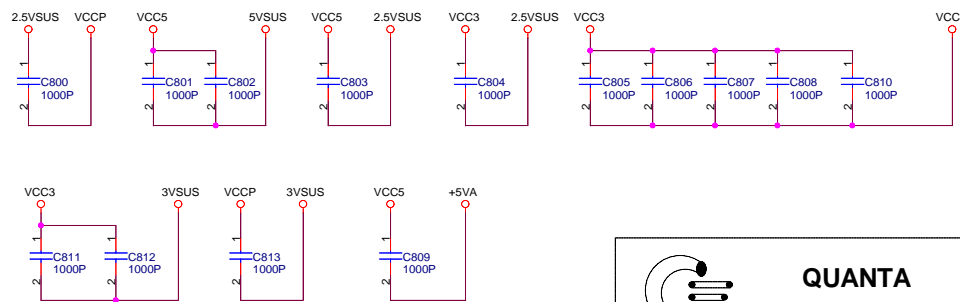
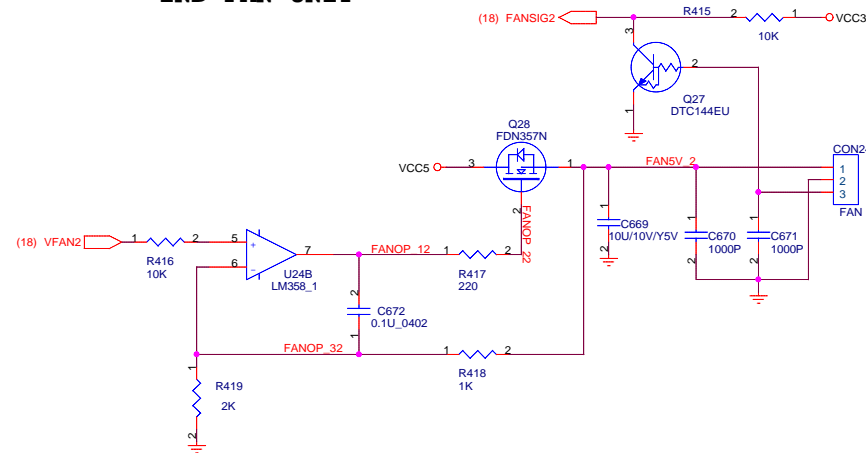
## CHARGER BOARD CONNECTOR(PWS-37)




## MAIN FAN UNIT



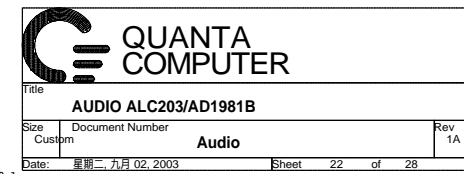
## 2ND FAN UNIT





**QUANTA  
COMPUTER**

Size	Document Number	Rev
	<b>LED,P/S,C/B CONNECTOR</b>	<b>1A</b>
Date: 星期四, 九月 04, 2003      Sheet 21 of 28		





GAIN0	GAIN1	AV(inv)
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

EXTERNAL SPEAKER

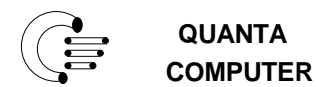
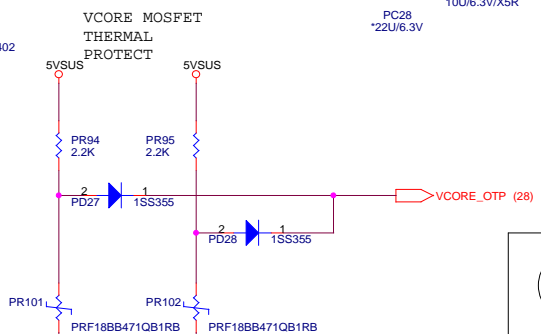
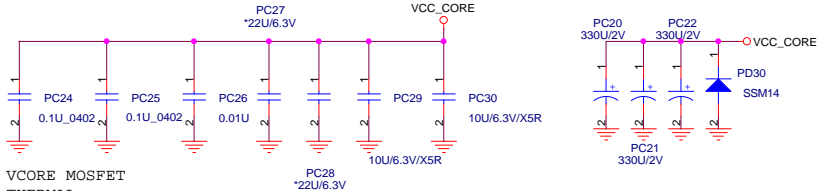
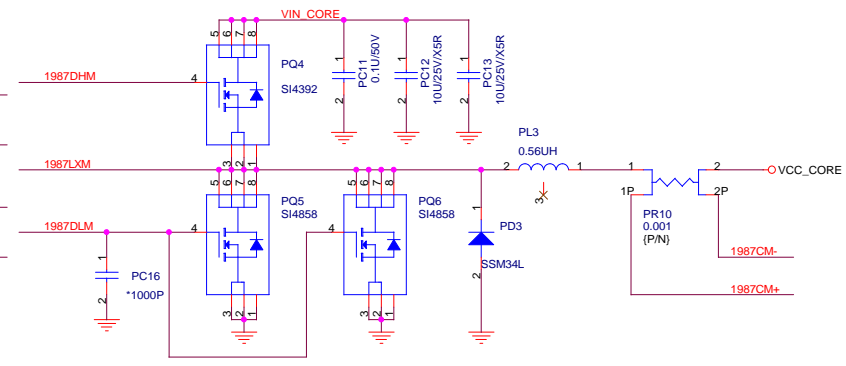
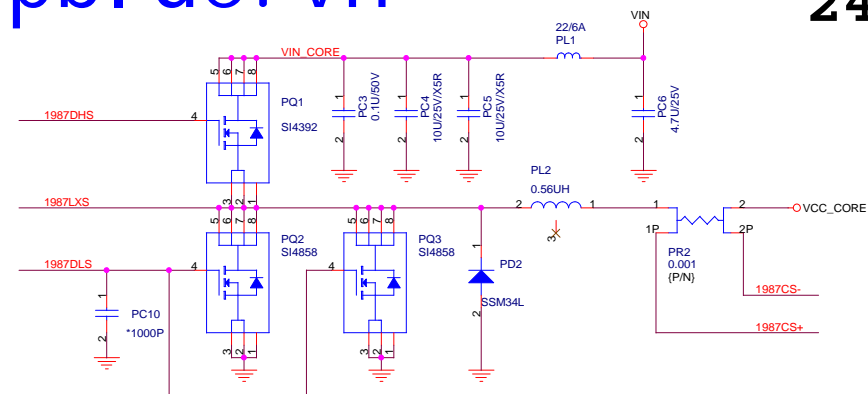
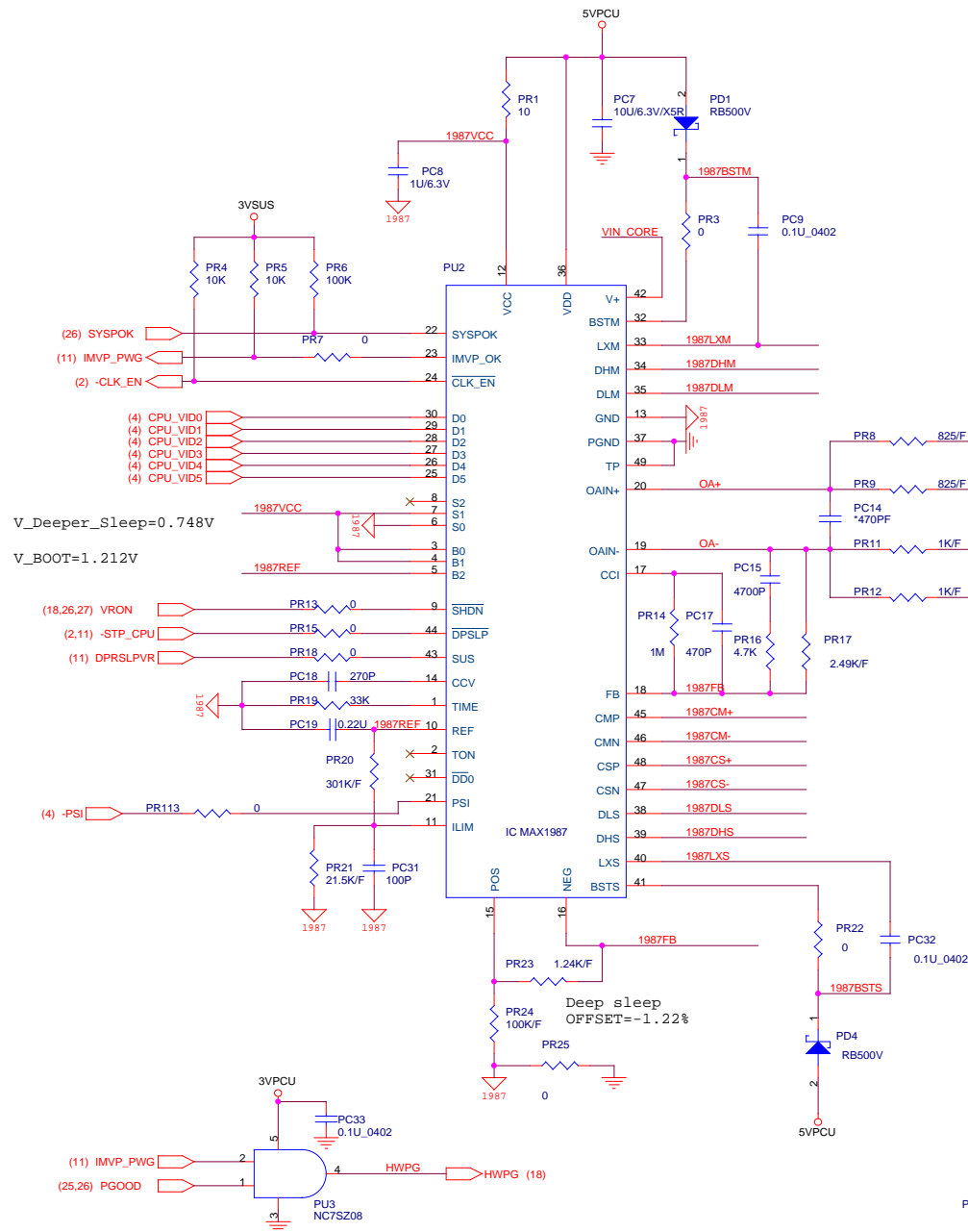
**Molex 52559-1092**



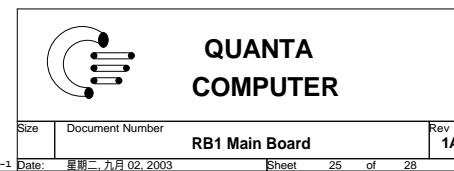
QUANTA  
COMPUTER

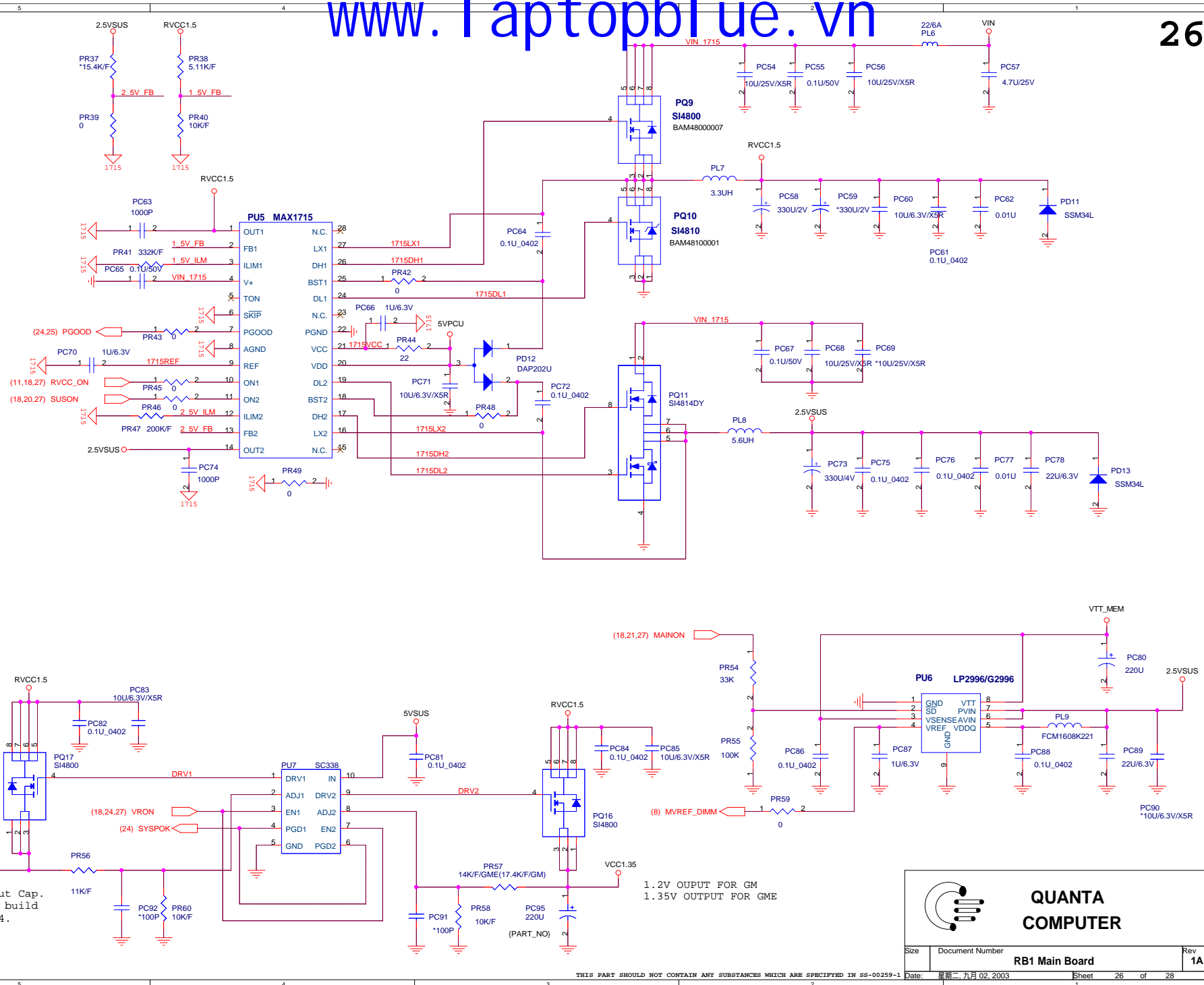
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Size	Document Number	Rev	
Custom	Audio	1A	
Date: 星期三 九月 03, 2003		Sheet	23 of 28





Size Custom	Document Number	Rev 1A
	RB1 Main Board	
Date: 星期三, 九月 03, 2003	Sheet 24 of 28	



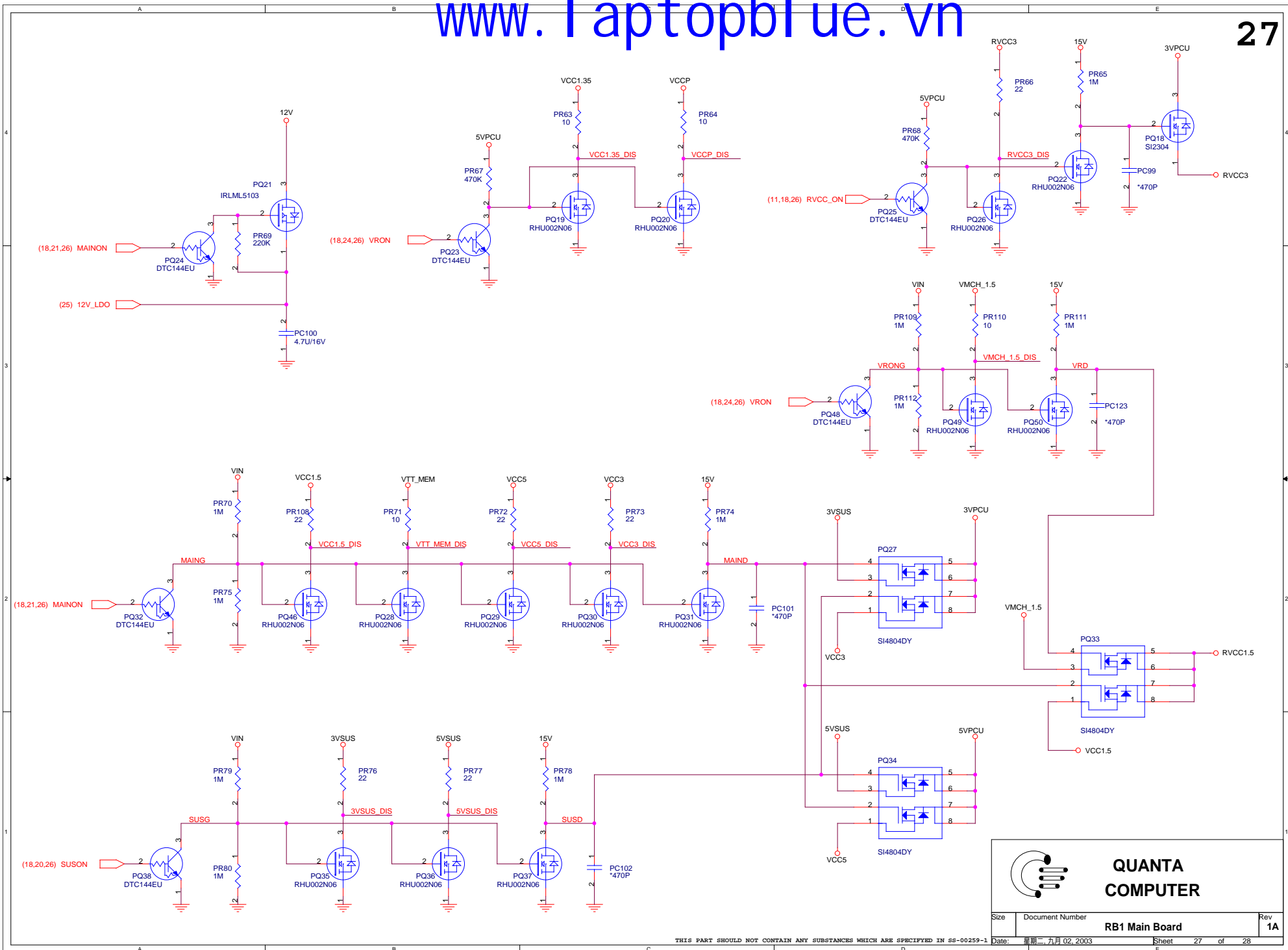


**QUANTA COMPUTER**

Size: Document Number: Rev: 1A

RB1 Main Board

Date: 星期二, 九月 02, 2003 Sheet: 26 of 28



**QUANTA COMPUTER**

Size: Document Number: **RB1 Main Board** Rev: **1A**

Date: 星期二, 九月 02, 2003 Sheet: 27 of 28

