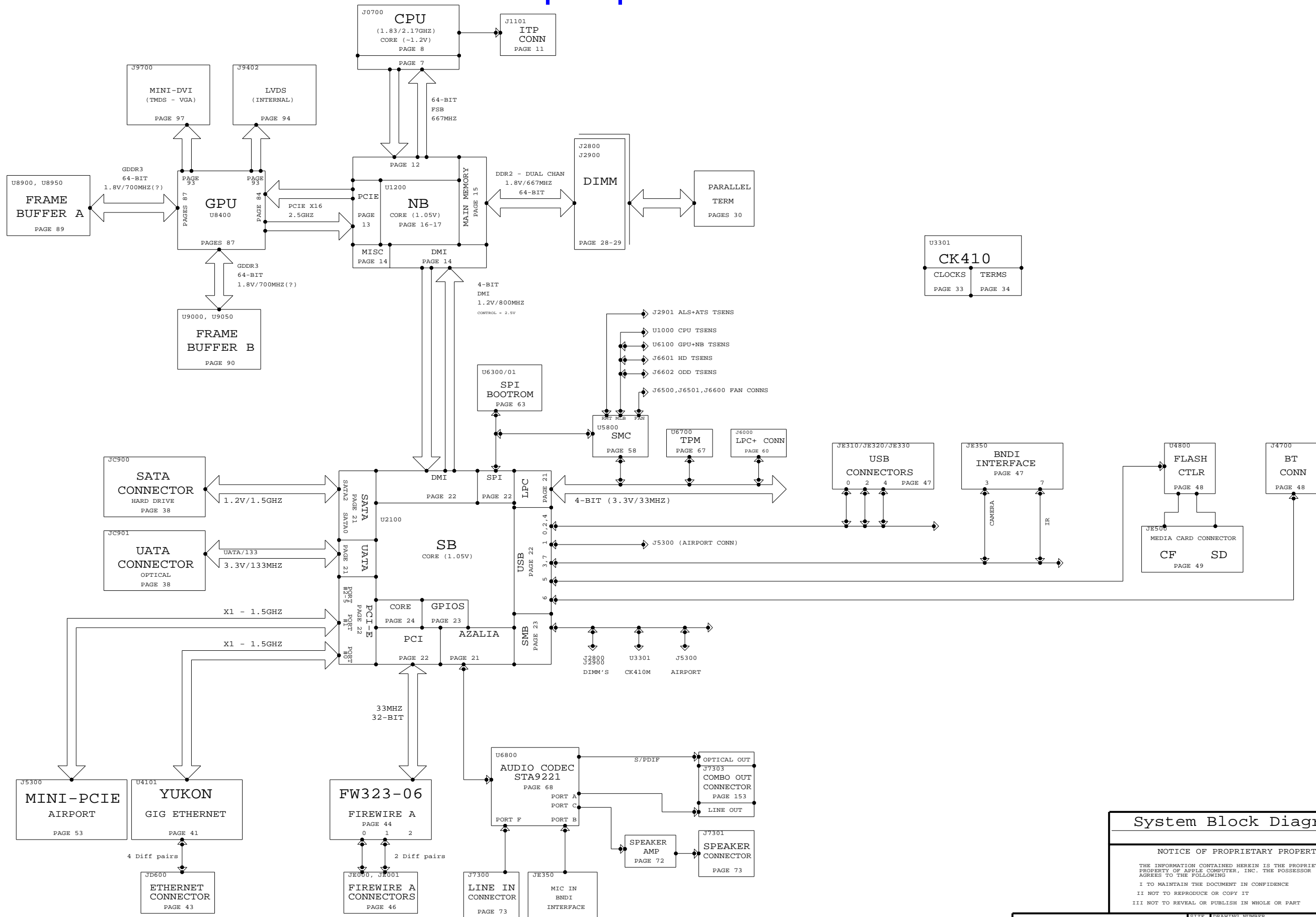


1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

PAGE	DRI	PDF	CIRCUIT
53	JD	JD 43	PCI-E - AIRPORT MINI-PCIE CONN
54	JD	JD 44	PCI-E - UNUSED PORTS
58	MS	MS 45	SMC - H8S2116
59	MS	MS 46	SMC - SMB BUSES, MISC
60	MS	MS 47	SMC - LPC+ CONN
61	JH	JH 48	SMC - GPU/NB THERMAL SENSOR
63	MS	JD 49	SMC - SPI BOOTROM
65	MS	MS 50	SMC - FANS
66	MS	MS 51	SMC - FANS
67	JD	JD 52	SMC - TPM
68	PT	JD 53	AUDIO - CODEC,VREG,MIC BIAS
72	PT	JD 54	AUDIO - INTERNAL SPEAKER AMP
73	PT	JD 55	AUDIO - I/O CONN'S,EMC
74	PT	JD 56	AUDIO - DETECT TRANSLATORS
75	RT	RT 57	VR - CPU CORE
76	RT	RT 58	VR - CPU I-V SENSE CKT
77	RT	RT 59	VR - "S0" 1.2V & 2.5V (GRAFIX)
78	RT	RT 60	VR - "S0" 1.8V
79	RT	RT 61	VR - "S3" 1.8V
80	RT	RT 62	VR - "S0" 1.5V
81	RT	RT 63	VR - "S0" 1.05V
83	RT	RT 64	VR - "S3" 3.3V AND 5V
84	JH	JH 65	GPU - M56 PCI-E
85	JH	JH 66	GPU - VCORE SUPPLY
86	JH	JH 67	GPU - M56 CORE PWR
87	JH	JH 68	GPU - M56 FRAME BUFFER
88	JH	JH 69	GPU - MISC
89	JH	JH 70	GPU - GDDR SDRAM A
90	JH	JH 71	GPU - GDDR SDRAM B
91	JH	JH 72	GPU - M56 GPIO,DVO,MISC
92	JH	JH 73	GPU - M56 CLOCKS
93	JH	JH 74	GPU - M56 VIDEO INTERFACES
94	JH	JH 75	GPU - INTERNAL DISPLAY CONN'S
95	JH	JH 76	GPU - TP'S
96	JH	JH 77	GPU - TMDS,INVERTER,EXT VGA
97	JH	JH 78	GPU - EXTERNAL DISPLAY CONN'S

A



System Block Diagram

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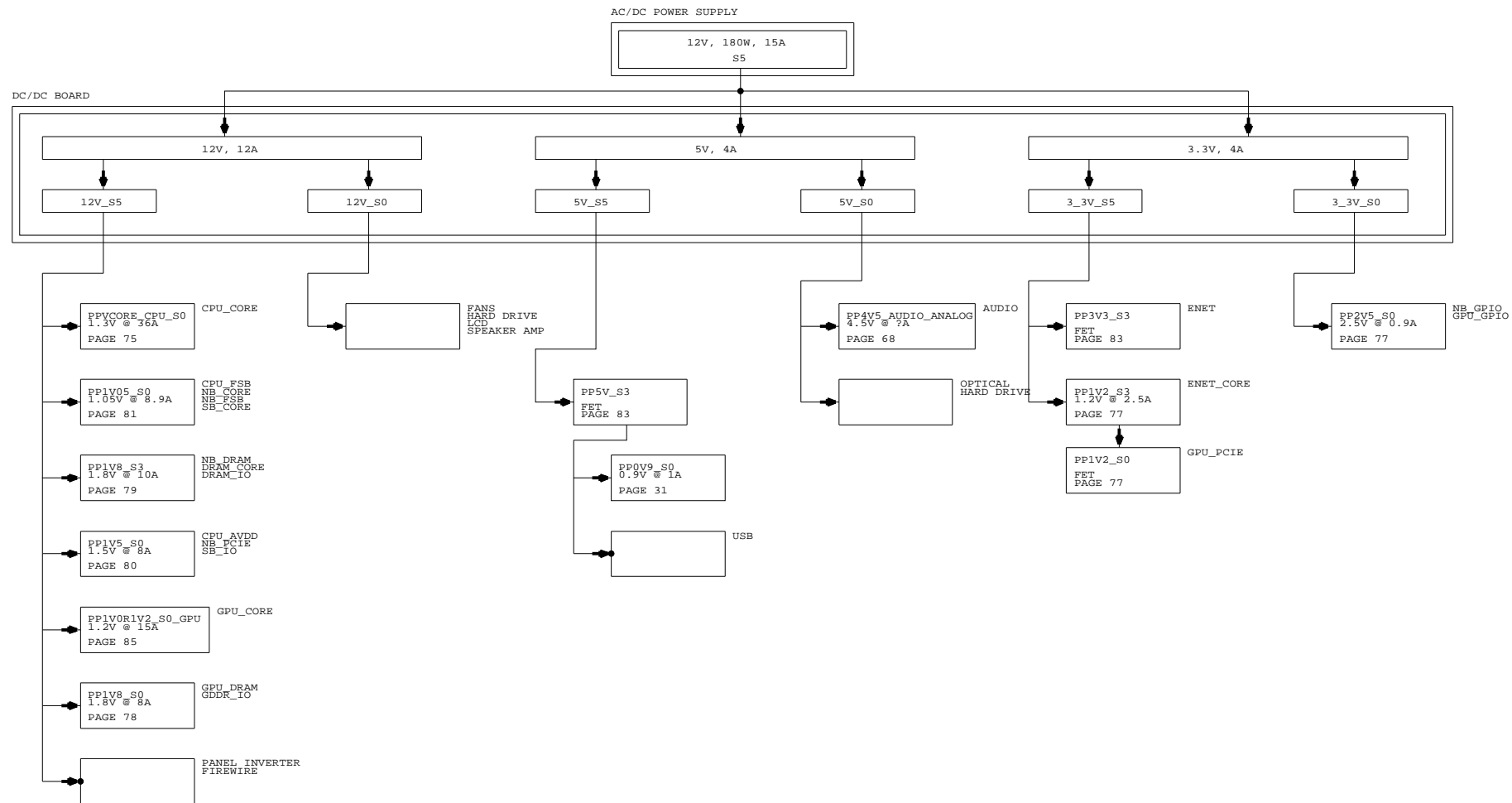
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	NONE	SHT	2 OF 111



Power Block Diagram

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APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHEET 3 OF 111

87654321

COMMON

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL	
338S0269	1	IC,945GM,NORTHBRIDGE	U1200	CRITICAL	
343S0385	1	IC,SB,652BGA	U2100	CRITICAL	
742-0048	1	BAT,COIN,3V,220MAH,CR2032	BT2600	CRITICAL	
359S0101	1	IC,CY28445-5,CLK GEN,68PIN QFP	U3301	CRITICAL	
338S0270	1	IC,88E8053,GIGABIT ENET XCVR,64P QFN MD	U4101	CRITICAL	
341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL	
338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL	
338S0274	1	IC,SMC,H58/2116,BLANK	U5800	CRITICAL	
341S1789	1	IC,TPM,TSSOP,28P	U6700	CRITICAL	LEMENU
353S1235	1	IC,CPU VREG,IMVP,TWO PHASE	U7500	CRITICAL	
338S0266	1	IC,ATI,M56P,GRAPHIC CTLR,880BGA,LF	U8400	CRITICAL	ATI_B24
338S0305	1	IC,ATI,M56P,GRAPHIC CTLR,880BGA,LF	U8400	CRITICAL	ATI_A24
128S0078	3	CAP,EL,AL,330UF,20%,16V,10X12.7MM,SMD,LF	C7517,C7518,C7910	CRITICAL	

(335S0382)

M38

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6949	1	PCB,SCHEM,MLB,M38	SCH1		17_INCH_LCD
820-1919	1	PCB,FAB,MLB,M38	MLB1		17_INCH_LCD
341T0003	1	EFI ROM,M38	U6301	CRITICAL	17_INCH_LCD
337S3241	1	M38/M39 LOW-SPEED CPU (QINY)	CPU	CRITICAL	CPU_M38
337S3242	1	M00-SPEED CPU (QINZ)	CPU	CRITICAL	CPU_M00

(335S0384)

M39

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6950	1	PCB,SCHEM,MLB,M39	SCH1		20_INCH_LCD
820-1888	1	PCB,FAB,MLB,M39	MLB1		20_INCH_LCD
341T0004	1	EFI ROM,M39	U6301	CRITICAL	20_INCH_LCD
337S3243	1	M39 HI-SPEED CPU (QHJG)	CPU	CRITICAL	CPU_M39

(335S0384)

M38 / M39

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0354	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_SAMSUNG
333S0358	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_HYNIX

M39 - CTO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0350	4	IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_SAMSUNG
333S0351	4	IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_HYNIX

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0096	126S0076		C7801	SANYO W16CE680EX 680UF 16V LF
126S0086	126S0078		C699,C940,C1900,C1901,C1968	SANYO W6CE330FS 330UF 6.3V LF
128S0080	128S0078		C7517,C7518,C7910	SANYO 168VP330W 330UF 16V SMD LF
197S0177	197S0020		Y4101	XTAL,25MHZ,50PPM,16PF,3.2X2.5 SMD,LF
338S0302	338S0266		U8400	IC,ATI,M36D,GRAPHIC CTLR,88FBGA,LF

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Table Items

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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	4	111

LAYOUT NOTE: PLACE NEAR J0700

7 5

FSB A L<6>

PP600

OMIT P4MM

7 5

FSB ADSTB L<0>

PP601

OMIT P4MM

7 5

FSB A L<27>

PP602

OMIT P4MM

7 5

FSB ADSTB L<1>

PP603

OMIT P4MM

7 5

FSB D L<0>

PP604

OMIT P4MM

7 5

FSB DSTBN L<0>

PP605

OMIT P4MM

7 5

FSB DSTBP L<0>

PP606

OMIT P4MM

7 5

FSB DINV L<0>

PP607

OMIT P4MM

7 5

FSB D L<16>

PP608

OMIT P4MM

7 5

FSB DSTBN L<1>

PP609

OMIT P4MM

7 5

FSB DSTBP L<1>

PP610

OMIT P4MM

7 5

FSB DINV L<1>

PP611

OMIT P4MM

7 5

FSB D L<41>

PP612

OMIT P4MM

7 5

FSB DSTBN L<2>

PP613

OMIT P4MM

7 5

FSB DSTBP L<2>

PP614

OMIT P4MM

7 5

FSB DINV L<2>

PP615

OMIT P4MM

7 5

FSB D L<59>

PP616

OMIT P4MM

7 5

FSB DSTBN L<3>

PP617

OMIT P4MM

7 5

FSB DSTBP L<3>

PP618

OMIT P4MM

7 5

FSB DINV L<3>

PP619

OMIT P4MM

7 5

FSB LOCK L

PP620

OMIT P4MM

12 11 7

FSB CPURST L

PP621

OMIT P4MM

21 7

CPU INIT L

PP622

OMIT P4MM

21 7

CPU A20M L

PP623

OMIT P4MM

21 7

CPU IGNNE L

PP624

OMIT P4MM

21 7

CPU STPLCK L

PP625

OMIT P4MM

21 7

CPU INTR

PP626

OMIT P4MM

21 7

CPU NMI

PP627

OMIT P4MM

21 7

CPU SMI L

PP628

OMIT P4MM

34 7

FSB CLK CPU P

PP629

OMIT P4MM

34 7

FSB CLK CPU N

PP630

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U1200

7 5

SB CLK100M SATA P

PP6C4

OMIT P4MM

34 21

SB CLK100M SATA N

PP6C5

OMIT P4MM

38 21

IDE PDIOR L

PP6C6

OMIT P4MM

38 21

IDE PDIORDY

PP6C7

OMIT P4MM

38 21

IDE PDD<9>

PP6C8

OMIT P4MM

34 22

PCI CLK SB

PP6D0

OMIT P4MM

41 22

PCIE A D2R P

PP6D1

OMIT P4MM

41 22

PCIE A D2R N

PP6D2

OMIT P4MM

53 22

PCIE B D2R P

PP5E1

OMIT P4MM

53 22

PCIE B D2R N

PP5E2

OMIT P4MM

22 14

DMI N2S P<0>

PP6D3

OMIT P4MM

22 14

DMI N2S N<0>

PP6D4

OMIT P4MM

34 22

SB CLK100M DMI P

PP6D5

OMIT P4MM

34 22

SB CLK100M DMI N

PP6D6

OMIT P4MM

58 44 23

PM SYSRST L

PP6D7

OMIT P4MM

58 44 23

PM CLKRUN L

PP6D8

OMIT P4MM

34 23

SB CLK14P3M TIMER

PP6D9

OMIT P4MM

34 23

SB CLK48M USBCTLR

PP6E0

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U1200

7 5

FSB A L<6>

PP631

OMIT P4MM

7 5

FSB ADSTB L<0>

PP632

OMIT P4MM

7 5

FSB A L<27>

PP633

OMIT P4MM

7 5

FSB ADSTB L<1>

PP634

OMIT P4MM

7 5

FSB D L<0>

PP635

OMIT P4MM

7 5

FSB DSTBN L<0>

PP636

OMIT P4MM

7 5

FSB DSTBP L<0>

PP637

OMIT P4MM

7 5

FSB DINV L<0>

PP638

OMIT P4MM

7 5

FSB D L<16>

PP639

OMIT P4MM

7 5

FSB DSTBN L<1>

PP640

OMIT P4MM

7 5

FSB DSTBP L<1>

PP641

OMIT P4MM

7 5

FSB DINV L<1>

PP642

OMIT P4MM

7 5

FSB D L<41>

PP643

OMIT P4MM

7 5

FSB DSTBN L<2>

PP644

OMIT P4MM

7 5

FSB DSTBP L<2>

PP645

OMIT P4MM

7 5

FSB DINV L<2>

PP646

OMIT P4MM

7 5

FSB D L<59>

PP647

OMIT P4MM

7 5

FSB DSTBN L<3>

PP648

OMIT P4MM

7 5

FSB DSTBP L<3>

PP649

OMIT P4MM

7 5

FSB DINV L<3>

PP650

OMIT P4MM

7 5

FSB LOCK L

PP651

OMIT P4MM

7 5

FSB HIT L

PP652

OMIT P4MM

7 5

FSB HITM L

PP653

OMIT P4MM

7 5

FSB BNR L

PP654

OMIT P4MM

7 5

FSB BREQ0 L

PP655

OMIT P4MM

7 5

FSB DBSY L

PP656

OMIT P4MM

7 5

FSB DPNR L

PP657

OMIT P4MM

7 5

FSB REQ L<0>

PP658

OMIT P4MM

7 5

FSB REQ L<1>

PP659

OMIT P4MM

7 5

FSB REQ L<2>

PP660

OMIT P4MM

7 5

FSB REQ L<3>

PP661

OMIT P4MM

7 5

FSB REQ L<4>

PP662

OMIT P4MM

34 12

FSB CLK NB P

PP663

OMIT P4MM

34 12

FSB CLK NB N

PP664

OMIT P4MM

75 26 14

VR PWRGOOD_DELAY

PP665

OMIT P4MM

14

NB_RST_IN_L_R

PP666

OMIT P4MM

34 14

NB CLK100M GCLKIN P

PP667

OMIT P4MM

34 14

NB CLK100M GCLKIN N

PP668

OMIT P4MM

22 14

DMI S2N N<0>

PP673

OMIT P4MM

22 14

DMI S2N P<0>

PP674

OMIT P4MM

19 14

MEM_VREF_NB_0

PP6E1

OMIT P4MM

19 14

MEM_VREF_NB_1

PP675

OMIT P4MM

28 15

MEM_A DQ<7>

PP676

OMIT P4MM

28 15

MEM_A DQ<14>

PP677

OMIT P4MM

28 15

MEM_A DQ<16>

PP678

OMIT P4MM

28 15

MEM_A DQ<25>

PP679

OMIT P4MM

28 15

MEM_A DQ<39>

PP680

OMIT P4MM

28 15

MEM_A DQ<47>

PP681

OMIT P4MM

28 15

MEM_A DQ<54>

PP682

OMIT P4MM

28 15

MEM_A DQ<59>

PP683

OMIT P4MM

28 15

MEM_A DQS P<0>

PP684

OMIT P4MM

28 15

MEM_A DQS N<0>

PP685

OMIT P4MM

28 15

MEM_A DQS P<1>

PP686

OMIT P4MM

28 15

MEM_A DQS P<2>

PP687

OMIT P4MM

28 15

MEM_A DQS N<2>

PP688

OMIT P4MM

28 15

MEM_A DQS N<3>

PP689

OMIT P4MM

28 15

MEM_A DQS P<3>

PP690

OMIT P4MM

28 15

MEM_A DQS N<3>

PP691

OMIT P4MM

28 15

MEM_A DQS P<4>

PP692

OMIT P4MM

28 15

MEM_A DQS N<4>

PP693

OMIT P4MM

28 15

MEM_A DQS P<5>

PP694

OMIT P4MM

28 15

MEM_A DQS N<5>

PP695

OMIT P4MM

28 15

MEM_A DQS P<6>

PP696

OMIT P4MM

28 15

MEM_A DQS N<6>

PP697

OMIT P4MM

28 15

MEM_A DQS P<7>

PP698

OMIT P4MM

28 15

MEM_A DQS N<7>

PP699

OMIT P4MM

29 16

MEM_B DQ<6>

PP6A0

OMIT P4MM

29 16

MEM_B DQ<8>

PP6A1

OMIT P4MM

29 16

MEM_B DQ<23>

PP6A2

OMIT P4MM

29 16

MEM_B DQ<25>

PP6A3

OMIT P4MM

29 16

MEM_B DQ<38>

PP6A4

OMIT P4MM

29 16

MEM_B DQ<44>

PP6A5

OMIT P4MM

29 16

MEM_B DQ<48>

PP6A6

OMIT P4MM

29 16

MEM_B DQ<62>

PP6A7

OMIT P4MM

29 16

MEM_B DQS P<0>

PP6A8

OMIT P4MM

29 16

MEM_B DQS N<0>

PP6A9

OMIT P4MM

29 16

MEM_B DQS P<1>

PP6B0

OMIT P4MM

29 16

MEM_B DQS N<1>

PP6B1

OMIT P4MM

29 16

MEM_B DQS P<2>

PP6B2

OMIT P4MM

29 16

MEM_B DQS N<2>

PP6B3

OMIT P4MM

29 16

MEM_B DQS P<3>

PP6B4

OMIT P4MM

29 16

MEM_B DQS N<3>

PP6B5

OMIT P4MM

29 16

MEM_B DQS P<4>

PP6B6

OMIT P4MM

29 16

MEM_B DQS N<4>

PP6B7

OMIT P4MM

29 16

MEM_B DQS P<5>

PP6B8

OMIT P4MM

29 16

MEM_B DQS N<5>

PP6B9

OMIT P4MM

29 16

MEM_B DQS P<6>

PP6C0

OMIT P4MM

29 16

MEM_B DQS N<6>

PP6C1

OMIT P4MM

29 16

MEM_B DQS P<7>

PP6C2

OMIT P4MM

29 16

MEM_B DQS N<7>

PP6C3

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U8400

89 87 5

FB A DQ<0>

PP8700

OMIT P4MM

89 87 5

FB A DQ<8>

PP8701

OMIT P4MM

89 87 5

FB A DQ<16>

PP8702

OMIT P4MM

89 87 5

FB A DQ<24>

PP8703

OMIT P4MM

89 87 5

FB A DQ<32>

PP8704

OMIT P4MM

89 87 5

FB A DQ<40>

PP8705

OMIT P4MM

89 87 5

FB A DQ<48>

PP8706

OMIT P4MM

89 87 5

FB A DQ<56>

PP8707

OMIT P4MM

89 87 5

FB A MA<3>

PP8708

OMIT P4MM

89 87 5

FB A RDQS<0>

PP8709

OMIT P4MM

89 87 5

FB A RDQS<1>

PP8710

OMIT P4MM

89 87 5

FB A RDQS<2>

PP8711

OMIT P4MM

89 87 5

FB A RDQS<3>

PP8712

OMIT P4MM

89 87 5

FB A RDQS<4>

PP8713

OMIT P4MM

89 87 5

FB A RDQS<5>

PP8714

OMIT P4MM

89 87 5

FB A RDQS<6>

PP8715

OMIT P4MM

89 87 5

FB A RDQS<7>

PP8716

OMIT P4MM

90 87 5

FB B DQ<0>

PP8720

OMIT P4MM

90 87 5

FB B DQ<8>

PP8721

OMIT P4MM

90 87 5

FB B DQ<16>

PP8722

OMIT P4MM

90 87 5

FB B DQ<24>

PP8723

OMIT P4MM

90 87 5

FB B DQ<32>

PP8724

OMIT P4MM

90 87 5

FB B DQ<40>

PP8725

OMIT P4MM

90 87 5

FB B DQ<48>

PP8726

OMIT P4MM

90 87 5

FB B DQ<56>

PP8727

OMIT P4MM

90 87 5

FB B MA<3>

PP8728

OMIT P4MM

90 87 5

FB B RDQS<0>

PP8729

OMIT P4MM

90 87 5

FB B RDQS<1>

PP8730

OMIT P4MM

90 87 5

FB B RDQS<2>

PP8731

OMIT P4MM

90 87 5

FB B RDQS<3>

PP8732

OMIT P4MM

90 87 5

FB B RDQS<4>

PP8733

OMIT P4MM

90 87 5

FB B RDQS<5>

PP8734

OMIT P4MM

90 87 5

FB B RDQS<6>

PP8735

OMIT P4MM

90 87 5

FB B RDQS<7>

PP8736

OMIT P4MM

84 34

GPU_CLK100M_PCIE_P

PP8400

OMIT P4MM

84 34

GPU_CLK100M_PCIE_N

PP8401

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U4101

41 34

ENET_CLK100M_PCIE_P

PP4100

OMIT P4MM

41 34

ENET_CLK100M_PCIE_N

PP4101

OMIT P4MM

PLACE NEAR R1210 AND R1211

12

NB_FSB_VREF

PP1200

OMIT P4MM

19 12 6

=PP1V05_S0_FSB_NB

PP1201

OMIT P4MM

12

NB_FSB_VREF

PP1202

OMIT P4MM

PLACE NEAR R0705 AND R0706

59 11 9 8 7 6

=PP1V05_S0_CPU

PP700

OMIT P4MM

7

CPU_GTLREF

PP701

OMIT P4MM

7

CPU_GTLREF

PP702

OMIT P4MM

PLACE NEAR R2800 AND R2801

29 28 6

=PP1V8_S3_MEM

PP2800

OMIT P4MM

29 28

MEM_VREF

PP2801

OMIT P4MM

29 28

MEM_VREF

PP2802

OMIT P4MM

MISC GROUND VIAS

ZH500

HOLE-VIA

1

ZH501

HOLE-VIA

1

ZH502

HOLE-VIA

1

ZH503

HOLE-VIA

1

ZH504

HOLE-VIA

1

ZH505

HOLE-VIA

1

ZH506

HOLE-VIA

1

ZH507

HOLE-VIA

1

ZH508

HOLE-VIA

1

ZH509

HOLE-VIA

1

ZH510

HOLE-VIA

1

ZH511

HOLE-VIA

1

ZH512

HOLE-VIA

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ZH513

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ZH525

HOLE-VIA

1

ZH526

HOLE-VIA

1

ZH527

HOLE-VIA

1

ZH528

HOLE-VIA

1

ZH529

HOLE-VIA

1

PLACE NEAR U8900

89 87

FB A_CKE<0>

PP8900

OMIT P4MM

89 87

FB A_CLK_P<0>

PP8901

OMIT P4MM

89 87

FB A_CLK_N<0>

PP8902

OMIT P4MM

89 87

FB A_CS_L<0>

PP8903

OMIT P4MM

89 87

FB A_WE_L<0>

PP8904

OMIT P4MM

89 87

FB A_CAS_L<0>

PP8905

OMIT P4MM

89 87

FB A_MA<3>

PP8906

OMIT P4MM

89 87

FB A_RAS_L<0>

PP8907

OMIT P4MM

90 89 88 5

DRAM_RST

PP8908

OMIT P4MM

90 87

FB B_WDQS<0>

PP8909

OMIT P4MM

90 87

FB B_WDQS<1>

PP8910

OMIT P4MM

90 87

FB A_WDQS<3>

PP8911

OMIT P4MM

90 87

FB A_WDQS<2>

PP8912

OMIT P4MM

90 87

FB A_DQ<0>

PP8913

OMIT P4MM

90 87 5

FB A_DQ<8>

PP8914

OMIT P4MM

90 87 5

FB A_DQ<16>

PP8915

OMIT P4MM

90 87 5

FB A_DQ<24>

PP8916

OMIT P4MM

PLACE NEAR U9000

90 87

FB B_CKE<0>

PP9000

OMIT P4MM

90 87

FB B_CLK_P<0>

PP9001

OMIT P4MM

90 87

FB B_CLK_N<0>

PP9002

OMIT P4MM

90 87

FB B_CS_L<0>

PP9003

OMIT P4MM

90 87

FB B_WE_L<0>

PP9004

OMIT P4MM

90 87

FB B_CAS_L<0>

PP9005

OMIT P4MM

90 87

FB B_RAS_L<0>

PP9006

OMIT P4MM

90 87 5

FB B_MA<3>

PP9007

OMIT P4MM

90 89 88 5

DRAM_RST

PP9008

OMIT P4MM

90 87

FB B_WDQS<0>

PP9009

OMIT P4MM

90 87

FB B_WDQS<1>

PP9010

OMIT P4MM

90 87

FB B_WDQS<3>

PP9011

OMIT P4MM

90 87

FB B_WDQS<2>

PP9012

OMIT P4MM

90 87 5

FB B_DQ<0>

PP9013

OMIT P4MM

90 87 5

FB B_DQ<8>

PP9014

OMIT P4MM

90 87 5

FB B_DQ<16>

PP9015

OMIT P4MM

90 87 5

FB B_DQ<24>

PP9016

OMIT P4MM

PLACE NEAR U8950

89 87

FB A_CKE<1>

PP8920

OMIT P4MM

89 87

FB A_CLK_P<1>

PP8921

OMIT P4MM

89 87

FB A_CLK_N<1>

PP8922

OMIT P4MM

89 87

FB A_CS_L<1>

PP8923

OMIT P4MM

89 87

FB A_WE_L<1>

PP8924

OMIT P4MM

89 87

FB A_CAS_L<1>

PP8925

OMIT P4MM

89 87

FB A_RAS_L<1>

PP8926

OMIT P4MM

89 87 5

FB A_MA<3>

PP8927

OMIT P4MM

90 89 88 5

DRAM_RST

PP8928

OMIT P4MM

89 87

FB B_WDQS<5>

PP8929

OMIT P4MM

89 87

FB A_WDQS<6>

PP8930

OMIT P4MM

89 87

FB B_WDQS<4>

PP8931

OMIT P4MM

89 87

FB A_WDQS<7>

PP8932

OMIT P4MM

89 87 5

FB B_DQ<32>

PP8933

OMIT P4MM

89 87 5

FB A_DQ<40>

PP8934

OMIT P4MM

89 87 5

FB B_DQ<48>

PP8935

OMIT P4MM

89 87 5

FB A_DQ<56>

PP8936

OMIT P4MM

PLACE NEAR U9050

90 87

FB B_CKE<1>

PP9020

OMIT P4MM

90 87

FB B_CLK_P<1>

PP9021

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90 87

FB B_CLK_N<1>

PP9022

OMIT P4MM

90 87

FB B_CS_L<1>

PP9023

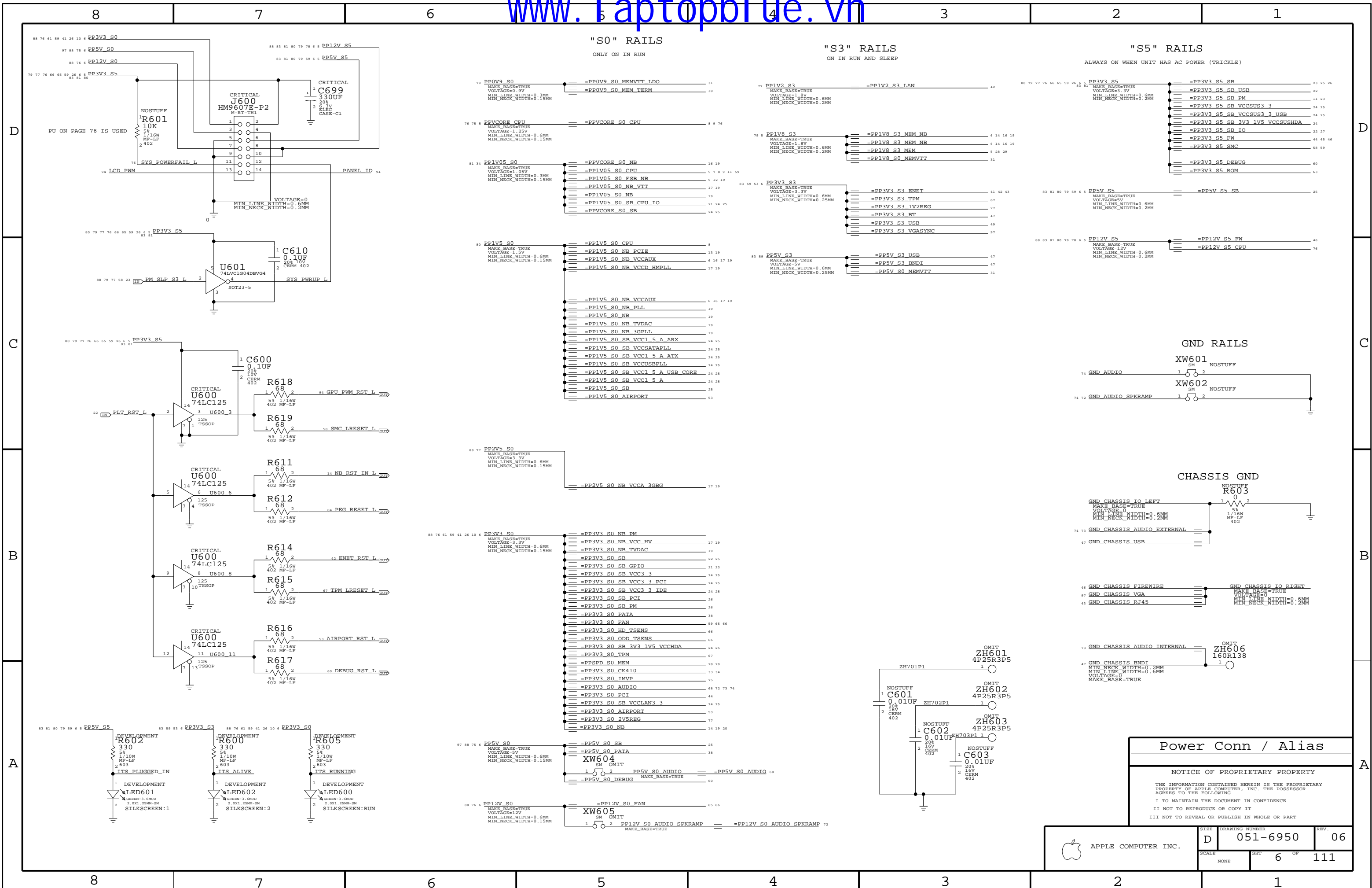
OMIT P4MM

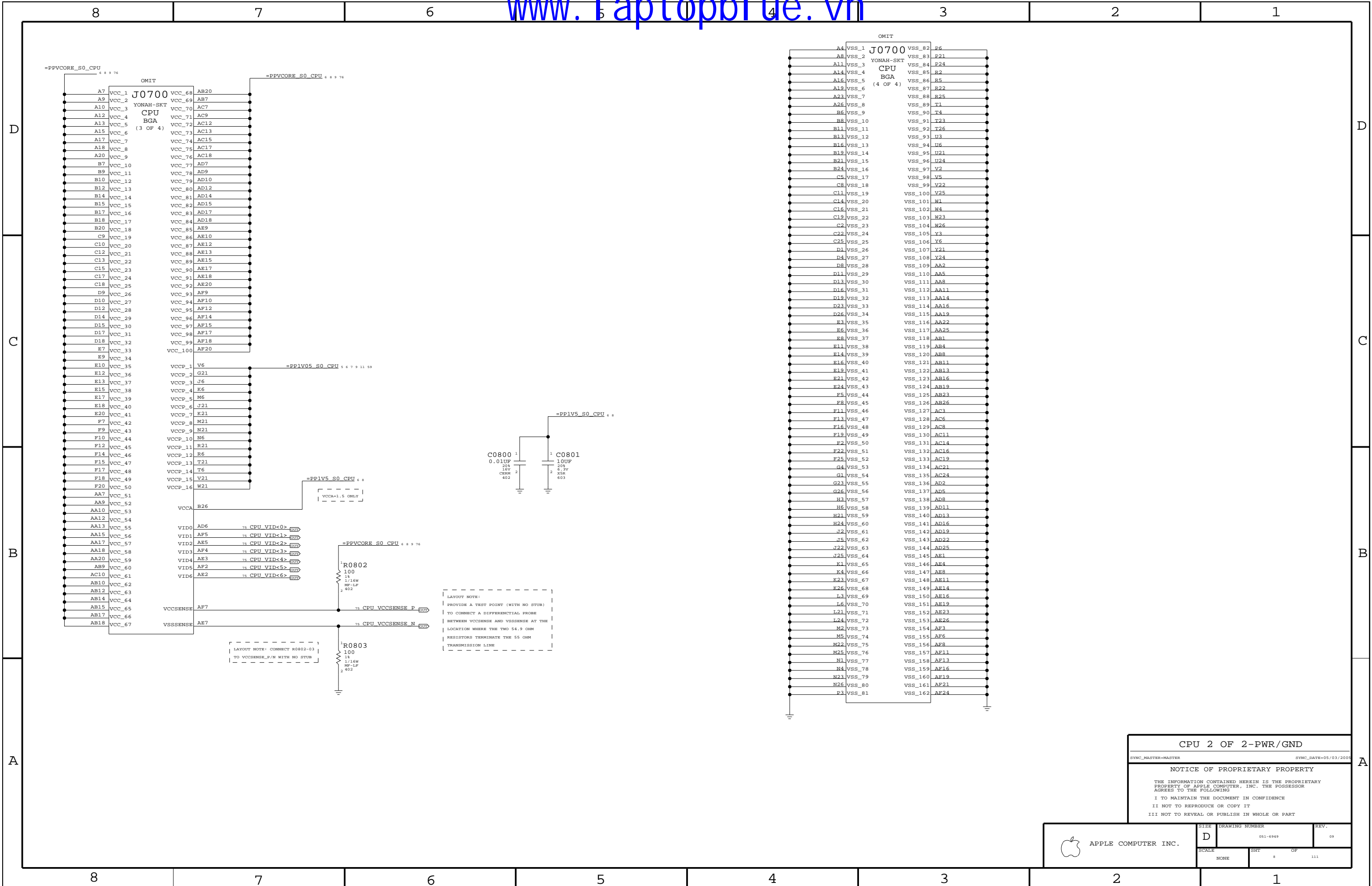
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FB B_WE_L<1>

PP9024

OMIT P4MM</





CPU 2 OF 2-PWR/GND

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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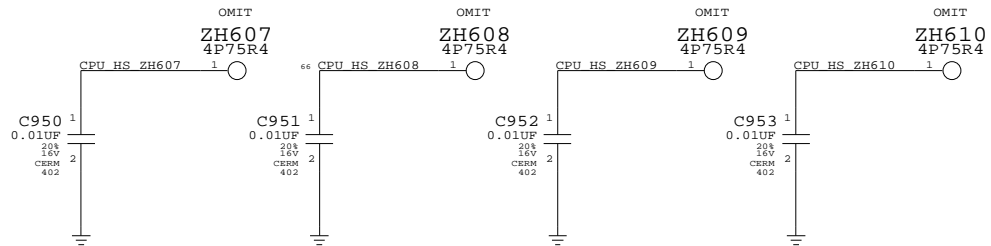
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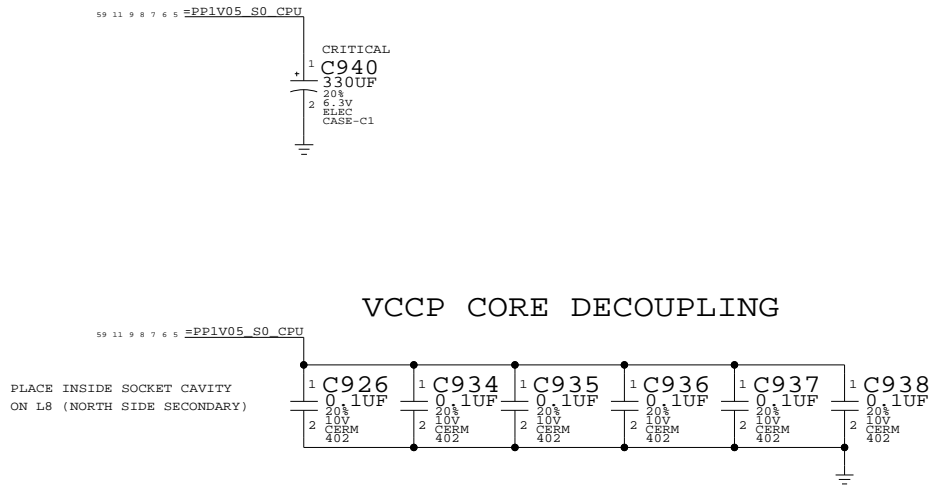
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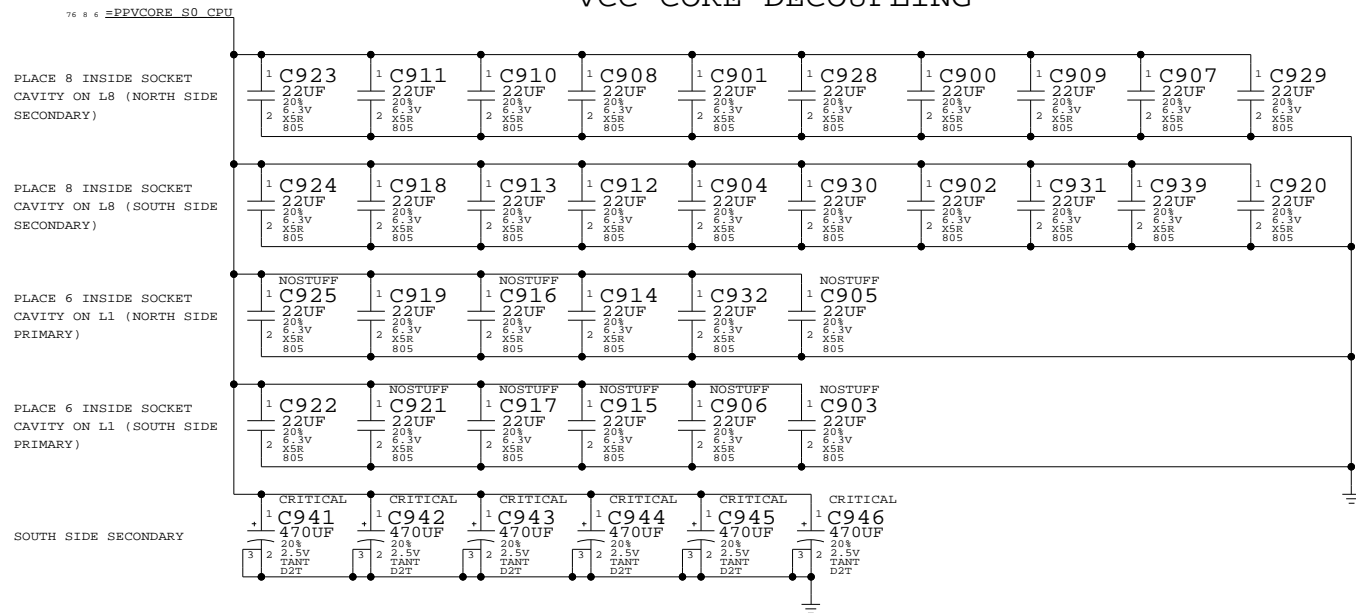
CPU HEATSINK MOUNTING HOLES



VCCP CORE DECOUPLING



VCC CORE DECOUPLING



CPU DECAPS & VID<>

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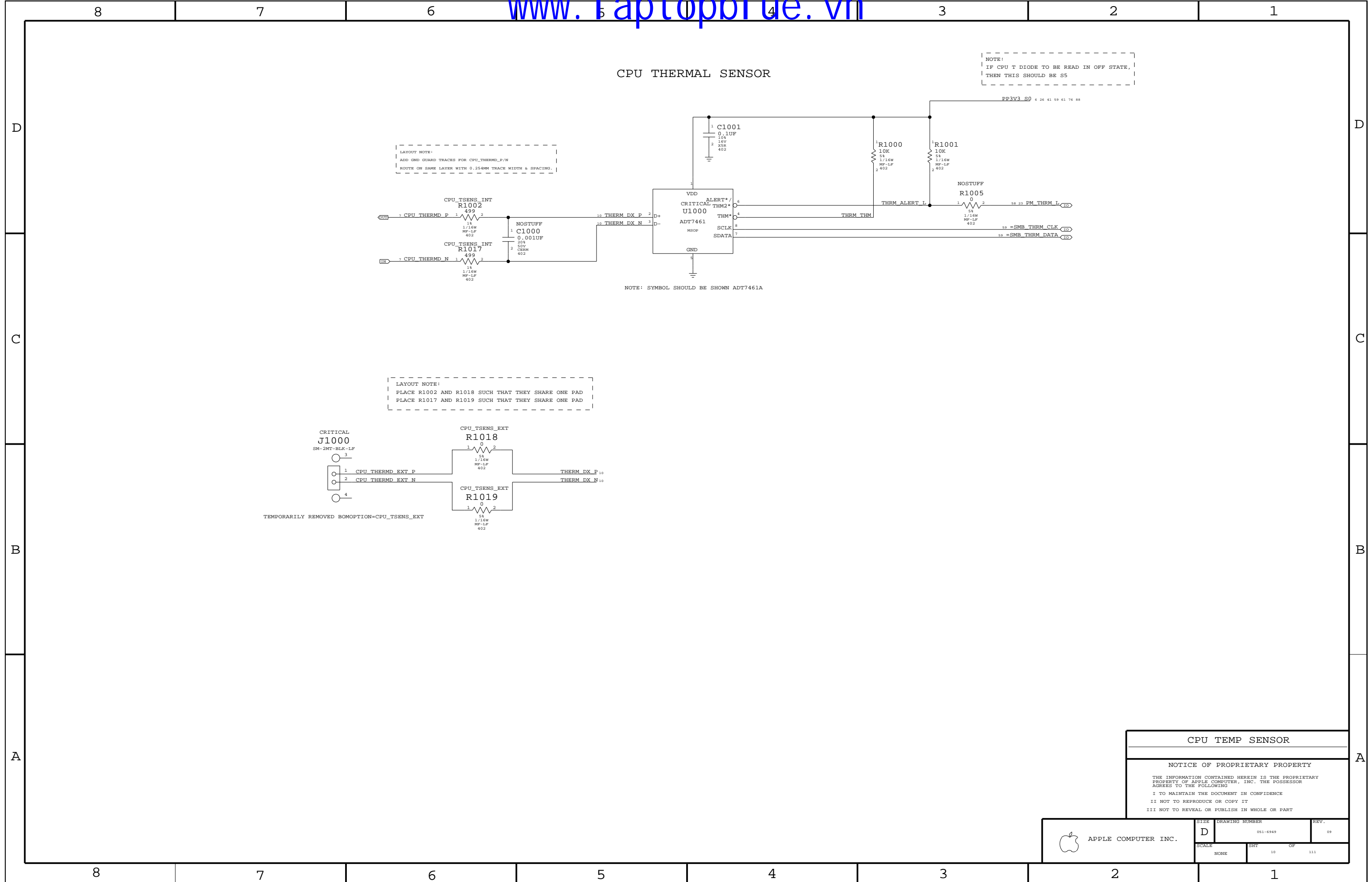
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



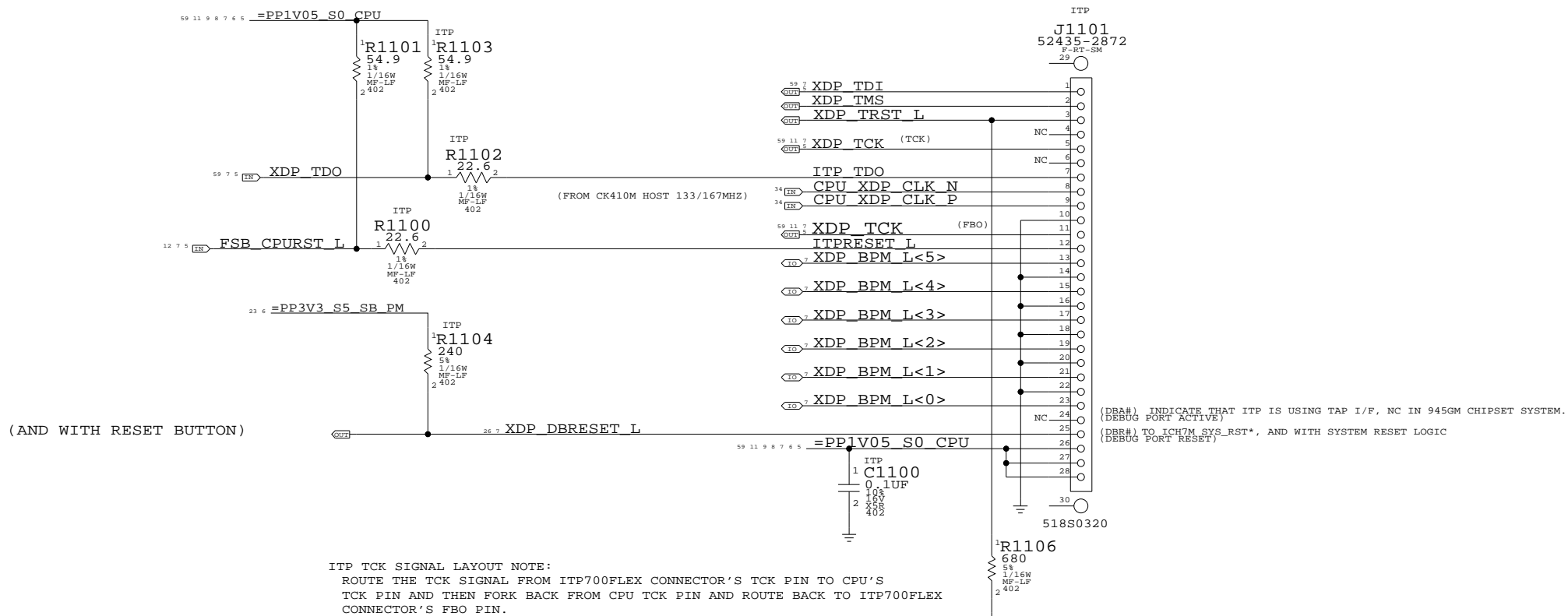
APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHT 9 OF 111



CPU ITP700FLEX DEBUG SUPPORT



CPU ITP700FLEX DEBUG

SYNC_MASTER=MASTER SYNC_DATE=5/23/05

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	D	051-6949	09
SCALE		SHT	OF
NONE		11	111

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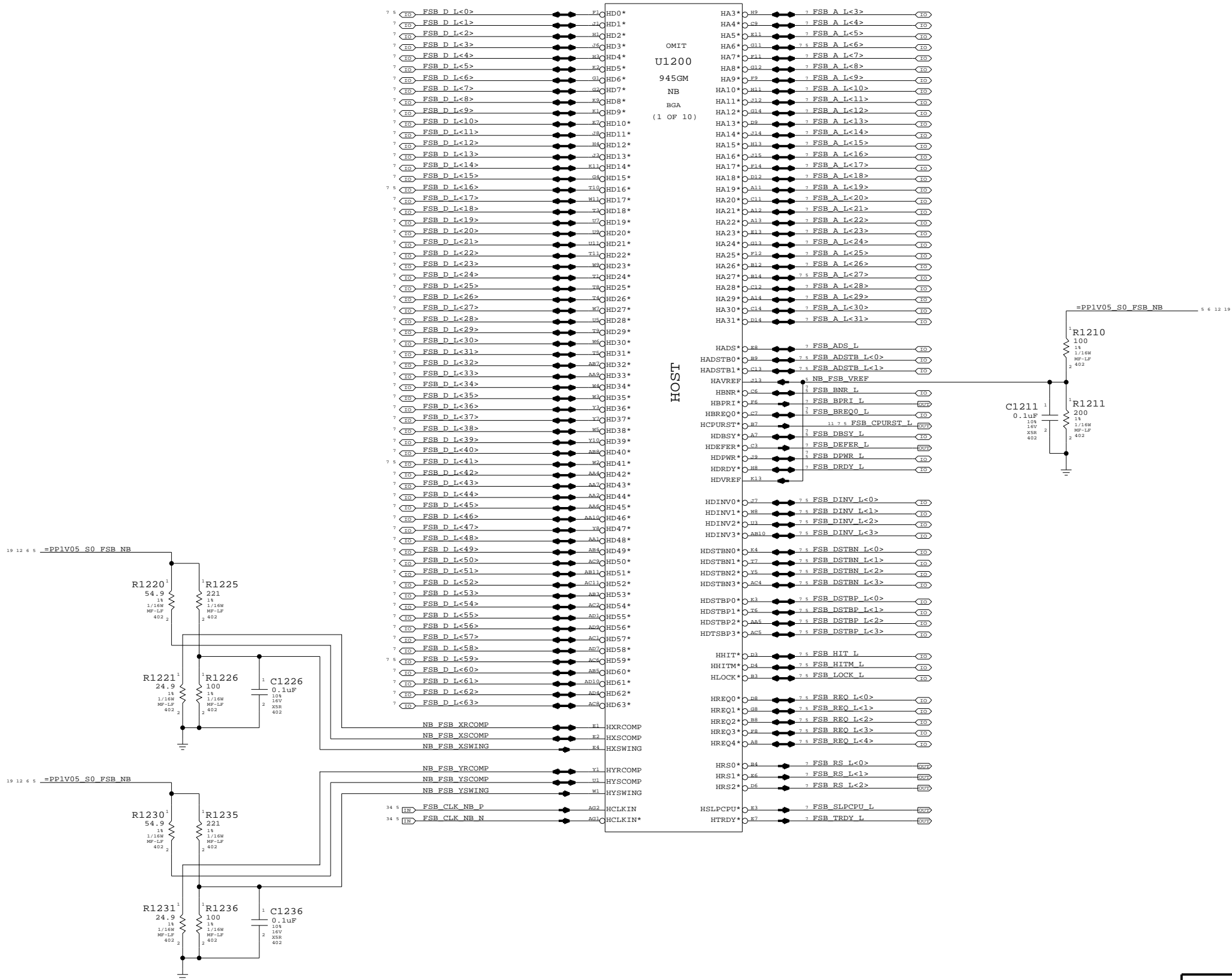
A

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NB CPU Interface

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

NOTICE OF PROPRIETARY PROPERTY

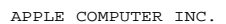
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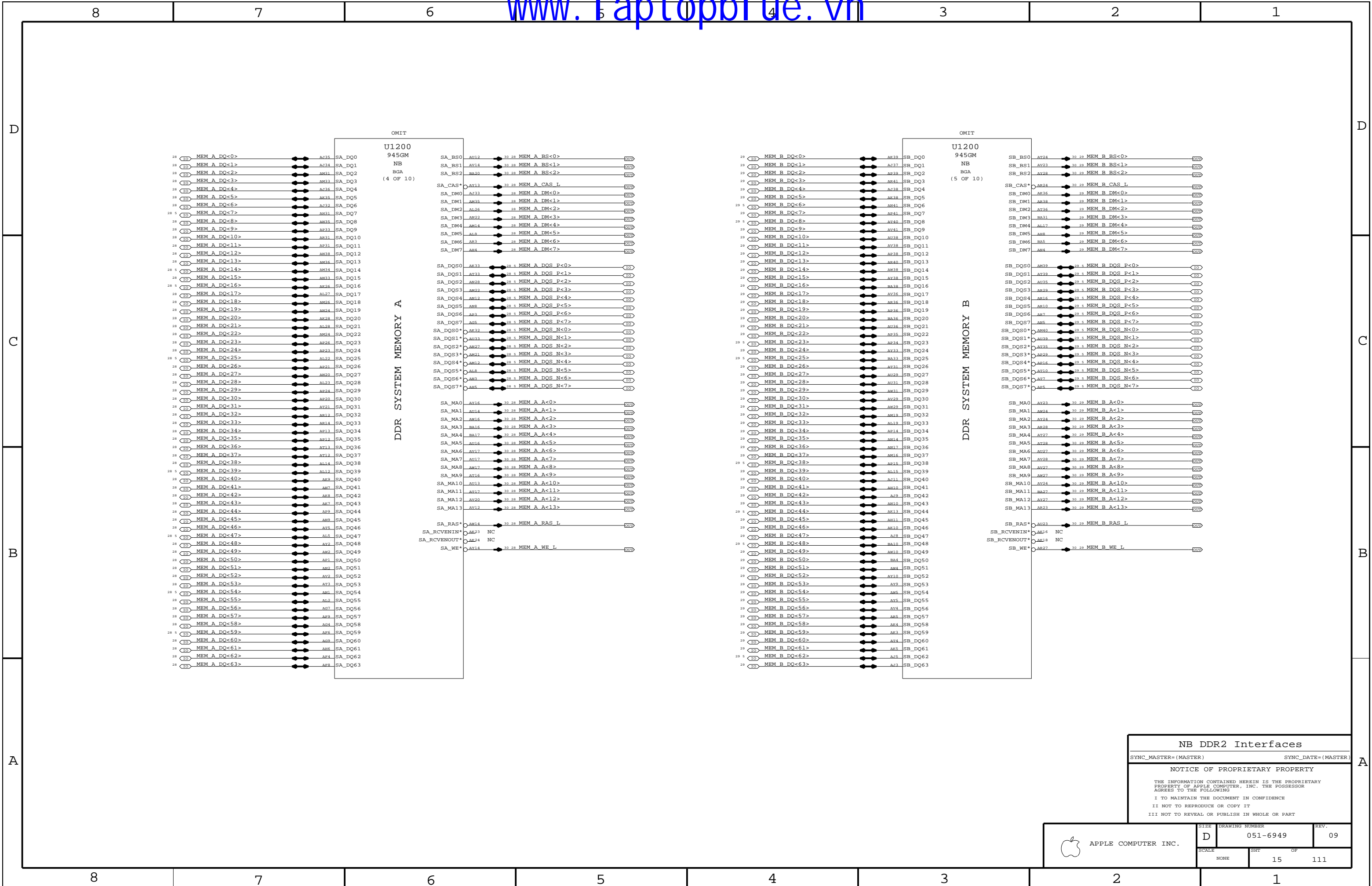
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NONE	14	111
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NB DDR2 Interfaces

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U1200
945GM
NB
BGA
(7 OF 10)
NCTF

```

=PP1V5 S0 NB VCCAUX

```

VCCAUX_NCTF0	AQ27
VCCAUX_NCTF1	AP27
VCCAUX_NCTF2	AG26
VCCAUX_NCTF3	AP26
VCCAUX_NCTF4	AG25
VCCAUX_NCTF5	AP25
VCCAUX_NCTF6	AG24
VCCAUX_NCTF7	AP24
VCCAUX_NCTF8	AG23
VCCAUX_NCTF9	AP23
VCCAUX_NCTF10	AG23
VCCAUX_NCTF11	AP22
VCCAUX_NCTF12	AG21
VCCAUX_NCTF13	AP21
VCCAUX_NCTF14	AG20
VCCAUX_NCTF15	AP20
VCCAUX_NCTF16	AG19
VCCAUX_NCTF17	AP19
VCCAUX_NCTF18	E19
VCCAUX_NCTF19	AG18
VCCAUX_NCTF20	AP18
VCCAUX_NCTF21	E18
VCCAUX_NCTF22	AG17
VCCAUX_NCTF23	AP17
VCCAUX_NCTF24	AG17
VCCAUX_NCTF25	AD17
VCCAUX_NCTF26	AG17
VCCAUX_NCTF27	AA17
VCCAUX_NCTF28	W17
VCCAUX_NCTF29	V17
VCCAUX_NCTF30	E17
VCCAUX_NCTF31	E17
VCCAUX_NCTF32	AG16
VCCAUX_NCTF33	AP16
VCCAUX_NCTF34	AG16
VCCAUX_NCTF35	AD16
VCCAUX_NCTF36	AG16
VCCAUX_NCTF37	AG16
VCCAUX_NCTF38	AA16
VCCAUX_NCTF39	V16
VCCAUX_NCTF40	W16
VCCAUX_NCTF41	E16
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VCCAUX_NCTF44	R16
VCCAUX_NCTF45	AG15
VCCAUX_NCTF46	AP15
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VCCAUX_NCTF48	AD15
VCCAUX_NCTF49	AG15
VCCAUX_NCTF50	AG15
VCCAUX_NCTF51	AA15
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VCCAUX_NCTF57	R15

SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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SIZE	DRAWING NUMBER	REV.
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D	051-6949
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APPLE COMPUTER INC.

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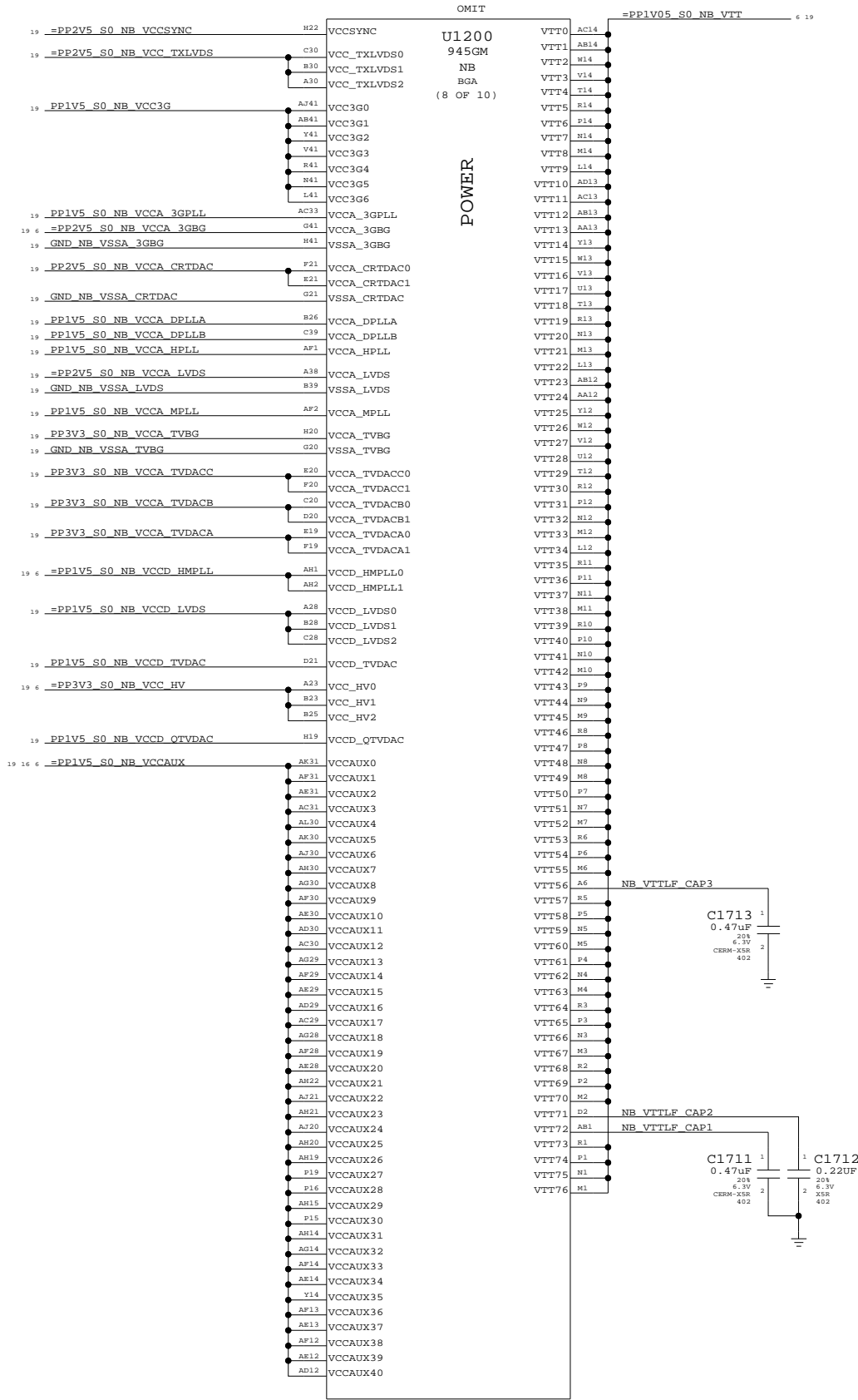
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NB Power 2

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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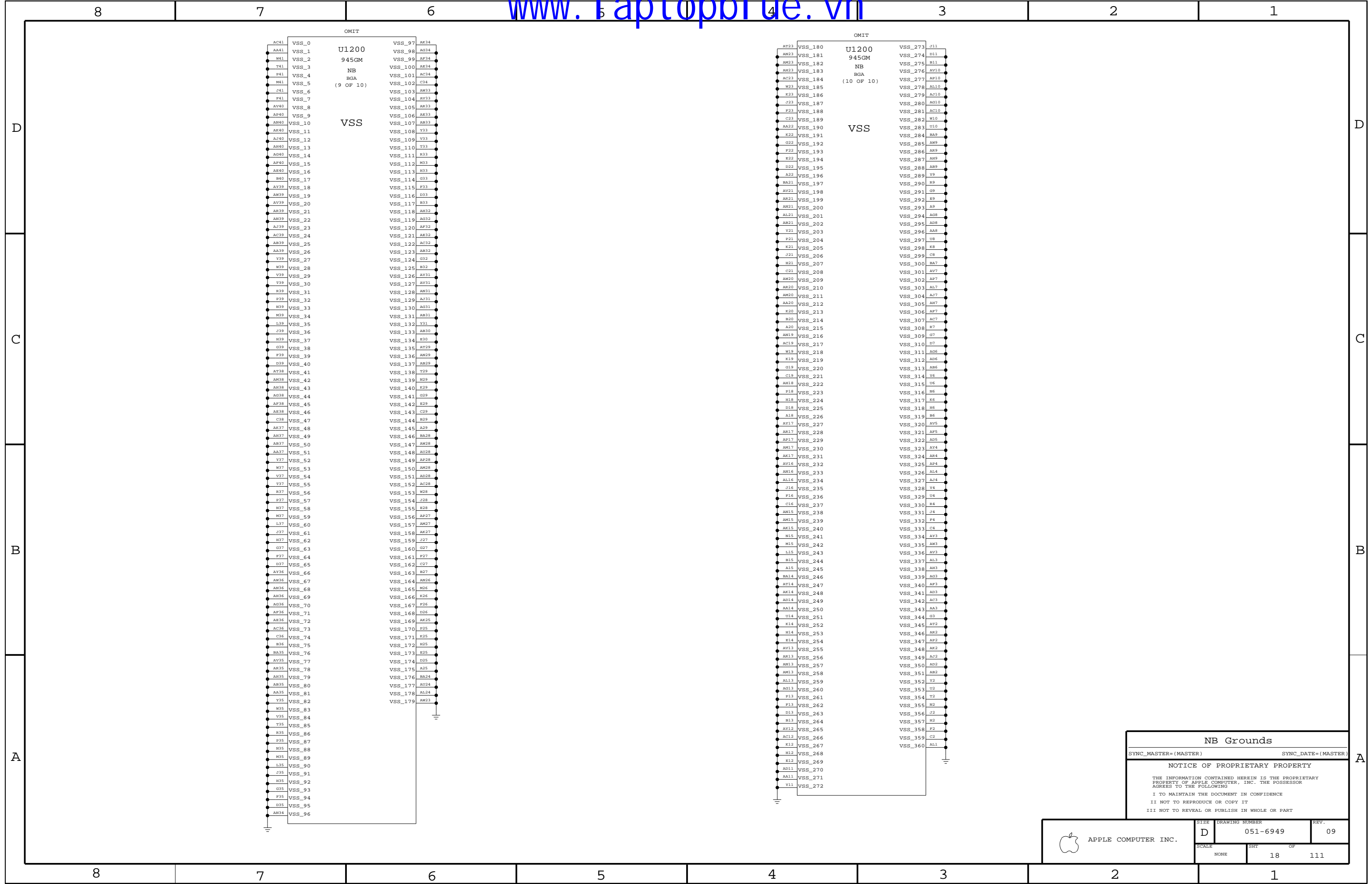
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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	17	111

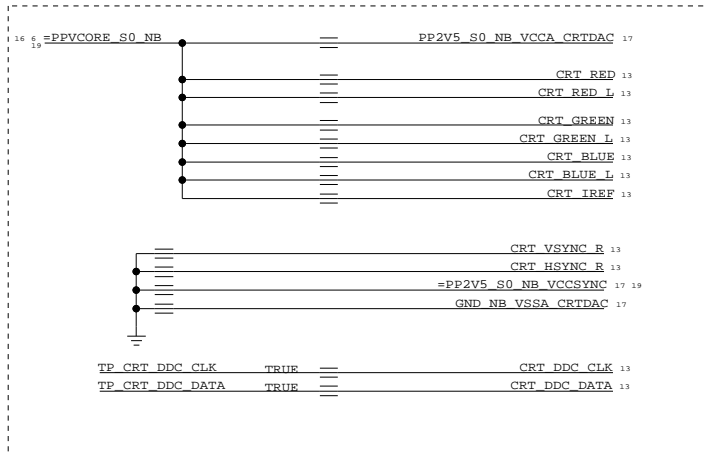


Power Interface

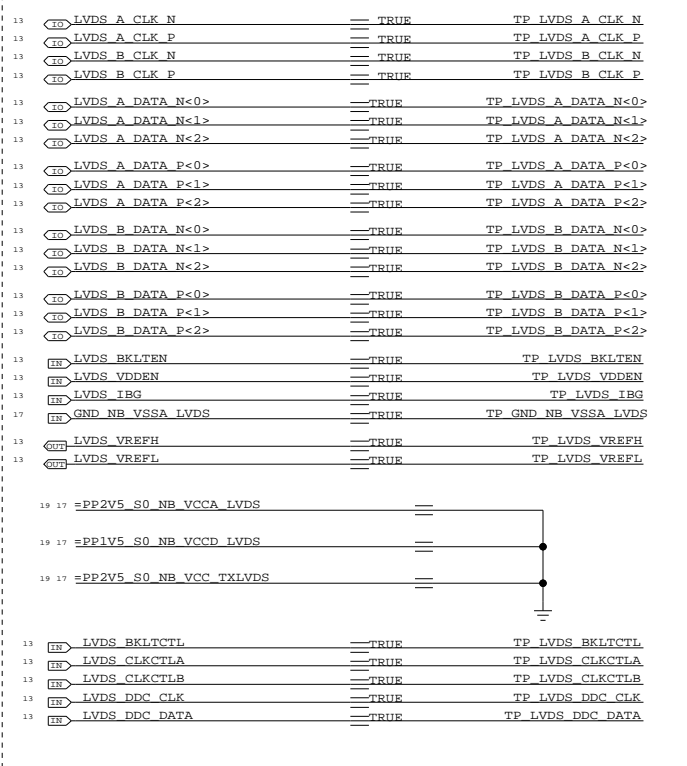
These are the power signals that leave the NB "block"

EN	=PP1V05_S0_FSB_NB	5 6 12
EN	=PPVCORE_S0_NB	6 16 19
EN	=PP1V05_S0_NB	6
EN	=PP1V05_S0_NB_VTT	6 17 19
EN	=PP1V5_S0_NB	6 19
EN	=PP1V5_S0_NB_PCIE	6 13
EN	=PP1V5_S0_NB_PLL	6 19
EN	=PP1V5_S0_NB_TVDAC	6 19
EN	=PP1V5_S0_NB_VCCD_HMPLL	6 17
EN	=PP1V5_S0_NB_VCCD_LVDS	17 19
EN	=PP1V5_S0_NB_VCCAUX	6 16 17 19
EN	=PP1V8_S3_MEM_NB	6 14 16 19
EN	=PP2V5_S0_NB_VCCSYN	17 19
EN	=PP2V5_S0_NB_VCC_TXLVDS	17 19
EN	=PP2V5_S0_NB_VCCA_3GBG	6 17 19
EN	=PP2V5_S0_NB_VCCA_LVDS	17 19
EN	=PP3V3_S0_NB	6 14 20
EN	=PP3V3_S0_NB_TVDAC	6
EN	=PP3V3_S0_NB_VCC_HV	6 17 19

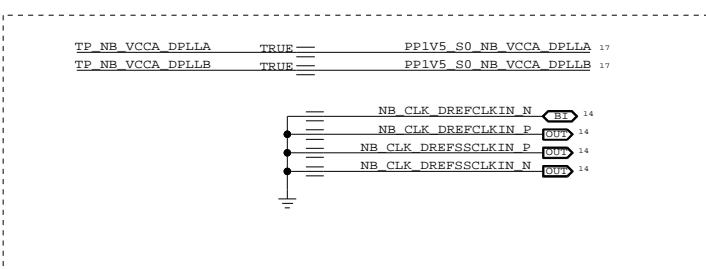
TVOUT DISABLE



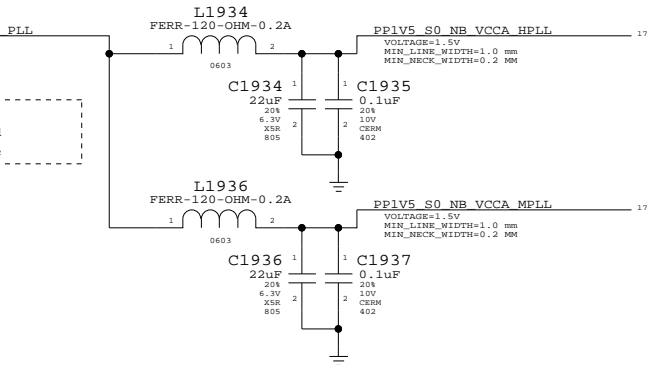
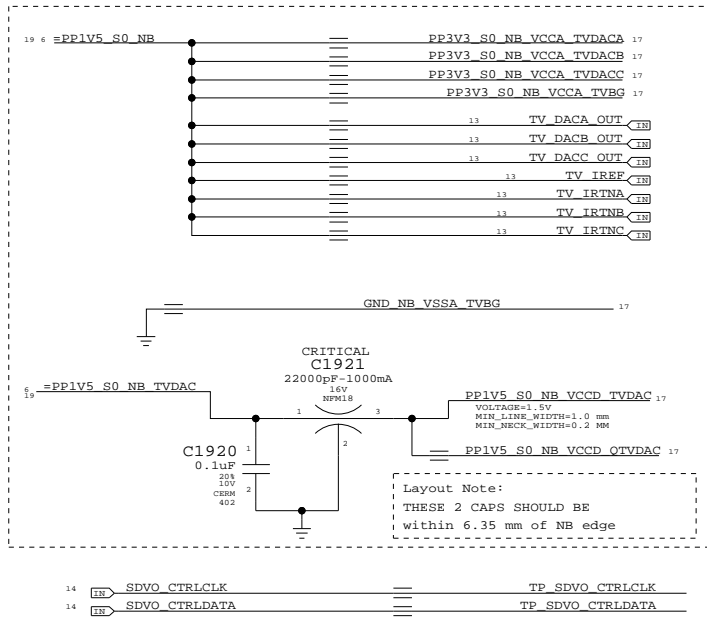
LVDS DISABLE



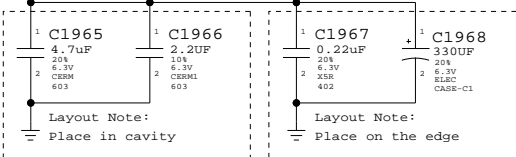
DISPLAY DISABLE



TVOUT DISABLE

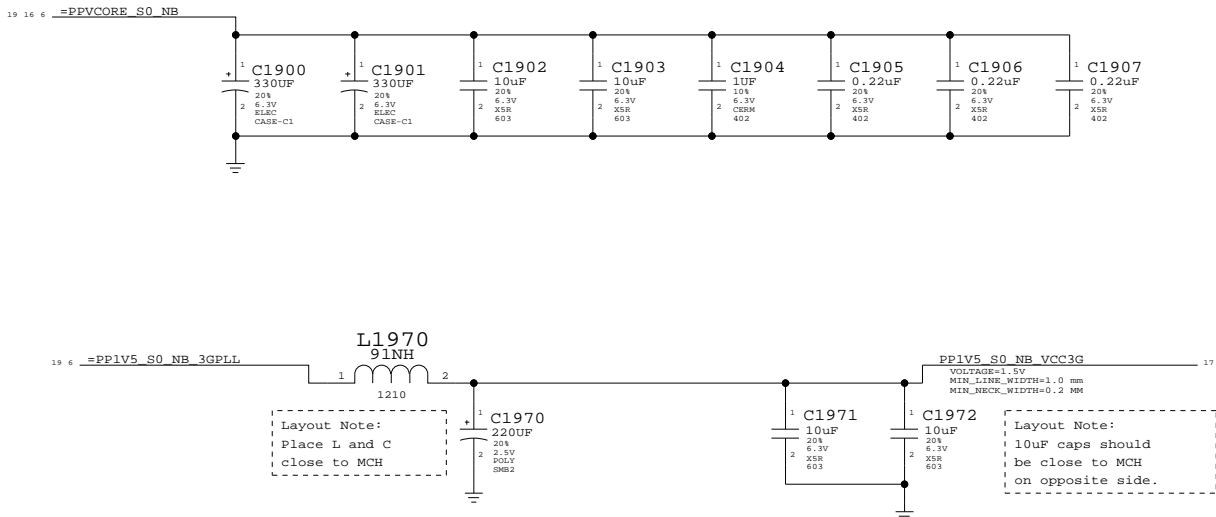
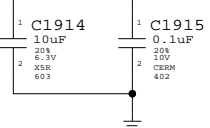
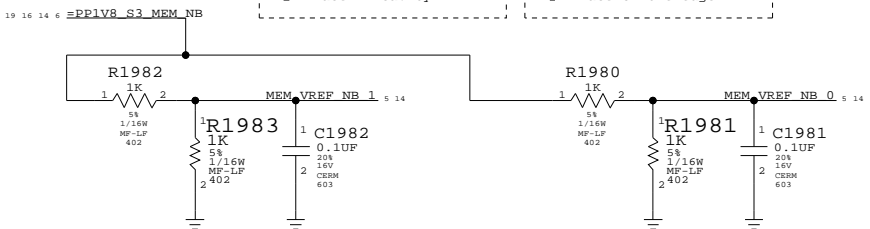


Layout Note:
These 4 0.1uF caps should
be within 5 mm of NB edge



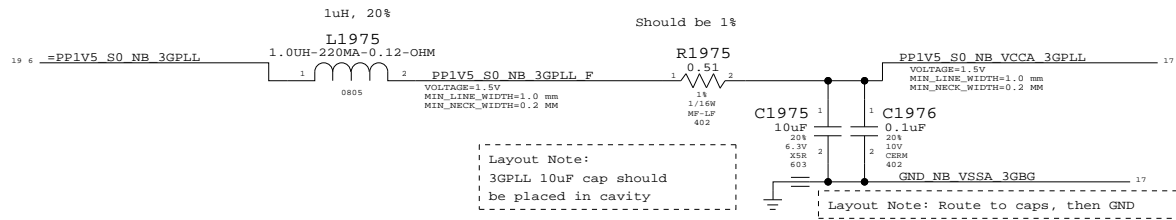
Layout Note:
Place in cavity

Layout Note:
Place on the edge



Layout Note:
Place L and C
close to MCH

Layout Note:
10uF caps should
be close to MCH
on opposite side.



Layout Note:
3GPLL 10uF cap should
be placed in cavity

Layout Note: Route to caps, then GND

NB (GM) Decoupling

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	19	111

D

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D

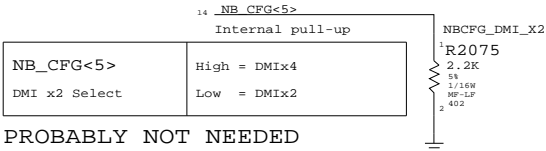
C

B

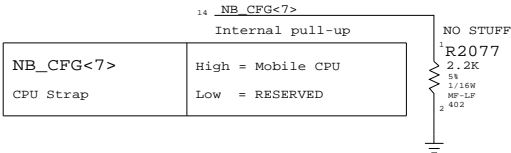
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NB_CFG<3>	RESERVED
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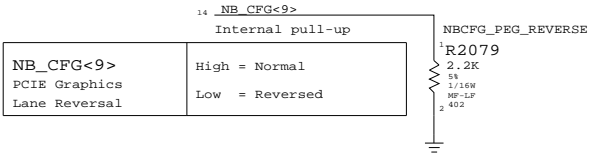
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NB_CFG<6>	RESERVED
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NB_CFG<8>	RESERVED
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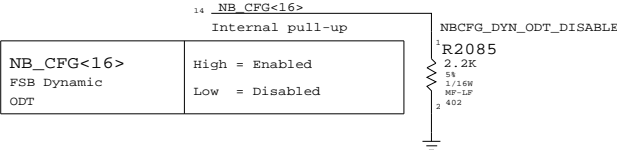
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NB_CFG<11>	RESERVED
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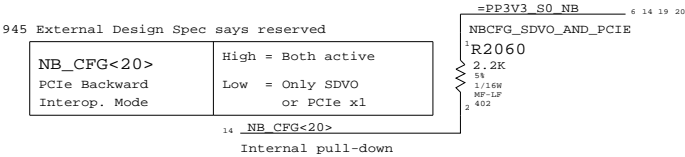
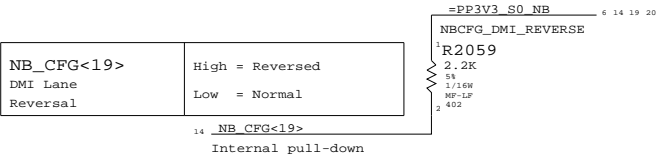
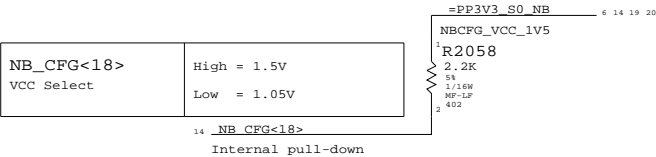
Internal pull-ups	
NB_CFG<13:12>	00 = Partial Clock Gating Disable 01 = XOR Mode Enabled 10 = All-Z Mode Enabled 11 = Normal Operation

NB_CFG<14>	RESERVED
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NB_CFG<15>	RESERVED
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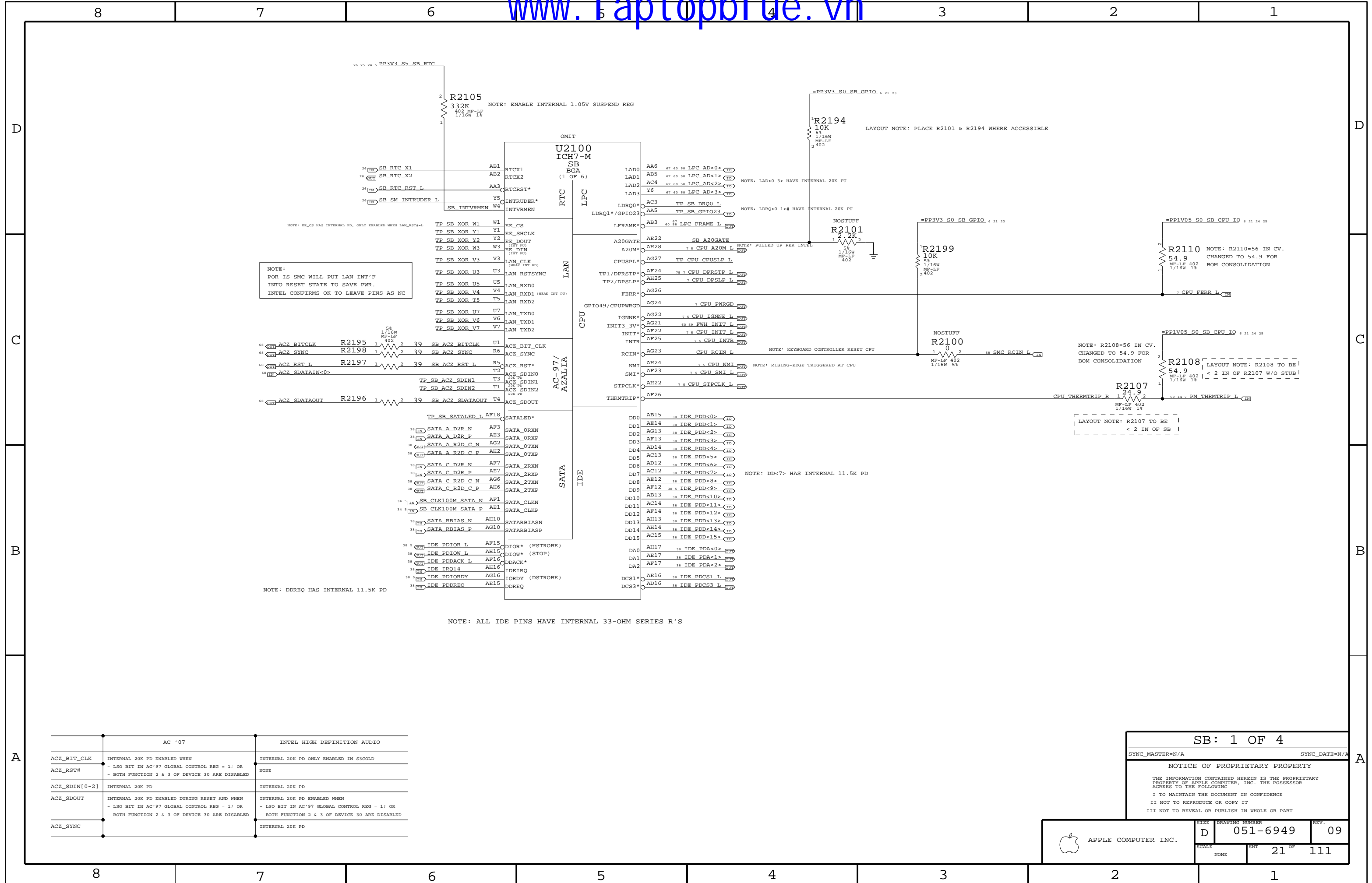
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PROBABLY NOT NEEDED

NB Config Straps		
SYNC_MASTER=(MASTER)		SYNC_DATE=(MASTER)
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	D	051-6949	09
SCALE		SHT	OF
NONE		20	111



SB: 1 OF 4

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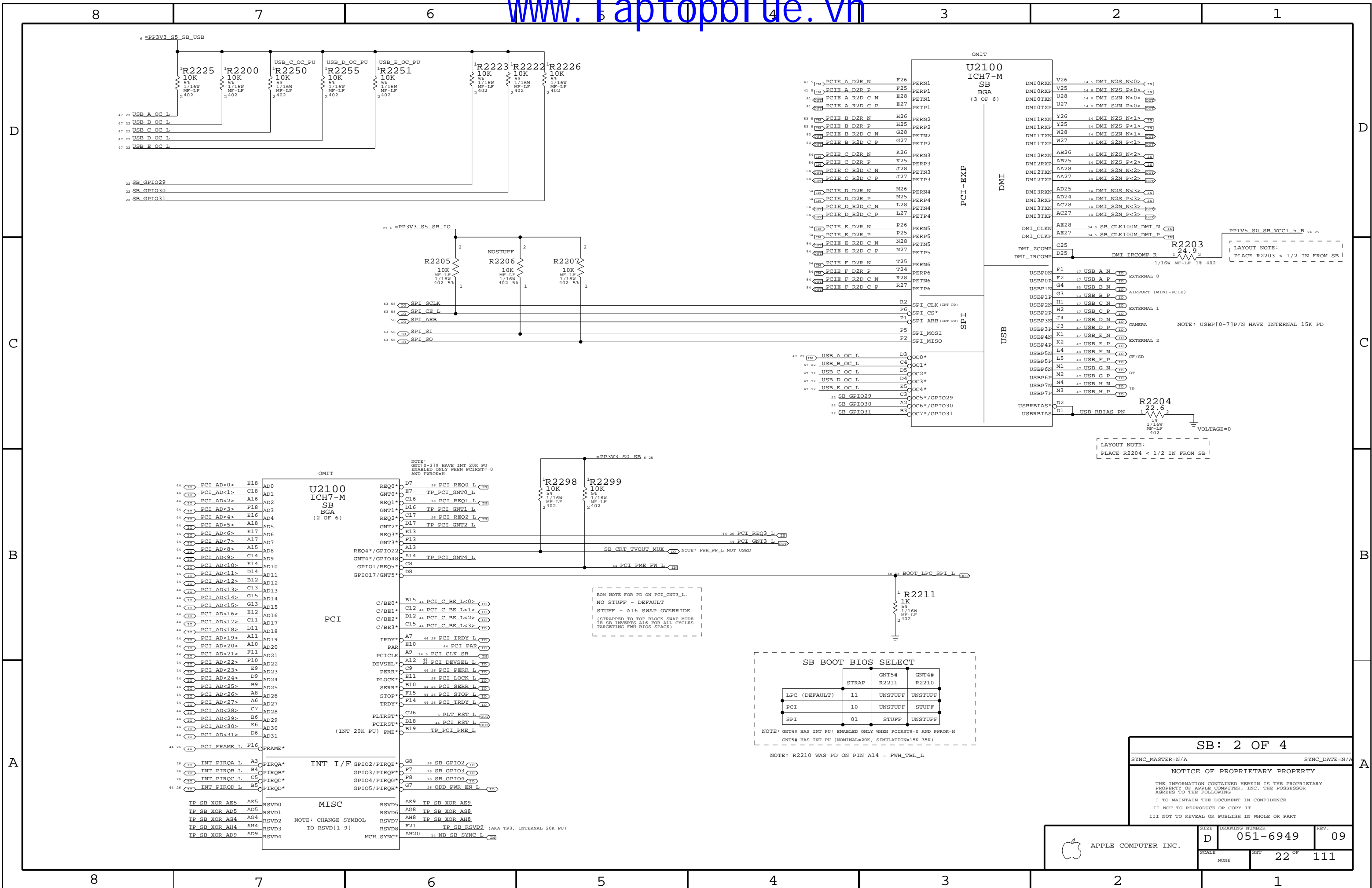
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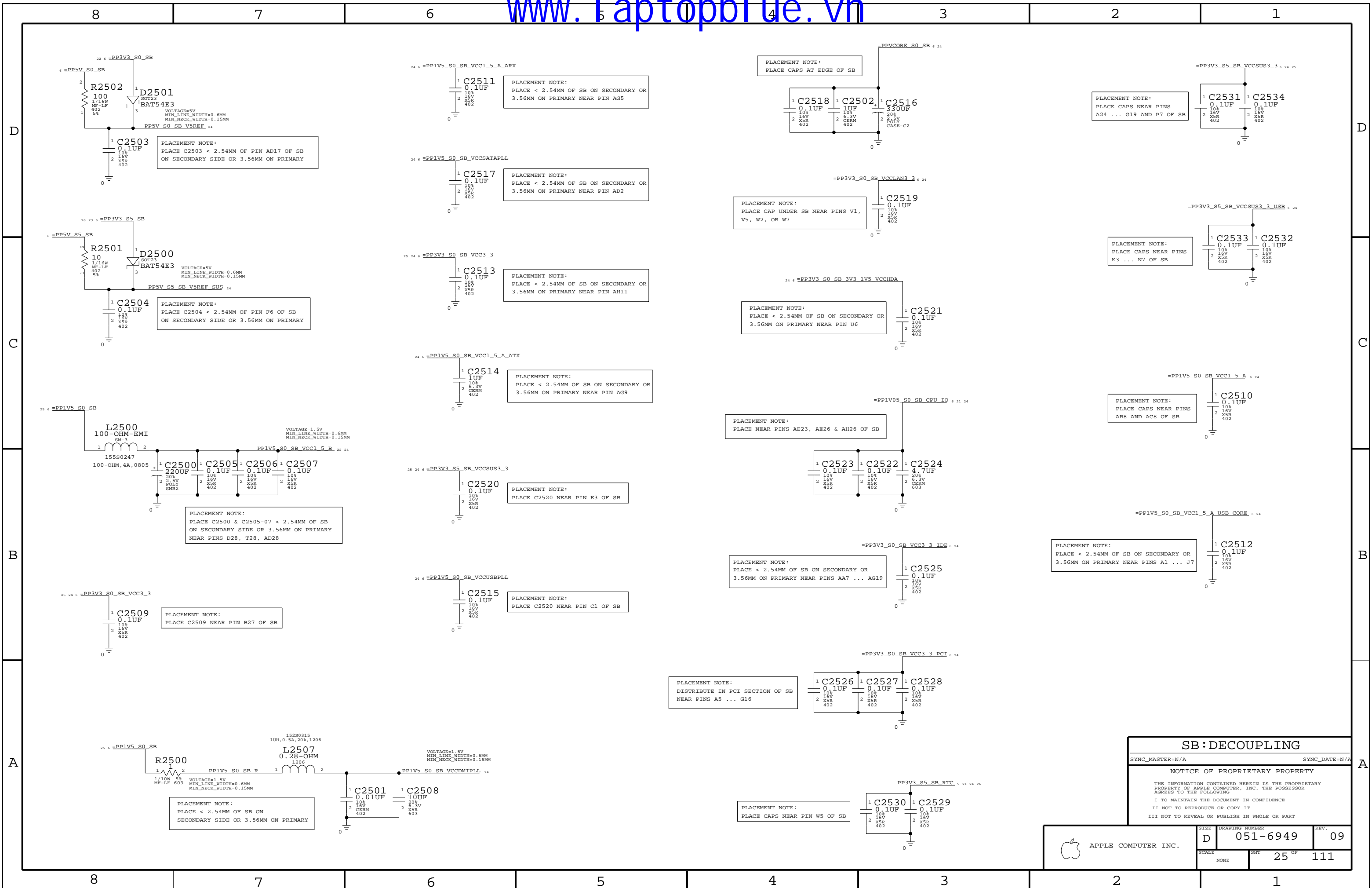
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SB: DECOUPLING

SYNC_MASTER=N/A

SYNC_DATE=N/A

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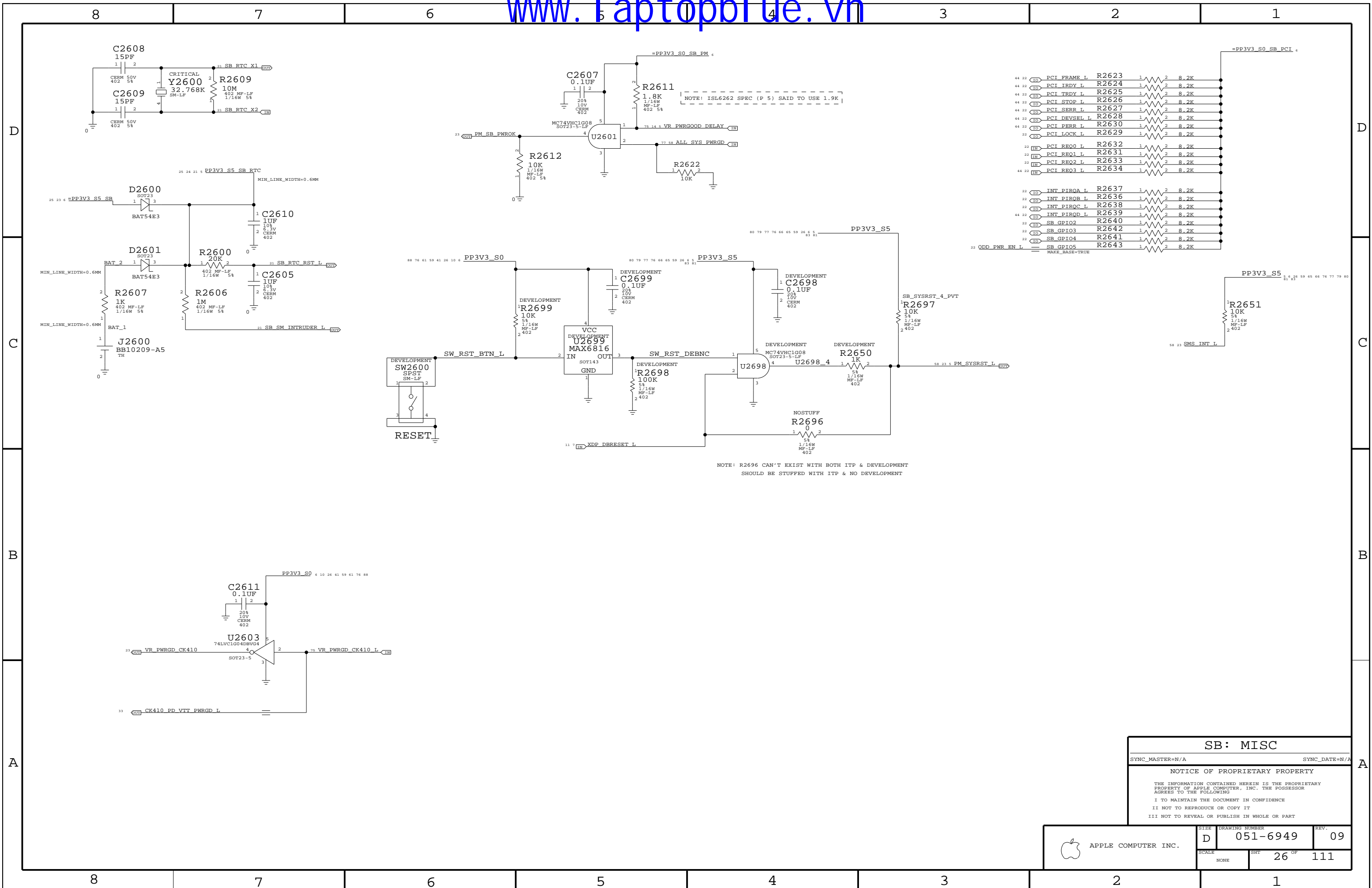
SCALE: NONE

D

SHEET: 25 OF 111

SIZE: DRAWING NUMBER: 051-6949

REV.: 09



SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

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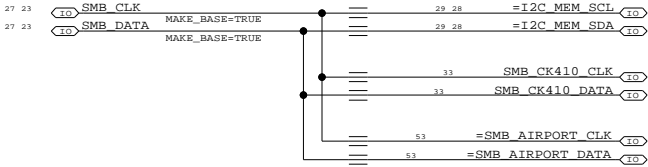
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D

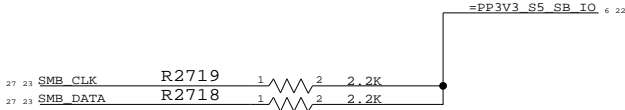
D

SB I2C BUSSES



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SB: SMB HUB

SYNC_MASTER=N/A SYNC_DATE=N/A

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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	27 OF 111
NONE		

Power aliases required by this page:

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- =PPSPD_S0_MEM (2.5V ~ 3.3V)

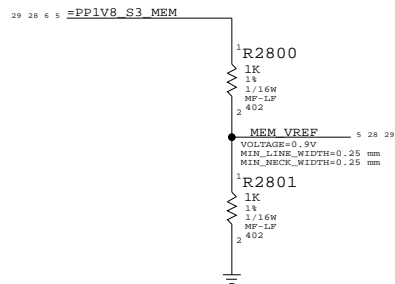
Signal aliases required by this page:

- =I2C_MEM_SCL
- =I2C_MEM_SDA

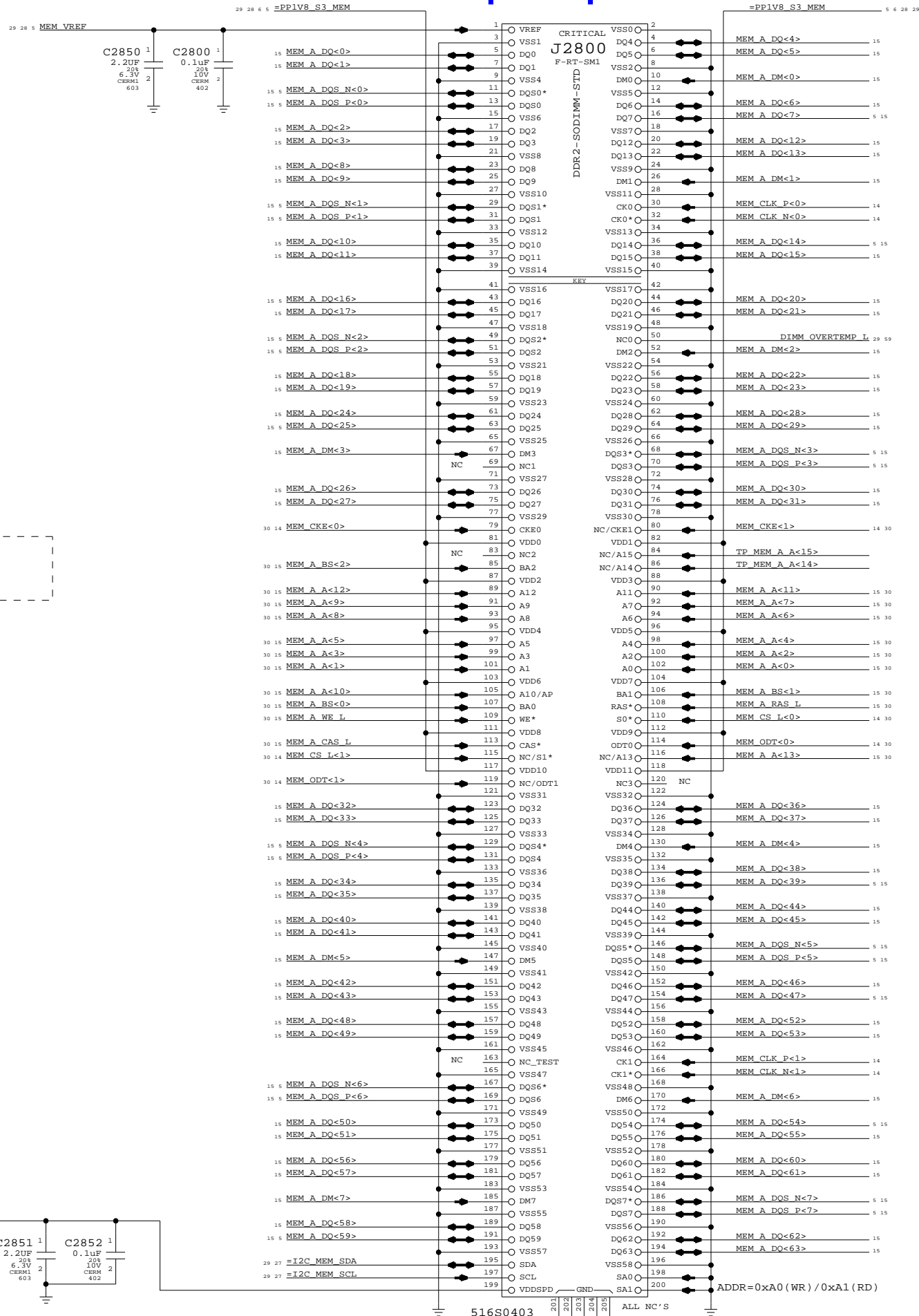
BOM options provided by this page:

(NONE)

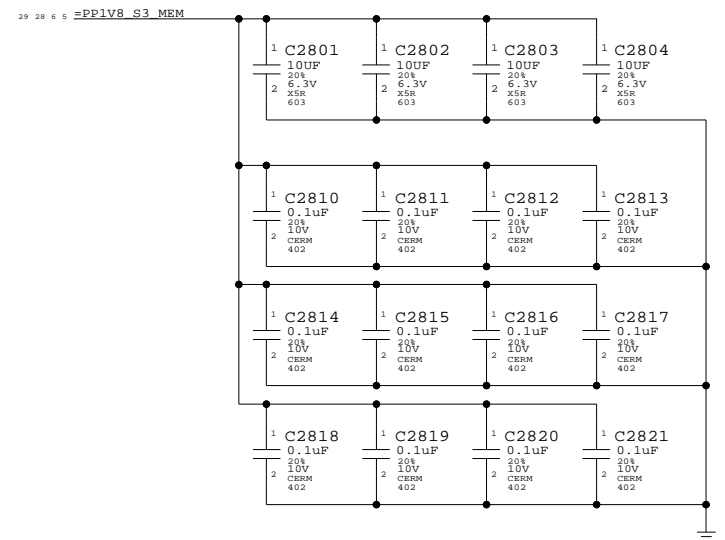
One 0.1uF per connector



Yellow uses 10K divider and TLV2463
to drive MCH and DIMM connectors.
(See Capell Valley pg 47)



(For return current)



SYNC_MASTER= (MASTER)	SYNC_DATE= (MASTER)
-----------------------	---------------------

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SIZE D	DRAWING NUMBER 051-6949	REV. 09
SCALE NONE	SHT 28	OF 111

Page Notes

Power aliases required by this page:

- =PPIV8_S3_MEM
- =PPSPD_S0_MEM (2.5V - 3.3V)

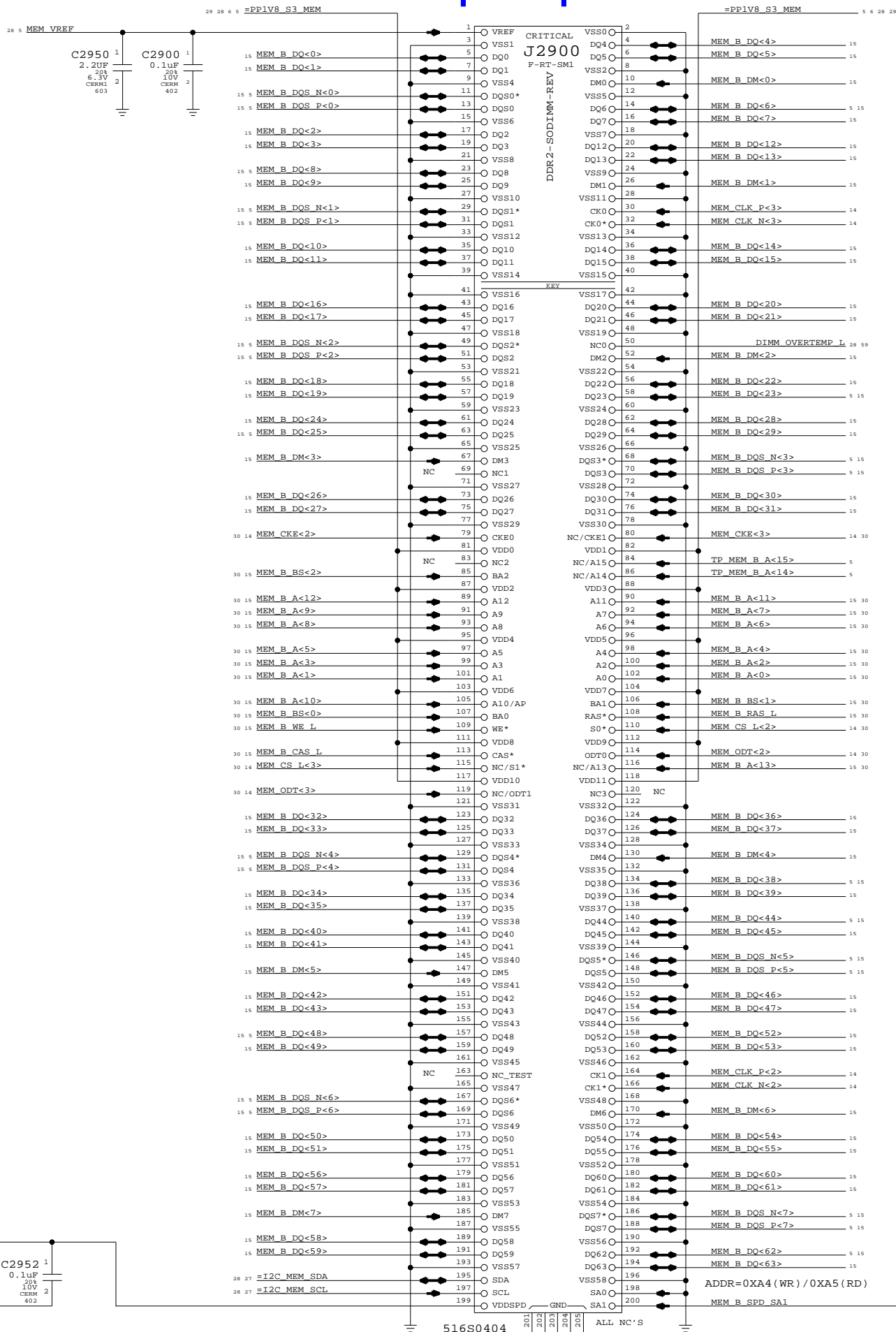
Signal aliases required by this page:

- =I2C_MEM_SCL
- =I2C_MEM_SDA

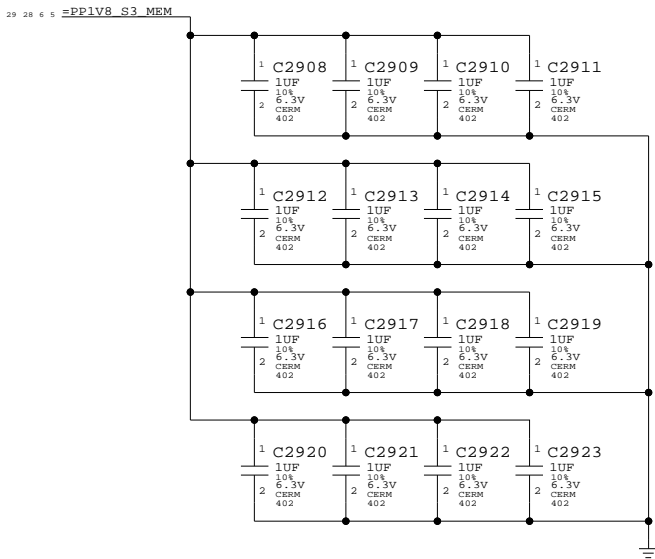
BOM options provided by this page:

(NONE)

NOTE: This page does not supply VREF.
The reference voltage must be provided
by another page.



DDR2 Bypass Caps
(For return current)



DDR2 SO-DIMM Connector B

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.

SCALE: NONE SHT: 29 OF: 111

SIZE: D DRAWING NUMBER: 051-6949 REV.: 09

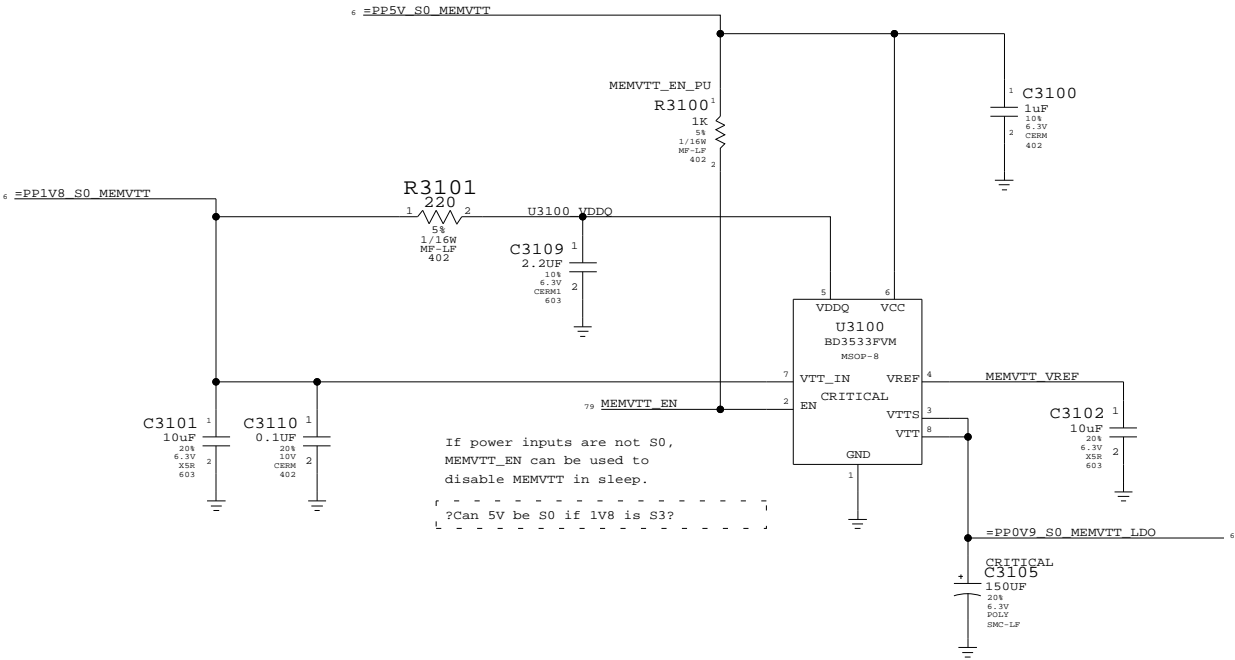
Page Notes

Power aliases required by this page:
- =PP5V_S0_MEMVTT
- =PP1V8_S0_MEMVTT
- =PP0V9_S0_MEMVTT_LDO

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

DDR2 Vtt Regulator



Memory Vtt Supply

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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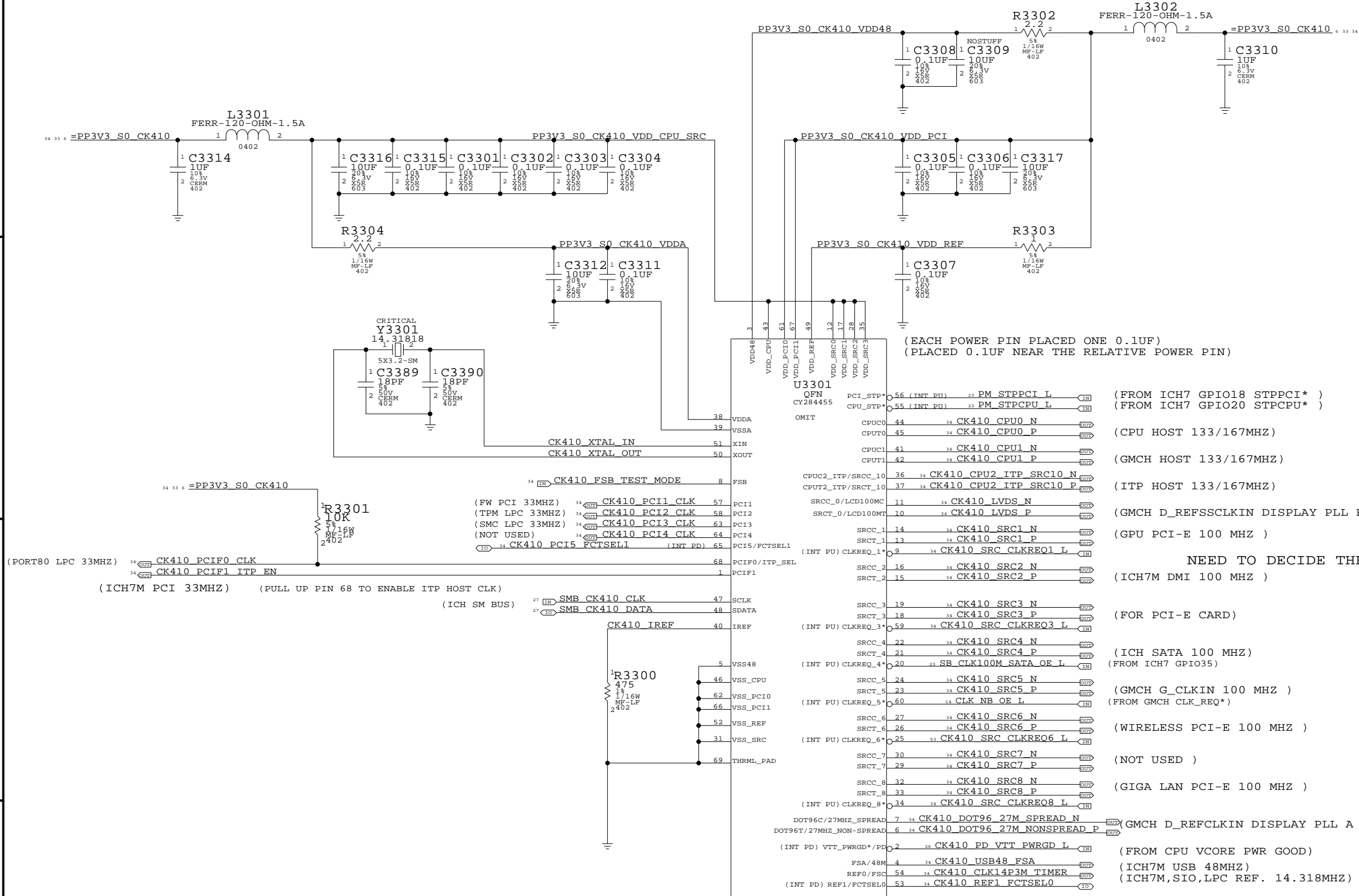
APPLE COMPUTER INC.

SIZE DRAWING NUMBER REV.

D 051-6949 09

SCALE SHEET OF

NONE 31 111



(EACH POWER PIN PLACED ONE 0.1UF)
(PLACED 0.1UF NEAR THE RELATIVE POWER PIN)

(FROM ICH7 GPIO18 STPPCI*)
(FROM ICH7 GPIO20 STPCPU*)

(CPU HOST 133/167MHZ)

(GMCH HOST 133/167MHZ)

(ITP HOST 133/167MHZ)

(GMCH D_REFSSCLKIN DISPLAY PLL B 100MHZ)

(GPU PCI-E 100 MHZ)

NEED TO DECIDE THE CLKREQ CONNECTION,TO GPIO?
(ICH7M DMI 100 MHZ)

(FOR PCI-E CARD)

(ICH SATA 100 MHZ)

(FROM ICH7 GPIO35)

(GMCH G_CLKIN 100 MHZ)

(FROM GMCH CLK_REQ*)

(WIRELESS PCI-E 100 MHZ)

(NOT USED)

(GIGA LAN PCI-E 100 MHZ)

(GMCH D_REFCLKIN DISPLAY PLL A 96MHZ)

(FROM CPU VCORE PWR GOOD)

(ICH7M USB 48MHZ)

(ICH7M,SIO,LPC REF. 14.318MHZ)

FCTSEL1	FCTSEL0	PIN 6	PIN 7	PIN 10	PIN 11
0	0	DOT96T	DOT96C	100MT_SST	100MC_SST
0	1	DOT96T	DOT96C	SRCT0	SRCC0
1	0	27M NON SPREAD	27M SPREAD	SRCT0	SRCC0
1	1	OFF LOW	TBD	SRCT0	SRCC0

* FOR INT. GRAPHIC SYSTEM

* FOR EXT. GRAPHIC SYSTEM

CLOCKS

SYNC_MASTER=CLOCK SYNC_DATE=06/03/2005

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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	33	111



D

C

B

A

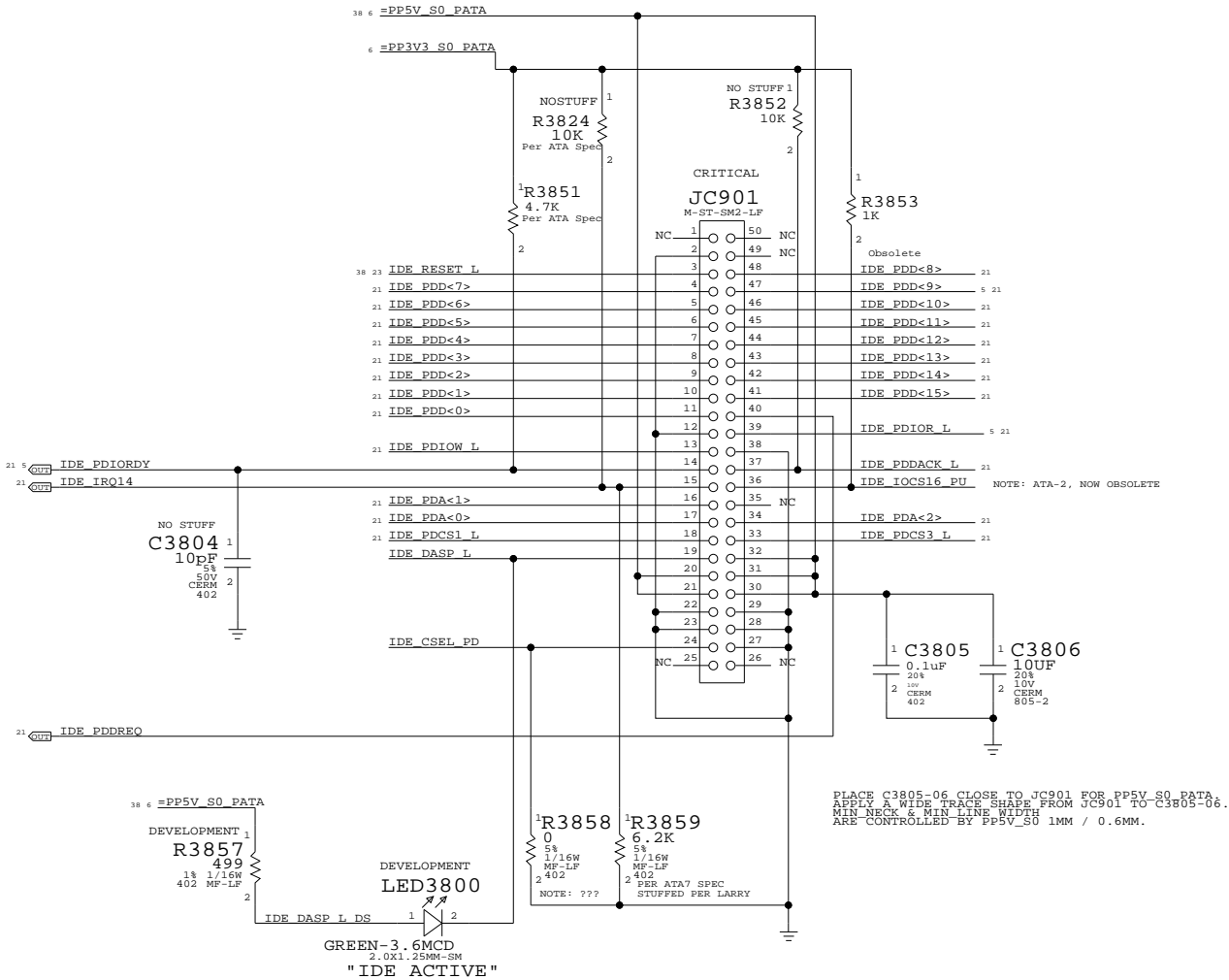
D

C

B

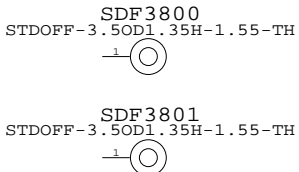
A

PATA CONNECTOR

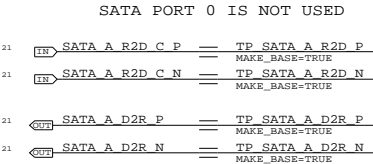
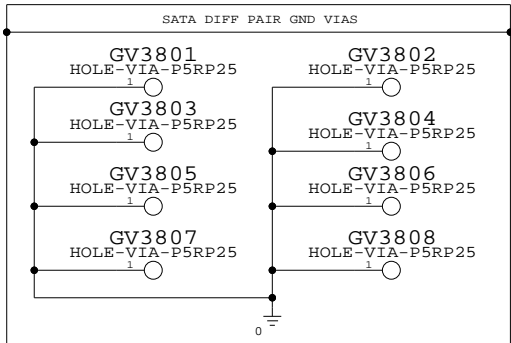
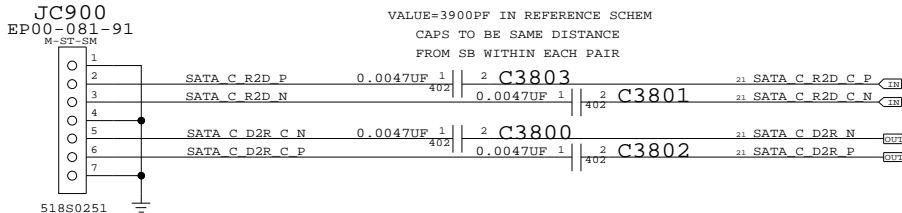


PLACE C3805-06 CLOSE TO JC901 FOR PP5V_S0_PATA.
APPLY A WIDE TRACE SHAPE FROM JC901 TO C3805-06.
WIN NCK & WIN LINE WITH
ARE CONTROLLED BY PP5V_S0 1MM / 0.6MM.

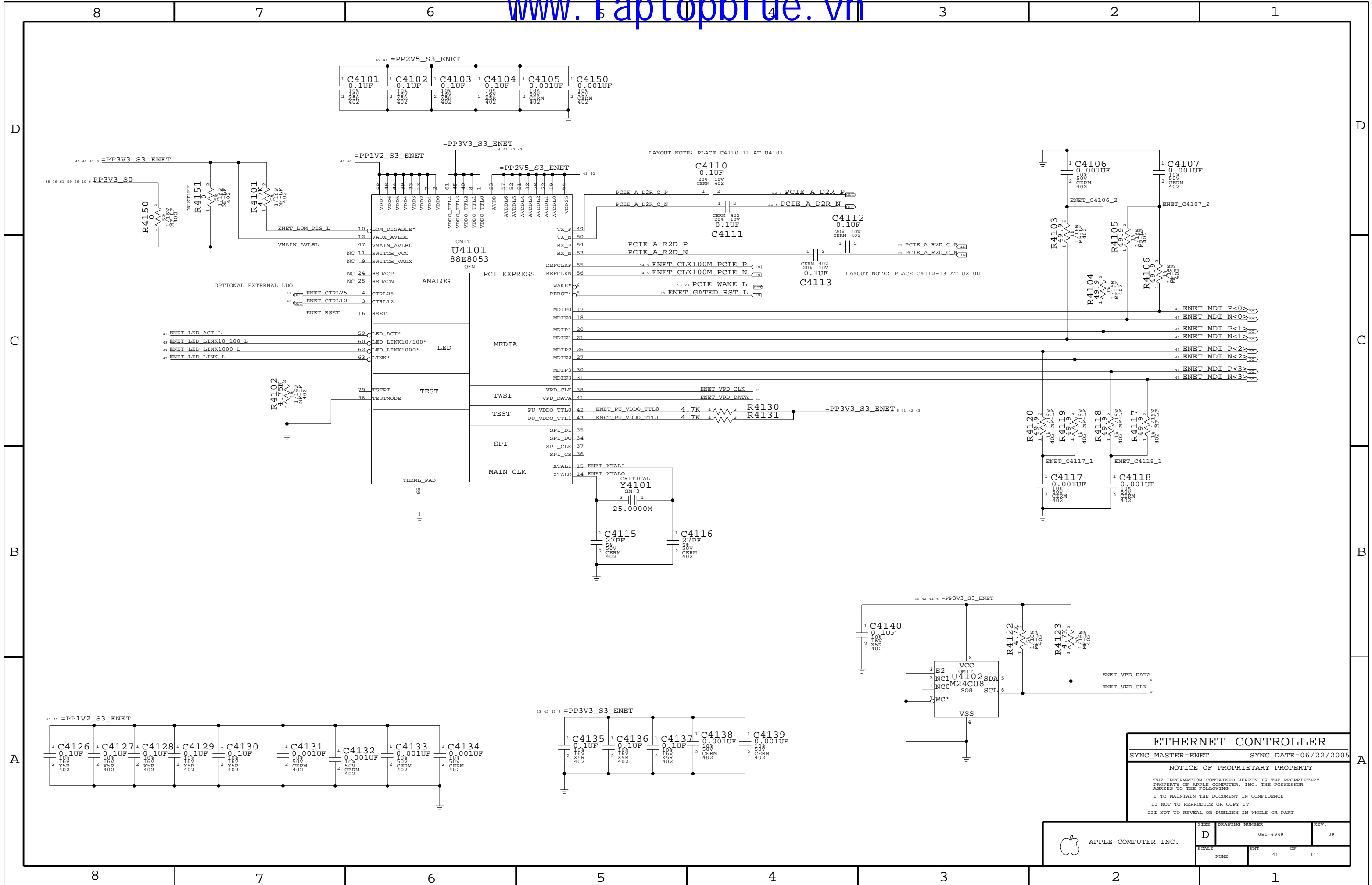
SMT NUTS FOR ODD ADAPTER



SATA CONNECTOR

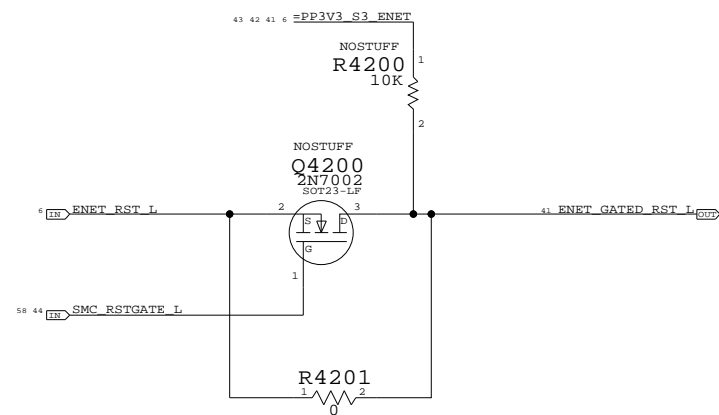


Disk Connectors		
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER
	D	051-6950
SCALE	SHT	OF
	NONE	38 111

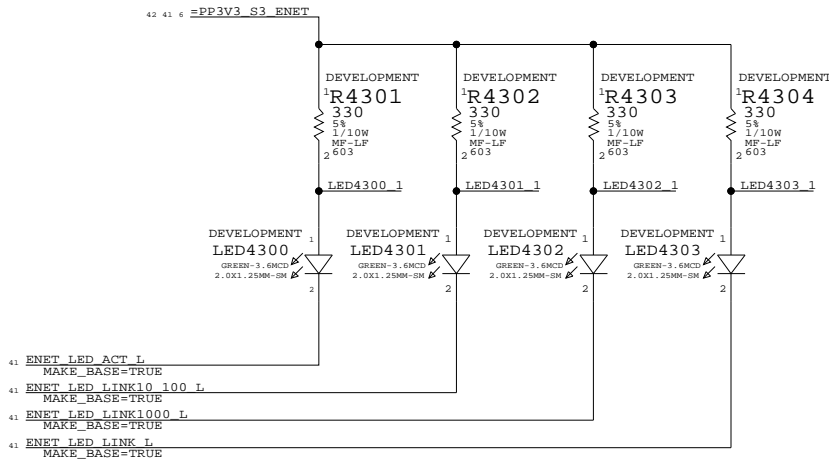
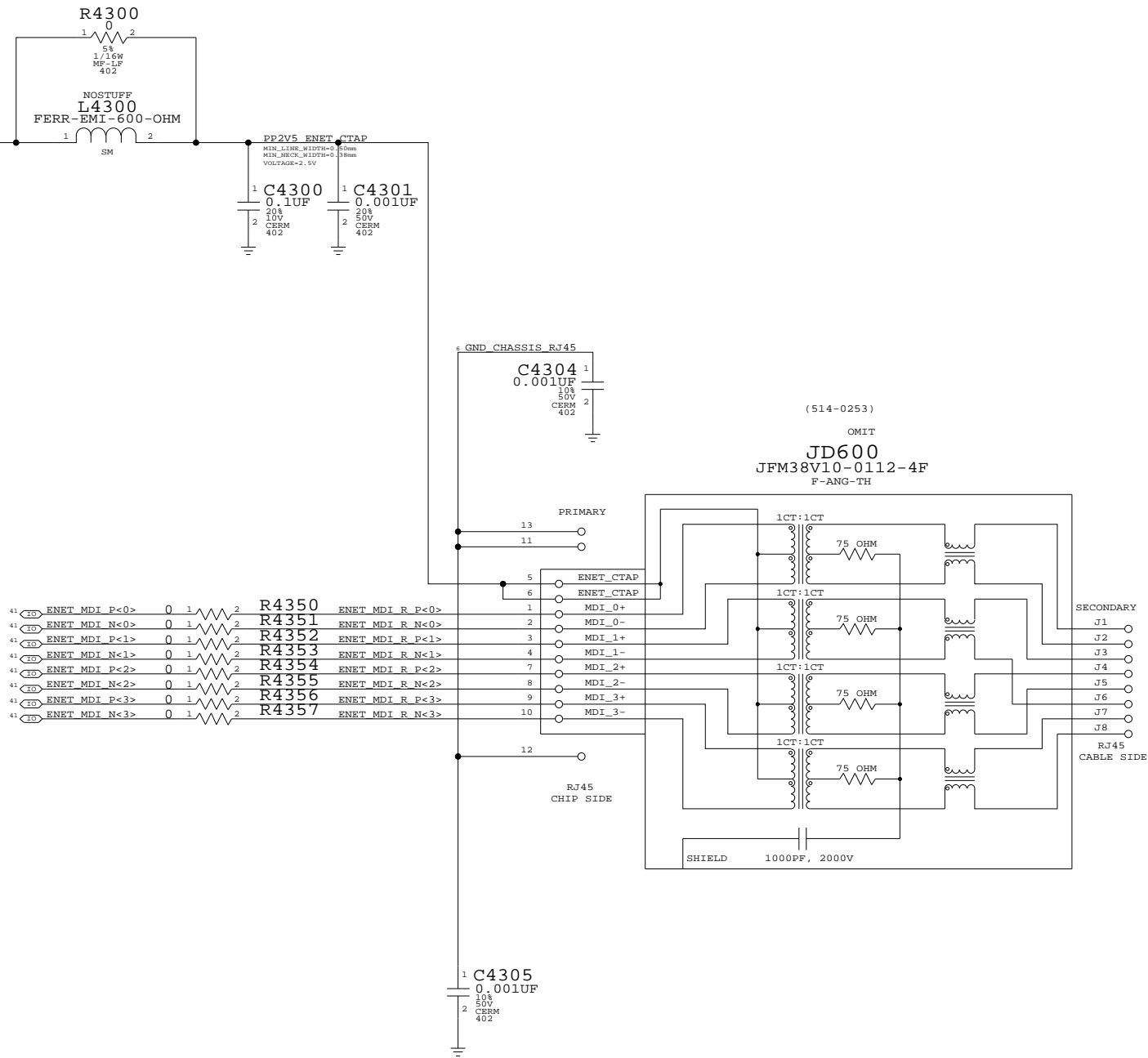


ETHERNET CONTROLLER
SYNC_MASTER=ENET SYNC_DATE=06/22/2005

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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0300	1	CON,RJ-45 7 DEGRESS	JD600	CRITICAL	17_INCH_LCD
514-0301	1	CON,RJ-45 7 DEGRESS	JD600	CRITICAL	20_INCH_LCD

ETHERNET CONNECTOR

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APPLE COMPUTER INC.

SIZE DRAWING NUMBER

D

051-6949

REV.

09

SCALE

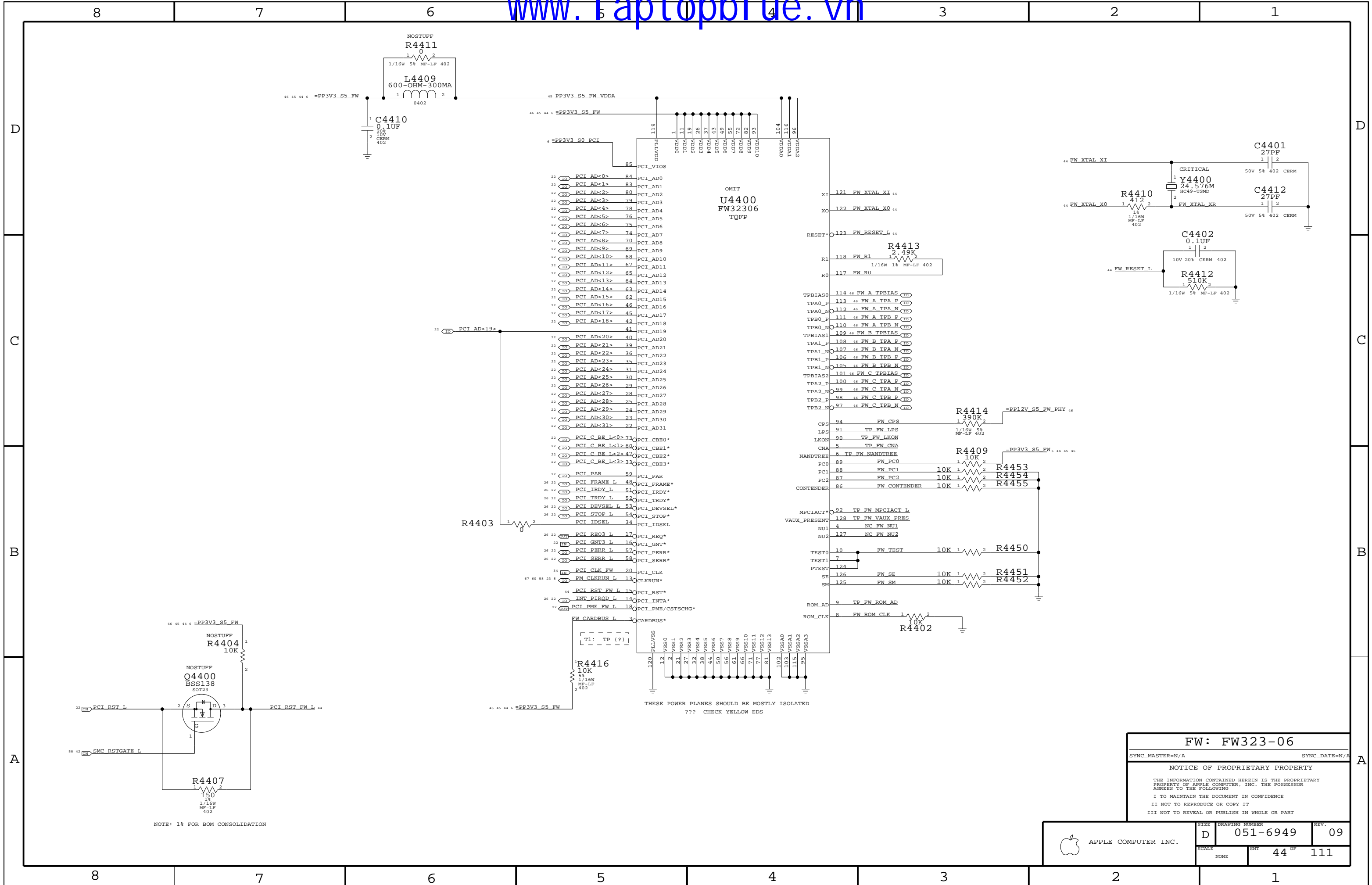
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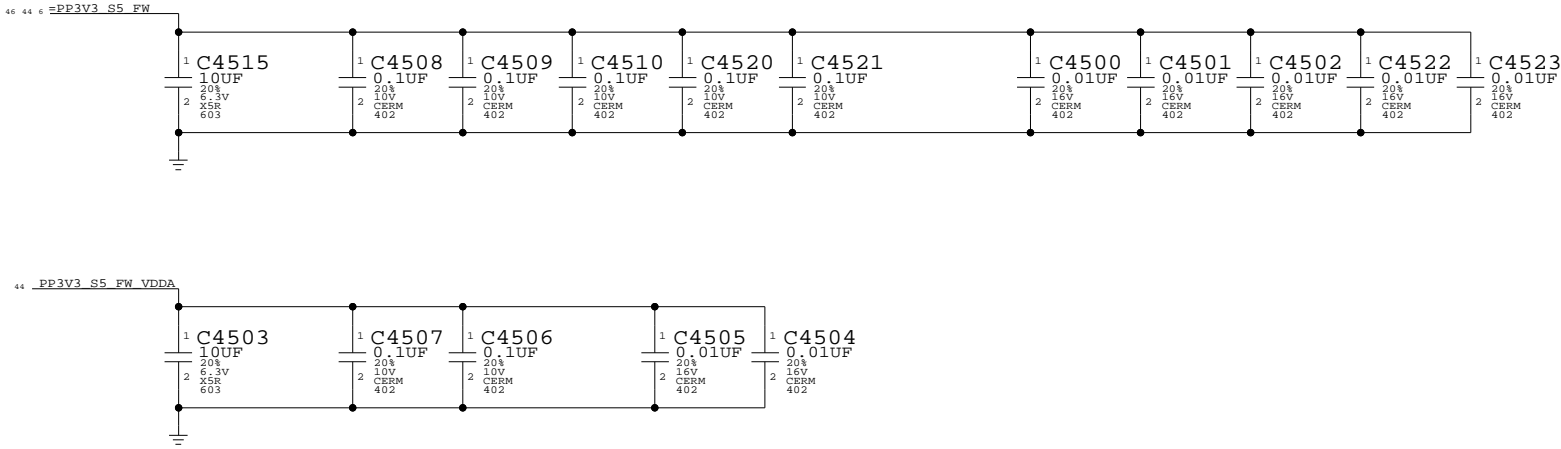
SHT

43

OF

111





FW: DECAPS

SYNC_MASTER=N/A SYNC_DATE=N/A

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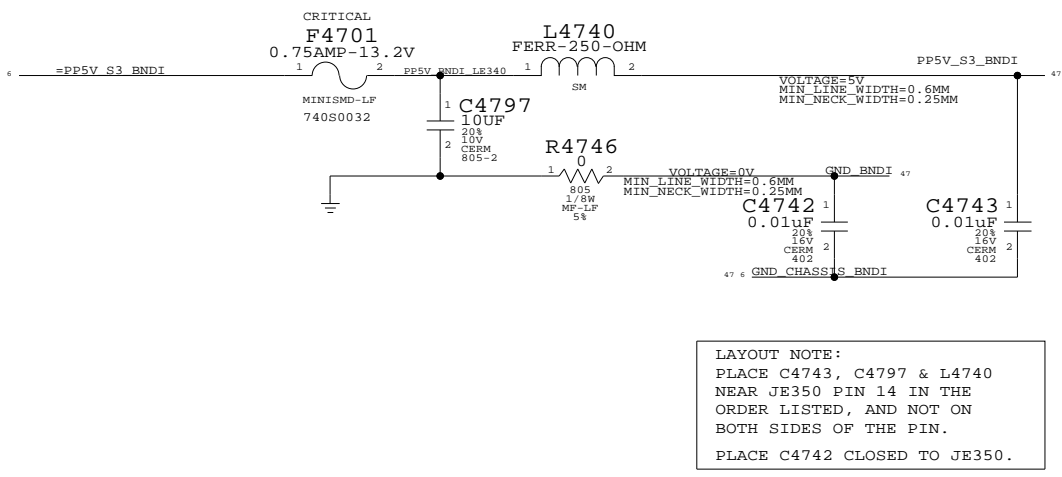
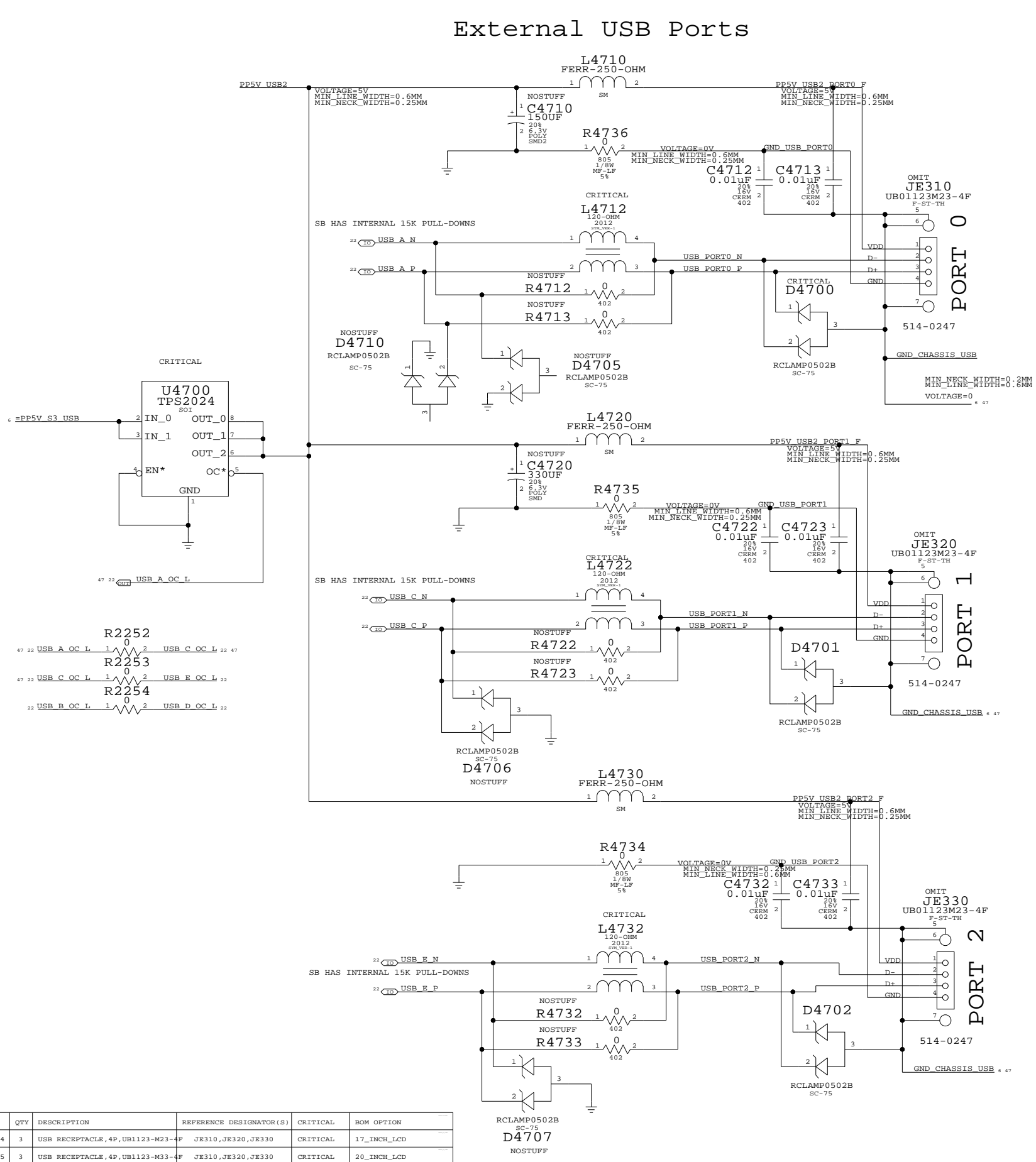
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	REV.
NONE	45 OF 111	

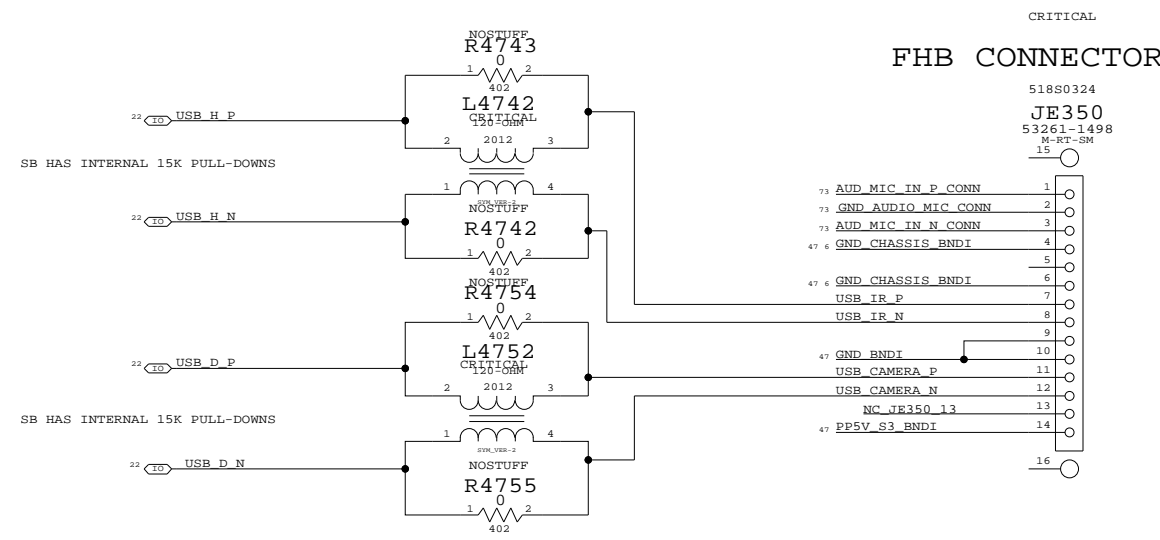


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0248	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	17_INCH_LCD
514-0248	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	17_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE000	CRITICAL	20_INCH_LCD
514-0251	1	CON, 1394A 7 DEGREES	JE001	CRITICAL	20_INCH_LCD

External USB Ports



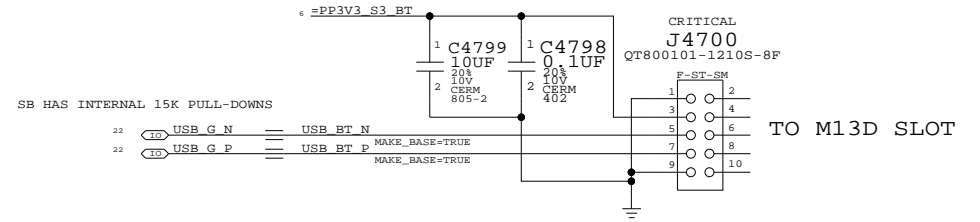
LAYOUT NOTE:
PLACE C4743, C4797 & L4740
NEAR JE350 PIN 14 IN THE
ORDER LISTED, AND NOT ON
BOTH SIDES OF THE PIN.
PLACE C4742 CLOSED TO JE350.



Connector Pinout Table:

1	AUD_MIC_IN_P_CONN
2	GND_AUDIO_MIC_CONN
3	AUD_MIC_IN_N_CONN
4	GND_CHASSIS_BNDI
5	GND_CHASSIS_BNDI
6	GND_CHASSIS_BNDI
7	USB_IR_P
8	USB_IR_N
9	GND_BNDI
10	USB_CAMERA_P
11	USB_CAMERA_N
12	NC_JE350_13
13	PP5V_S3_BNDI
14	
15	
16	

BLUETOOTH



NOTE: STANDOFFS FOR J4700
SDF4700
STDOFF-40D4.5H-1.35-TH
SDF4701
STDOFF-40D4.5H-1.35-TH

USB Device Interfaces

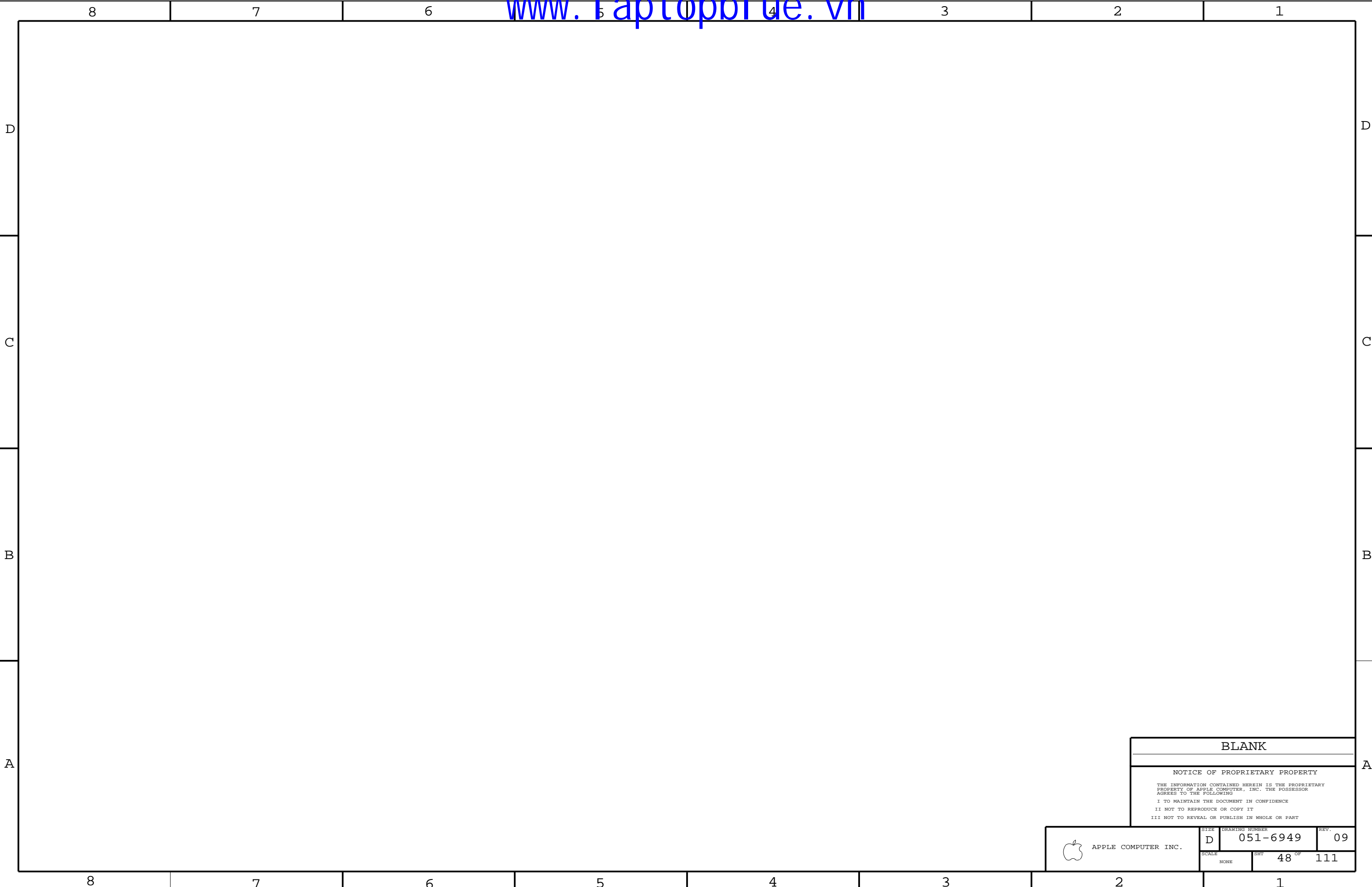
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PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0294	3	USB RECEPTACLE,4P,UB1123-M23-4F	JE310,JE320,JE330	CRITICAL	17_INCH_LCD
514-0295	3	USB RECEPTACLE,4P,UB1123-M33-4F	JE310,JE320,JE330	CRITICAL	20_INCH_LCD

APPLE COMPUTER INC.

SCALE	D	SHT	OF
NONE	47		111

SIZE	DRAWING NUMBER	REV.
	051-6950	06



BLANK


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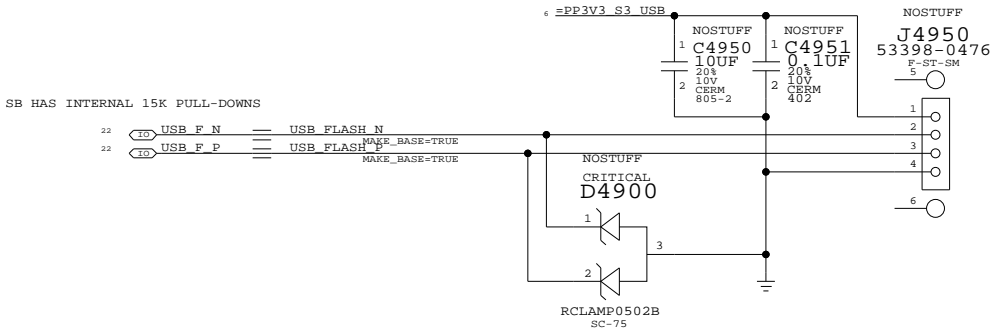
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 APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6949	REV. 09
	SCALE NONE	SHT 48 OF	111

CARD-READER USB CONN



Flash Connector

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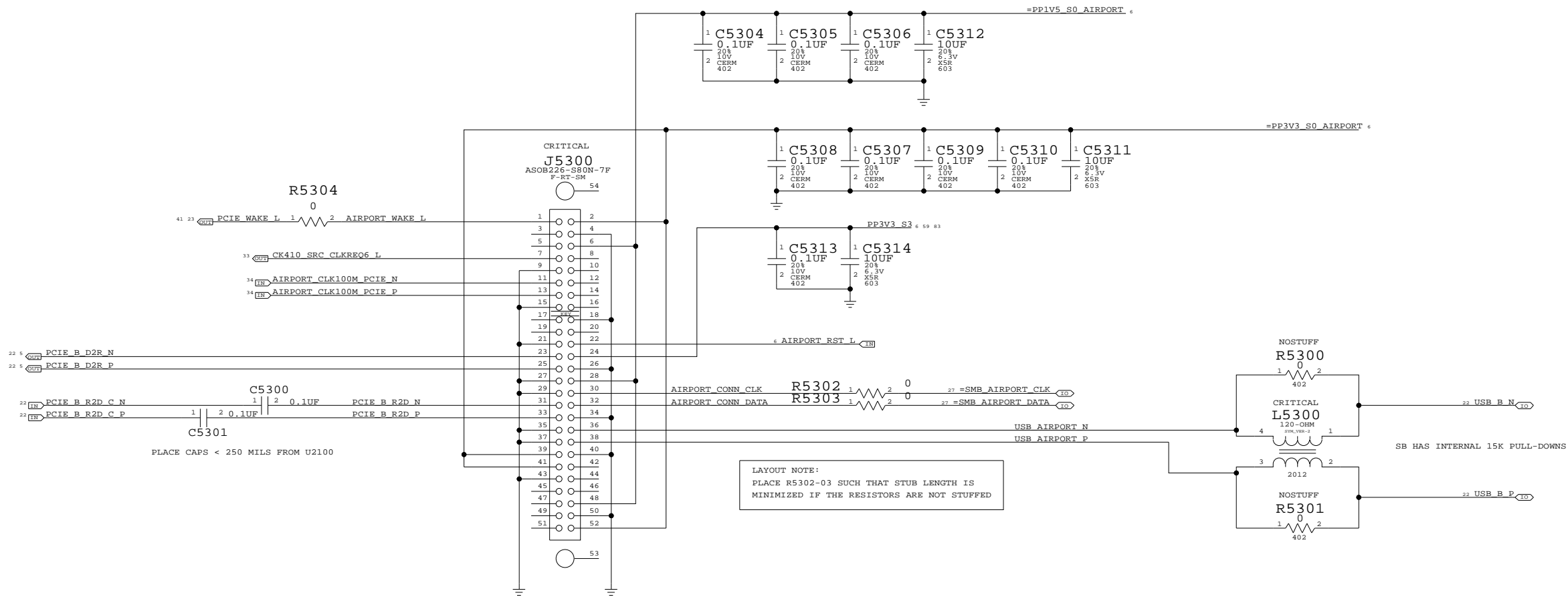
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	49 OF 111
NONE		



NOTE: STANDOFFS FOR J5300

SDF5300
STDOFF-40D5.6H-1.35-TH

SDF5301
STDOFF-40D5.6H-1.35-TH

AIRPORT CONN

SYNC_MASTER=N/ASYNC_DATE=N/A

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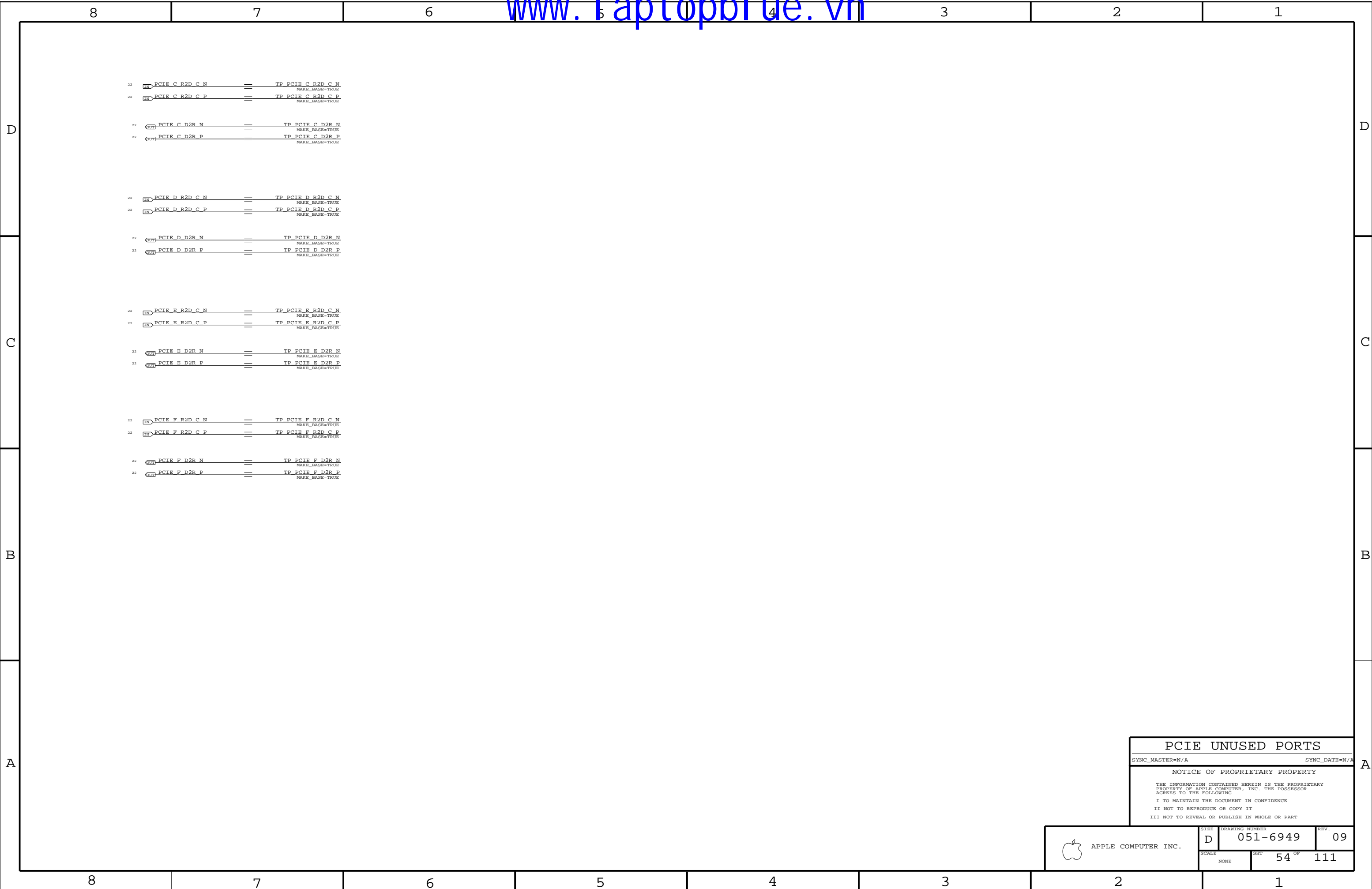
SIZE: D

DRAWING NUMBER: 051-6949

REV.: 09

SCALE: NONE

SHT: 53 OF 111



PCIE UNUSED PORTS

SYNC_MASTER=N/A

SYNC_DATE=N/A

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APPLE COMPUTER INC.

SIZE

D

DRAWING NUMBER

051-6949

REV.

09

SCALE

NONE

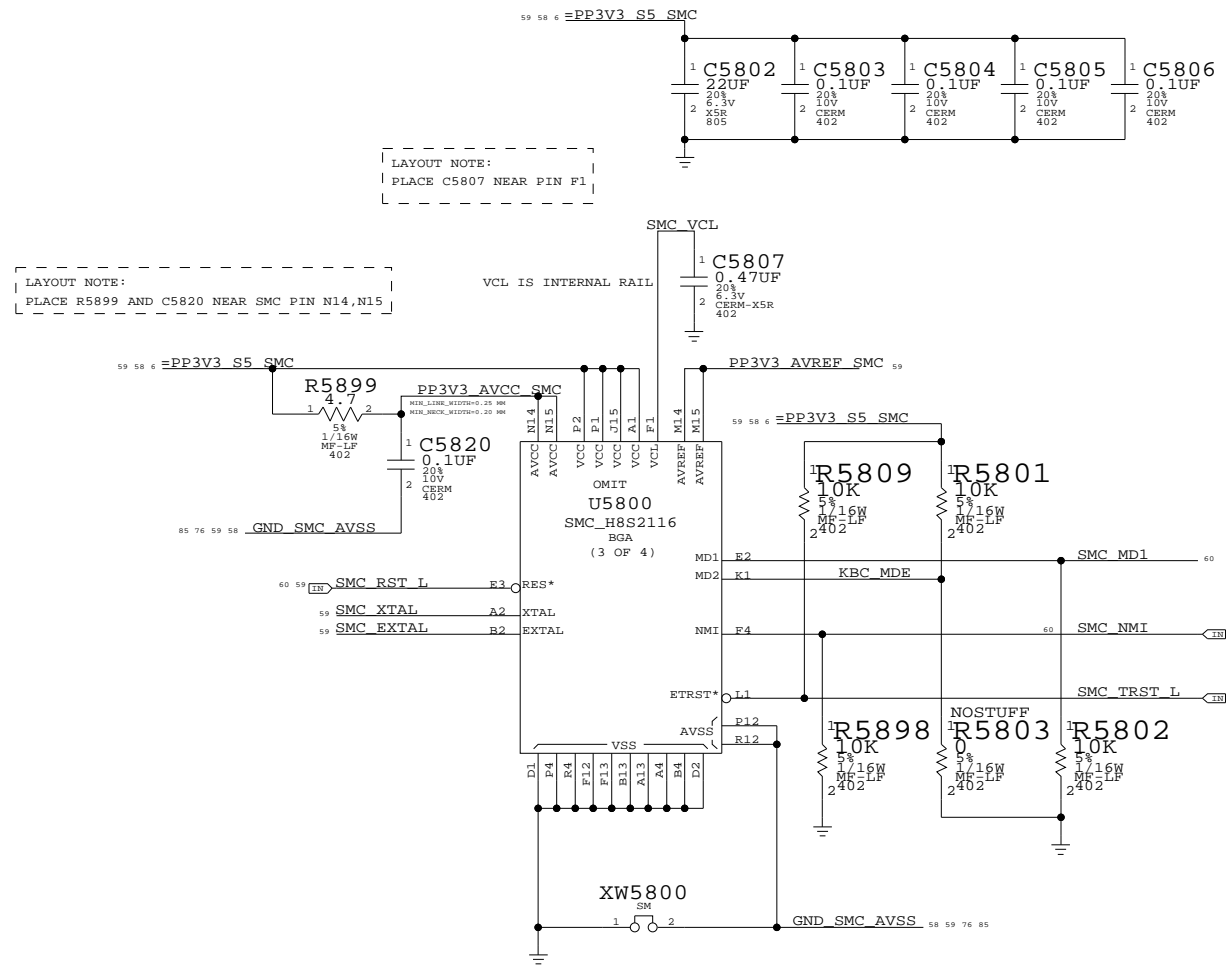
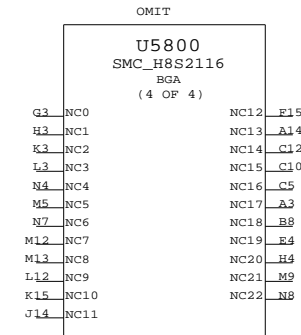
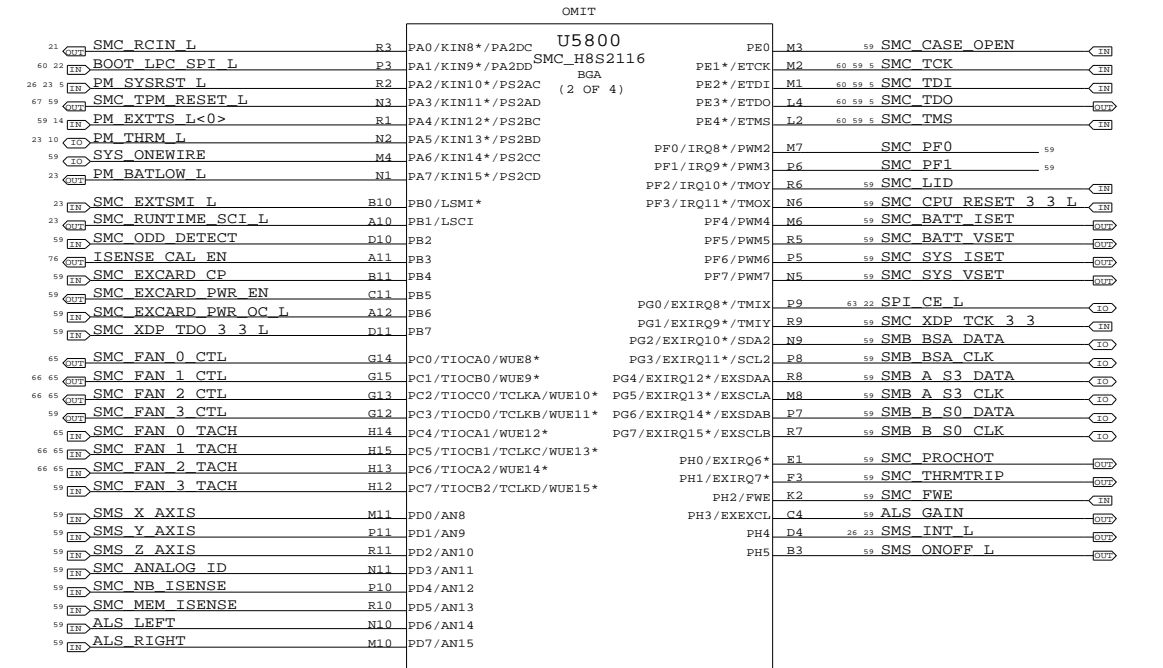
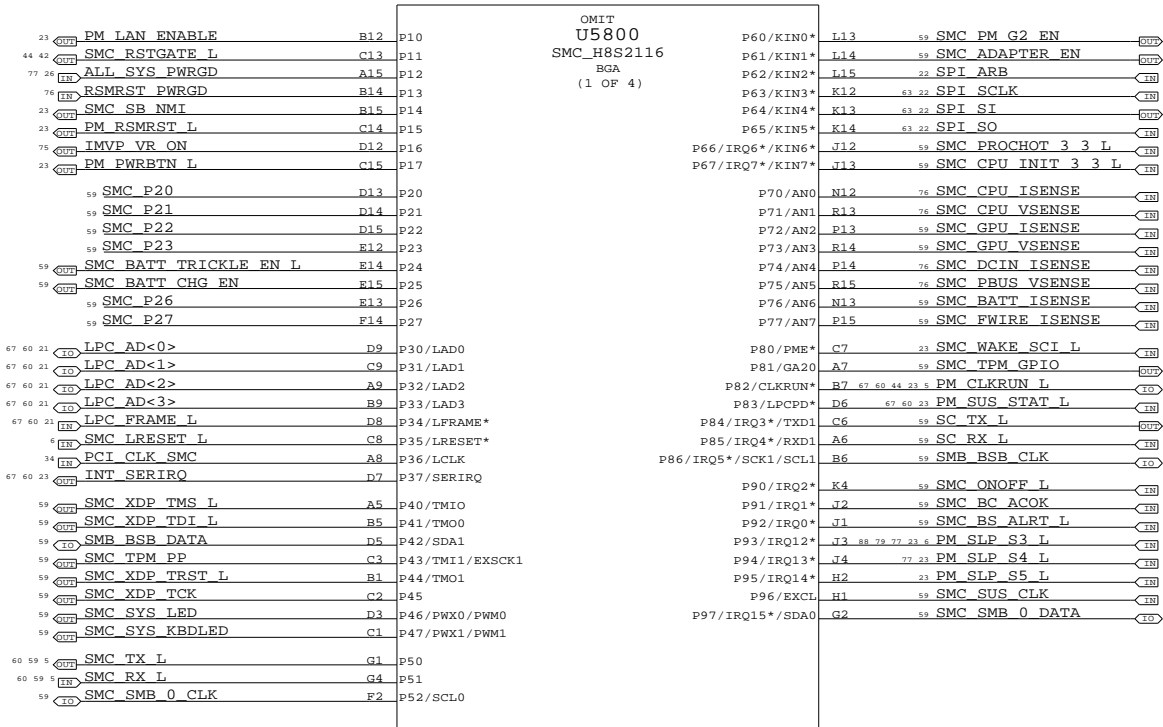
SHT

54

OF

111

UNUSED PINS HAVE THE FORMAT
SMC_XXX WHERE XXX IS THE PORT NUMBER.
THEY ARE SET BY SOFTWARE TO BE
DRIVEN OUTPUTS ALWAYS SO THEY
CAN BE LEFT NO-CONNECTED.



SMC

SYNC_MASTER=N/A SYNC_DATE=N/A

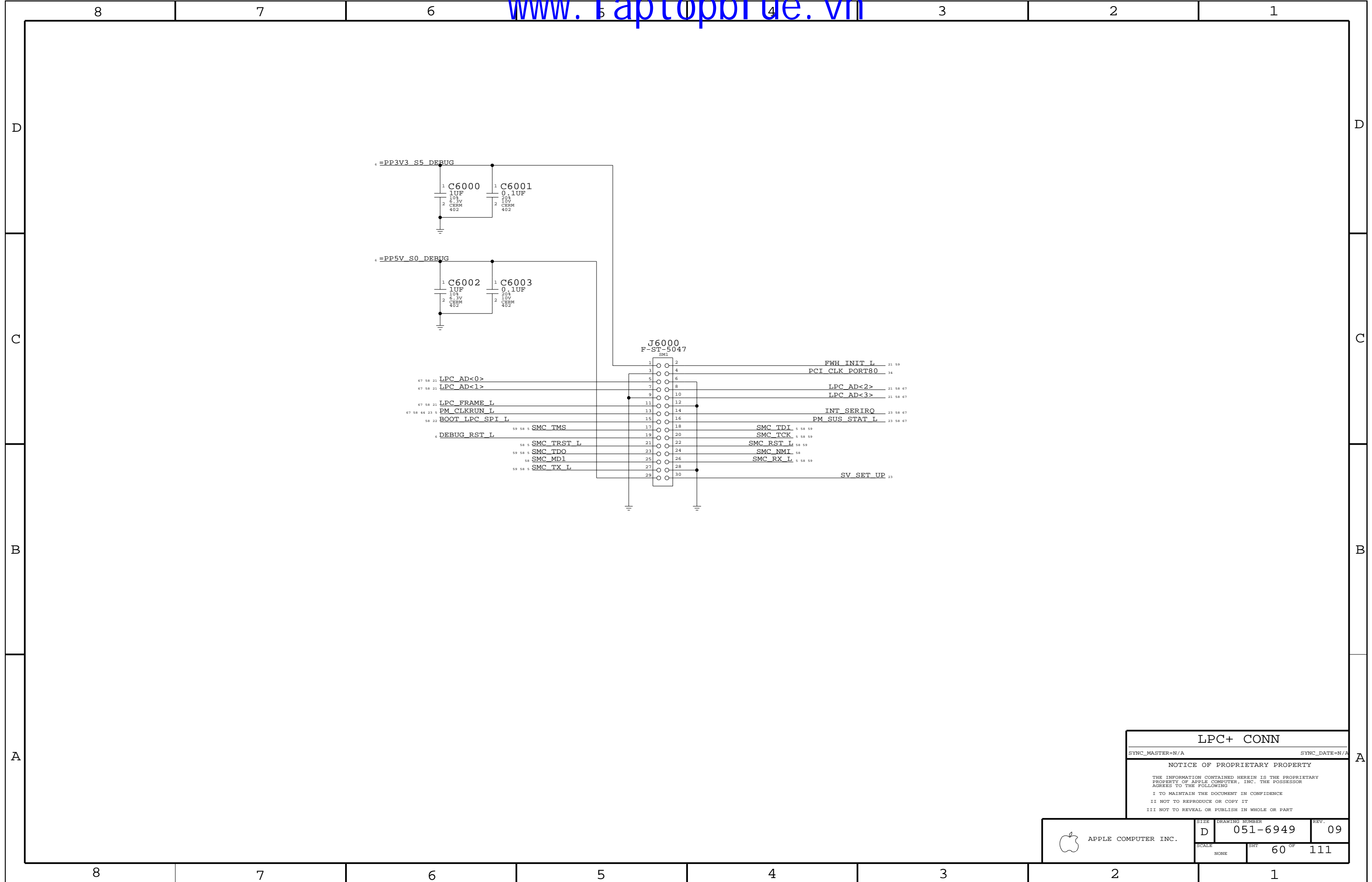
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LPC+ CONN

SYNC_MASTER=N/A SYNC_DATE=N/A

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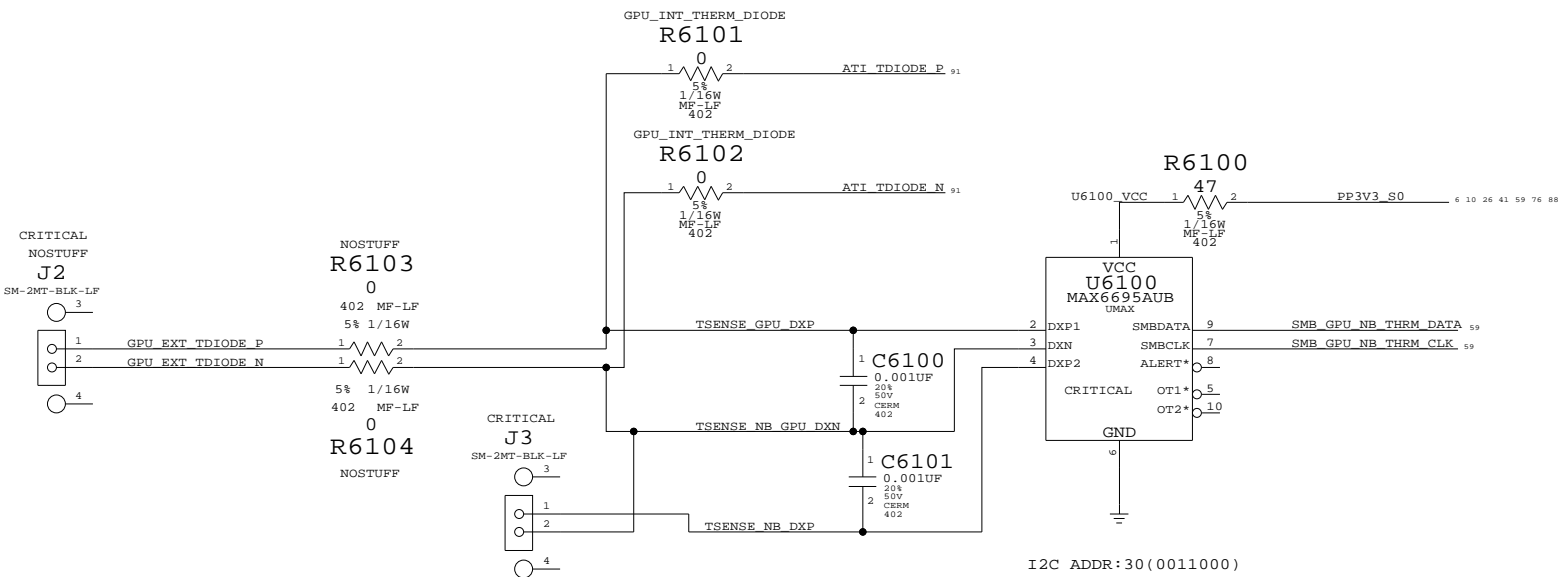
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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	60	111



GPU+NB THERMAL

SYNC_MASTER=N/A

SYNC_DATE=N/A

NOTICE OF PROPRIETARY PROPERTY

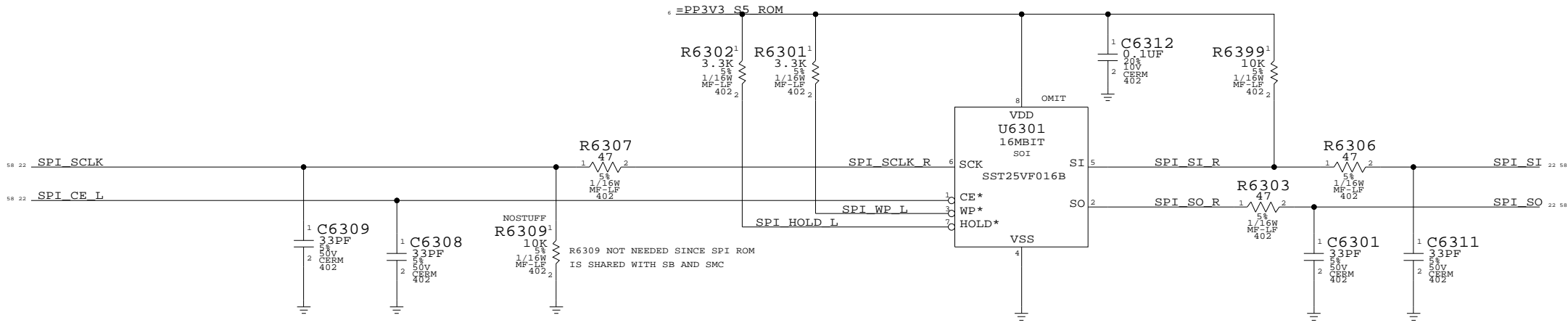
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE		SHT	OF
NONE		61	111



R6306-07 SHOULD BE PLACED LESS THAN 2.54MM FROM U2100
R6303 SHOULD BE PLACED LESS THAN 2.54MM FROM U6301

SPI BOOTROM

SYNC_MASTER=MASTER

SYNC_DATE=5/23/05

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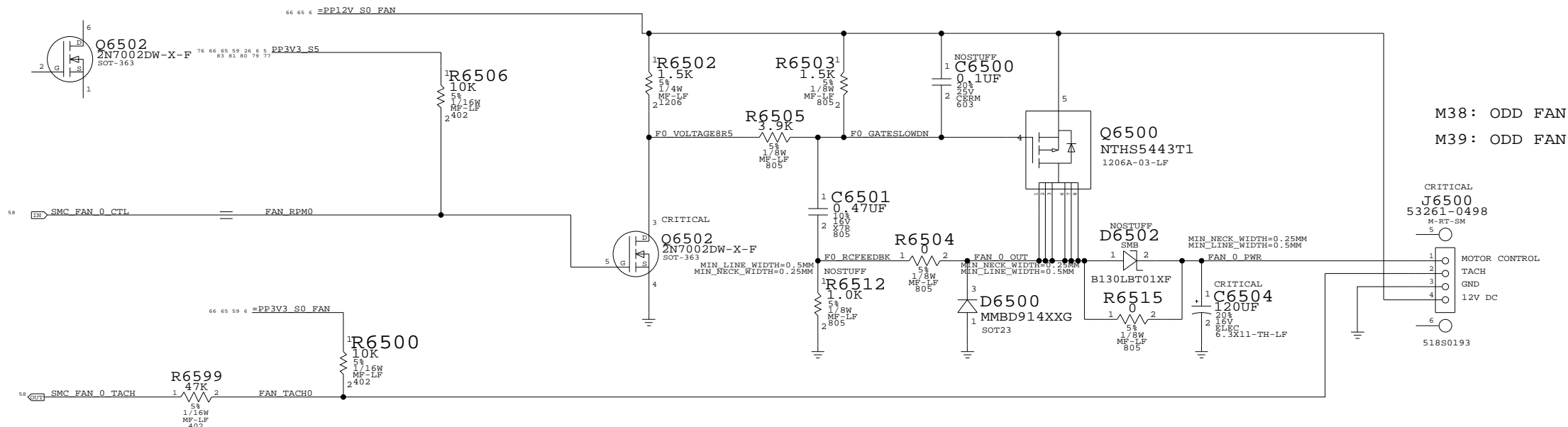
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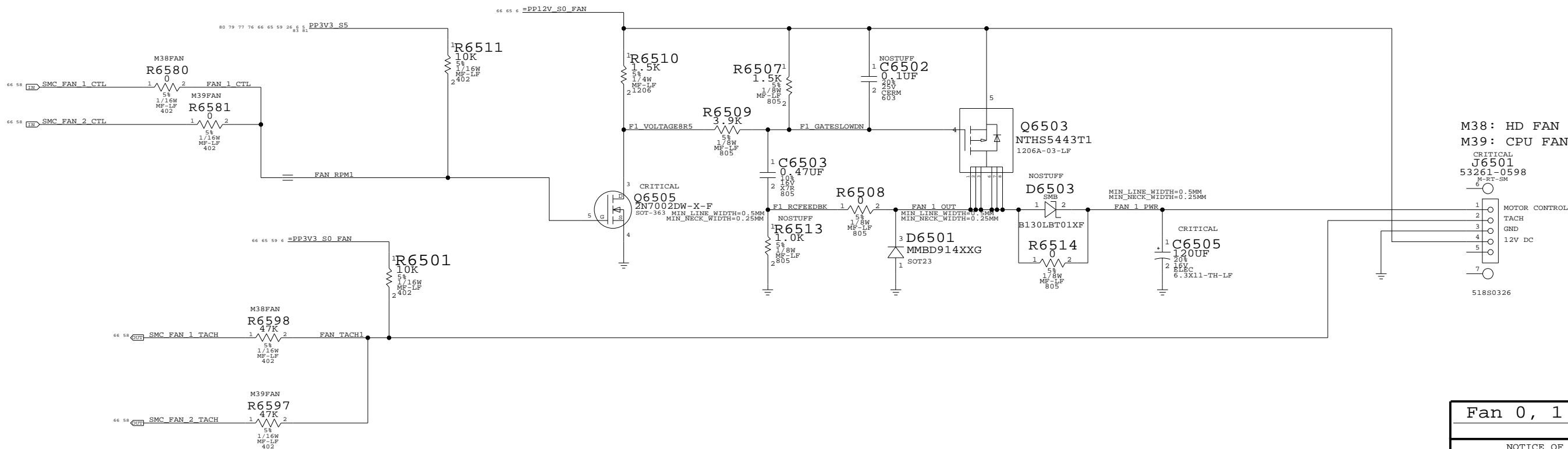
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-6949		09
SCALE		SHT	OF	
NONE		63	111	

FAN 0



NOTE: ADDED TO PROTECT SMC

FAN 1



Fan 0, 1 & System Temp

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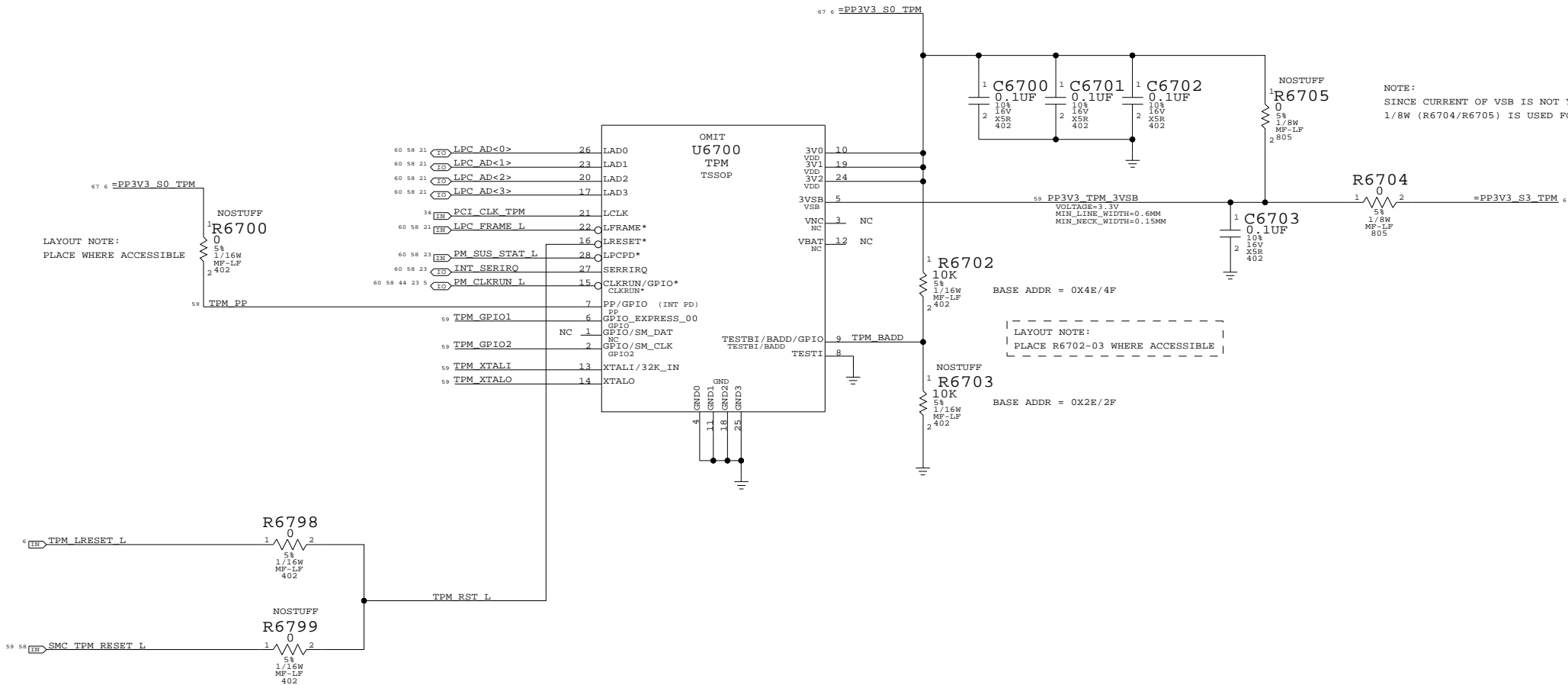


APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	65	111

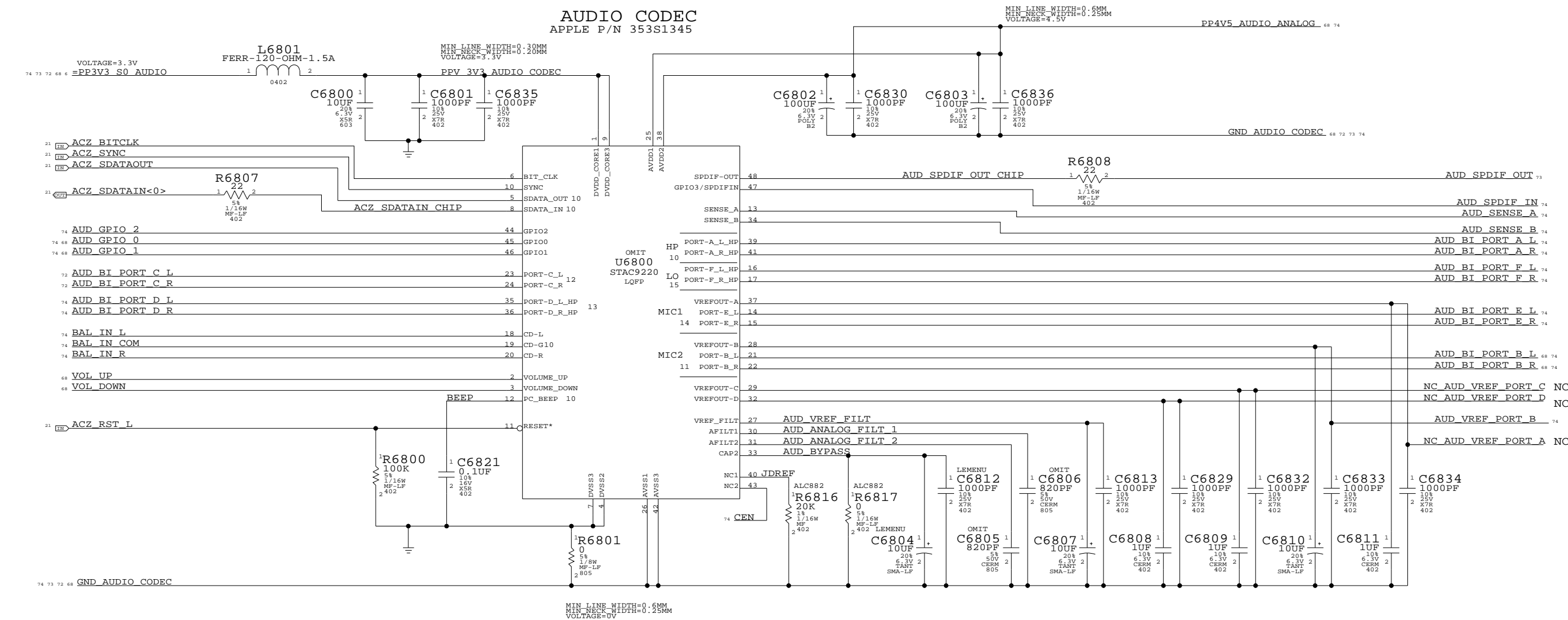


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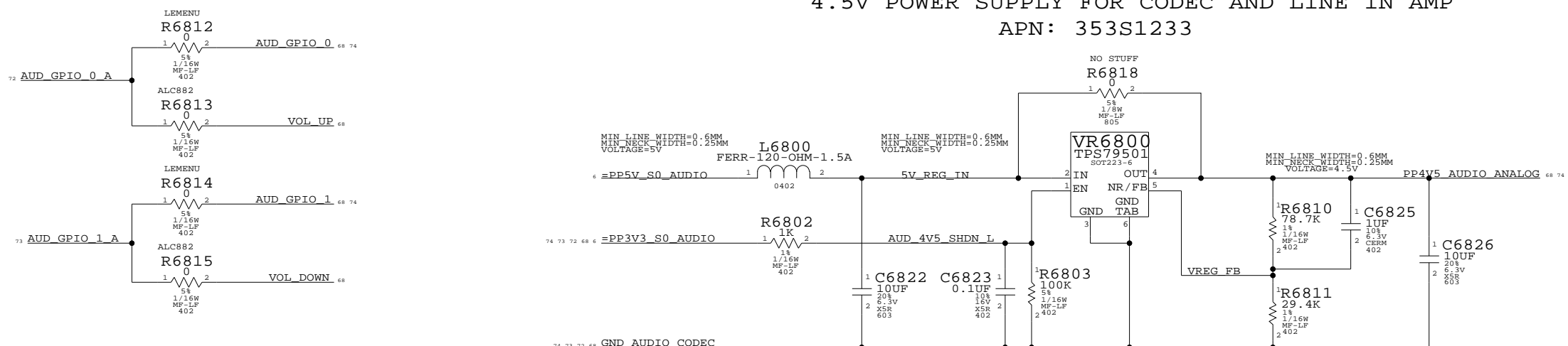


TPM		
SYNC_MASTER=N/A		SYNC_DATE=N/A
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT		67 OF 111
NONE			



4.5V POWER SUPPLY FOR CODEC AND LINE IN AMP
APN: 353S1233



AUDIO: CODEC

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

NOTICE OF PROPRIETARY PROPERTY

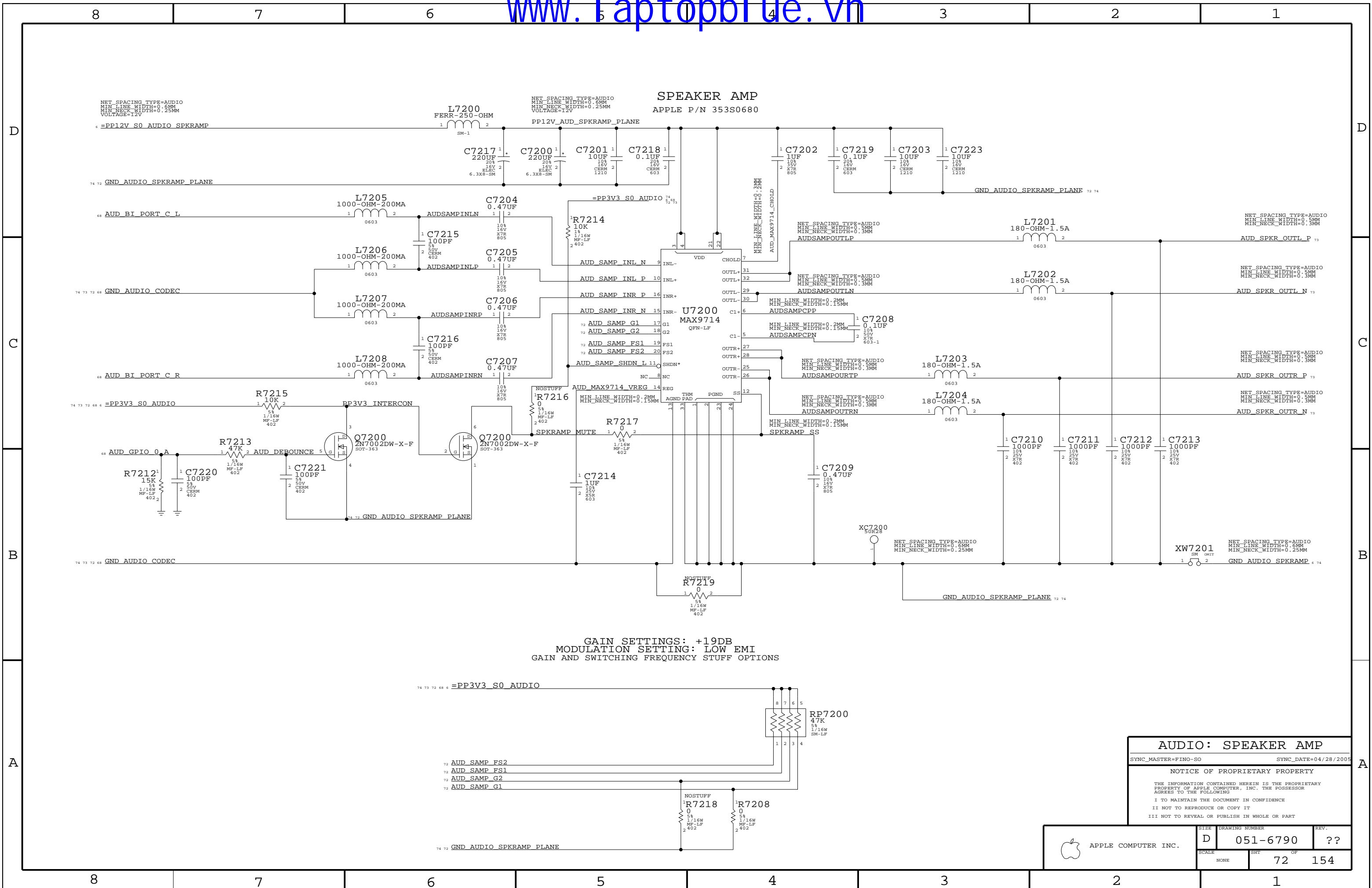
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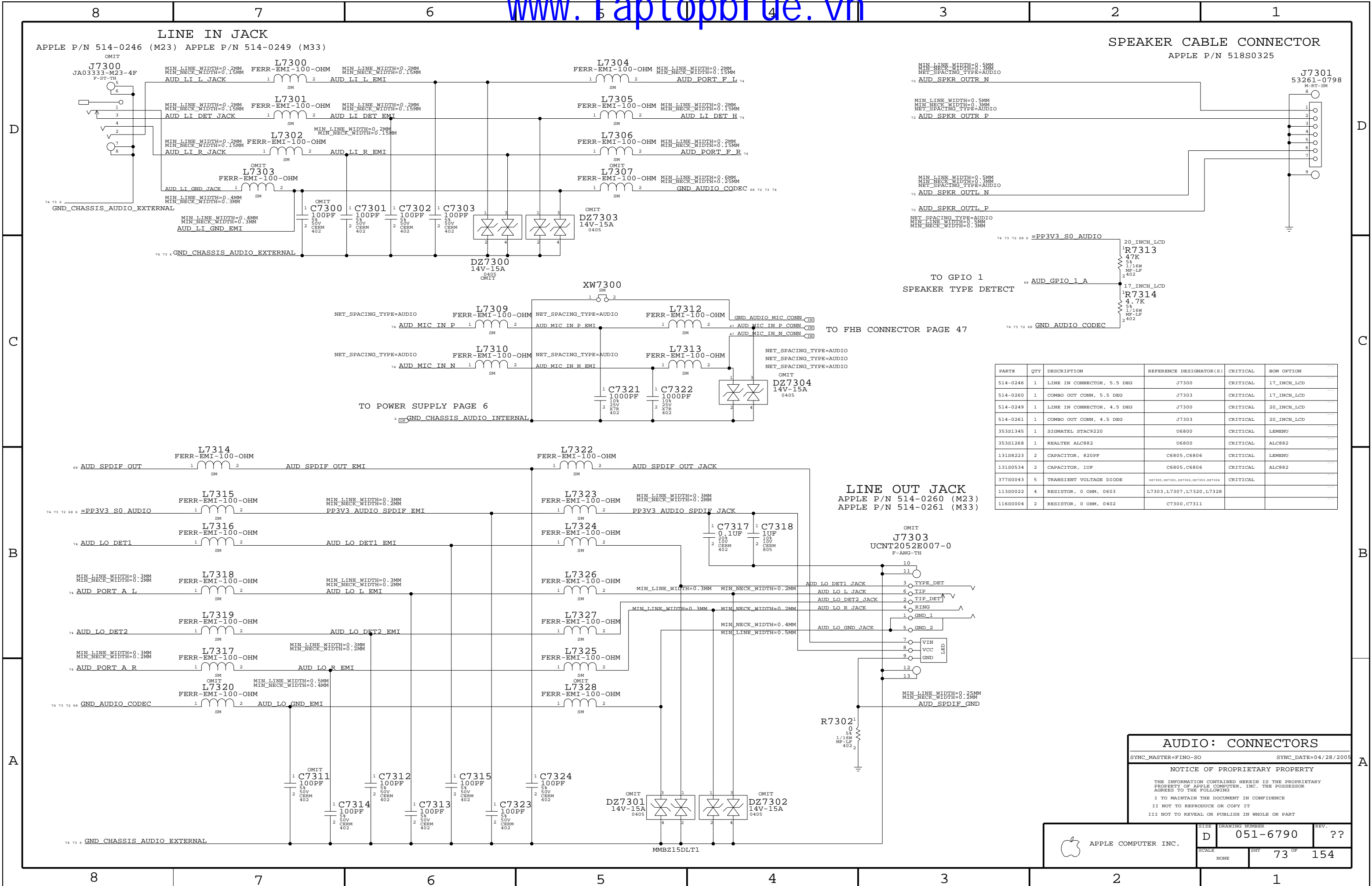
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-6790	??	
SCALE	NONE	SHT	68	OF 154



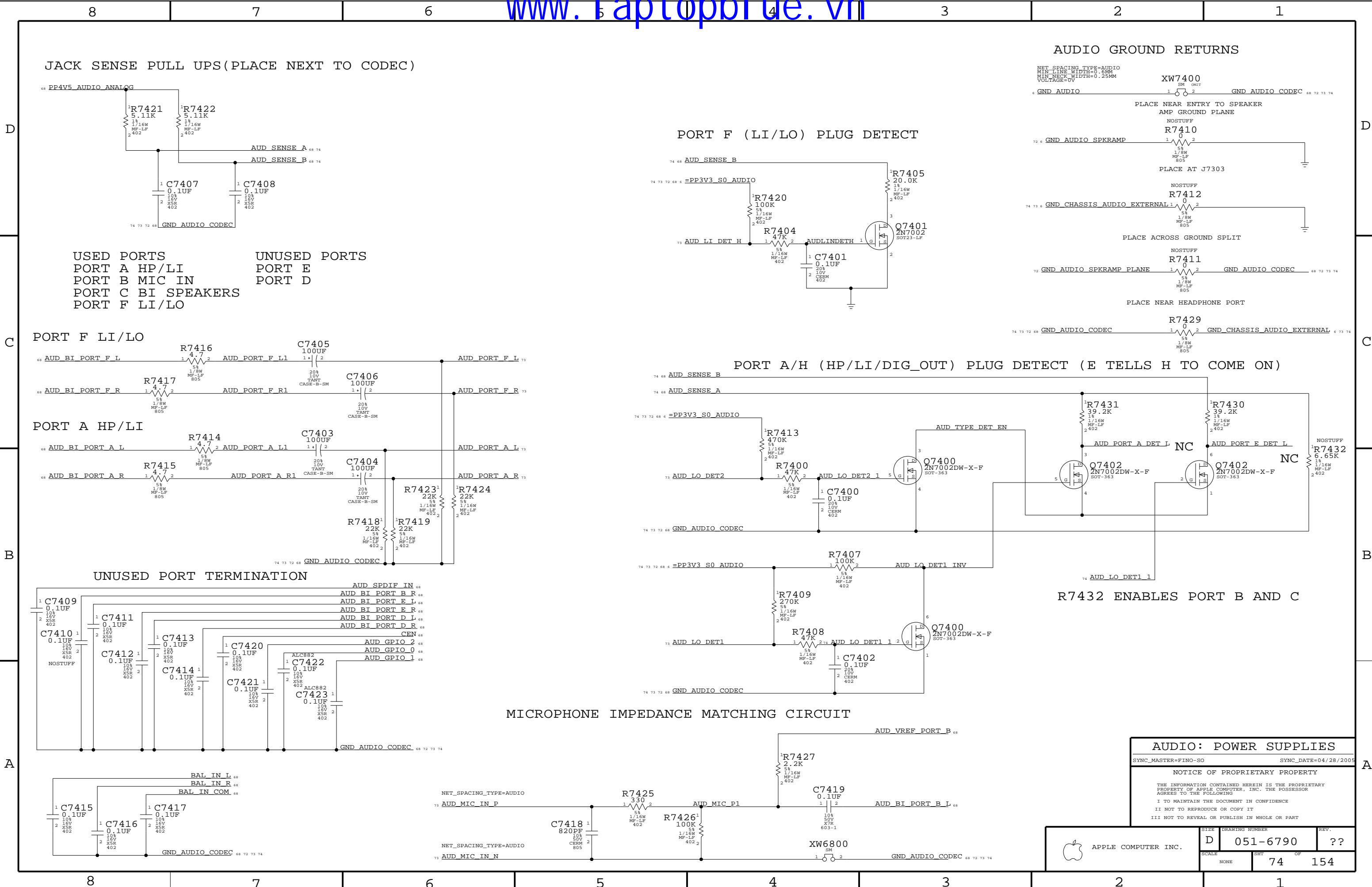
GAIN SETTINGS: +19DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

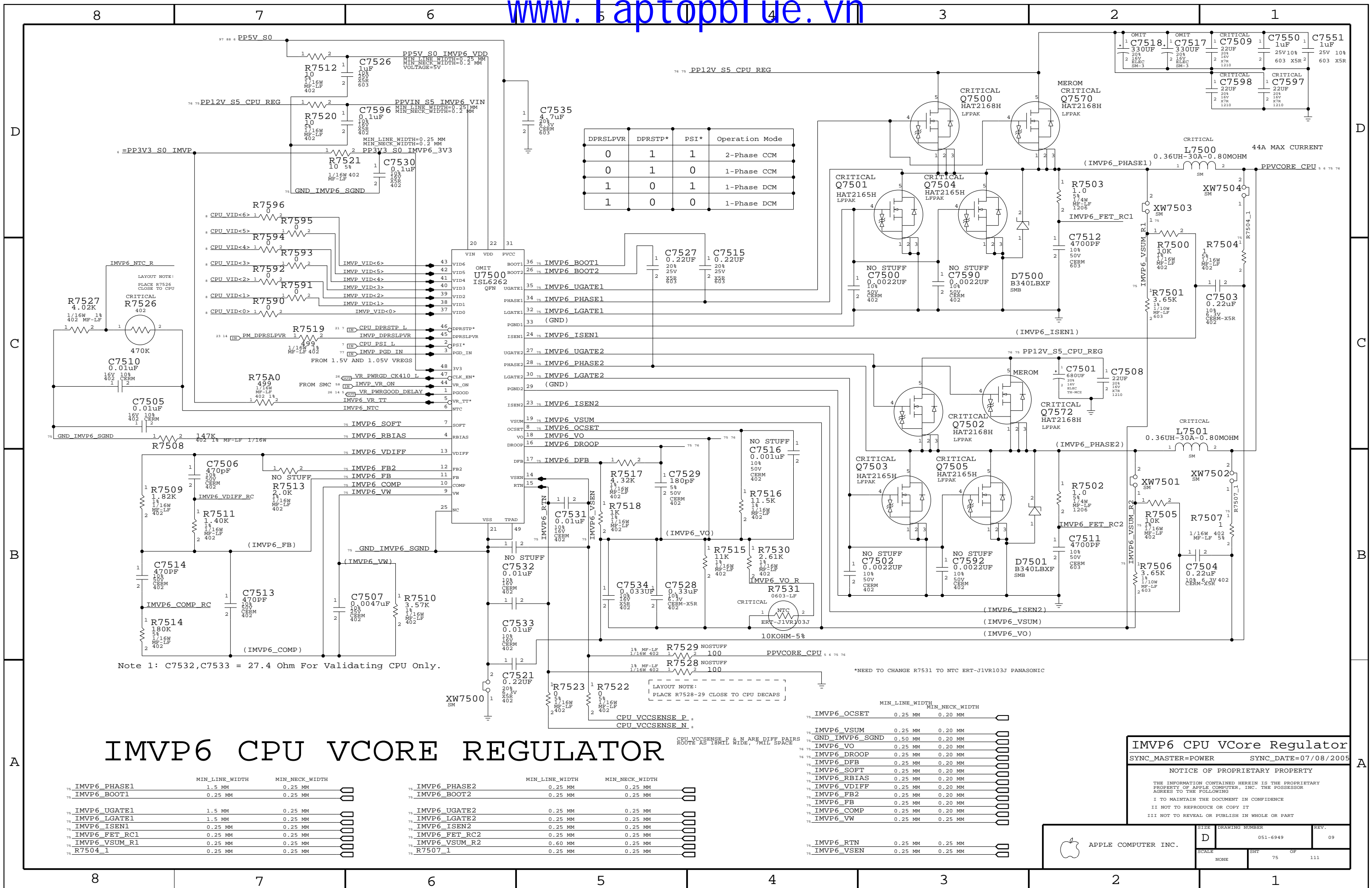
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SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-6790	??	
SCALE		SHT	OF	
NONE		72	154	



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0246	1	LINE IN CONNECTOR, 5.5 DEG	J7300	CRITICAL	17_INCH_LCD
514-0260	1	COMBO OUT CONN, 5.5 DEG	J7303	CRITICAL	17_INCH_LCD
514-0249	1	LINE IN CONNECTOR, 4.5 DEG	J7300	CRITICAL	20_INCH_LCD
514-0261	1	COMBO OUT CONN, 4.5 DEG	J7303	CRITICAL	20_INCH_LCD
353S1345	1	SIGMATEL STAC9220	U6800	CRITICAL	LEMENU
353S1268	1	REALTEK ALC882	U6800	CRITICAL	ALC882
131S8223	2	CAPACITOR, 820PF	C6805,C6806	CRITICAL	LEMENU
131S0534	2	CAPACITOR, 1UF	C6805,C6806	CRITICAL	ALC882
377S0043	5	TRANSIENT VOLTAGE DIODE	DZ7300,DZ7301,DZ7302,DZ7303,DZ7304	CRITICAL	
113S0022	4	RESISTOR, 0 OHM, 0603	L7303,L7307,L7320,L7328		
116S0004	2	RESISTOR, 0 OHM, 0402	C7300,C7311		

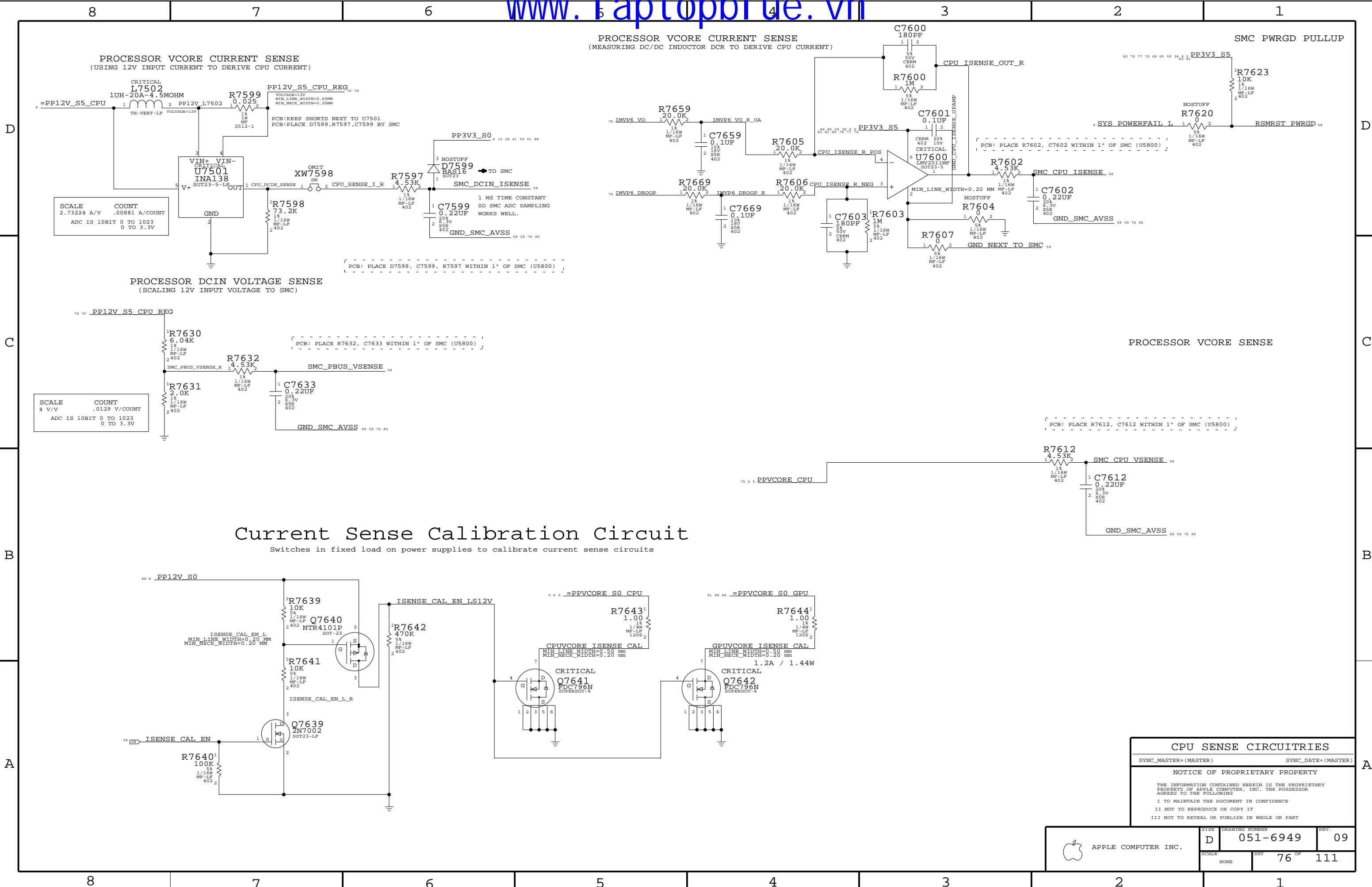




IMVP6 CPU VCore Regulator
SYNC_MASTER=POWER SYNC_DATE=07/08/2005

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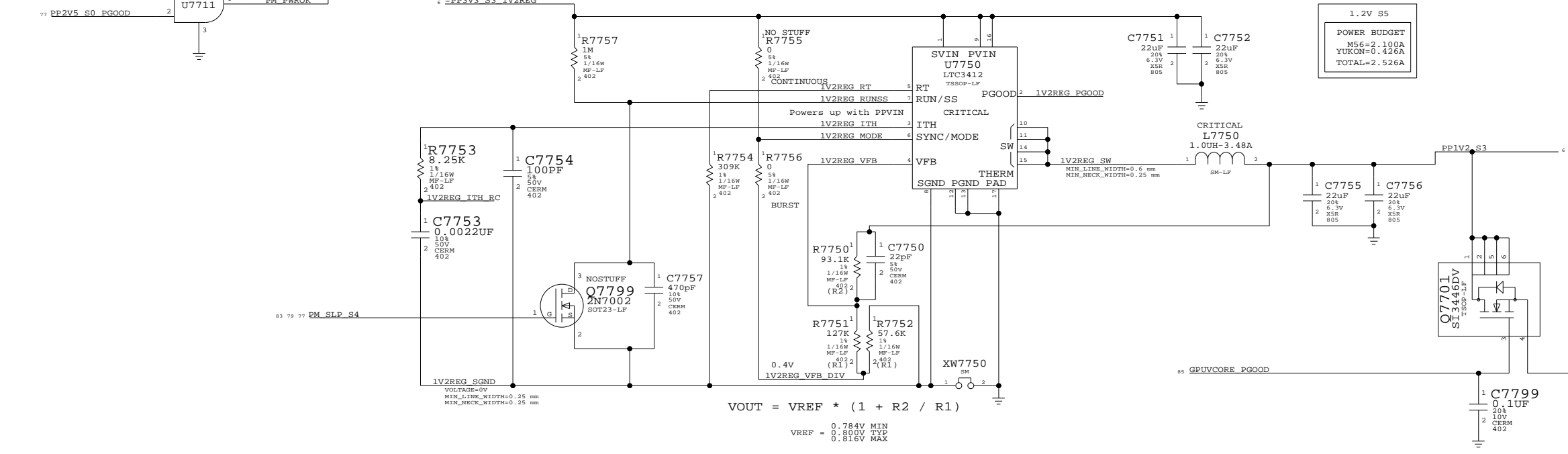
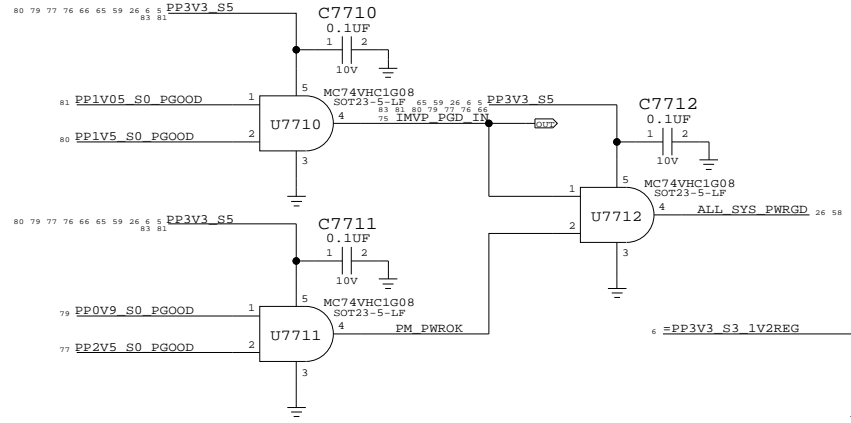
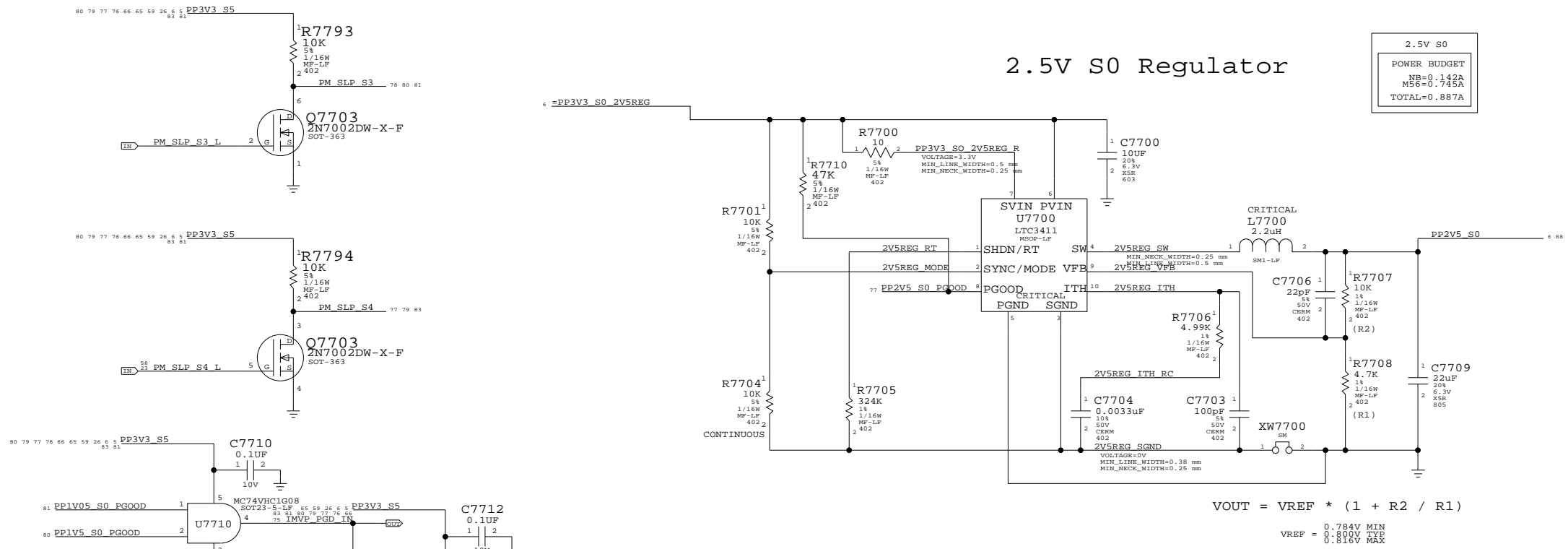
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2.5V & 1.2V GRAPHICS REGULATORS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

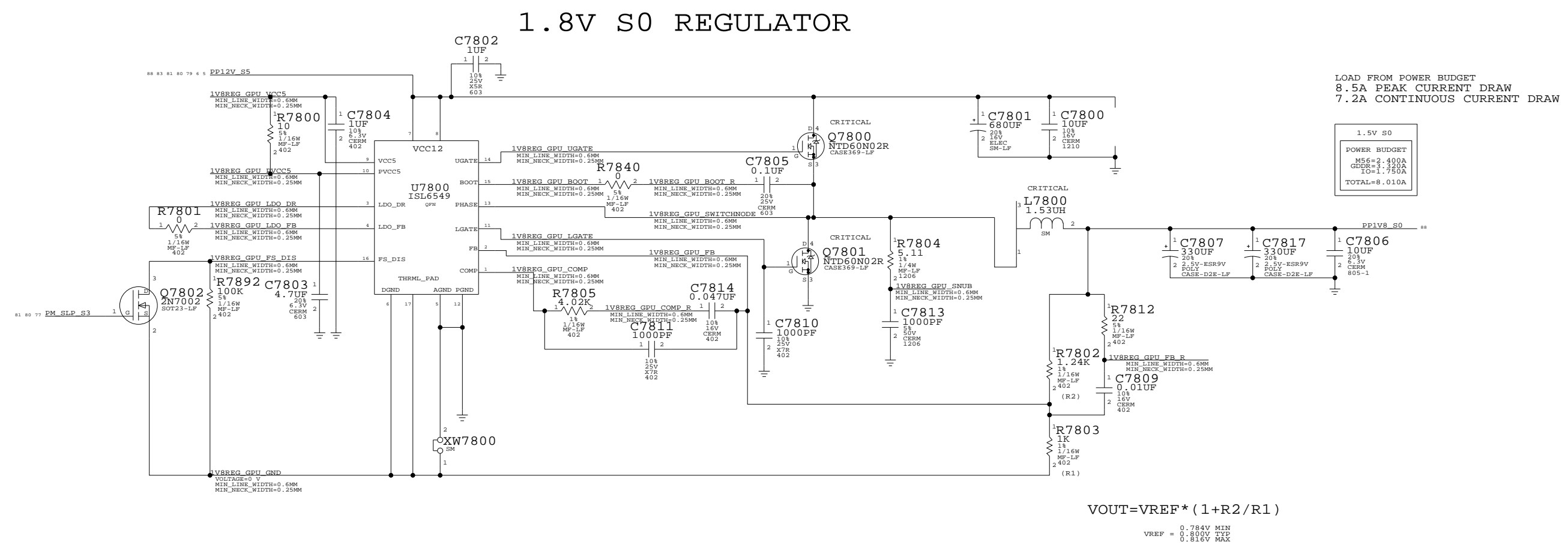
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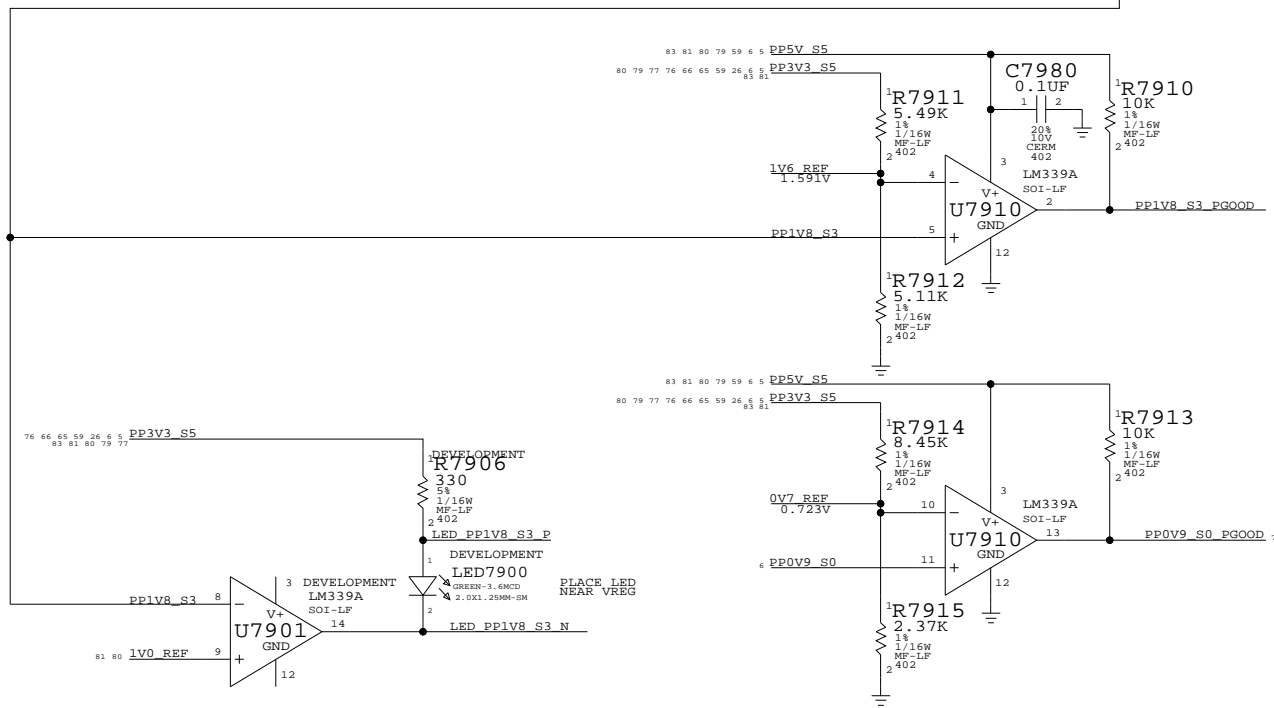
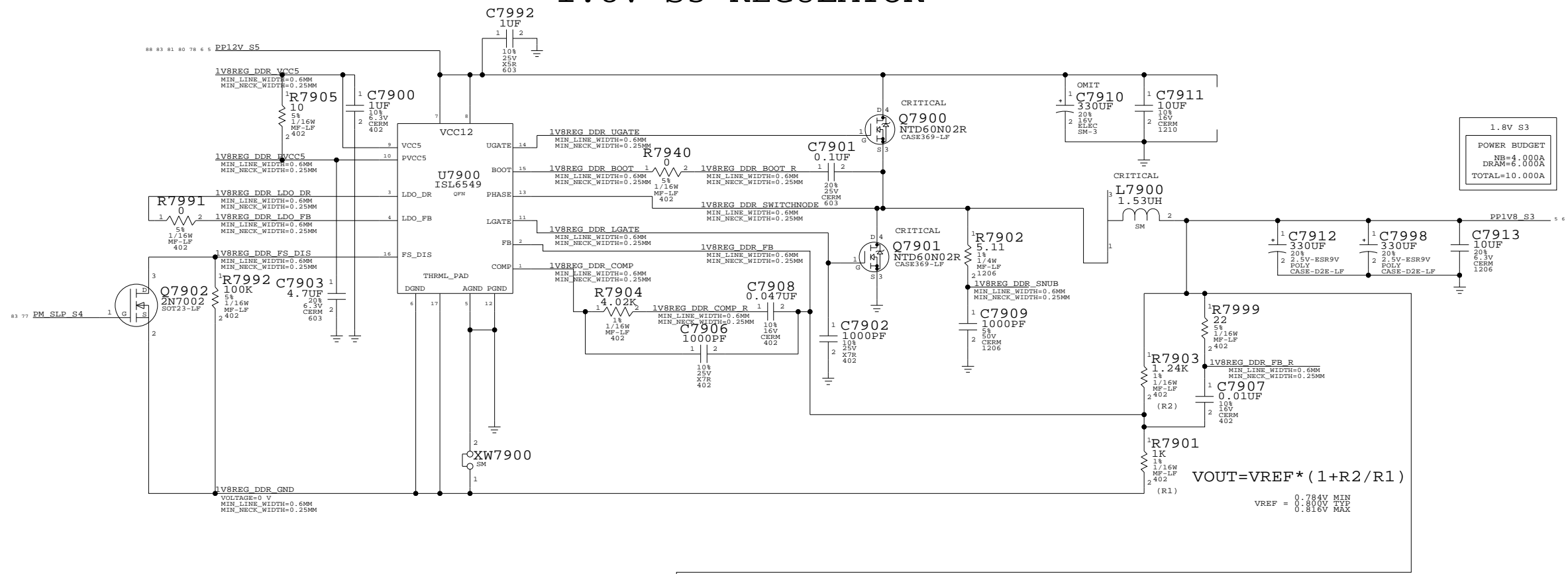
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1.8V GDDR REGULATOR		
SYNC_MASTER=(MASTER)		SYNC_DATE=(MASTER)
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE	SHT		
NONE	78 OF 111		

1.8V S3 REGULATOR



1.8V Vreg

SYNC_MASTER=M23-PC SYNC_DATE=04/12/2005

NOTICE OF PROPRIETARY PROPERTY

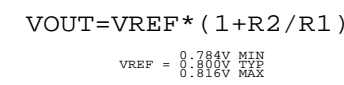
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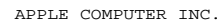
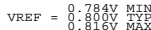
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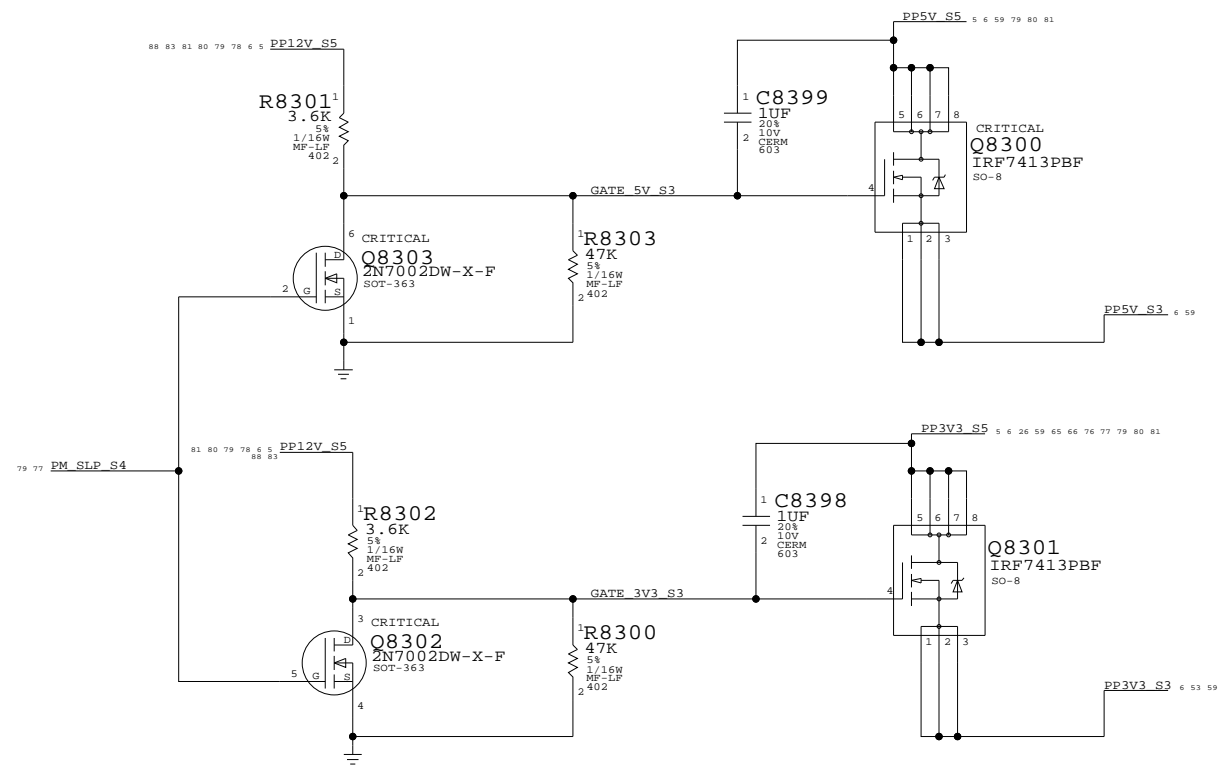
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SYNC_MASTER=FINO-PC SYNC_DATE=04/12/2005

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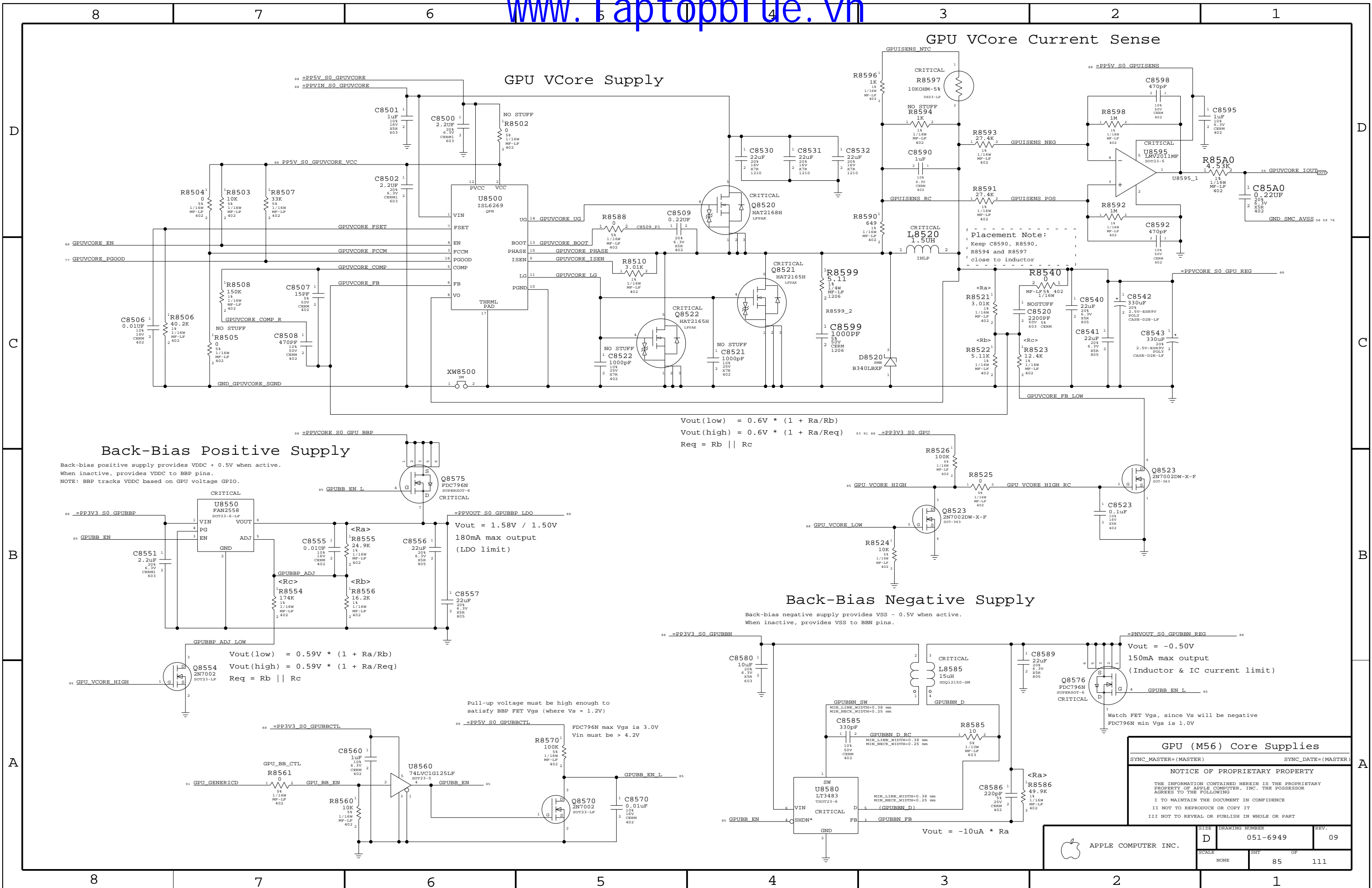


APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
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D	051-6949
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SCALE	SHT	83	OF	111
NONE				



Power aliases required by this page:

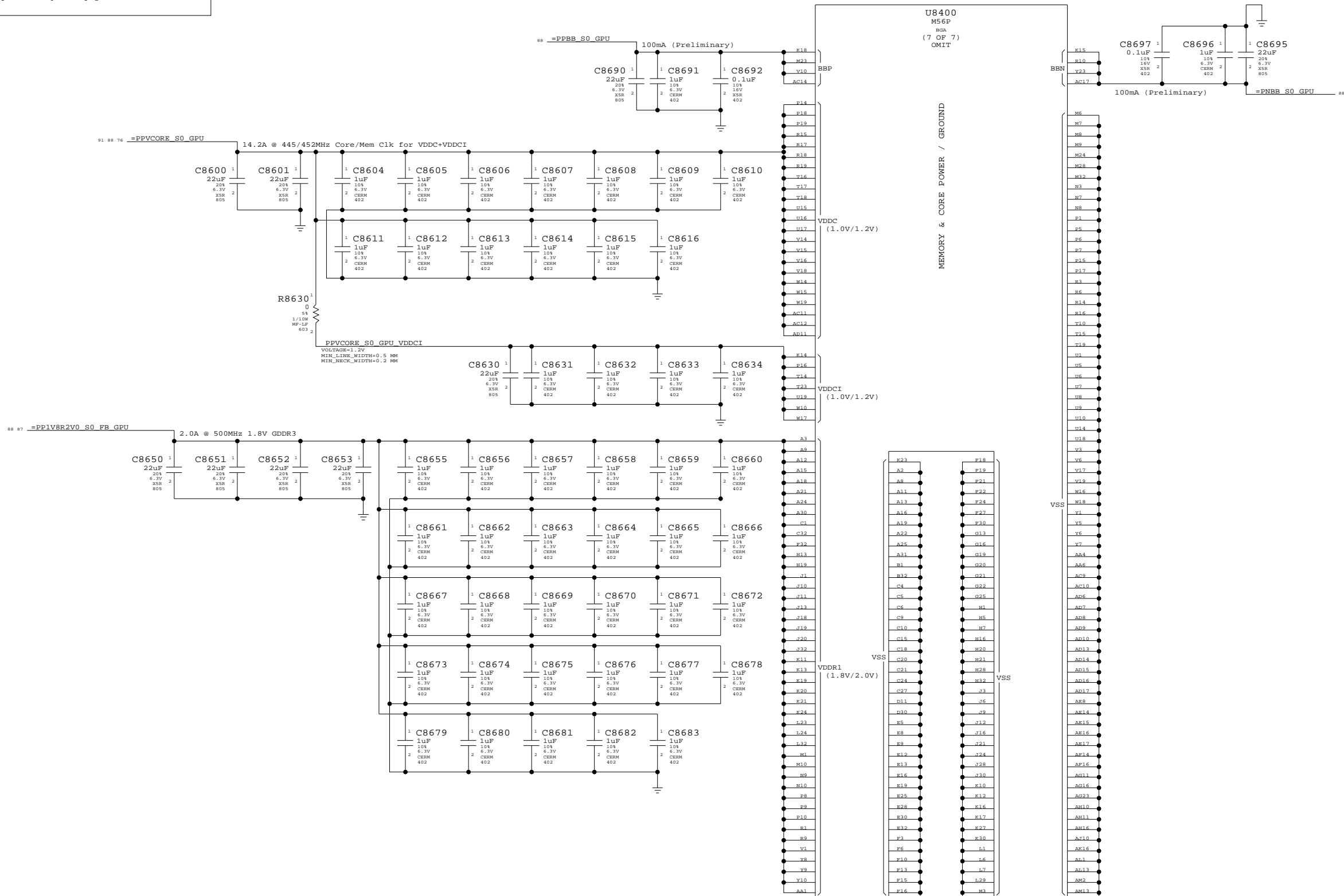
- PP1V5_GPU_VDD15
- PP1VR1V3_GPU_VCORE

Signal aliases required by this page:

(NONE)

BOM options provided by this page:

(NONE)



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ATI M56 Core Power

SYNC_MASTER=(MASTER)                                SYNC_DATE=(MASTER)

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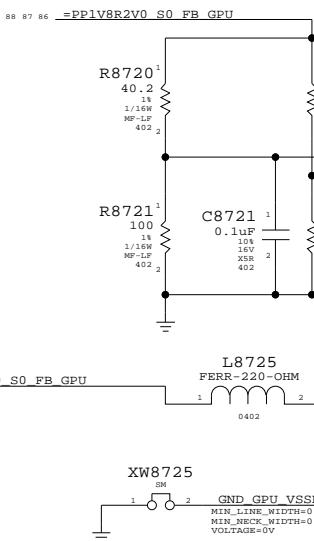
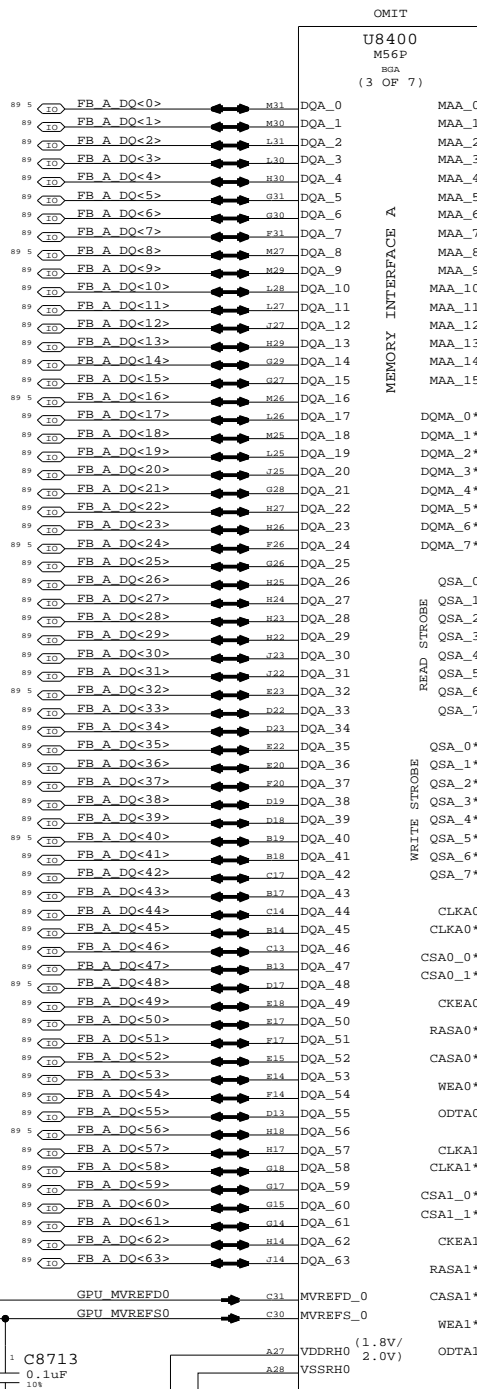
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Page Notes

Power aliases required by this page:
- =PP1V8R2V0_S0_FB_GPU

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)



ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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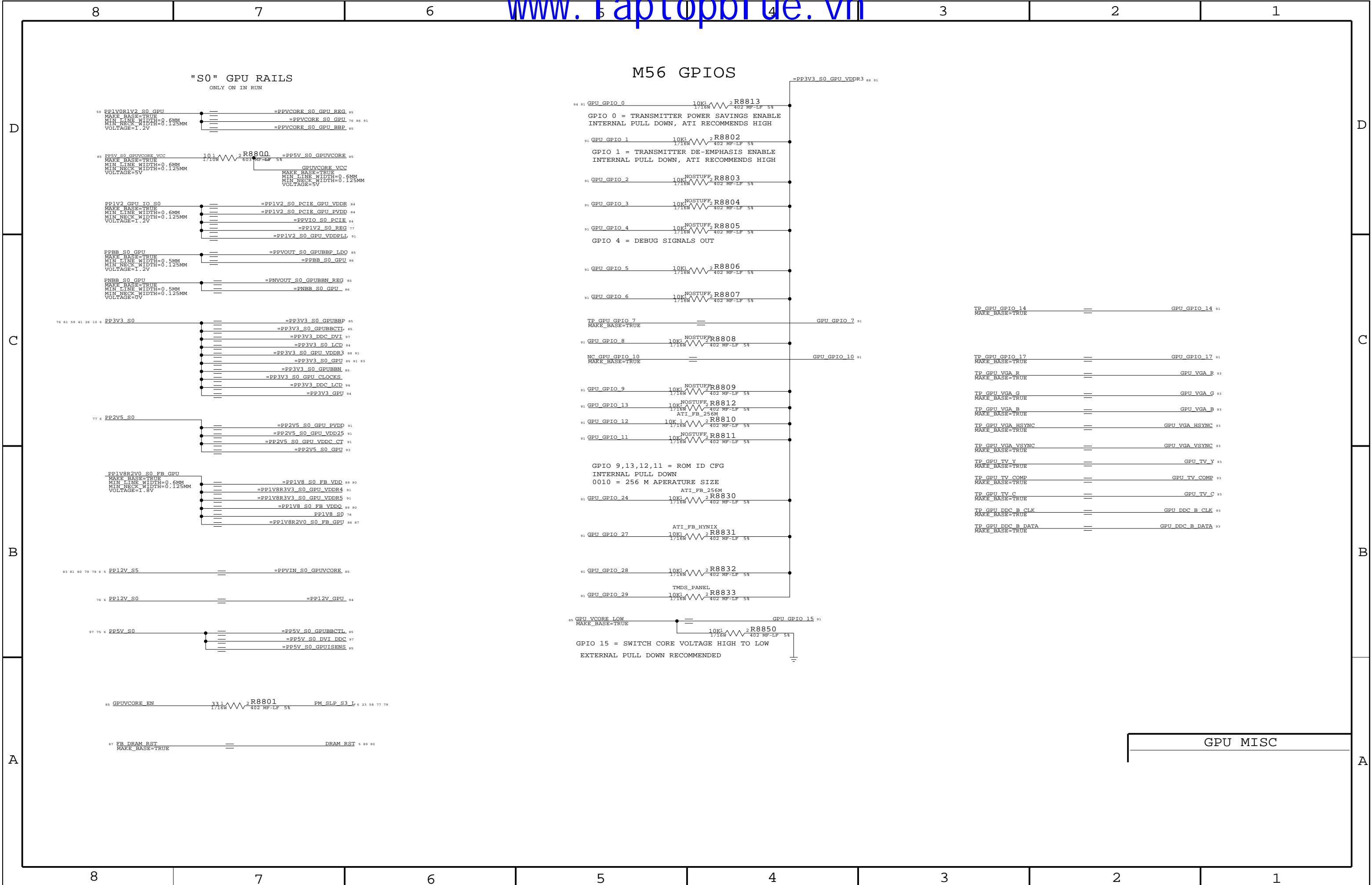
II NOT TO REPRODUCE OR COPY IT

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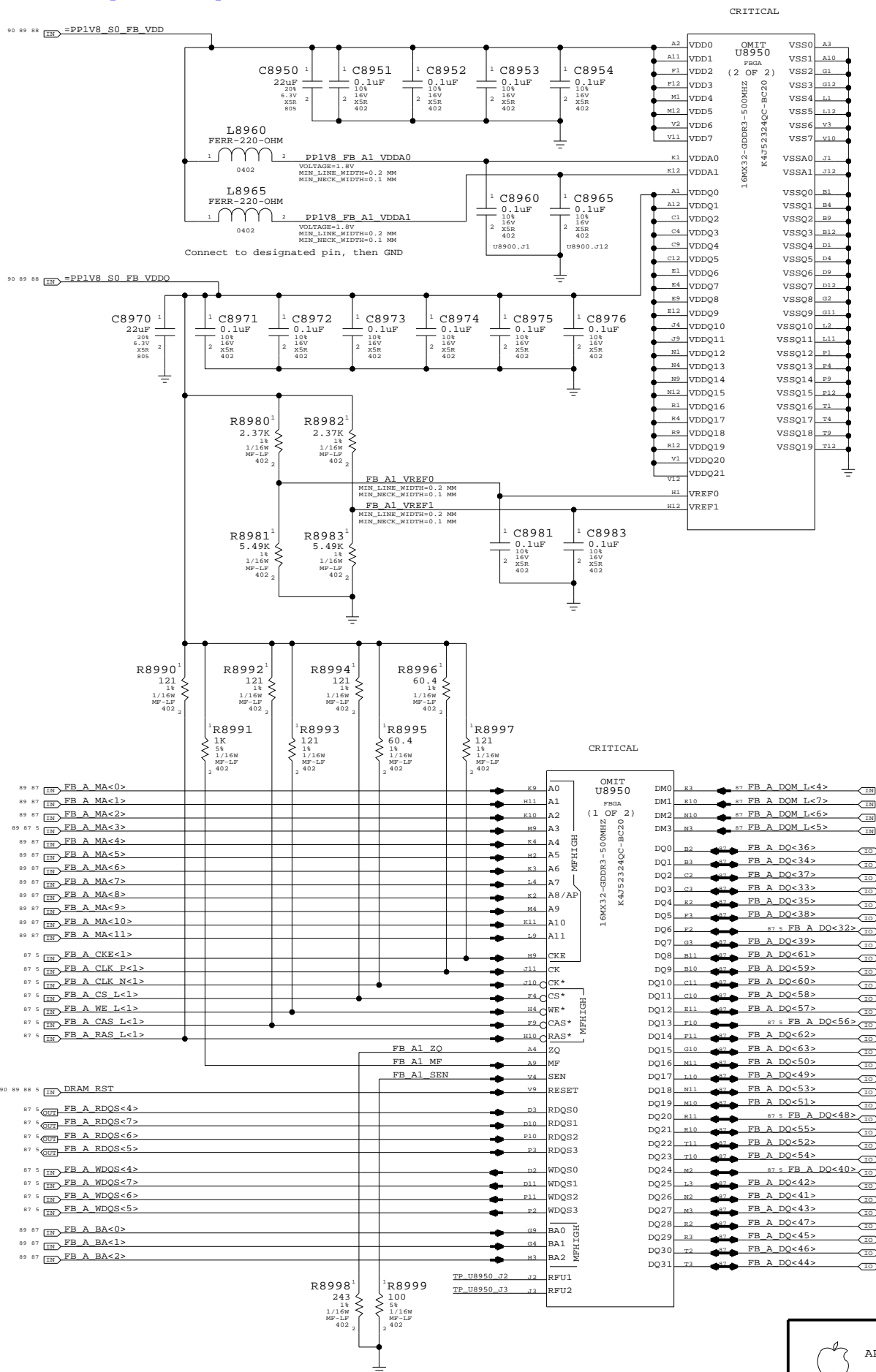
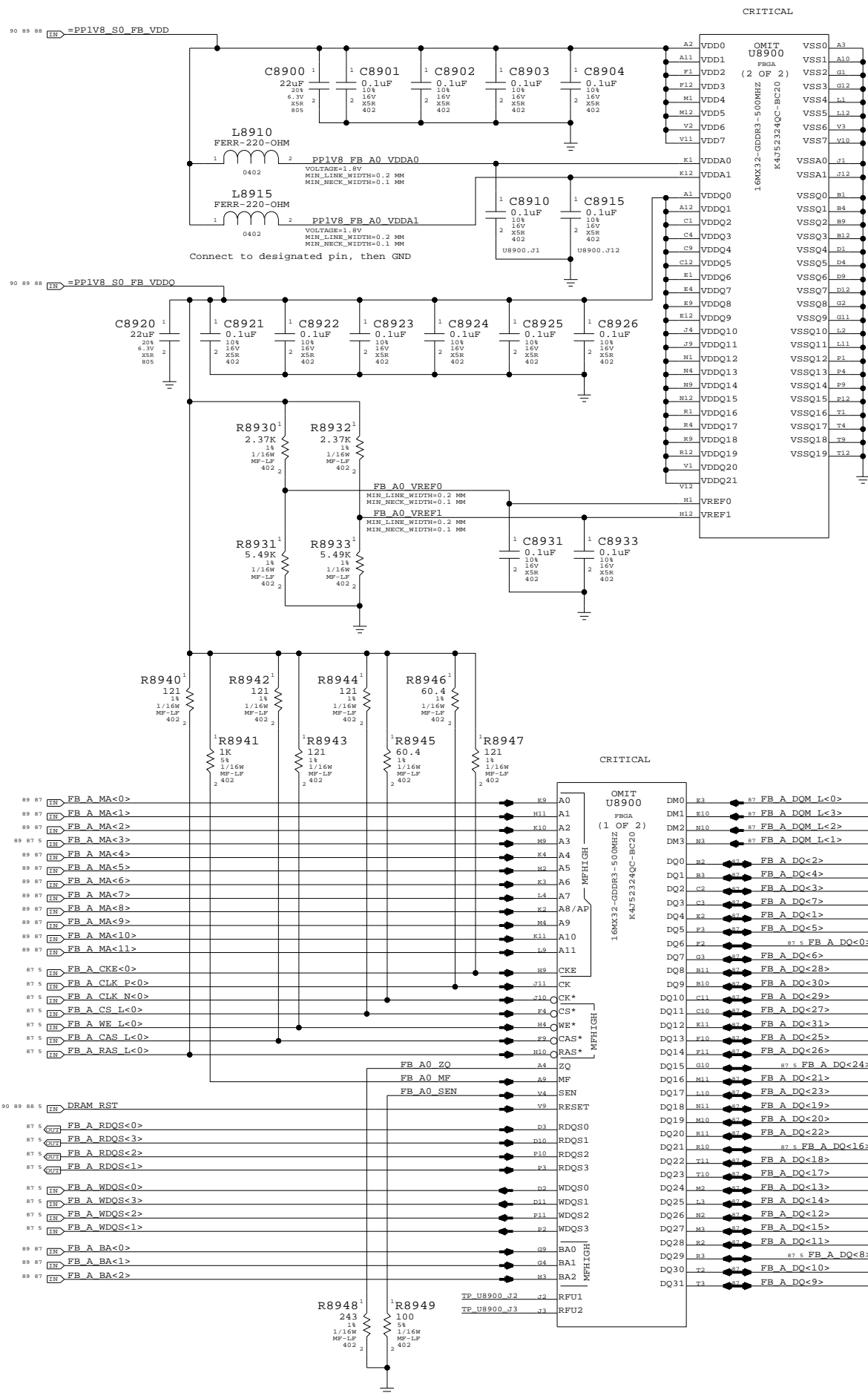


APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	87	111



Power aliases required by this page:

- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQSignal aliases required by this page:
(NONE)BOM options provided by this page:
(NONE)

GDDR3 Frame Buffer A

SYNC_MASTER=(MASTER)

SYNC_DATE=(MASTER)

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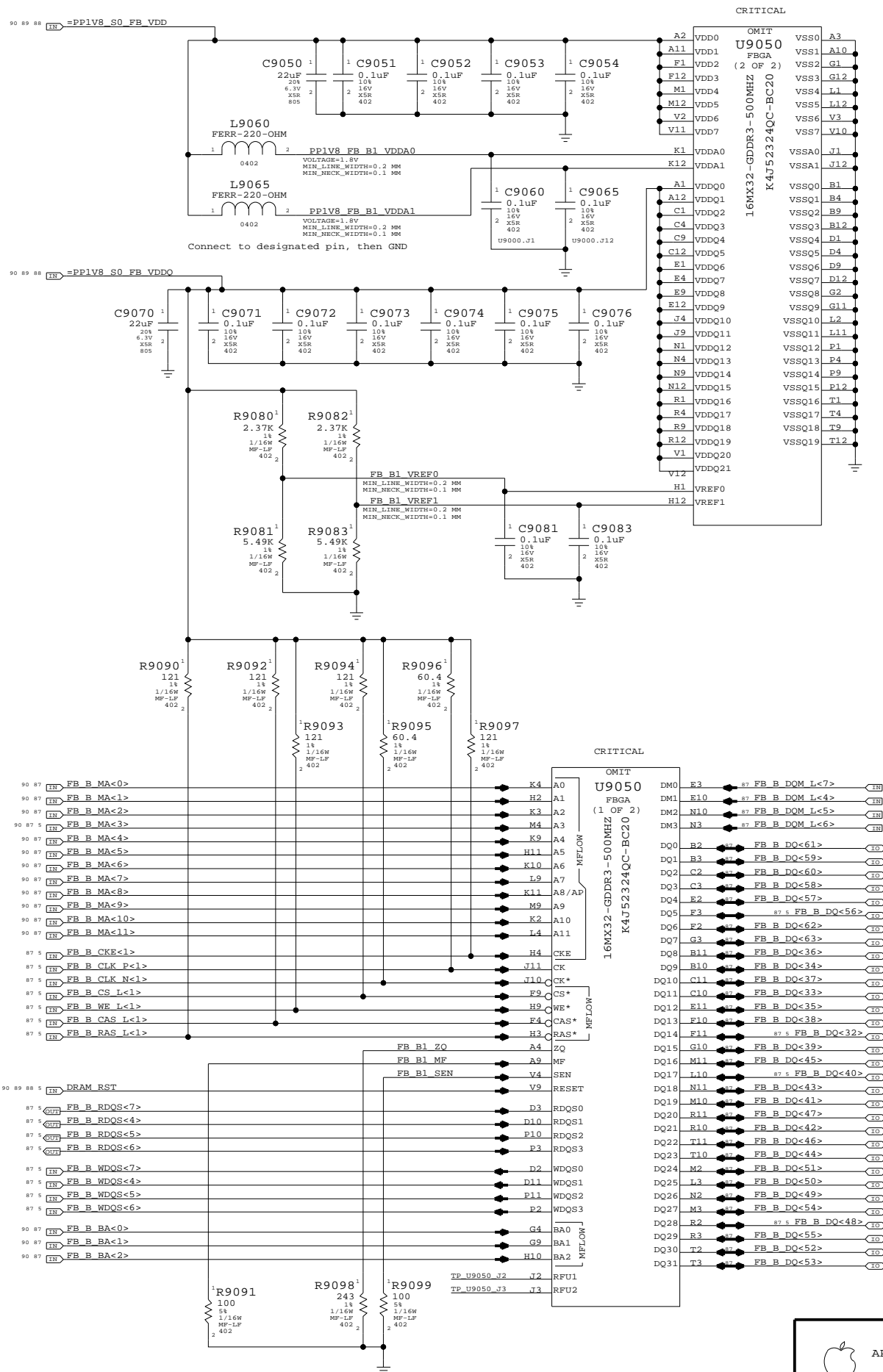
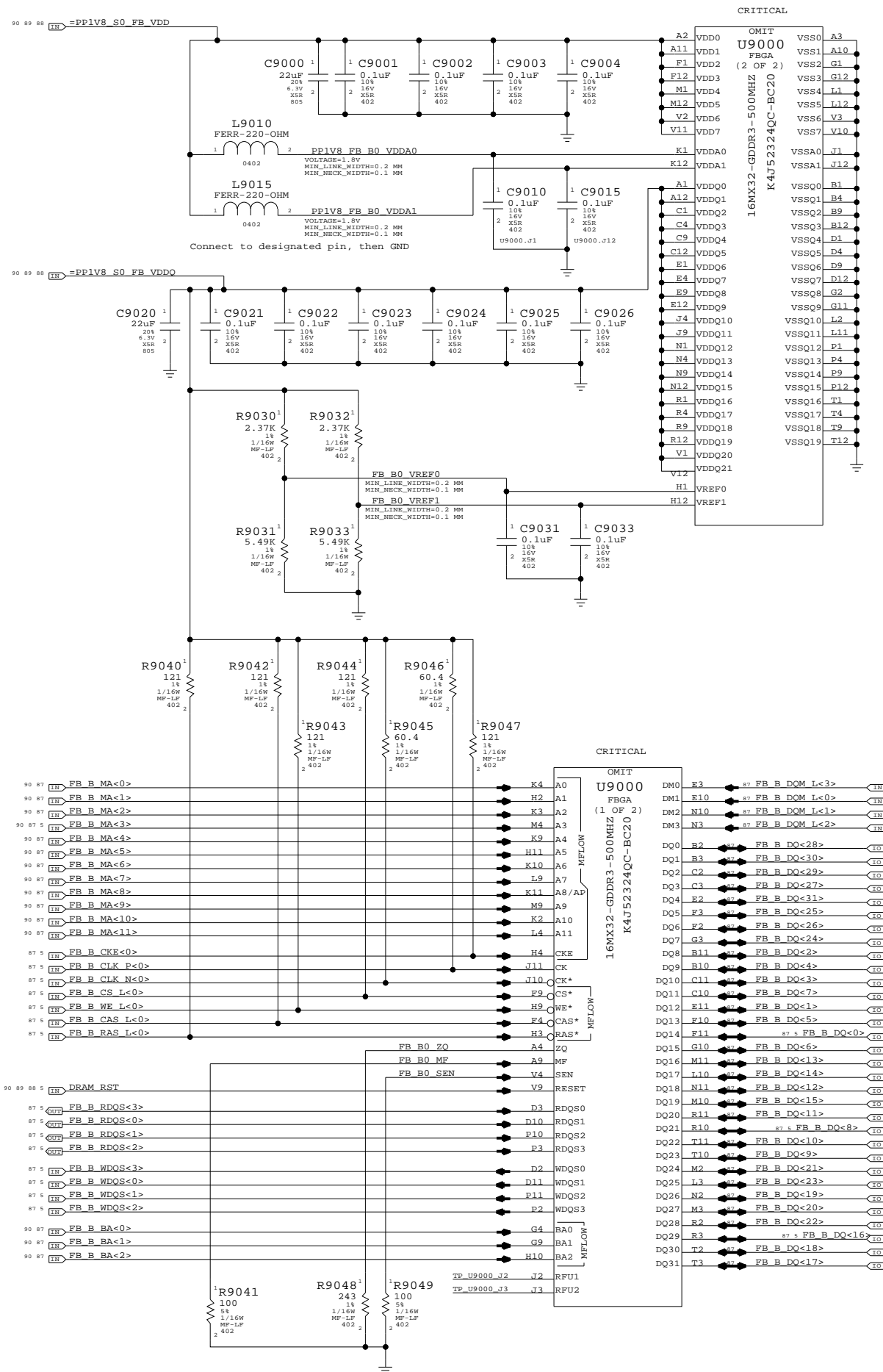
SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	89	111

Power aliases required by this page:

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- =PP1V8_S0_FB_VDD
- =PP1V8_S0_FB_VDDQ
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Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)



GDDR3 Frame Buffer B

SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
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SIZE	DRAWING NUMBER
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
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D	051-6950	06
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D	100	100	100
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SCALE	SHT	OF
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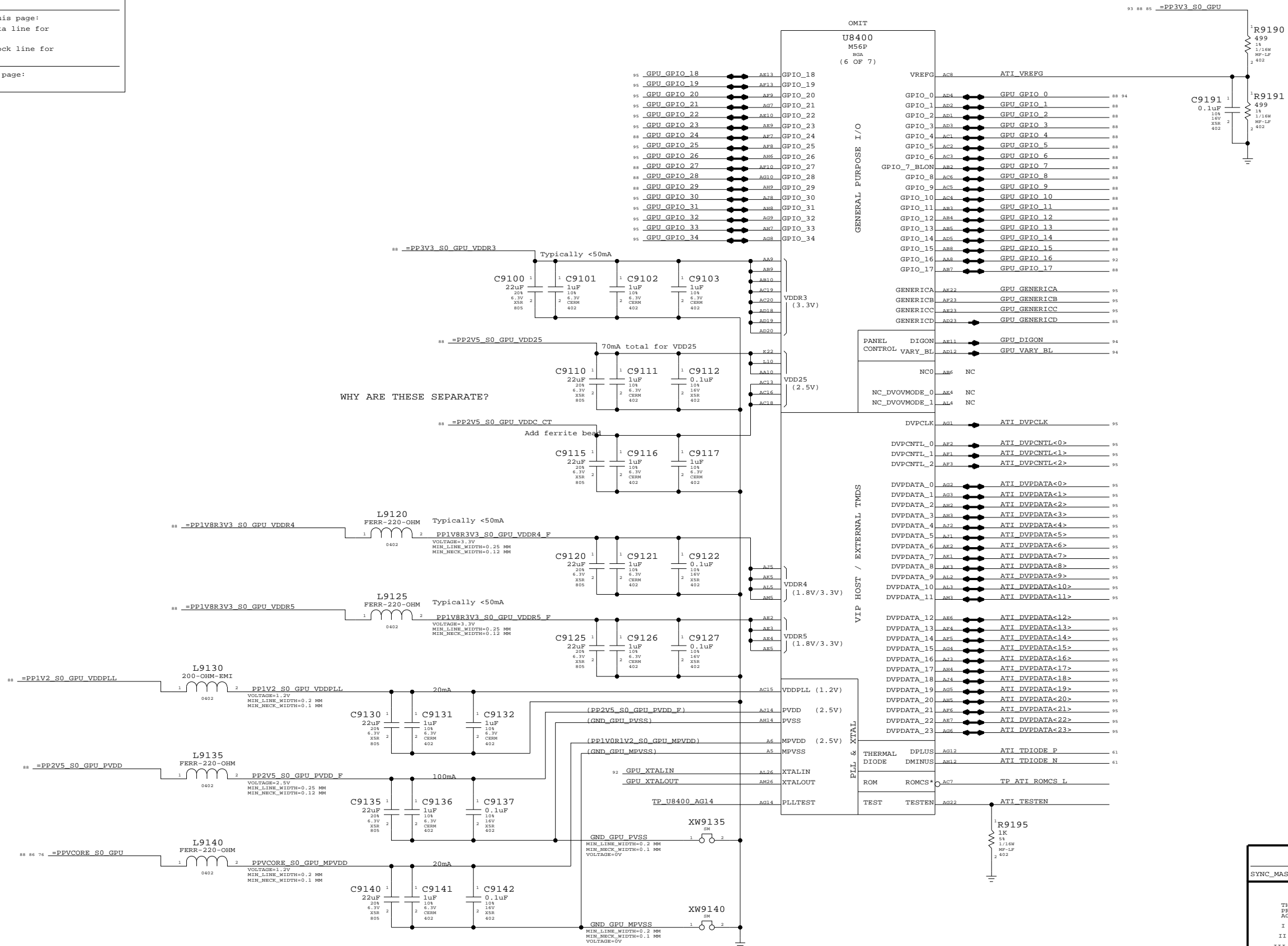
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- =PP3V3_GPU_GPIOS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLL

Signal aliases required by this page:

- =I2C_GPU_TMDS_SDA - I2C data line for external TMDS transmitters
- =I2C_GPU_TMDS_SCL - I2C clock line for external TMDS transmitters

BOM options provided by this page:
(NONE)



ATI M56 GPIO/DVO/Misc

SYNC_MASTER=(MASTER)SYNC_DATE=(MASTER)

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APPLE COMPUTER INC.

SIZE D

DRAWING NUMBER 051-6949

REV. 09

SCALE NONE

SHT 91

OF 111

87654321

Page Notes

Power aliases required by this page:
- =PP3V3_GPU_CLOCKS - =PP3V3_GPU_PWRSEQ
- =PPVIN_GPU_LVDDR_LDO - =PP2V5_GPU_PWRSEQ
- =PP2V5_GPU_LVDDR_LDO - =PP1V8_GPU_PWRSEQ
 - =PP1V5_GPU_PWRSEQ

Signal aliases required by this page:
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BOM options provided by this page:
- GPU_SS - GPU_LVDDR_2V8

D

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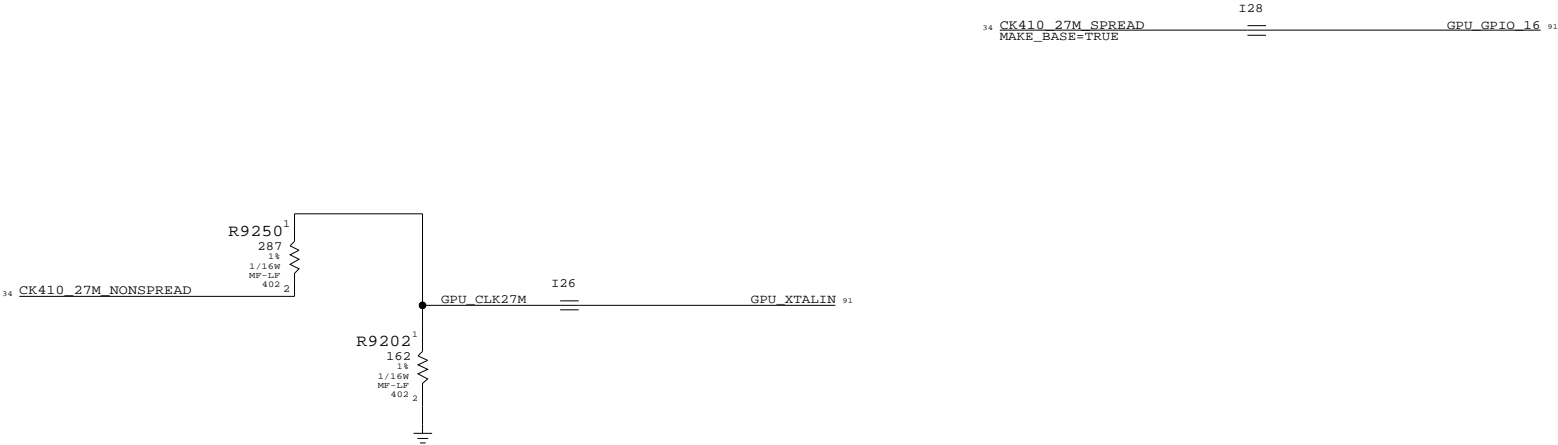
C

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87654321

GPU CLOCKS		
SYNC_MASTER=BOZEMAN		SYNC_DATE=05/21/2009
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6949	09
SCALE		SHT	OF
NONE		92	111

Page Notes

Power aliases required by this page:

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- =PP1V8R2V5_S0_GPU_LVDDR

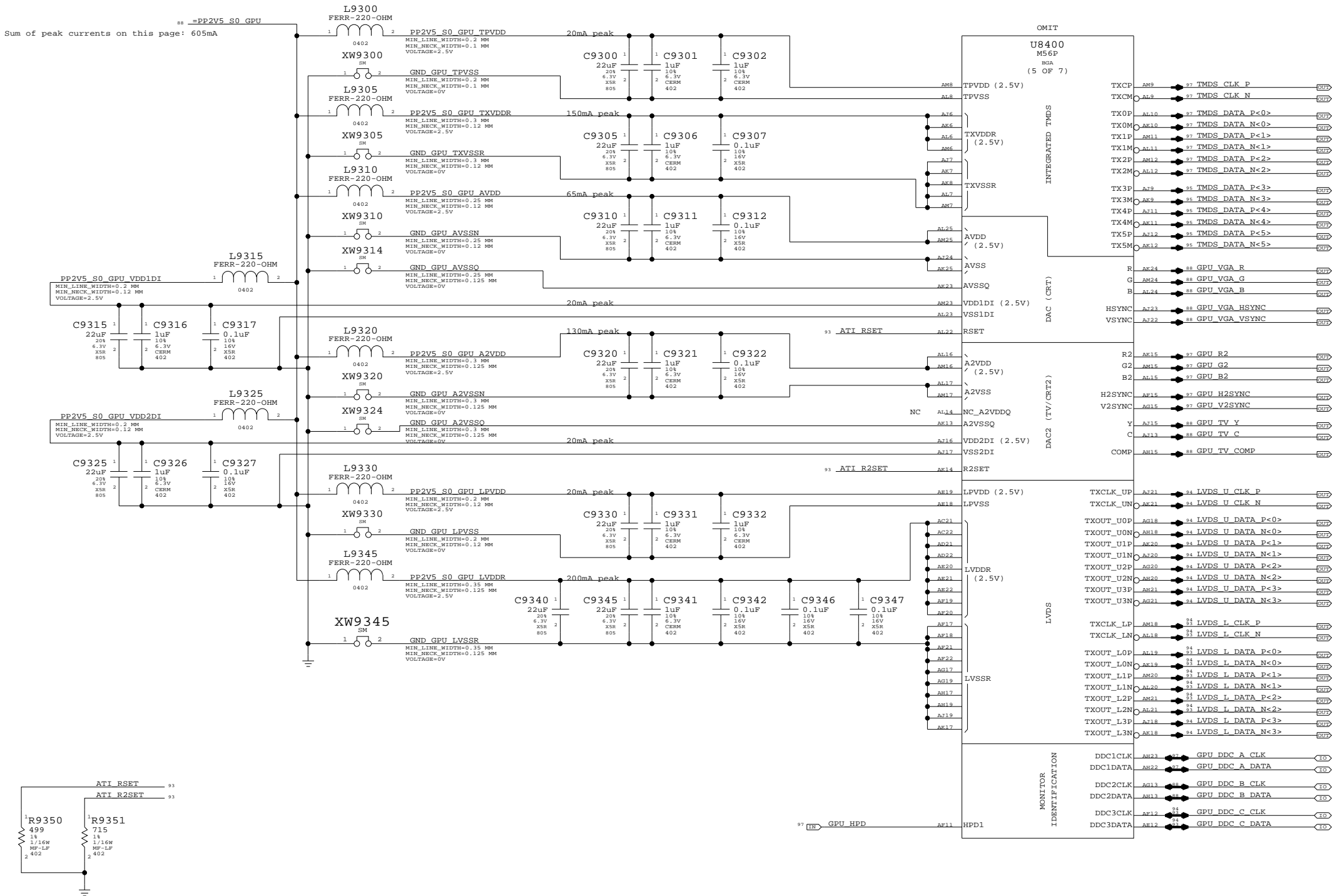
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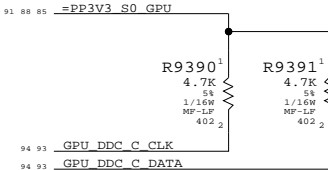
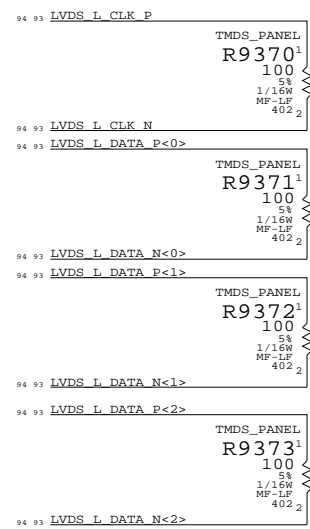
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TERMINATION FOR TMDS USAGE OF LVDS PINS
PLACE CLOSE TO GPU (U8400)



Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb



ATI M56 Video Interfaces

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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INVERTER INTERFACE

LCD (LVDS) INTERFACE

www.laptoplib.vn

INVERTER INTERFACE

LCD (LVDS) INTERFACE

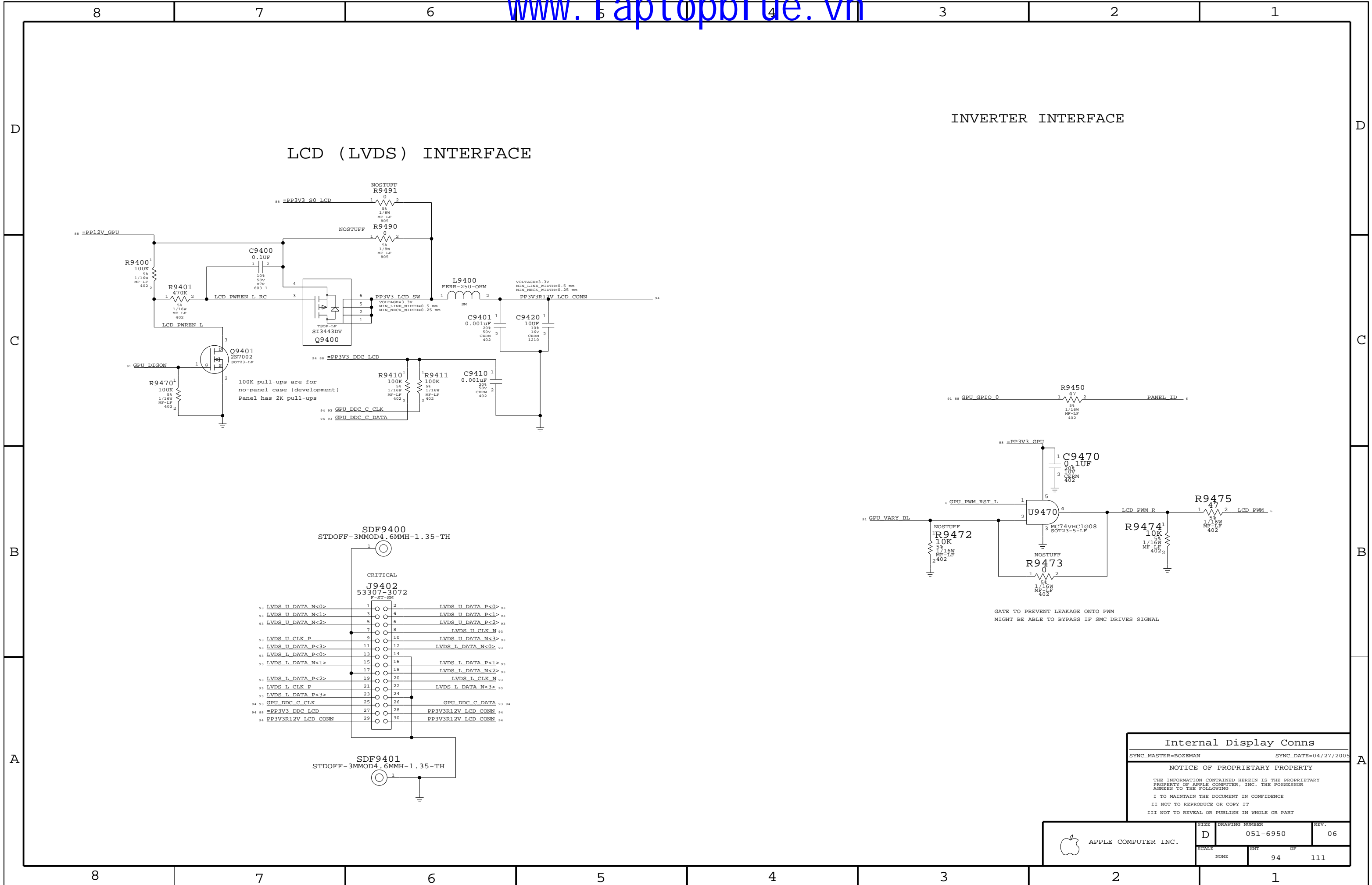
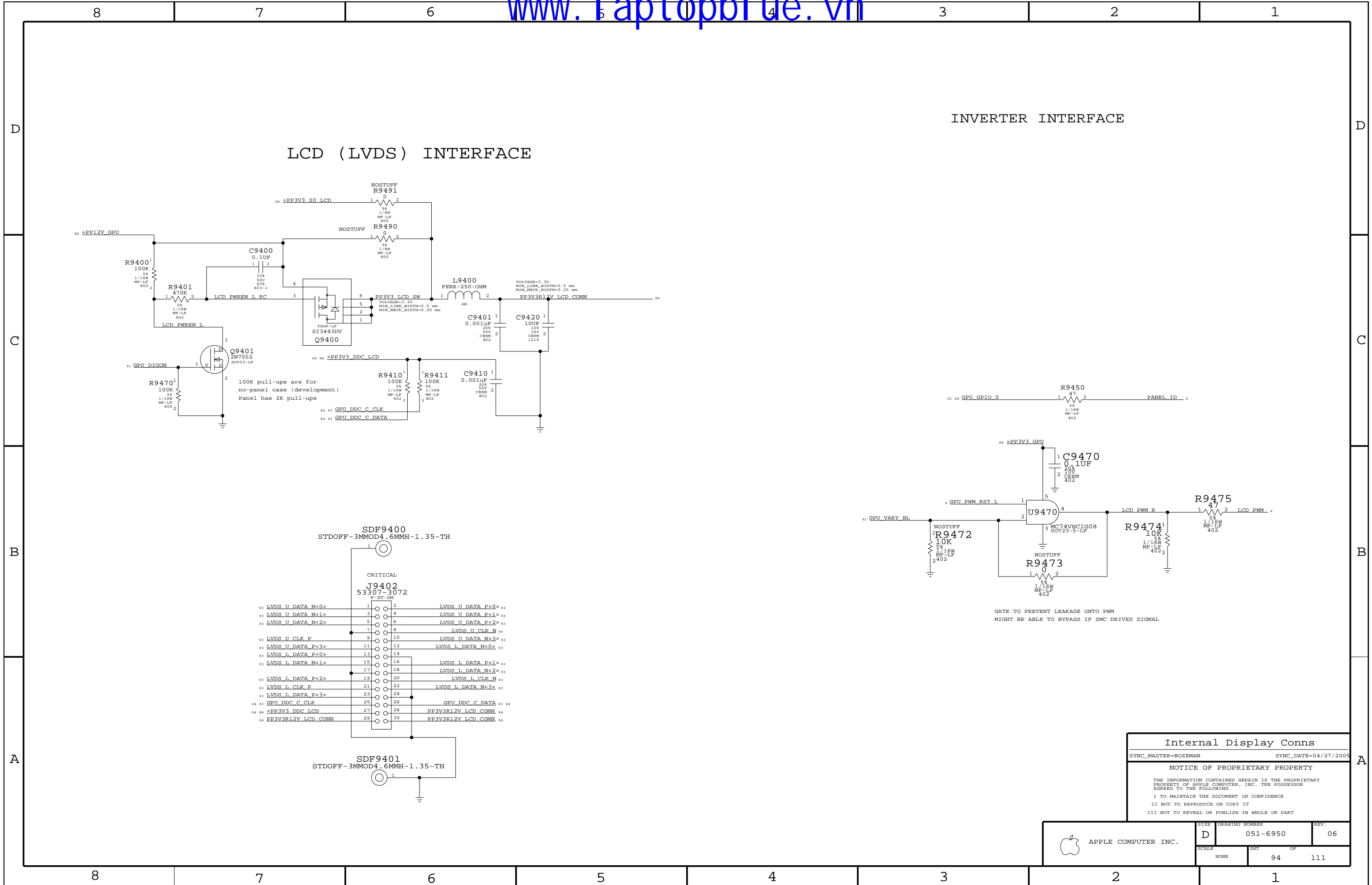
100K pull-ups are for no-panel case (development)
Panel has 2K pull-ups

GATE TO PREVENT LEAKAGE ONTO PWM
MIGHT BE ABLE TO BYPASS IF SMC DRIVES SIGNAL

Internal Display Conns	
SYNC_MASTER=BOZEMAN	SYNC_DATE=04/27/2005
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SIZE	DRAWING NUMBER	REV.
D	051-6950	06

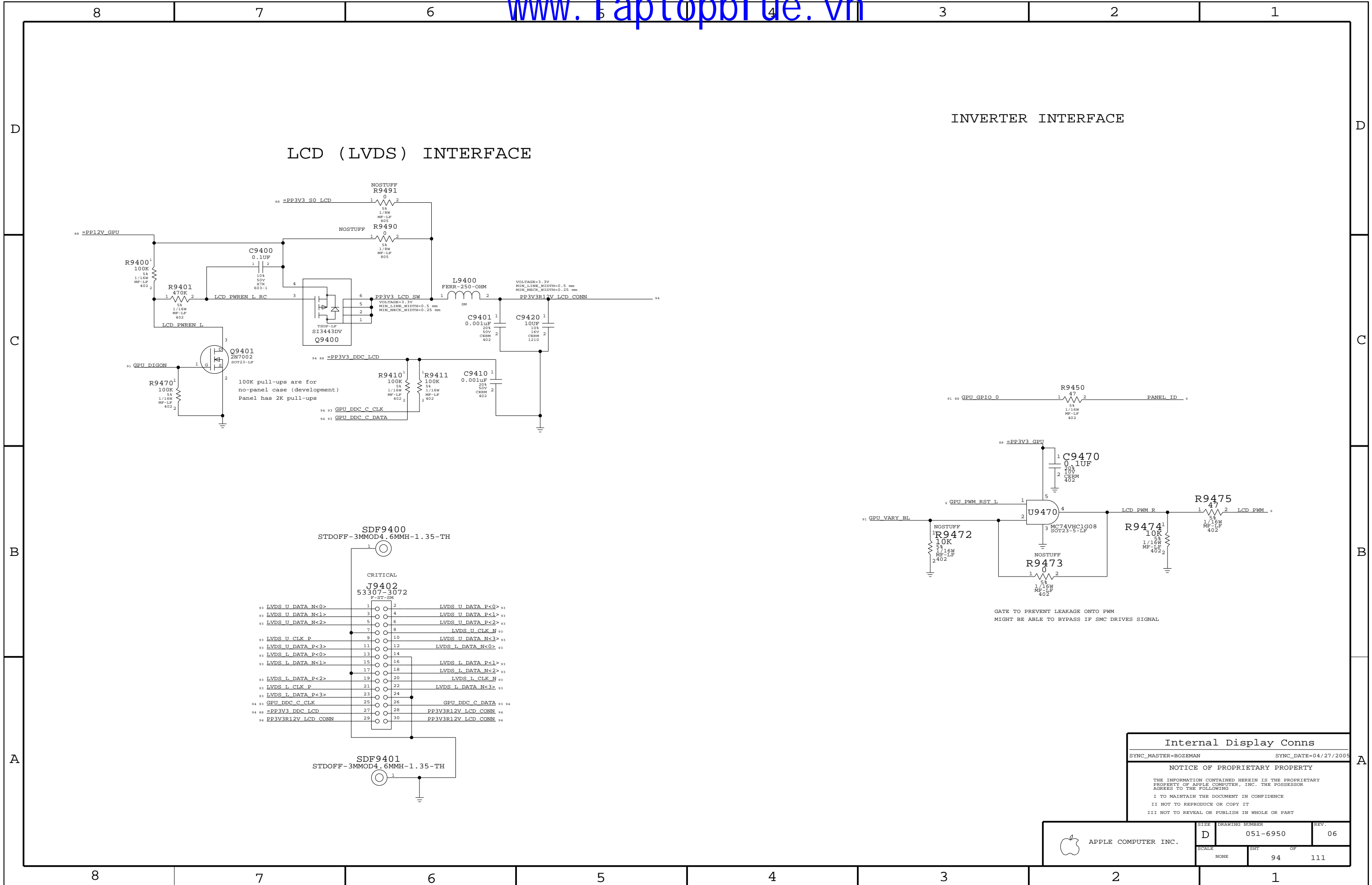
SCALE	SHT	OF
NONE	94	111



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INVERTER INTERFACE

LCD (LVDS) INTERFACE



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INVERTER INTERFACE

LCD (LVDS) INTERFACE

100K pull-ups are for no-panel case (development)
Panel has 2K pull-ups

CRITICAL

Internal Display Conns

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APPLE COMPUTER INC.

051-6950

06

94

111

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INVERTER INTERFACE

LCD (LVDS) INTERFACE

100K pull-ups are for no-panel case (development)
Panel has 2K pull-ups

CRITICAL

Internal Display Conns

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051-6950

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M56 TPS

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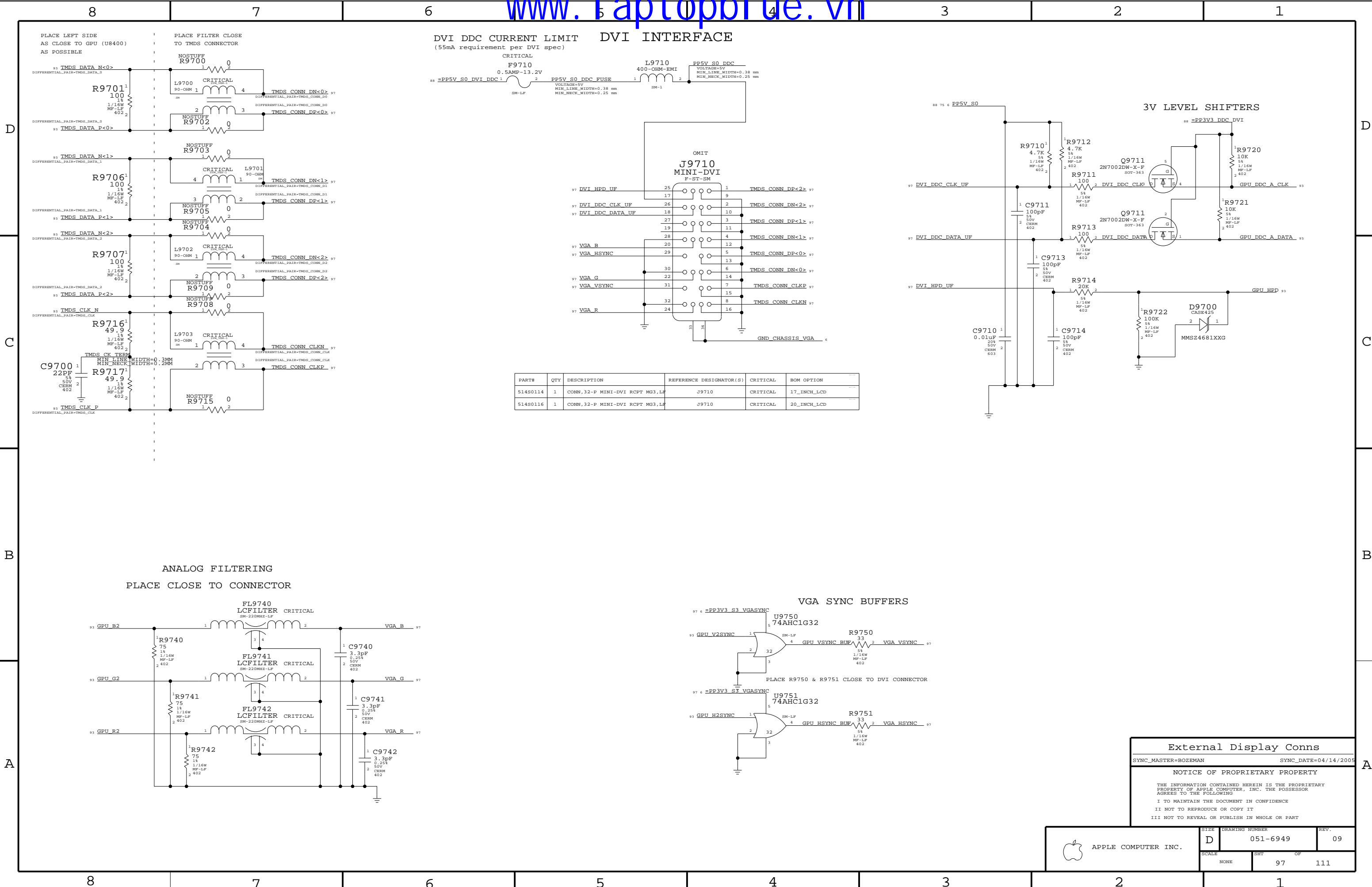
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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	95	111



[illegible]

ADM

FB_
FB_
FB_

FB_B_A

2

IMVP6_UGATE1	IMVP6_UGATE1 ~ @m39.lib.M39	75A8 75C5	MEM_A_A<4>	MEM_A_A<4> ~ @m39.lib.M39	1585 2883	MEM_B_DM<0>	MEM_B_DM<0> ~ @m39.lib.M39	1502 29D3	NB_CFG<5>	NB_CFG<5> ~ @m39.lib.M39	14C6 20C7
IMVP6_UGATE2	IMVP6_UGATE2 ~ @m39.lib.M39	75A6 75C5	MEM_A_A<5>	MEM_A_A<5> ~ @m39.lib.M39	1585 2886	MEM_B_DM<1>	MEM_B_DM<1> ~ @m39.lib.M39	1502 29D3	NB_CFG<6>	NB_CFG<6> ~ @m39.lib.M39	14C6
IMVP6_VDIFFF	IMVP6_VDIFFF ~ @m39.lib.M39	75A4 75B6	MEM_A_A<6>	MEM_A_A<6> ~ @m39.lib.M39	1585 28C3	MEM_B_DM<2>	MEM_B_DM<2> ~ @m39.lib.M39	1502 29C3	NB_CFG<7>	NB_CFG<7> ~ @m39.lib.M39	14C6 20C7
IMVP6_VDIFFF_RC	IMVP6_VDIFFF_RC ~ @m39.lib.M39	75B7	MEM_A_A<7>	MEM_A_A<7> ~ @m39.lib.M39	1585 28C3	MEM_B_DM<3>	MEM_B_DM<3> ~ @m39.lib.M39	1502 29C6	NB_CFG<8>	NB_CFG<8> ~ @m39.lib.M39	14C6
IMVP6_VO	IMVP6_VO ~ @m39.lib.M39	75A4 75B4 76D5	MEM_A_A<8>	MEM_A_A<8> ~ @m39.lib.M39	1585 28C6	MEM_B_DM<4>	MEM_B_DM<4> ~ @m39.lib.M39	1502 29B3	NB_CFG<9>	NB_CFG<9> ~ @m39.lib.M39	14C6 20B7
IMVP6_VO_R	IMVP6_VO_R ~ @m39.lib.M39	75B4	MEM_A_A<9>	MEM_A_A<9> ~ @m39.lib.M39	1585 28C6	MEM_B_DM<5>	MEM_B_DM<5> ~ @m39.lib.M39	1502 29A6	NB_CFG<10>	NB_CFG<10> ~ @m39.lib.M39	14C6
IMVP6_VO_R_OA	IMVP6_VO_R_OA ~ @m39.lib.M39	76D4	MEM_A_A<10>	MEM_A_A<10> ~ @m39.lib.M39	1585 28B6	MEM_B_DM<6>	MEM_B_DM<6> ~ @m39.lib.M39	1502 29A3	NB_CFG<11>	NB_CFG<11> ~ @m39.lib.M39	14C6
IMVP6_VR_TT	IMVP6_VR_TT ~ @m39.lib.M39	75C7	MEM_A_A<11>	MEM_A_A<11> ~ @m39.lib.M39	1585 28C3	MEM_B_DM<7>	MEM_B_DM<7> ~ @m39.lib.M39	1502 29A6	NB_CFG<12>	NB_CFG<12> ~ @m39.lib.M39	14C6
IMVP6_VSYM	IMVP6_VSYM ~ @m39.lib.M39	75A4 75B5	MEM_A_A<12>	MEM_A_A<12> ~ @m39.lib.M39	1585 28C6	MEM_B_DM<8>	MEM_B_DM<8> ~ @m39.lib.M39	1502 29D6	NB_CFG<13>	NB_CFG<13> ~ @m39.lib.M39	14C6
IMVP6_VSYM	IMVP6_VSYM ~ @m39.lib.M39	75A4 75C5	MEM_A_A<13>	MEM_A_A<13> ~ @m39.lib.M39	1585 28B3	MEM_B_DM<9>	MEM_B_DM<9> ~ @m39.lib.M39	1504 29D6	NB_CFG<14>	NB_CFG<14> ~ @m39.lib.M39	14C6
IMVP6_VSYM_R1	IMVP6_VSYM_R1 ~ @m39.lib.M39	75A8 75C2	MEM_A_BS<0>	MEM_A_BS<0> ~ @m39.lib.M39	1505 28B6	MEM_B_DQ<2>	MEM_B_DQ<2> ~ @m39.lib.M39	1504 29D6	NB_CFG<15>	NB_CFG<15> ~ @m39.lib.M39	14C6
IMVP6_VSYM_R2	IMVP6_VSYM_R2 ~ @m39.lib.M39	75A6 75B2	MEM_A_BS<2..0>	MEM_A_BS<2..0> ~ @m39.lib.M39	30C6	MEM_B_DQ<3>	MEM_B_DQ<3> ~ @m39.lib.M39	1504 29D6	NB_CFG<16>	NB_CFG<16> ~ @m39.lib.M39	14C6 20C5
IMVP6_VW	IMVP6_VW ~ @m39.lib.M39	75A4 75B6	MEM_A_BS<1>	MEM_A_BS<1> ~ @m39.lib.M39	1505 28B3	MEM_B_DQ<4>	MEM_B_DQ<4> ~ @m39.lib.M39	1504 29D3	NB_CFG<17>	NB_CFG<17> ~ @m39.lib.M39	14C6
IMVP_DPRSLEPR	IMVP_DPRSLEPR ~ @m39.lib.M39	75C6	MEM_A_BS<2>	MEM_A_BS<2> ~ @m39.lib.M39	1505 28C6	MEM_B_DQ<5>	MEM_B_DQ<5> ~ @m39.lib.M39	1504 29D3	NB_CFG<18>	NB_CFG<18> ~ @m39.lib.M39	14C6 20B5
IMVP_PGD_IN	IMVP_PGD_IN ~ @m39.lib.M39	75C6 77C7	MEM_A_CAS_L	MEM_A_CAS_L ~ @m39.lib.M39	1505 28B6 30B6	MEM_B_DQ<6>	MEM_B_DQ<6> ~ @m39.lib.M39	5A7 15D4 29D3	NB_CFG<19>	NB_CFG<19> ~ @m39.lib.M39	14C6 20B5
IMVP_VID<0>	IMVP_VID<0> ~ @m39.lib.M39	75C6	MEM_A_DM<0>	MEM_A_DM<0> ~ @m39.lib.M39	1505 28D3	MEM_B_DQ<7>	MEM_B_DQ<7> ~ @m39.lib.M39	1504 29D3	NB_CFG<20>	NB_CFG<20> ~ @m39.lib.M39	14B6 20A5
IMVP_VID<1>	IMVP_VID<1> ~ @m39.lib.M39	75C7	MEM_A_DM<1>	MEM_A_DM<1> ~ @m39.lib.M39	1505 28D3	MEM_B_DQ<8>	MEM_B_DQ<8> ~ @m39.lib.M39	5A7 15C4 29D6	NB_CLK100M_GCLKIN_N	NB_CLK100M_GCLKIN_N ~ @m39.lib.M39	5C7 14C4 34B4 34C2
IMVP_VID<2>	IMVP_VID<2> ~ @m39.lib.M39	75C7	MEM_A_DM<2>	MEM_A_DM<2> ~ @m39.lib.M39	1505 28C3	MEM_B_DQ<9>	MEM_B_DQ<9> ~ @m39.lib.M39	1504 29D6	NB_CLK100M_GCLKIN_P	NB_CLK100M_GCLKIN_P ~ @m39.lib.M39	5C7 14C4 34B4 34C2
IMVP_VID<3>	IMVP_VID<3> ~ @m39.lib.M39	75C7	MEM_A_DM<3>	MEM_A_DM<3> ~ @m39.lib.M39	1505 28C6	MEM_B_DQ<10>	MEM_B_DQ<10> ~ @m39.lib.M39	1504 29D6	NB_FSB_VREF	NB_FSB_VREF ~ @m39.lib.M39	12C4 22C4
IMVP_VID<4>	IMVP_VID<4> ~ @m39.lib.M39	75C7	MEM_A_DM<4>	MEM_A_DM<4> ~ @m39.lib.M39	1505 28B3	MEM_B_DQ<11>	MEM_B_DQ<11> ~ @m39.lib.M39	1504 29D6	NB_FSB_XRCOMP	NB_FSB_XRCOMP ~ @m39.lib.M39	12A6
IMVP_VID<5>	IMVP_VID<5> ~ @m39.lib.M39	75C7	MEM_A_DM<5>	MEM_A_DM<5> ~ @m39.lib.M39	1505 28A6	MEM_B_DQ<12>	MEM_B_DQ<12> ~ @m39.lib.M39	1504 29D3	NB_FSB_XSCOMP	NB_FSB_XSCOMP ~ @m39.lib.M39	12A6
IMVP_VID<6>	IMVP_VID<6> ~ @m39.lib.M39	75C7	MEM_A_DM<6>	MEM_A_DM<6> ~ @m39.lib.M39	1505 28A3	MEM_B_DQ<13>	MEM_B_DQ<13> ~ @m39.lib.M39	1504 29D3	NB_FSB_XSWING	NB_FSB_XSWING ~ @m39.lib.M39	12A6
IMVP_VR_ON	IMVP_VR_ON ~ @m39.lib.M39	58D7 75C6	MEM_A_DM<7>	MEM_A_DM<7> ~ @m39.lib.M39	1505 28A6	MEM_B_DQ<14>	MEM_B_DQ<14> ~ @m39.lib.M39	1504 29D3	NB_FSB_YRCOMP	NB_FSB_YRCOMP ~ @m39.lib.M39	12A6
INT_PIRQA_L	INT_PIRQA_L ~ @m39.lib.M39	22A7 26D2	MEM_A_DQ<0>	MEM_A_DQ<0> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<15>	MEM_B_DQ<15> ~ @m39.lib.M39	1504 29D3	NB_FSB_YSCOMP	NB_FSB_YSCOMP ~ @m39.lib.M39	12A6
INT_PIRQB_L	INT_PIRQB_L ~ @m39.lib.M39	22A7 26D2	MEM_A_DQ<1>	MEM_A_DQ<1> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<16>	MEM_B_DQ<16> ~ @m39.lib.M39	1504 29C6	NB_FSB_YSWING	NB_FSB_YSWING ~ @m39.lib.M39	12A6
INT_PIRQC_L	INT_PIRQC_L ~ @m39.lib.M39	22A7 26D2	MEM_A_DQ<2>	MEM_A_DQ<2> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<17>	MEM_B_DQ<17> ~ @m39.lib.M39	1504 29C6	NB_RST_IN_L	NB_RST_IN_L ~ @m39.lib.M39	6B7 14B7
INT_PIRQD_L	INT_PIRQD_L ~ @m39.lib.M39	22A7 26D2 44B5	MEM_A_DQ<3>	MEM_A_DQ<3> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<18>	MEM_B_DQ<18> ~ @m39.lib.M39	1504 29C6	NB_RST_IN_L_R	NB_RST_IN_L_R ~ @m39.lib.M39	5C7 14B6
INT_SERR_IRQ	INT_SERR_IRQ ~ @m39.lib.M39	22C8 58C7 60C3 67C6	MEM_A_DQ<4>	MEM_A_DQ<4> ~ @m39.lib.M39	1507 28D3	MEM_B_DQ<19>	MEM_B_DQ<19> ~ @m39.lib.M39	1504 29C6	NB_SB_SYMC_L	NB_SB_SYMC_L ~ @m39.lib.M39	14B6 22A6
ISENSE_CAL_EN	ISENSE_CAL_EN ~ @m39.lib.M39	58B7 76A8	MEM_A_DQ<5>	MEM_A_DQ<5> ~ @m39.lib.M39	1507 28D3	MEM_B_DQ<20>	MEM_B_DQ<20> ~ @m39.lib.M39	1504 29C3	NB_TV_DCONSEL0	NB_TV_DCONSEL0 ~ @m39.lib.M39	14C6
ISENSE_CAL_EN_L	ISENSE_CAL_EN_L ~ @m39.lib.M39	76B7	MEM_A_DQ<6>	MEM_A_DQ<6> ~ @m39.lib.M39	1507 28D3	MEM_B_DQ<21>	MEM_B_DQ<21> ~ @m39.lib.M39	1504 29C3	NB_TV_DCONSEL1	NB_TV_DCONSEL1 ~ @m39.lib.M39	14C6
ISENSE_CAL_EN_LS12V	ISENSE_CAL_EN_LS12V ~ @m39.lib.M39	76B6	MEM_A_DQ<7>	MEM_A_DQ<7> ~ @m39.lib.M39	5B7 15D7 28D3	MEM_B_DQ<22>	MEM_B_DQ<22> ~ @m39.lib.M39	1504 29C3	NB_VCCSM_LF1	NB_VCCSM_LF1 ~ @m39.lib.M39	16B4
ISENSE_CAL_EN_L_R	ISENSE_CAL_EN_L_R ~ @m39.lib.M39	76A7	MEM_A_DQ<8>	MEM_A_DQ<8> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<23>	MEM_B_DQ<23> ~ @m39.lib.M39	5A7 15C4 29C3	NB_VCCSM_LF2	NB_VCCSM_LF2 ~ @m39.lib.M39	16B4
ITPRESET_L	ITPRESET_L ~ @m39.lib.M39	11B3	MEM_A_DQ<9>	MEM_A_DQ<9> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<24>	MEM_B_DQ<24> ~ @m39.lib.M39	1504 29C6	NB_VCCSM_LF4	NB_VCCSM_LF4 ~ @m39.lib.M39	16B8
ITP_TDO	ITP_TDO ~ @m39.lib.M39	11B3	MEM_A_DQ<10>	MEM_A_DQ<10> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<25>	MEM_B_DQ<25> ~ @m39.lib.M39	5A7 15C4 29C6	NB_VCCSM_LF5	NB_VCCSM_LF5 ~ @m39.lib.M39	16B8
ITS_ALIVE	ITS_ALIVE ~ @m39.lib.M39	6A7	MEM_A_DQ<11>	MEM_A_DQ<11> ~ @m39.lib.M39	1507 28D6	MEM_B_DQ<26>	MEM_B_DQ<26> ~ @m39.lib.M39	1504 29C6	NB_VTTLF_CAP1	NB_VTTLF_CAP1 ~ @m39.lib.M39	17A4
ITS_PLUGGED_IN	ITS_PLUGGED_IN ~ @m39.lib.M39	6A8	MEM_A_DQ<12>	MEM_A_DQ<12> ~ @m39.lib.M39	1507 28D3	MEM_B_DQ<27>	MEM_B_DQ<27> ~ @m39.lib.M39	1504 29C6	NB_VTTLF_CAP2	NB_VTTLF_CAP2 ~ @m39.lib.M39	17A4
ITS_RUNNING	ITS_RUNNING ~ @m39.lib.M39	6A6	MEM_A_DQ<13>	MEM_A_DQ<13> ~ @m39.lib.M39	1507 28D3	MEM_B_DQ<28>	MEM_B_DQ<28> ~ @m39.lib.M39	1504 29C3	NB_VTTLF_CAP3	NB_VTTLF_CAP3 ~ @m39.lib.M39	17B4
JDRF	JDRF ~ @m39.lib.M39	68C4	MEM_A_DQ<14>	MEM_A_DQ<14> ~ @m39.lib.M39	5B7 15C7 28D3	MEM_B_DQ<29>	MEM_B_DQ<29> ~ @m39.lib.M39	1504 29C3	NC_AUD_VREF_PORT_A	NC_AUD_VREF_PORT_A ~ @m39.lib.M39	68C1
KBC_MDE	KBC_MDE ~ @m39.lib.M39	58C2	MEM_A_DQ<15>	MEM_A_DQ<15> ~ @m39.lib.M39	1507 28D3	MEM_B_DQ<30>	MEM_B_DQ<30> ~ @m39.lib.M39	1504 29C3	NC_AUD_VREF_PORT_C	NC_AUD_VREF_PORT_C ~ @m39.lib.M39	68C1
LCD_PWM	LCD_PWM ~ @m39.lib.M39	6D8 94B1	MEM_A_DQ<16>	MEM_A_DQ<16> ~ @m39.lib.M39	5B7 15C7 28C6	MEM_B_DQ<31>	MEM_B_DQ<31> ~ @m39.lib.M39	1504 29C3	NC_AUD_VREF_PORT_D	NC_AUD_VREF_PORT_D ~ @m39.lib.M39	68C1
LCD_PWM_R	LCD_PWM_R ~ @m39.lib.M39	94B2	MEM_A_DQ<17>	MEM_A_DQ<17> ~ @m39.lib.M39	1507 28C6	MEM_B_DQ<32>	MEM_B_DQ<32> ~ @m39.lib.M39	1504 29B6	NC_FW_NU1	NC_FW_NU1 ~ @m39.lib.M39	44B3
LCD_PWREN_L	LCD_PWREN_L ~ @m39.lib.M39	94B2	MEM_A_DQ<18>	MEM_A_DQ<18> ~ @m39.lib.M39	1507 28C6	MEM_B_DQ<33>	MEM_B_DQ<33> ~ @m39.lib.M39	1504 29B6	NC_FW_NU2	NC_FW_NU2 ~ @m39.lib.M39	44B3
LCD_PWREN_L_RC	LCD_PWREN_L_RC ~ @m39.lib.M39	94C7	MEM_A_DQ<19>	MEM_A_DQ<19> ~ @m39.lib.M39	1507 28C6	MEM_B_DQ<34>	MEM_B_DQ<34> ~ @m39.lib.M39	15B4 29B6	NC_JE350_13	NC_JE350_13 ~ @m39.lib.M39	47B1
LED4300_1	LED4300_1 ~ @m39.lib.M39	43D2	MEM_A_DQ<20>	MEM_A_DQ<20> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<35>	MEM_B_DQ<35> ~ @m39.lib.M39	15B4 29B6	NC_SMC_P20	NC_SMC_P20 ~ @m39.lib.M39	59D5
LED4301_1	LED4301_1 ~ @m39.lib.M39	43D1	MEM_A_DQ<21>	MEM_A_DQ<21> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<36>	MEM_B_DQ<36> ~ @m39.lib.M39	15B4 29B3	SMC_P20 ~ @m39.lib.M39	SMC_P20 ~ @m39.lib.M39	59D5 59D6
LED4302_1	LED4302_1 ~ @m39.lib.M39	43D1	MEM_A_DQ<22>	MEM_A_DQ<22> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<37>	MEM_B_DQ<37> ~ @m39.lib.M39	15B4 29B3	NC_SMC_P21	NC_SMC_P21 ~ @m39.lib.M39	59D5
LED4303_1	LED4303_1 ~ @m39.lib.M39	43D1	MEM_A_DQ<23>	MEM_A_DQ<23> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<38>	MEM_B_DQ<38> ~ @m39.lib.M39	5A7 15D4 29B3	SMC_P21 ~ @m39.lib.M39	SMC_P21 ~ @m39.lib.M39	59D5 59D6
LED_PP1V05_S0_N	LED_PP1V05_S0_N ~ @m39.lib.M39	81A4	MEM_A_DQ<24>	MEM_A_DQ<24> ~ @m39.lib.M39	1507 28C6	MEM_B_DQ<39>	MEM_B_DQ<39> ~ @m39.lib.M39	15B4 29B3	NC_SMC_P22	NC_SMC_P22 ~ @m39.lib.M39	59D5
LED_PP1V05_S0_P	LED_PP1V05_S0_P ~ @m39.lib.M39	81A4	MEM_A_DQ<25>	MEM_A_DQ<25> ~ @m39.lib.M39	5B7 15C7 28C6	MEM_B_DQ<40>	MEM_B_DQ<40> ~ @m39.lib.M39	15B4 29B6	NC_SMC_P22	NC_SMC_P22 ~ @m39.lib.M39	59D5 59D6
LED_PP1V5_S0_N	LED_PP1V5_S0_N ~ @m39.lib.M39	80A3	MEM_A_DQ<26>	MEM_A_DQ<26> ~ @m39.lib.M39	1507 28C6	MEM_B_DQ<41>	MEM_B_DQ<41> ~ @m39.lib.M39	15B4 29B6	NC_SMC_P23	NC_SMC_P23 ~ @m39.lib.M39	59D5
LED_PP1V5_S0_P	LED_PP1V5_S0_P ~ @m39.lib.M39	80A4	MEM_A_DQ<27>	MEM_A_DQ<27> ~ @m39.lib.M39	1507 28C6	MEM_B_DQ<42>	MEM_B_DQ<42> ~ @m39.lib.M39	15B4 29B6	NC_SMC_P23	NC_SMC_P23 ~ @m39.lib.M39	59D5 59D6
LED_PP1V8_S3_N	LED_PP1V8_S3_N ~ @m39.lib.M39	79A4	MEM_A_DQ<28>	MEM_A_DQ<28> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<43>	MEM_B_DQ<43> ~ @m39.lib.M39	15B4 29A6	NC_SMC_P26	NC_SMC_P26 ~ @m39.lib.M39	59D5
LED_PP1V8_S3_P	LED_PP1V8_S3_P ~ @m39.lib.M39	79A4	MEM_A_DQ<29>	MEM_A_DQ<29> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<44>	MEM_B_DQ<44> ~ @m39.lib.M39	5A7 15B4 29B3	NC_SMC_P26	NC_SMC_P26 ~ @m39.lib.M39	59D5 59D6
LPC_AD<0>	LPC_AD<0> ~ @m39.lib.M39	21D4 58D7 60C6 67C6	MEM_A_DQ<30>	MEM_A_DQ<30> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<45>	MEM_B_DQ<45> ~ @m39.lib.M39	15B4 29B3	NC_SMC_P27	NC_SMC_P27 ~ @m39.lib.M39	59D5
LPC_AD<1>	LPC_AD<1> ~ @m39.lib.M39	21D4 58D7 60C6 67C6	MEM_A_DQ<31>	MEM_A_DQ<31> ~ @m39.lib.M39	1507 28C3	MEM_B_DQ<46>	MEM_B_DQ<46> ~ @m39.lib.M39	15B4 29A3	SMC_P27 ~ @m39.lib.M39	SMC_P27 ~ @m39.lib.M39	59D5 59D6
LPC_AD<2>	LPC_AD<2> ~ @m39.lib.M39	21D4 58C7 60C3 67C6	MEM_A_DQ<32>	MEM_A_DQ<32> ~ @m39.lib.M39	1507 28B6	MEM_B_DQ<47>	MEM_B_DQ<47> ~ @m39.lib.M39	5A7 15B4 29A3	ODD_FWR_EN_L	ODD_FWR_EN_L ~ @m39.lib.M39	22D6 26C3
LPC_AD<3>	LPC_AD<3> ~ @m39.lib.M39	21D4 58C7 60C3 67C6	MEM_A_DQ<33>	MEM_A_DQ<33> ~ @m39.lib.M39	1507 28B6	MEM_B_DQ<48>	MEM_B_DQ<48> ~ @m39.lib.M39	5A7 15B4 29A6	GB_OTP05	GB_OTP05 ~ @m39.lib.M39	16C2
LPC_FRAME_L	LPC_FRAME_L ~ @m39.lib.M39	21C5 58C7 60C6 67C6	MEM_A_DQ<34>	MEM_A_DQ<34> ~ @m39.lib.M39	15B7 28B6	MEM_B_DQ<49>	MEM_B_DQ<49> ~ @m39.lib.M39	15A4 29A6	POV48_SMC_LSREF	POV48_SMC_LSREF ~ @m39.lib.M39	59A6 59A8 59B7
LVDS_A_CLK_N	LVDS_A_CLK_N ~ @m39.lib.M39	13D5 19D2	MEM_A_DQ<35>	MEM_A_DQ<35> ~ @m39.lib.M39	15B7 28B6	MEM_B_DQ<50>	MEM_B_DQ<50> ~ @m39.lib.M39	15B4 29A6	PANEL_ID	PANEL_ID ~ @m39.lib.M39	6D6 94C2
LVDS_A_CLK_P	LVDS_A_CLK_P ~ @m39.lib.M39	19D1	MEM_A_DQ<36>	MEM_A_DQ<36> ~ @m39.lib.M39	15B7 28B3	MEM_B_DQ<51>	MEM_B_DQ<51> ~ @m39.lib.M39	15B4 29A6	PATA_FWR_EN_L	PATA_FWR_EN_L ~ @m39.lib.M39	23B3 23C3
LVDS_A_DATA_N<0>	LVDS_A_DATA_N<0> ~ @m39.lib.M39	13C5 19D2	MEM_A_DQ<37>	MEM_A_DQ<37> ~ @m39.lib.M39	15B7 28B3	MEM_B_DQ<52>	MEM_B_DQ<52> ~ @m39.lib.M39	15B4 29A3	PCIE_A_D2R_C_N	PCIE_A_D2R_C_N ~ @m39.lib.M39	41D5
LVDS_A_DATA_N<1>	LVDS_A_DATA_N<1> ~ @m39.lib.M39	19D1	MEM_A_DQ<38>	MEM_A_DQ<38> ~ @m39.lib.M39	15B7 28B3	MEM_B_DQ<53>	MEM_B_DQ<53> ~ @m39.lib.M39	15B4 29A3	PCIE_A_D2R_C_P	PCIE_A_D2R_C_P ~ @m39.lib.M39	41D5
LVDS_A_DATA_N<2>	LVDS_A_DATA_N<2> ~ @m39.lib.M39	13C5 19D2	MEM_A_DQ<39>	MEM_A_DQ<39> ~ @m39.lib.M39	5B7 15B7 28B3	MEM_B_DQ<54>	MEM_B_DQ<54> ~ @m39.lib.M39	15B4 29A3	PCIE_A_D2R_N	PCIE_A_D2R_N ~ @m39.lib.M39	5B8 22D4 41D4
LVDS_A_DATA_N<3>	LVDS_A_DATA_N<3> ~ @m39.lib.M39	13C5 19D2	MEM_A_DQ<40>	MEM_A_DQ<40> ~ @m39.lib.M39	15B7 28B6	MEM_B_DQ<55>	MEM_B_DQ<55> ~ @m39.lib.M39	15B4 29A3	PCIE_A_D2R_P	PCIE_A_D2R_P ~ @m39.lib.M39	5C8 22D4 41D4

	8	7	6	5	4	3	2	1									
	PCI_AD<3>	PCI_AD<3> - @m39_lib.M39	22B7 44D5	PEG_R2D_C_N<3>	PEG_R2D_C_N<3> - @m39_lib.M39	13B3 84B5	PP1V8_FB_A0_VDDA0	PP1V8_FB_A0_VDDA0 - @m39_lib.M39	89D7	SB_RTC_X2	SB_RTC_X2 - @m39_lib.M39	21D6 26D7	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	14B6 19A2		
	PCI_AD<4>	PCI_AD<4> - @m39_lib.M39	22B7 44D5	PEG_R2D_C_N<4>	PEG_R2D_C_N<4> - @m39_lib.M39	13B3 84B5	PP1V8_FB_A0_VDDA1	PP1V8_FB_A0_VDDA1 - @m39_lib.M39	89D7	SB_SM_INTRUDER_L	SB_SM_INTRUDER_L - @m39_lib.M39	21D6 26C7	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<5>	PCI_AD<5> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<5>	PEG_R2D_C_N<5> - @m39_lib.M39	13B3 84B5	PP1V8_FB_A1_VDDA0	PP1V8_FB_A1_VDDA0 - @m39_lib.M39	89D4	SB_SPKR	SB_SPKR - @m39_lib.M39	23C5	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	14B6 19A2		
	PCI_AD<6>	PCI_AD<6> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<6>	PEG_R2D_C_N<6> - @m39_lib.M39	13B3 84C5	PP1V8_FB_A1_VDDA1	PP1V8_FB_A1_VDDA1 - @m39_lib.M39	89D4	SC_RX_L	SC_RX_L - @m39_lib.M39	58C5 59B6 59C4	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	14B6 19A2		
	PCI_AD<7>	PCI_AD<7> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<7>	PEG_R2D_C_N<7> - @m39_lib.M39	13B3 84C5	PP1V8_FB_B0_VDDA0	PP1V8_FB_B0_VDDA0 - @m39_lib.M39	90D7	SC_TX_L	SC_TX_L - @m39_lib.M39	58C5 59B4 59B6	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	14B6 19A2		
	PCI_AD<8>	PCI_AD<8> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<8>	PEG_R2D_C_N<8> - @m39_lib.M39	13B3 84C5	PP1V8_FB_B0_VDDA1	PP1V8_FB_B0_VDDA1 - @m39_lib.M39	90D7	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	14B6 19A2	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<9>	PCI_AD<9> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<9>	PEG_R2D_C_N<9> - @m39_lib.M39	13B3 84C5	PP1V8_FB_B1_VDDA0	PP1V8_FB_B1_VDDA0 - @m39_lib.M39	90D4	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	14B6 19A2	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<10>	PCI_AD<10> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<10>	PEG_R2D_C_N<10> - @m39_lib.M39	13B3 84C5	PP1V8_FB_B1_VDDA1	PP1V8_FB_B1_VDDA1 - @m39_lib.M39	90D4	SDVO_CTRLCCLK	SDVO_CTRLCCLK - @m39_lib.M39	14B6 19A2	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<11>	PCI_AD<11> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<11>	PEG_R2D_C_N<11> - @m39_lib.M39	13B3 84C5	PP1V8_S3_PGOOD	PP1V8_S3_PGOOD - @m39_lib.M39	79B2	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<12>	PCI_AD<12> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<12>	PEG_R2D_C_N<12> - @m39_lib.M39	13B3 84D5	PP2V5_S0_PGOOD	PP2V5_S0_PGOOD - @m39_lib.M39	77B8 77C5	NC_SMC_ANALOG_ID	NC_SMC_ANALOG_ID - @m39_lib.M39	59C5	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<13>	PCI_AD<13> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<13>	PEG_R2D_C_N<13> - @m39_lib.M39	13B3 84D5	PP2V5_ENET_CTAP	PP2V5_ENET_CTAP - @m39_lib.M39	43D7	SMC_BSA_CLK	SMC_BSA_CLK - @m39_lib.M39	58B5 59C1	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<14>	PCI_AD<14> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<14>	PEG_R2D_C_N<14> - @m39_lib.M39	13B3 84D5	PP2V5_S0_GPU_A2VDD	PP2V5_S0_GPU_A2VDD - @m39_lib.M39	93B7	SMC_BSA_DATA	SMC_BSA_DATA - @m39_lib.M39	58B5 59C1	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<15>	PCI_AD<15> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_N<15>	PEG_R2D_C_N<15> - @m39_lib.M39	13B3 84D5	PP2V5_S0_GPU_AVDD	PP2V5_S0_GPU_AVDD - @m39_lib.M39	93C7	SMC_BSB_CLK	SMC_BSB_CLK - @m39_lib.M39	58C5 59D1	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<16>	PCI_AD<16> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_P<0>	PEG_R2D_C_P<0> - @m39_lib.M39	13B3 84B5	PP2V5_S0_GPU_LPVDV	PP2V5_S0_GPU_LPVDV - @m39_lib.M39	93B7	SMC_BSB_DATA	SMC_BSB_DATA - @m39_lib.M39	58C7 59D1	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<17>	PCI_AD<17> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_P<1>	PEG_R2D_C_P<1> - @m39_lib.M39	13B3 84B5	PP2V5_S0_GPU_LVDDR	PP2V5_S0_GPU_LVDDR - @m39_lib.M39	93B7	SMC_LINK_ALERT_L	SMC_LINK_ALERT_L - @m39_lib.M39	23D5	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<18>	PCI_AD<18> - @m39_lib.M39	22B7 44C5	PEG_R2D_C_P<2>	PEG_R2D_C_P<2> - @m39_lib.M39	13B3 84B5	PP2V5_S0_GPU_PVDD_F	PP2V5_S0_GPU_PVDD_F - @m39_lib.M39	91A6	SMC_ADAPTER_EN	SMC_ADAPTER_EN - @m39_lib.M39	58D5 59D5	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<19>	PCI_AD<19> - @m39_lib.M39	22A7 44C6	PEG_R2D_C_P<3>	PEG_R2D_C_P<3> - @m39_lib.M39	13B3 84B5	PP2V5_S0_GPU_TPVDD	PP2V5_S0_GPU_TPVDD - @m39_lib.M39	93C7	TP_SMC_ADAPTER_EN	TP_SMC_ADAPTER_EN - @m39_lib.M39	59D3	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<20>	PCI_AD<20> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<4>	PEG_R2D_C_P<4> - @m39_lib.M39	13B3 84B5	PP2V5_S0_GPU_TXVDDR	PP2V5_S0_GPU_TXVDDR - @m39_lib.M39	93C7	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	58A7 59C6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<21>	PCI_AD<21> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<5>	PEG_R2D_C_P<5> - @m39_lib.M39	13B3 84B5	PP2V5_S0_GPU_VDD1D1	PP2V5_S0_GPU_VDD1D1 - @m39_lib.M39	93C8	NC_SMC_ANALOG_ID	NC_SMC_ANALOG_ID - @m39_lib.M39	59C5	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<22>	PCI_AD<22> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<6>	PEG_R2D_C_P<6> - @m39_lib.M39	13B3 84C5	PP2V5_S0_GPU_VDD2D1	PP2V5_S0_GPU_VDD2D1 - @m39_lib.M39	93B8	SMC_BATT_CHG_EN	SMC_BATT_CHG_EN - @m39_lib.M39	58D7 59D6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<23>	PCI_AD<23> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<7>	PEG_R2D_C_P<7> - @m39_lib.M39	13B3 84C5	PP3V3R12V_LCD_CONN	PP3V3R12V_LCD_CONN - @m39_lib.M39	94A6 94A6 94A7 94C5	NC_SMC_BATT_CHG_EN	NC_SMC_BATT_CHG_EN - @m39_lib.M39	59D5	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<24>	PCI_AD<24> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<8>	PEG_R2D_C_P<8> - @m39_lib.M39	13B3 84C5	PP3V3_AUDIO_SPDIF_EM	PP3V3_AUDIO_SPDIF_EM - @m39_lib.M39	73B7	SMC_BATT_ISENSE	SMC_BATT_ISENSE - @m39_lib.M39	58D5 59B6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<25>	PCI_AD<25> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<9>	PEG_R2D_C_P<9> - @m39_lib.M39	13B3 84C5	I	PP3V3_AUDIO_SPDIF_JACK	PP3V3_AUDIO_SPDIF_JACK - @m39_lib.M39	73B5	UNUSED_SMC_SENSE	UNUSED_SMC_SENSE - @m39_lib.M39	58D5 59B6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1	
	PCI_AD<26>	PCI_AD<26> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<10>	PEG_R2D_C_P<10> - @m39_lib.M39	13B3 84C5	PP3V3_AUDIO_SPDIF_JA	PP3V3_AUDIO_SPDIF_JA - @m39_lib.M39	73B5	SMC_WFIRE_ISENSE	SMC_WFIRE_ISENSE - @m39_lib.M39	58D5 59B6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<27>	PCI_AD<27> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<11>	PEG_R2D_C_P<11> - @m39_lib.M39	13B3 84D5	CK	CK		UNUSED_SMC_SENSE	UNUSED_SMC_SENSE - @m39_lib.M39	58D5 59B6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<28>	PCI_AD<28> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<12>	PEG_R2D_C_P<12> - @m39_lib.M39	13A3 84D5	PP3V3_AVCC_SMC	PP3V3_AVCC_SMC - @m39_lib.M39	58D3	SMC_WFIRE_ISET	SMC_WFIRE_ISET - @m39_lib.M39	58D5 59B6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<29>	PCI_AD<29> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<13>	PEG_R2D_C_P<13> - @m39_lib.M39	13A3 84D5	PP3V3_AVRFP_SMC	PP3V3_AVRFP_SMC - @m39_lib.M39	58D2 59A3	SMC_BATT_ISET	SMC_BATT_ISET - @m39_lib.M39	58B5 59D6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<30>	PCI_AD<30> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<14>	PEG_R2D_C_P<14> - @m39_lib.M39	13A3 84D5	PP3V3_FW_RSD	PP3V3_FW_RSD - @m39_lib.M39	46A4 46A6 46B5 46C5 46D5	NC_SMC_BATT_ISET	NC_SMC_BATT_ISET - @m39_lib.M39	59D7 59D6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_AD<31>	PCI_AD<31> - @m39_lib.M39	22A7 44B5	PEG_R2D_C_P<15>	PEG_R2D_C_P<15> - @m39_lib.M39	13A3 84D5	PP3V3_FW_ESD_F	PP3V3_FW_ESD_F - @m39_lib.M39	46A7	SMC_BATT_TRICKLE_EN_L	SMC_BATT_TRICKLE_EN_L - @m39_lib.M39	58D7 59D6	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_CLK_FW	PCI_CLK_FW - @m39_lib.M39	34C4 44B5	PEG_R2D_N<0>	PEG_R2D_N<0> - @m39_lib.M39	84D4	PP3V3_INTERCON	PP3V3_INTERCON - @m39_lib.M39	72C7	NC_SMC_BATT_TRICKLE_EN_L	NC_SMC_BATT_TRICKLE_EN_L - @m39_lib.M39	59D5	TP_SDVO_CTRLCCLK	TP_SDVO_CTRLCCLK - @m39_lib.M39	19A1		
	PCI_CLK_PORT80	PCI_CLK_PORT80 - @m39_lib.M39	34B4 60C3	PEG_R2D_N<1													

D	SMLINK<0>	SMLINK<0> - @m39_lib.M39	23D5	TP_PCI_GNT4_L	TP_PCI_GNT4_L - @m39_lib.M39	22B6	U600_3	=PP1V5_S0_AIRPORT - @m39_lib.M39	6C4 53D3
	SMLINK<1>	SMLINK<1> - @m39_lib.M39	23D5	TP_PCI_PME_L	TP_PCI_PME_L - @m39_lib.M39	22A6	U600_6	U600_3 - @m39_lib.M39	6C7
	SMS_INT_L	SMS_INT_L - @m39_lib.M39	23C3 26C2 58B5	TP_SB_ACZ_SDIN1	TP_SB_ACZ_SDIN1 - @m39_lib.M39	21C6	U600_8	U600_6 - @m39_lib.M39	6B7
	SMS_ONOFF_L	SMS_ONOFF_L - @m39_lib.M39	58A5 59B4	TP_SB_ACZ_SDIN2	TP_SB_ACZ_SDIN2 - @m39_lib.M39	21C6	U600_11	U600_8 - @m39_lib.M39	6B7
	SMS_X_AXIS	SMS_X_AXIS - @m39_lib.M39	58B7 59B6	TP_SB_DRQ0_L	TP_SB_DRQ0_L - @m39_lib.M39	21D4	U2698_4	U600_11 - @m39_lib.M39	6A7
		NC_SMS_X_AXIS - @m39_lib.M39	59B5	TP_SB_GPI06	TP_SB_GPI06 - @m39_lib.M39	23C5	U2698_4	U2698_4 - @m39_lib.M39	26C4
	SMS_Y_AXIS	SMS_Y_AXIS - @m39_lib.M39	58B7 59B6	TP_SB_GPI023	TP_SB_GPI023 - @m39_lib.M39	21D5	U3100_VDDQ	U3100_VDDQ - @m39_lib.M39	31C5
		NC_SMS_Y_AXIS - @m39_lib.M39	59B5	TP_SB_GPI025_DO_NOT_USE	TP_SB_GPI025_DO_NOT_USE - @m39_lib.M39	23C3	U6100_VCC	U6100_VCC - @m39_lib.M39	61C4
	SMS_Z_AXIS	SMS_Z_AXIS - @m39_lib.M39	58A7 59B6	USB	@m39_lib.M39		U8595_1	U8595_1 - @m39_lib.M39	85D2
		NC_SMS_Z_AXIS - @m39_lib.M39	59B5	TP_SB_GPI038	TP_SB_GPI038 - @m39_lib.M39	23C3	USB_AIRPORT_N	USB_AIRPORT_N - @m39_lib.M39	53B4
C	SPARE_SRC3_N	SPARE_SRC3_N - @m39_lib.M39	34D2	TP_SB_RSVD9	TP_SB_RSVD9 - @m39_lib.M39	22A6	USB_AIRPORT_P	USB_AIRPORT_P - @m39_lib.M39	53B4
	SPARE_SRC3_P	SPARE_SRC3_P - @m39_lib.M39	34D2	TP_SB_SATALED_L	TP_SB_SATALED_L - @m39_lib.M39	21C6	USB_A_N	USB_A_N - @m39_lib.M39	22C2 47D7
	SPARE_SRC7_N	SPARE_SRC7_N - @m39_lib.M39	34D2	TP_SB_XOR_AD5	TP_SB_XOR_AD5 - @m39_lib.M39	22A7	USB_A_OC_L	USB_A_OC_L - @m39_lib.M39	22C4 22D8 47B8 47B8
	SPARE_SRC7_P	SPARE_SRC7_P - @m39_lib.M39	34D2	TP_SB_XOR_AD9	TP_SB_XOR_AD9 - @m39_lib.M39	22A7	USB_A_P	USB_A_P - @m39_lib.M39	22C2 47C7
	SPI_ARB	SPI_ARB - @m39_lib.M39	22C6 58D5	TP_SB_XOR_AE5	TP_SB_XOR_AE5 - @m39_lib.M39	22A7	USB_B_N	USB_B_N - @m39_lib.M39	22C2 53B2
	SPI_CE_L	SPI_CE_L - @m39_lib.M39	22C6 58B5 63C7	TP_SB_XOR_AE9	TP_SB_XOR_AE9 - @m39_lib.M39	22A6	USB_B_OC_L	USB_B_OC_L - @m39_lib.M39	22C4 22D8 47B8
	SPI_HOLD_L	SPI_HOLD_L - @m39_lib.M39	63C4	TP_SB_XOR_AG4	TP_SB_XOR_AG4 - @m39_lib.M39	22A7	USB_B_P	USB_B_P - @m39_lib.M39	22C2 53B2
	SPI_SCLK	SPI_SCLK - @m39_lib.M39	22C6 58D5 63C7	TP_SB_XOR_AG8	TP_SB_XOR_AG8 - @m39_lib.M39	22A6	USB_CAMERA_N	USB_CAMERA_N - @m39_lib.M39	47B2
	SPI_SCLK_R	SPI_SCLK_R - @m39_lib.M39	63C4	TP_SB_XOR_AH4	TP_SB_XOR_AH4 - @m39_lib.M39	22A7	USB_CAMERA_P	USB_CAMERA_P - @m39_lib.M39	47B2
	SPI_SI	SPI_SI - @m39_lib.M39	22C6 58D5 63C1	TP_SB_XOR_AH8	TP_SB_XOR_AH8 - @m39_lib.M39	22A6	USB_C_N	USB_C_N - @m39_lib.M39	22C2 47B7
B	SPI_SI_R	SPI_SI_R - @m39_lib.M39	63C3	TP_SB_XOR_T5	TP_SB_XOR_T5 - @m39_lib.M39	21C6	USB_C_OC_L	USB_C_OC_L - @m39_lib.M39	22C4 22D8 47B7 47B8
	SPI_SO	SPI_SO - @m39_lib.M39	22C6 58D5 63C1	TP_SB_XOR_U3	TP_SB_XOR_U3 - @m39_lib.M39	21C6	USB_C_P	USB_C_P - @m39_lib.M39	22C2 47B7
	SPI_SO_R	SPI_SO_R - @m39_lib.M39	63C3	TP_SB_XOR_U5	TP_SB_XOR_U5 - @m39_lib.M39	21C6	USB_D_N	USB_D_N - @m39_lib.M39	22C2 47B3
	SPI_WP_L	SPI_WP_L - @m39_lib.M39	63C4	TP_SB_XOR_U7	TP_SB_XOR_U7 - @m39_lib.M39	21C6	USB_D_OC_L	USB_D_OC_L - @m39_lib.M39	22C4 22D8 47B7
	SPKRAMP_MUTE	SPKRAMP_MUTE - @m39_lib.M39	72B5	TP_SB_XOR_V3	TP_SB_XOR_V3 - @m39_lib.M39	21C6	USB_D_P	USB_D_P - @m39_lib.M39	22C2 47B3
	SPKRAMP_SS	SPKRAMP_SS - @m39_lib.M39	72B4	TP_SB_XOR_V4	TP_SB_XOR_V4 - @m39_lib.M39	21C6	USB_E_N	USB_E_N - @m39_lib.M39	22C2 47A7
	SUS_CLK_SB	SUS_CLK_SB - @m39_lib.M39	23C3 59B5	TP_SB_XOR_V6	TP_SB_XOR_V6 - @m39_lib.M39	21C6	USB_E_OC_L	USB_E_OC_L - @m39_lib.M39	22C4 22D8 47B7
		SMC_SUS_CLK - @m39_lib.M39	58C5 59B6	TP_SB_XOR_V7	TP_SB_XOR_V7 - @m39_lib.M39	21C6	USB_E_P	USB_E_P - @m39_lib.M39	22C2 47A7
	SV_SET_UP	SV_SET_UP - @m39_lib.M39	23B6 23C3 60B3	TP_SB_XOR_W1	TP_SB_XOR_W1 - @m39_lib.M39	21C6	USB_F_N	USB_F_N - @m39_lib.M39	22C2 49C6
	SW_RST_BTN_L	SW_RST_BTN_L - @m39_lib.M39	5D1 26C6	TP_SB_XOR_W3	TP_SB_XOR_W3 - @m39_lib.M39	21C6	USB_FLASH_N	USB_FLASH_N - @m39_lib.M39	49C5
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	SYS_ONEWIRE	SYS_ONEWIRE - @m39_lib.M39	58B7 59B4	TP_U5999_P13	TP_U5999_P13 - @m39_lib.M39	59A5	USB_BT_N	USB_BT_N - @m39_lib.M39	47A3
	SYS_POWERFAIL_L	SYS_POWERFAIL_L - @m39_lib.M39	6D8 76D2	TP_U5999_P14	TP_U5999_P14 - @m39_lib.M39	59A5	USB_G_P	USB_G_P - @m39_lib.M39	22C2 47A3
	SYS_PWRUP_L	SYS_PWRUP_L - @m39_lib.M39	6C7	TP_U8400_AG14	TP_U8400_AG14 - @m39_lib.M39	91A5	USB_BT_P	USB_BT_P - @m39_lib.M39	4715
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	THRM_ALERT_L	THRM_ALERT_L - @m39_lib.M39	10D3	TP_U8950_J2	TP_U8950_J2 - @m39_lib.M39	89A4	USB_IR_N	USB_IR_N - @m39_lib.M39	47C2
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R7413	RES_402	m39[74B4]	R8001	RES_402	m39[80C7]	R8999	RES_402	m39[89A4]	U2603	SN74LVC1G04_SOT23-5	m39[26A7]	LF
R7414	RES_805	m39[74B7]	R8002	RES_1206	m39[80C4]	R9030	RES_402	m39[90C7]	U2698	MC74VHC1G08_SOT23-5-	m39[26C4]	LF
R7415	RES_805	m39[74B8]	R8003	RES_402	m39[80C3]	R9031	RES_402	m39[90C7]				
R7416	RES_805	m39[74C7]	R8004	RES_402	m39[80C5]	R9032	RES_402	m39[90C7]	U2699	MAX6816_SOT143	m39[26C5]	
R7417	RES_805	m39[74C8]	R8005	RES_402	m39[80D7]	R9033	RES_402	m39[90C7]	U3100	LREG_BD35333FVM_MSOP-	m39[31C5]	8
R7418	RES_402	m39[74B6]	R8007	RES_402	m39[80A4]	R9040	RES_402	m39[90B8]				
R7419	RES_402	m39[74B6]	R8010	RES_402	m39[80B2]	R9041	RES_402	m39[90A8]	U3301	CLK_GEN_CY284455_QFN	m39[33C5]	
R7420	RES_402	m39[74D4]	R8011	RES_402	m39[80B3]	R9042	RES_402	m39[90B7]	U4101	88E8053_QFN	m39[41D5]	
R7421	RES_402	m39[74D0]	R8012	RES_402	m39[80B8]	R9043	RES_402	m39[90B7]	U4102	REFR04M040C08_S08	m39[41D2]	
R7422	RES_402	m39[74D7]	R8040	RES_402	m39[80C5]	R9044	RES_402	m39[90B7]	U4400	FW12306_TOFP	m39[44D5]	
R7423	RES_402	m39[74B6]	R8092	RES_402	m39[80C7]	R9045	RES_402	m39[90B7]	U4700	SW1_TPS2024_S01	m39[47C8]	
R7424	RES_402	m39[74B6]	R8099	RES_402	m39[80C3]	R9046	RES_402	m39[90B7]	U5800	SMC_H8S2116_BGA	m39[58A8 58C3 58C6 58D6]	
R7425	RES_402	m39[74A5]	R8101	RES_402	m39[81C3]	R9047	RES_402	m39[90B7]	U5900	VDET_RNSVD_SOT23-5	m39[59D8]	
R7426	RES_402	m39[74A4]	R8102	RES_1206	m39[81C4]	R9048	RES_402	m39[90A7]	U5940	VREF_REF3133_SOT23-3	m39[59A4]	
R7427	RES_402	m39[74A4]	R8103	RES_402	m39[81C3]	R9049	RES_402	m39[90A7]	U5999	COMPARATOR_LMV339_TS	m39[59A6 59A6 59A8 59A8]	SOP
R7429	RES_805	m39[74C2]	R8104	RES_402	m39[81C5]	R9080	RES_402	m39[90C4]				
R7430	RES_402	m39[74C1]	R8105	RES_402	m39[81D7]	R9081	RES_402	m39[90C4]	U6100	MAX6695_UMAX	m39[61C4]	
R7431	RES_402	m39[74C2]	R8107	RES_402	m39[81A4]	R9082	RES_402	m39[90C4]	U6301	FLASH_SST25VF016B_SO	m39[63D3]	
R7432	RES_402	m39[74B1]	R8110	RES_402	m39[81B3]	R9083	RES_402	m39[90C4]				
R7500	RES_402	m39[75C2]	R8140	RES_402	m39[81C5]	R9090	RES_402	m39[90B5]	U6700	TPM_TSSOP	m39[67C5]	
R7501	RES_603	m39[75C2]	R8190	RES_402	m39[81C3]	R9091	RES_402	m39[90A4]	U6800	AUDIO_STAC9220_LQFP	m39[68D5]	
R7502	RES_1206	m39[75B2]	R8191	RES_402	m39[81C7]	R9092	RES_402	m39[90B4]	U7200	MAX9714_QFN-LF	m39[72C5]	
R7503	RES_1206	m39[75D2]	R8192	RES_402	m39[81C7]	R9093	RES_402	m39[90B4]	U7500	ISL6262_QFN	m39[75C6]	
R7504	RES_402	m39[75C1]	R8198	RES_402	m39[81A5]	R9094	RES_402	m39[90B4]	U7501	INA138_SOT23-5-LF	m39[75D7]	
R7505	RES_402	m39[75B2]	R8199	RES_402	m39[81A5]	R9095	RES_402	m39[90B4]	U7600	OPAMP_LMV2011_SOT23-	m39[76D3]	5
R7506	RES_603	m39[75B2]	R8300	RES_402	m39[83B4]	R9096	RES_402	m39[90B4]	U7700	LTC3411_MSOP-LF	m39[77D5]	
R7507	RES_402	m39[75B1]	R8301	RES_402	m39[83C5]	R9097	RES_402	m39[90B4]	U7710	MC74VHC1G08_SOT23-5-	m39[77C7]	LF
R7508	RES_402	m39[75B8]	R8302	RES_402	m39[83B5]	R9098	RES_402	m39[90A4]	U7711	MC74VHC1G08_SOT23-5-	m39[77B7]	LF
R7509	RES_402	m39[75B8]	R8303	RES_402	m39[83C4]	R9099	RES_402	m39[90A4]	U7712	MC74VHC1G08_SOT23-5-	m39[77B7]	LF
R7510	RES_402	m39[75B6]	R8495	RES_402	m39[84A2]	R9190	RES_402	m39[91D2]				
R7511	RES_402	m39[75B7]	R8496	RES_402	m39[84A2]	R9191	RES_402	m39[91D2]				
R7512	RES_402	m39[75D7]	R8497	RES_402	m39[84A2]	R9195	RES_402	m39[91A3]				
R7513	RES_402	m39[75B7]	R8502	RES_402	m39[85D6]	R9202	RES_402	m39[92C6]				
R7514	RES_402	m39[75B8]	R8503	RES_402	m39[85D7]	R9250	RES_402	m39[92C6]	U7750	LTC3412_TSSOP-LF	m39[77B5]	
R7515	RES_402	m39[75B4]	R8504	RES_402	m39[85D7]	R9350	RES_402	m39[93A8]	U7800	ISL6549_QFN	m39[78C6]	
R7516	RES_402	m39[75B4]	R8505	RES_402	m39[85C7]	R9351	RES_402	m39[93A8]	U7900	ISL6549_QFN	m39[79D6]	
R7517	RES_402	m39[75B5]	R8506	RES_402	m39[85C8]	R9370	RES_402	m39[93D1]	U7901	COMPARATOR_LM339A_SO	m39[79A5]	I-LF
R7518	RES_402	m39[75B5]	R8507	RES_402	m39[85D7]	R9371	RES_402	m39[93D1]				
R7519	RES_402	m39[75C7]	R8508	RES_402	m39[85C7]	R9372	RES_402	m39[93C1]	U7901	COMPARATOR_LM339A_SO	m39[80A4]	I-LF
R7520	RES_402	m39[75D7]	R8510	RES_402	m39[85C5]	R9373	RES_402	m39[93C1]				
R7521	RES_402	m39[75D7]	R8521	RES_402	m39[85C1]	R9390	RES_402	m39[93A1]	U7901	COMPARATOR_LM339A_SO	m39[81A5]	I-LF
R7522	RES_402	m39[75A5]	R8522	RES_402	m39[85C3]	R9391	RES_402	m39[93A1]				
R7523	RES_402	m39[75A5]	R8523	RES_402	m39[85C3]	R9400	RES_402	m39[94C8]	U7910	COMPARATOR_LM339A_SO	m39[79A3 79B3]	I-LF
R7526	THERMISTE_402	m39[75C8]	R8524	RES_402	m39[85B3]	R9401	RES_402	m39[94C7]				
R7527	RES_402	m39[75C8]	R8525	RES_402	m39[85B3]	R9410	RES_402	m39[94C6]	U7910	COMPARATOR_LM339A_SO	m39[80B2]	I-LF
R7528	RES_402	m39[75A5]	R8526	RES_402	m39[85B3]	R9411	RES_402	m39[94C6]				
R7529	RES_402	m39[75A5]	R8540	RES_402	m39[85C2]	R9450	RES_402	m39[94C2]	U7910	COMPARATOR_LM339A_SO	m39[81B3]	I-LF
R7530	RES_402	m39[75B4]	R8554	RES_402	m39[85B7]	R9470	RES_402	m39[94C7]				
R7531	THERMISTE_0603-LF	m39[75B4]	R8555	RES_402	m39[85B6]	R9472	RES_402	m39[94B3]	U8000	ISL6549_QFN	m39[80D6]	
R7590	RES_402	m39[75C7]	R8556	RES_402	m39[85B6]	R9473	RES_402	m39[94B2]	U8100	ISL6549_QFN	m39[81D6]	
R7591	RES_402	m39[75C7]	R8560	RES_402	m39[85A6]	R9474	RES_402	m39[94B2]	U8400	ATI_M56P_BGA	m39[84C8 84D4]	
R7592	RES_402	m39[75C7]	R8561	RES_402	m39[85A7]	R9475	RES_402	m39[94B1]	U8400	ATI_M56P_BGA	m39[86D4]	
R7593	RES_402	m39[75C7]	R8570	RES_402	m39[85A5]	R9490	RES_402	m39[94C6]	U8400	ATI_M56P_BGA	m39[87D2 87D6]	
R7594	RES_402	m39[75C7]	R8585	RES_603	m39[85A3]	R9491	RES_805	m39[94D6]	U8400	ATI_M56P_BGA	m39[91D4]	
R7595	RES_402	m39[75C7]	R8586	RES_402	m39[85A3]	R9700	RES_402	m39[97D7]	U8400	ATI_M56P_BGA	m39[93C4]	
R7596	RES_402	m39[75D7]	R8588	RES_402	m39[85C5]	R9701	RES_402	m39[97D8]	U8500	ISL6269_QFN	m39[85D6]	
R7597	RES_402	m39[76D6]	R8590	RES_402	m39[85C3]	R9702	RES_402	m39[97D7]	U8550	FAN2558_SOT23-6-LF	m39[85B7]	
R7598	RES_402	m39[76C7]	R8591	RES_402	m39[85D3]	R9703	RES_402	m39[97D7]	U8560	74LVC1G125LF_SOT23-5	m39[85A6]	
R7599	RES_2512-1	m39[76D7]	R8592	RES_402	m39[85D2]	R9704	RES_402	m39[97C7]	U8580	LT3483_TSOT23-6	m39[85A4]	
R7600	RES_402	m39[76D3]	R8593	RES_402	m39[85D3]	R9705	RES_402	m39[97C7]	U8595	OPAMP_LMV2011_SOT23-	m39[85D2]	5
R7602	RES_402	m39[76D3]	R8594	RES_402	m39[85D3]	R9706	RES_402	m39[97D8]	U8900	SGRAM_16MX32_GDDR3_1	m39[89D6 89B6]	36H_FPGA
R7603	RES_402	m39[76C3]	R8596	RES_402	m39[85D3]	R9707	RES_402	m39[97C8]				
R7604	RES_402	m39[76C3]	R8597	THERMISTE_0603-LF	m39[85D1]	R9708	RES_402	m39[97C7]	U8950	SGRAM_16MX32_GDDR3_1	m39[89D3 89B3]	36H_FPGA
R7605	RES_402	m39[76D4]	R8598	RES_402	m39[85D2]	R9709	RES_402	m39[97C7]				
R7606	RES_402	m39[76D4]	R8599	RES_1206	m39[85C4]	R9710	RES_402	m39[97D2]	U9000	SGRAM_16MX32_GDDR3_1	m39[90D6 90B6]	36_FPGA
R7607	RES_402	m39[76C3]	R8630	RES_603	m39[86C7]	R9711	RES_402	m39[97D2]				
R7612	RES_402	m39[76B2]	R8710	RES_402	m39[87B8]	R9712	RES_402	m39[97D2]	U9050	SGRAM_16MX32_GDDR3_1	m39[90D3 90B3]	36_FPGA
R7620	RES_402	m39[76D2]	R8711	RES_402	m39[87A8]	R9713	RES_402	m39[97C2]				
R7623	RES_402	m39[76D1]	R8712	RES_402	m39[87B7]	R9714	RES_402	m39[97C2]	U9470	MC74VHC1G08_SOT23-5-	m39[94B2]	LF
R7630	RES_402	m39[76C8]	R8713	RES_402	m39[87A7]	R9715	RES_402	m39[97C7]				
R7631	RES_402	m39[76C8]	R8720	RES_402	m39[87B4]	R9716	RES_402	m39[97C8]	U9750	74AHC1G32_SM-LF	m39[97A4]	
R7632	RES_402	m39[76C7]	R8721	RES_402	m39[87A4]	R9717	RES_402	m39[97C8]	U9751	74AHC1G32_SM-LF	m39[97A4]	
R7639	RES_402	m39[76B7]	R8722	RES_402	m39[87B4]	R9720	RES_402	m39[97D1]	VR6800	LM6G_TPS79501_SOT223	m39[68A4]	-6
R7640	RES_402	m39[76A7]	R8723	RES_402	m39[87A4]	R9721	RES_402	m39[97D1]				
R7641	RES_402	m39[76A7]	R8730	RES_402	m39[87A3]	R9722	RES_402	m39[97C2]				
R7642	RES_402	m39[76A6]	R8731	RES_402	m39[87A3]	R9740	RES_402	m39[97A8]	XC7200	MTGHOLE	m39[72B3]	
R7643	RES_1206	m39[76B5]	R8732	RES_402	m39[87A3]	R9741	RES_402	m39[97A8]	XW601	SHORT_SM	m39[6C2]	
R7644	RES_1206	m39[76B4]	R8733	RES_402	m39[87A1]	R9742	RES_402	m39[97A7]	XW602	SHORT_SM	m39[6C2]	
R7659	RES_402	m39[76D5]	R8800	RES_603	m39[88D7]	R9750	RES_402	m39[97A3]	XW604	SHORT_SM	m39[6A5]	
R7669	RES_402	m39[76D4]	R8801	RES_402	m39[88A7]	R9751	RES_402	m39[97A3]	XW605	SHORT_SM	m39[6A5]	
R7700	RES_402	m39[77D5]	R8802	RES_402	m39[88D4]	RP2300	RPAK4P_SM-LF	m39[23D5]	XW5800	SHORT_SM	m39[58B3]	
R7701	RES_402	m39[77C5]	R8803	RES_402	m39[88D4]	RP3000	RPAK4P_SM-LF	m39[30B4 30C4 30D4 30D4]	XW5900	SHORT_SM	m39[59B1]	
R7704	RES_402	m39[77C5]	R8804	RES_402	m39[88D4]	RP3001	RPAK4P_SM-LF	m39[30C4 30A4 30A4 30D4]	XW6800	SHORT_SM	m39[74A4]	
R7705	RES_402	m39[77C5]	R8805	RES_402	m39[88C4]	RP3002	RPAK4P_SM-LF	m39[30A4 30A4 30A4 30D4]	XW7201	SHORT_SM	m39[72B2]	
R7706	RES_402	m39[77C4]	R8806	RES_402	m39[88C4]	RP3003	RPAK4P_SM-LF	m39[30C4 30C4 30C4 30D4]	XW7301	SHORT_SM	m39[73C5]	
R7707	RES_402	m39[77C3]	R8807	RES_402	m39[88C4]	RP3004	RPAK4P_SM-LF	m39[30C4 30C4 30D4]	XW7400	SHORT_SM	m39[74D2]	
R7708	RES_402	m39[77C3]	R8808	RES_402	m39[88C4]	RP3005	RPAK4P_SM-LF	m39[30B4 30A4 30A4 30D4]	XW7500	SHORT_SM	m39[75A6]	
R7710	RES_402	m39[77D5]	R8809	RES_402	m39[88C4]	RP3006	RPAK4P_SM-LF	m39[30B4 30B4 30A4 30D4]	XW7501	SHORT_SM	m39[75B2]	
R7750	RES_402	m39[77A5]	R8810	RES_402	m39[88C4]	RP3007	RPAK4P_SM-LF	m39[30C4 30C4 30C4 30C4]	XW7502	SHORT_SM	m39[75B1]	
R7751	RES_402	m39[77A5]	R8811	RES_402	m39[88B4]	RP3008	RPAK4P_SM-LF	m39[30C4 30C4 30C4 30C4]	XW7503	SHORT_SM	m39[75D2]	
R7752	RES_402	m39[77A5]	R8812	RES_402	m39[88C4]	RP3009	RPAK4P_SM-LF	m39[30B4 30B4 30				

