

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

PAGE	DRI	PDF	CIRCUIT
53	JD	JD 43	PCI-E - AIRPORT MINI-PCIE CONN
54	JD	JD 44	PCI-E - UNUSED PORTS
58	MS	MS 45	SMC - H8S2116
59	MS	MS 46	SMC - SMB BUSES, MISC
60	MS	MS 47	SMC - LPC+ CONN
61	JH	JH 48	SMC - GPU/NB THERMAL SENSOR
63	MS	JD 49	SMC - SPI BOOTROM
65	MS	MS 50	SMC - FANS
66	MS	MS 51	SMC - FANS
67	JD	JD 52	SMC - TPM
68	PT	JD 53	AUDIO - CODEC,VREG,MIC BIAS
72	PT	JD 54	AUDIO - INTERNAL SPEAKER AMP
73	PT	JD 55	AUDIO - I/O CONN'S,EMC
74	PT	JD 56	AUDIO - DETECT TRANSLATORS
75	RT	RT 57	VR - CPU CORE
76	RT	RT 58	VR - CPU I-V SENSE CKT
77	RT	RT 59	VR - "S0" 1.2V & 2.5V (GRAFIX)
78	RT	RT 60	VR - "S0" 1.8V
79	RT	RT 61	VR - "S3" 1.8V
80	RT	RT 62	VR - "S0" 1.5V
81	RT	RT 63	VR - "S0" 1.05V
83	RT	RT 64	VR - "S3" 3.3V AND 5V
84	JH	JH 65	GPU - M56 PCI-E
85	JH	JH 66	GPU - VCORE SUPPLY
86	JH	JH 67	GPU - M56 CORE PWR
87	JH	JH 68	GPU - M56 FRAME BUFFER
88	JH	JH 69	GPU - MISC
89	JH	JH 70	GPU - GDDR SDRAM A
90	JH	JH 71	GPU - GDDR SDRAM B
91	JH	JH 72	GPU - M56 GPIO,DVO,MISC
92	JH	JH 73	GPU - M56 CLOCKS
93	JH	JH 74	GPU - M56 VIDEO INTERFACES
94	JH	JH 75	GPU - INTERNAL DISPLAY CONN'S
95	JH	JH 76	GPU - TP'S
96	JH	JH 77	GPU - TMDS,INVERTER,EXT VGA
97	JH	JH 78	GPU - EXTERNAL DISPLAY CONN'S

D

C

B

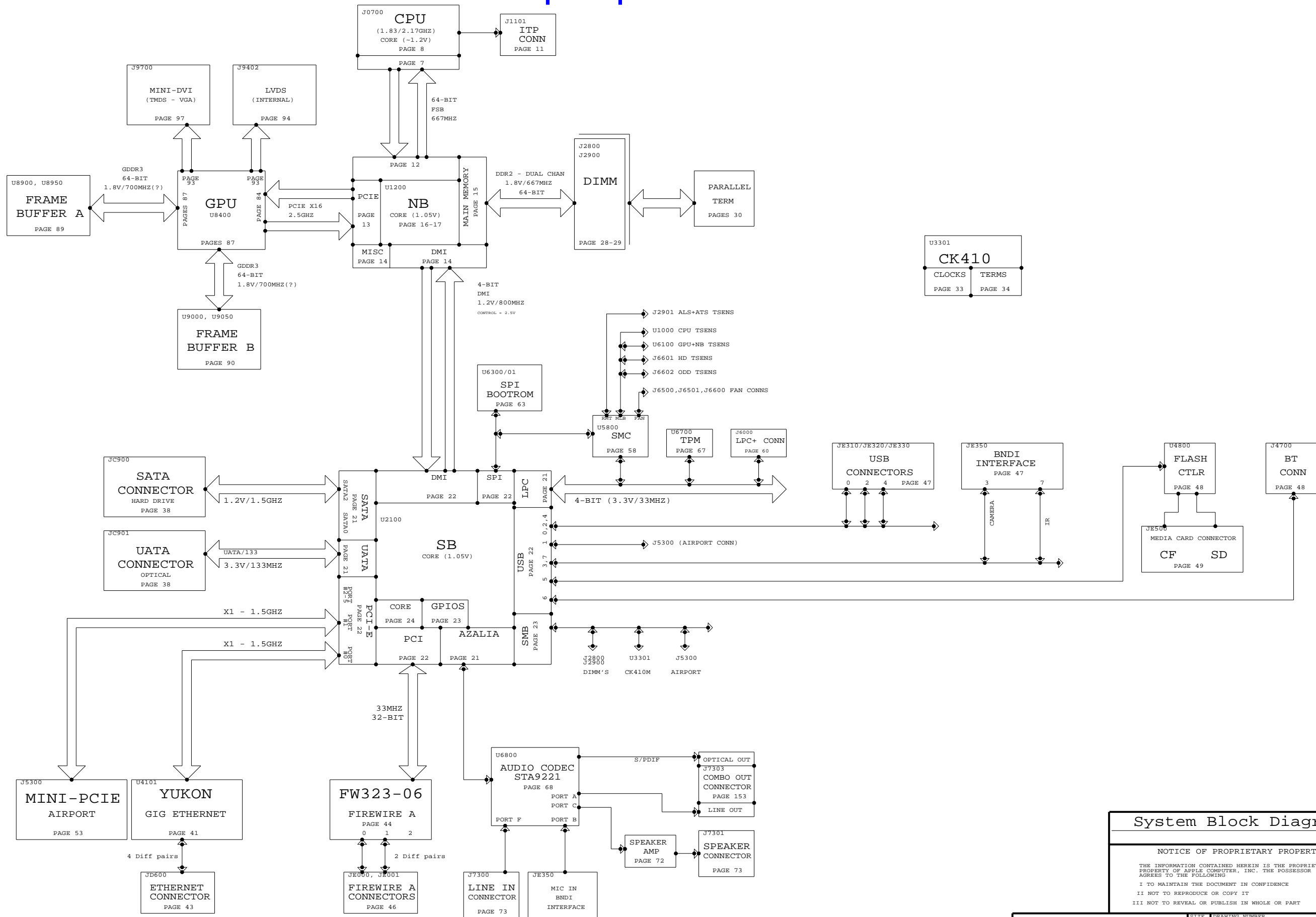
A

D

C

B

A



System Block Diagram

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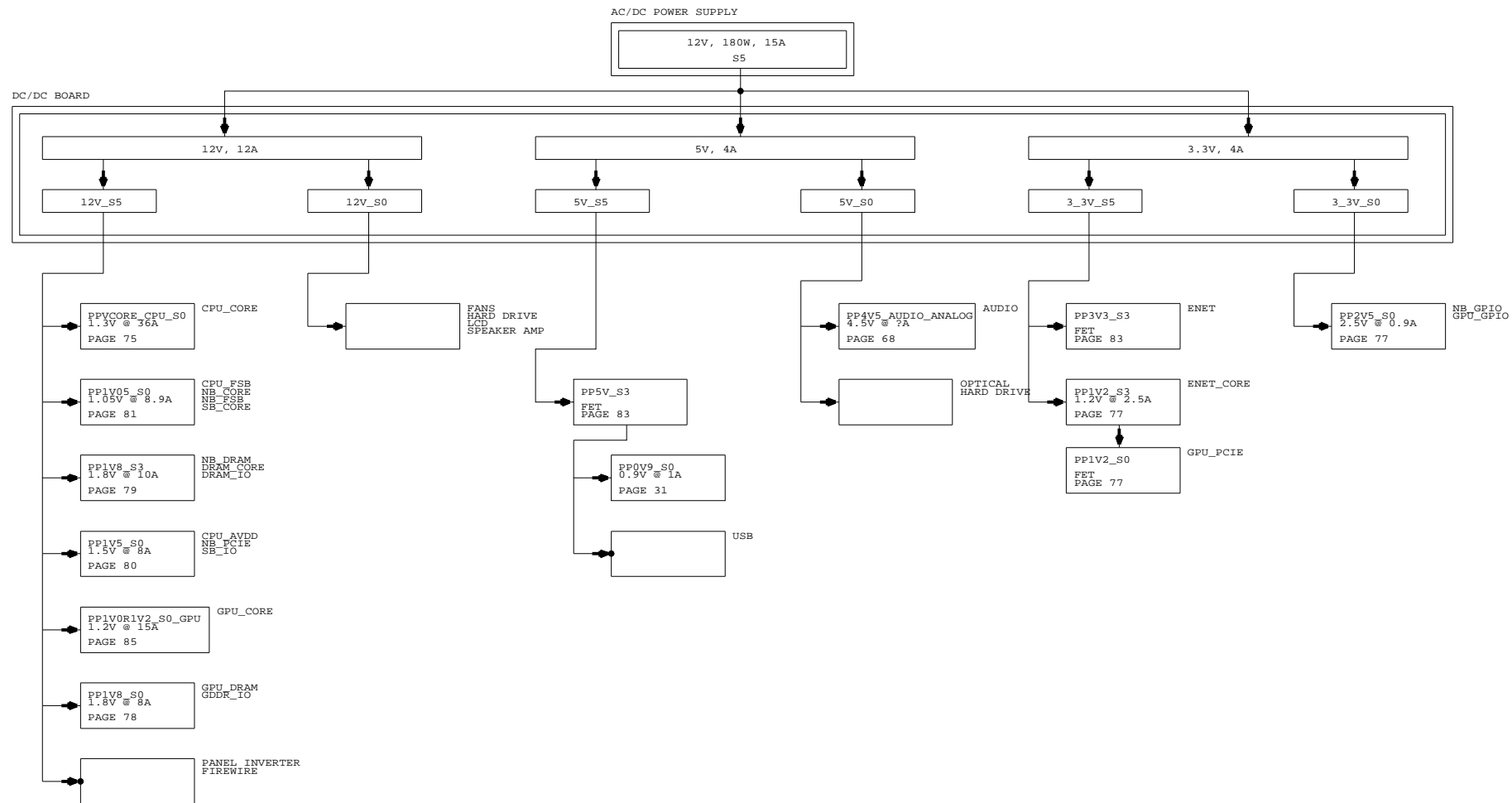
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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	2	111



Power Block Diagram

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APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHEET 3 OF 111

87654321

COMMON

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
511S0025	1	IC,CPU-SKT,479BGA	J0700	CRITICAL	
338S0269	1	IC,945GM,NORTHBRIDGE	U1200	CRITICAL	
343S0385	1	IC,SB,652BGA	U2100	CRITICAL	
742-0048	1	BAT,COIN,3V,220MAH,CR2032	BT2600	CRITICAL	
359S0101	1	IC,CY28445-5,CLK GEN,68PIN QFP	U3301	CRITICAL	
338S0270	1	IC,88E053,GIGABIT ENET XCVR,64P QFN MD	U4101	CRITICAL	
341S1797	1	IC,ENET LAN ROM	U4102	CRITICAL	
338S0279	1	IC,FW32306,1394A LINK,TQFP	U4400	CRITICAL	
338S0274	1	IC,SMC,H58/2116,BLANK	U5800	CRITICAL	
341S1789	1	IC,TPM,TSSOP,28P	U6700	CRITICAL	LEMENU
353S1235	1	IC,CPU VREG,IMVP,TWO PHASE	U7500	CRITICAL	
338S0266	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	ATI_B24
338S0305	1	IC,ATI,M56P,GRAFIX CTLR,880BGA,LF	U8400	CRITICAL	ATI_A24
128S0078	3	CAP,EL,AL,330UF,20%,16V,10X12.7MM,SMD,LF	C7517,C7518,C7910	CRITICAL	

(335S0382)

M38

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6949	1	PCB,SCHEM,MLB,M38	SCH1		17_INCH_LCD
820-1919	1	PCB,FAB,MLB,M38	MLB1		17_INCH_LCD
341T0003	1	EFI ROM,M38	U6301	CRITICAL	17_INCH_LCD
337S3241	1	M38/M39 LOW-SPEED CPU (QINY)	CPU	CRITICAL	CPU_M38
337S3242	1	M00-SPEED CPU (QINZ)	CPU	CRITICAL	CPU_M00

(335S0384)

M39

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-6950	1	PCB,SCHEM,MLB,M39	SCH1		20_INCH_LCD
820-1888	1	PCB,FAB,MLB,M39	MLB1		20_INCH_LCD
341T0004	1	EFI ROM,M39	U6301	CRITICAL	20_INCH_LCD
337S3243	1	M39 HI-SPEED CPU (QHJG)	CPU	CRITICAL	CPU_M39

(335S0384)

M38 / M39

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0354	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_SAMSUNG
333S0358	4	IC,SGRAM,GDDR3,8MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_128M_HYNIX

M39 - CTO

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
333S0350	4	IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_SAMSUNG
333S0351	4	IC,SGRAM,GDDR3,16MX32,700MHZ,136FBGA	U8900,U8950,U9000,U9050	CRITICAL	ATI_FB_256M_HYNIX

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
126S0096	126S0076		C7801	SANYO W16CE680EX 680UF 16V LF
126S0086	126S0078		C699,C940,C1900,C1901,C1968	SANYO W6CE330FS 330UF 6.3V LF
128S0080	128S0078		C7517,C7518,C7910	SANYO 168VP330W 330UF 16V SMD LF
197S0177	197S0020		Y4101	XTAL,25MHZ,50PPM,16PF,3.2X2.5 SMD,LF
338S0302	338S0266		U8400	IC,ATI,M36D,GRAFIX CTLR,88FBGA,LF

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Table Items

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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	4	111

LAYOUT NOTE: PLACE NEAR J0700

7 5

FSB A L<6>

PP600

OMIT P4MM

7 5

FSB ADSTB L<0>

PP601

OMIT P4MM

7 5

FSB A L<27>

PP602

OMIT P4MM

7 5

FSB ADSTB L<1>

PP603

OMIT P4MM

7 5

FSB D L<0>

PP604

OMIT P4MM

7 5

FSB DSTBN L<0>

PP605

OMIT P4MM

7 5

FSB DSTBP L<0>

PP606

OMIT P4MM

7 5

FSB DINV L<0>

PP607

OMIT P4MM

7 5

FSB D L<16>

PP608

OMIT P4MM

7 5

FSB DSTBN L<1>

PP609

OMIT P4MM

7 5

FSB DSTBP L<1>

PP610

OMIT P4MM

7 5

FSB DINV L<1>

PP611

OMIT P4MM

7 5

FSB D L<41>

PP612

OMIT P4MM

7 5

FSB DSTBN L<2>

PP613

OMIT P4MM

7 5

FSB DSTBP L<2>

PP614

OMIT P4MM

7 5

FSB DINV L<2>

PP615

OMIT P4MM

7 5

FSB D L<59>

PP616

OMIT P4MM

7 5

FSB DSTBN L<3>

PP617

OMIT P4MM

7 5

FSB DSTBP L<3>

PP618

OMIT P4MM

7 5

FSB DINV L<3>

PP619

OMIT P4MM

7 5

FSB LOCK L

PP620

OMIT P4MM

12 11 7

FSB CPURST L

PP621

OMIT P4MM

21 7

CPU INIT L

PP622

OMIT P4MM

21 7

CPU A20M L

PP623

OMIT P4MM

21 7

CPU IGNNE L

PP624

OMIT P4MM

21 7

CPU STPLCK L

PP625

OMIT P4MM

21 7

CPU INTR

PP626

OMIT P4MM

21 7

CPU NMI

PP627

OMIT P4MM

21 7

CPU SMI L

PP628

OMIT P4MM

34 7

FSB CLK CPU P

PP629

OMIT P4MM

34 7

FSB CLK CPU N

PP630

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U1200

7 5

FSB A L<6>

PP631

OMIT P4MM

7 5

FSB ADSTB L<0>

PP632

OMIT P4MM

7 5

FSB A L<27>

PP633

OMIT P4MM

7 5

FSB ADSTB L<1>

PP634

OMIT P4MM

7 5

FSB D L<0>

PP635

OMIT P4MM

7 5

FSB DSTBN L<0>

PP636

OMIT P4MM

7 5

FSB DSTBP L<0>

PP637

OMIT P4MM

7 5

FSB DINV L<0>

PP638

OMIT P4MM

7 5

FSB D L<16>

PP639

OMIT P4MM

7 5

FSB DSTBN L<1>

PP640

OMIT P4MM

7 5

FSB DSTBP L<1>

PP641

OMIT P4MM

7 5

FSB DINV L<1>

PP642

OMIT P4MM

7 5

FSB D L<41>

PP643

OMIT P4MM

7 5

FSB DSTBN L<2>

PP644

OMIT P4MM

7 5

FSB DSTBP L<2>

PP645

OMIT P4MM

7 5

FSB DINV L<2>

PP646

OMIT P4MM

7 5

FSB D L<59>

PP647

OMIT P4MM

7 5

FSB DSTBN L<3>

PP648

OMIT P4MM

7 5

FSB DSTBP L<3>

PP649

OMIT P4MM

7 5

FSB DINV L<3>

PP650

OMIT P4MM

7 5

FSB LOCK L

PP651

OMIT P4MM

7 5

FSB HIT L

PP652

OMIT P4MM

7 5

FSB HITM L

PP653

OMIT P4MM

7 5

FSB BNR L

PP654

OMIT P4MM

7 5

FSB BREQ0 L

PP655

OMIT P4MM

7 5

FSB DBSY L

PP656

OMIT P4MM

7 5

FSB DPNR L

PP657

OMIT P4MM

7 5

FSB REQ L<0>

PP658

OMIT P4MM

7 5

FSB REQ L<1>

PP659

OMIT P4MM

7 5

FSB REQ L<2>

PP660

OMIT P4MM

7 5

FSB REQ L<3>

PP661

OMIT P4MM

7 5

FSB REQ L<4>

PP662

OMIT P4MM

34 12

FSB CLK NB P

PP663

OMIT P4MM

34 12

FSB CLK NB N

PP664

OMIT P4MM

75 26 14

VR PWRGOOD_DELAY

PP665

OMIT P4MM

14

NB_RST_IN_L_R

PP666

OMIT P4MM

34 14

NB_CLK100M_GCLKIN_P

PP667

OMIT P4MM

34 14

NB_CLK100M_GCLKIN_N

PP668

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U8400

89 87 5

FB A DQ<0>

PP8700

OMIT P4MM

89 87 5

FB A DQ<8>

PP8701

OMIT P4MM

89 87 5

FB A DQ<16>

PP8702

OMIT P4MM

89 87 5

FB A DQ<24>

PP8703

OMIT P4MM

89 87 5

FB A DQ<32>

PP8704

OMIT P4MM

89 87 5

FB A DQ<40>

PP8705

OMIT P4MM

89 87 5

FB A DQ<48>

PP8706

OMIT P4MM

89 87 5

FB A DQ<56>

PP8707

OMIT P4MM

89 87 5

FB A MA<3>

PP8708

OMIT P4MM

89 87 5

FB A RDQS<0>

PP8709

OMIT P4MM

89 87 5

FB A RDQS<1>

PP8710

OMIT P4MM

89 87 5

FB A RDQS<2>

PP8711

OMIT P4MM

89 87 5

FB A RDQS<3>

PP8712

OMIT P4MM

89 87 5

FB A RDQS<4>

PP8713

OMIT P4MM

89 87 5

FB A RDQS<5>

PP8714

OMIT P4MM

89 87 5

FB A RDQS<6>

PP8715

OMIT P4MM

89 87 5

FB A RDQS<7>

PP8716

OMIT P4MM

89 87 5

FB B DQ<0>

PP8720

OMIT P4MM

89 87 5

FB B DQ<8>

PP8721

OMIT P4MM

89 87 5

FB B DQ<16>

PP8722

OMIT P4MM

89 87 5

FB B DQ<24>

PP8723

OMIT P4MM

89 87 5

FB B DQ<32>

PP8724

OMIT P4MM

89 87 5

FB B DQ<40>

PP8725

OMIT P4MM

89 87 5

FB B DQ<48>

PP8726

OMIT P4MM

89 87 5

FB B DQ<56>

PP8727

OMIT P4MM

89 87 5

FB B MA<3>

PP8728

OMIT P4MM

89 87 5

FB B RDQS<0>

PP8729

OMIT P4MM

89 87 5

FB B RDQS<1>

PP8730

OMIT P4MM

89 87 5

FB B RDQS<2>

PP8731

OMIT P4MM

89 87 5

FB B RDQS<3>

PP8732

OMIT P4MM

89 87 5

FB B RDQS<4>

PP8733

OMIT P4MM

89 87 5

FB B RDQS<5>

PP8734

OMIT P4MM

89 87 5

FB B RDQS<6>

PP8735

OMIT P4MM

89 87 5

FB B RDQS<7>

PP8736

OMIT P4MM

84 34

GPU_CLK100M_PCIE_P

PP8400

OMIT P4MM

84 34

GPU_CLK100M_PCIE_N

PP8401

OMIT P4MM

LAYOUT NOTE: PLACE NEAR U4101

41 34

ENET_CLK100M_PCIE_P

PP4100

OMIT P4MM

41 34

ENET_CLK100M_PCIE_N

PP4101

OMIT P4MM

PLACE NEAR R1210 AND R1211

12

NB_FSB_VREF

PP1200

OMIT P4MM

19 12 6

PP1V05_S0_FSB_NB

PP1201

OMIT P4MM

12

NB_FSB_VREF

PP1202

OMIT P4MM

PLACE NEAR R0705 AND R0706

59 11 9 8 7 6

PP1V05_S0_CPU

PP700

OMIT P4MM

7

CPU_GTLREF

PP701

OMIT P4MM

7

CPU_GTLREF

PP702

OMIT P4MM

PLACE NEAR R2800 AND R2801

29 28 6

PP1V8_S3_MEM

PP2800

OMIT P4MM

29 28

MEM_VREF

PP2801

OMIT P4MM

29 28

MEM_VREF

PP2802

OMIT P4MM

MISC GROUND VIAS

ZH500

HOLE-VIA

1

ZH501

HOLE-VIA

1

ZH502

HOLE-VIA

1

ZH503

HOLE-VIA

1

ZH504

HOLE-VIA

1

ZH505

HOLE-VIA

1

ZH506

HOLE-VIA

1

ZH507

HOLE-VIA

1

ZH508

HOLE-VIA

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ZH526

HOLE-VIA

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ZH527

HOLE-VIA

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ZH528

HOLE-VIA

1

ZH529

HOLE-VIA

1

LAYOUT NOTE: PLACE NEAR U2100

34 21

SB_CLK100M_SATA_P

PP6C4

OMIT P4MM

34 21

SB_CLK100M_SATA_N

PP6C5

OMIT P4MM

38 21

IDE_PDIO_R_L

PP6C6

OMIT P4MM

38 21

IDE_PDIO_RDY

PP6C7

OMIT P4MM

38 21

IDE_PDD<9>

PP6C8

OMIT P4MM

34 22

PCI_CLK_SB

PP6D0

OMIT P4MM

41 22

PCIE_A_D2R_P

PP6D1

OMIT P4MM

41 22

PCIE_A_D2R_N

PP6D2

OMIT P4MM

53 22

PCIE_B_D2R_P

PP5E1

OMIT P4MM

53 22

PCIE_B_D2R_N

PP5E2

OMIT P4MM

22 14

DMI_N2S_P<0>

PP6D3

OMIT P4MM

22 14

DMI_N2S_N<0>

PP6D4

OMIT P4MM

34 22

SB_CLK100M_DMI_P

PP6D5

OMIT P4MM

34 22

SB_CLK100M_DMI_N

PP6D6

OMIT P4MM

58 26 23

PM_SYSRST_L

PP6D7

OMIT P4MM

58 26 23

PM_CLKRUN_L

PP6D8

OMIT P4MM

34 23

SB_CLK14P3M_TIMER

PP6D9

OMIT P4MM

34 23

SB_CLK48M_USBCCLR

PP6E0

OMIT P4MM

22 14

DMI_S2N_N<0>

PP673

OMIT P4MM

22 14

DMI_S2N_P<0>

PP674

OMIT P4MM

19 14

MEM_VREF_NB_0

PP6E1

OMIT P4MM

19 14

MEM_VREF_NB_1

PP675

OMIT P4MM

28 15

MEM_A_DQ<7>

PP676

OMIT P4MM

28 15

MEM_A_DQ<14>

PP677

OMIT P4MM

28 15

MEM_A_DQ<16>

PP678

OMIT P4MM

28 15

MEM_A_DQ<25>

PP679

OMIT P4MM

28 15

MEM_A_DQ<39>

PP680

OMIT P4MM

28 15

MEM_A_DQ<47>

PP681

OMIT P4MM

28 15

MEM_A_DQ<54>

PP682

OMIT P4MM

28 15

MEM_A_DQ<59>

PP683

OMIT P4MM

28 15

MEM_A_DQS_P<0>

PP684

OMIT P4MM

28 15

MEM_A_DQS_N<0>

PP685

OMIT P4MM

28 15

MEM_A_DQS_P<1>

PP686

OMIT P4MM

28 15

MEM_A_DQS_P<2>

PP687

OMIT P4MM

28 15

MEM_A_DQS_N<2>

PP688

OMIT P4MM

28 15

MEM_A_DQS_N<3>

PP689

OMIT P4MM

28 15

MEM_A_DQS_P<3>

PP690

OMIT P4MM

28 15

MEM_A_DQS_N<3>

PP691

OMIT P4MM

28 15

MEM_A_DQS_P<4>

PP692

OMIT P4MM

28 15

MEM_A_DQS_N<4>

PP693

OMIT P4MM

28 15

MEM_A_DQS_P<5>

PP694

OMIT P4MM

28 15

MEM_A_DQS_N<5>

PP695

OMIT P4MM

28 15

MEM_A_DQS_P<6>

PP696

OMIT P4MM

28 15

MEM_A_DQS_N<6>

PP697

OMIT P4MM

28 15

MEM_A_DQS_P<7>

PP698

OMIT P4MM

28 15

MEM_A_DQS_N<7>

PP699

OMIT P4MM

29 16

MEM_B_DQ<6>

PP6A0

OMIT P4MM

29 16

MEM_B_DQ<8>

PP6A1

OMIT P4MM

29 16

MEM_B_DQ<23>

PP6A2

OMIT P4MM

29 16

MEM_B_DQ<25>

PP6A3

OMIT P4MM

29 16

MEM_B_DQ<38>

PP6A4

OMIT P4MM

29 16

MEM_B_DQ<44>

PP6A5

OMIT P4MM

29 16

MEM_B_DQ<48>

PP6A6

OMIT P4MM

29 16

MEM_B_DQ<62>

PP6A7

OMIT P4MM

29 16

MEM_B_DQS_P<0>

PP6A8

OMIT P4MM

29 16

MEM_B_DQS_N<0>

PP6A9

OMIT P4MM

29 16

MEM_B_DQS_P<1>

PP6B0

OMIT P4MM

29 16

MEM_B_DQS_N<1>

PP6B1

OMIT P4MM

29 16

MEM_B_DQS_P<2>

PP6B2

OMIT P4MM

29 16

MEM_B_DQS_N<2>

PP6B3

OMIT P4MM

29 16

MEM_B_DQS_P<3>

PP6B4

OMIT P4MM

29 16

MEM_B_DQS_N<3>

PP6B5

OMIT P4MM

29 16

MEM_B_DQS_P<4>

PP6B6

OMIT P4MM

29 16

MEM_B_DQS_N<4>

PP6B7

OMIT P4MM

29 16

MEM_B_DQS_P<5>

PP6B8

OMIT P4MM

29 16

MEM_B_DQS_N<5>

PP6B9

OMIT P4MM

29 16

MEM_B_DQS_P<6>

PP6C0

OMIT P4MM

29 16

MEM_B_DQS_N<6>

PP6C1

OMIT P4MM

29 16

MEM_B_DQS_P<7>

PP6C2

OMIT P4MM

29 16

MEM_B_DQS_N<7>

PP6C3

OMIT P4MM

PLACE NEAR U8900

89 87

FB A_CKE<0>

PP8900

OMIT P4MM

89 87

FB A_CLK_P<0>

PP8901

OMIT P4MM

89 87

FB A_CLK_N<0>

PP8902

OMIT P4MM

89 87

FB A_CS_L<0>

PP8903

OMIT P4MM

89 87

FB A_WE_L<0>

PP8904

OMIT P4MM

89 87

FB A_CAS_L<0>

PP8905

OMIT P4MM

89 87

FB A_MA<3>

PP8906

OMIT P4MM

89 87

FB A_RAS_L<0>

PP8907

OMIT P4MM

90 89 88 5

DRAM_RST

PP8908

OMIT P4MM

89 87

FB B_WDQS<0>

PP8909

OMIT P4MM

89 87

FB B_WDQS<1>

PP8910

OMIT P4MM

89 87

FB A_WDQS<3>

PP8911

OMIT P4MM

89 87

FB A_WDQS<2>

PP8912

OMIT P4MM

89 87

FB A_DQ<0>

PP8913

OMIT P4MM

89 87

FB A_DQ<8>

PP8914

OMIT P4MM

89 87

FB A_DQ<16>

PP8915

OMIT P4MM

89 87

FB A_DQ<24>

PP8916

OMIT P4MM

PLACE NEAR U9000

90 87

FB B_CKE<0>

PP9000

OMIT P4MM

90 87

FB B_CLK_P<0>

PP9001

OMIT P4MM

90 87

FB B_CLK_N<0>

PP9002

OMIT P4MM

90 87

FB B_CS_L<0>

PP9003

OMIT P4MM

90 87

FB B_WE_L<0>

PP9004

OMIT P4MM

90 87

FB B_CAS_L<0>

PP9005

OMIT P4MM

90 87

FB B_RAS_L<0>

PP9006

OMIT P4MM

90 87 5

FB B_MA<3>

PP9007

OMIT P4MM

90 89 88 5

DRAM_RST

PP9008

OMIT P4MM

90 87

FB B_WDQS<0>

PP9009

OMIT P4MM

90 87

FB B_WDQS<1>

PP9010

OMIT P4MM

90 87

FB B_WDQS<3>

PP9011

OMIT P4MM

90 87

FB B_WDQS<2>

PP9012

OMIT P4MM

90 87 5

FB B_DQ<0>

PP9013

OMIT P4MM

90 87 5

FB B_DQ<8>

PP9014

OMIT P4MM

90 87 5

FB B_DQ<16>

PP9015

OMIT P4MM

90 87 5

FB B_DQ<24>

PP9016

OMIT P4MM

PLACE NEAR U8950

89 87

FB A_CKE<1>

PP8920

OMIT P4MM

89 87

FB A_CLK_P<1>

PP8921

OMIT P4MM

89 87

FB A_CLK_N<1>

PP8922

OMIT P4MM

89 87

FB A_CS_L<1>

PP8923

OMIT P4MM

89 87

FB A_WE_L<1>

PP8924

OMIT P4MM

89 87

FB A_CAS_L<1>

PP8925

OMIT P4MM

89 87

FB A_RAS_L<1>

PP8926

OMIT P4MM

89 87 5

FB A_MA<3>

PP8927

OMIT P4MM

90 89 88 5

DRAM_RST

PP8928

OMIT P4MM

89 87

FB B_WDQS<5>

PP8929

OMIT P4MM

89 87

FB A_WDQS<6>

PP8930

OMIT P4MM

89 87

FB B_WDQS<4>

PP8931

OMIT P4MM

89 87

FB A_WDQS<7>

PP8932

OMIT P4MM

89 87 5

FB B_DQ<32>

PP8933

OMIT P4MM

89 87 5

FB A_DQ<40>

PP8934

OMIT P4MM

89 87 5

FB B_DQ<48>

PP8935

OMIT P4MM

89 87 5

FB A_DQ<56>

PP8936

OMIT P4MM

PLACE NEAR U9050

90 87

FB B_CKE<1>

PP9020

OMIT P4MM

90 87

FB B_CLK_P<1>

PP9021

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FB B_CLK_N<1>

PP9022

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90 87

FB B_CS_L<1>

PP9023

OMIT P4MM

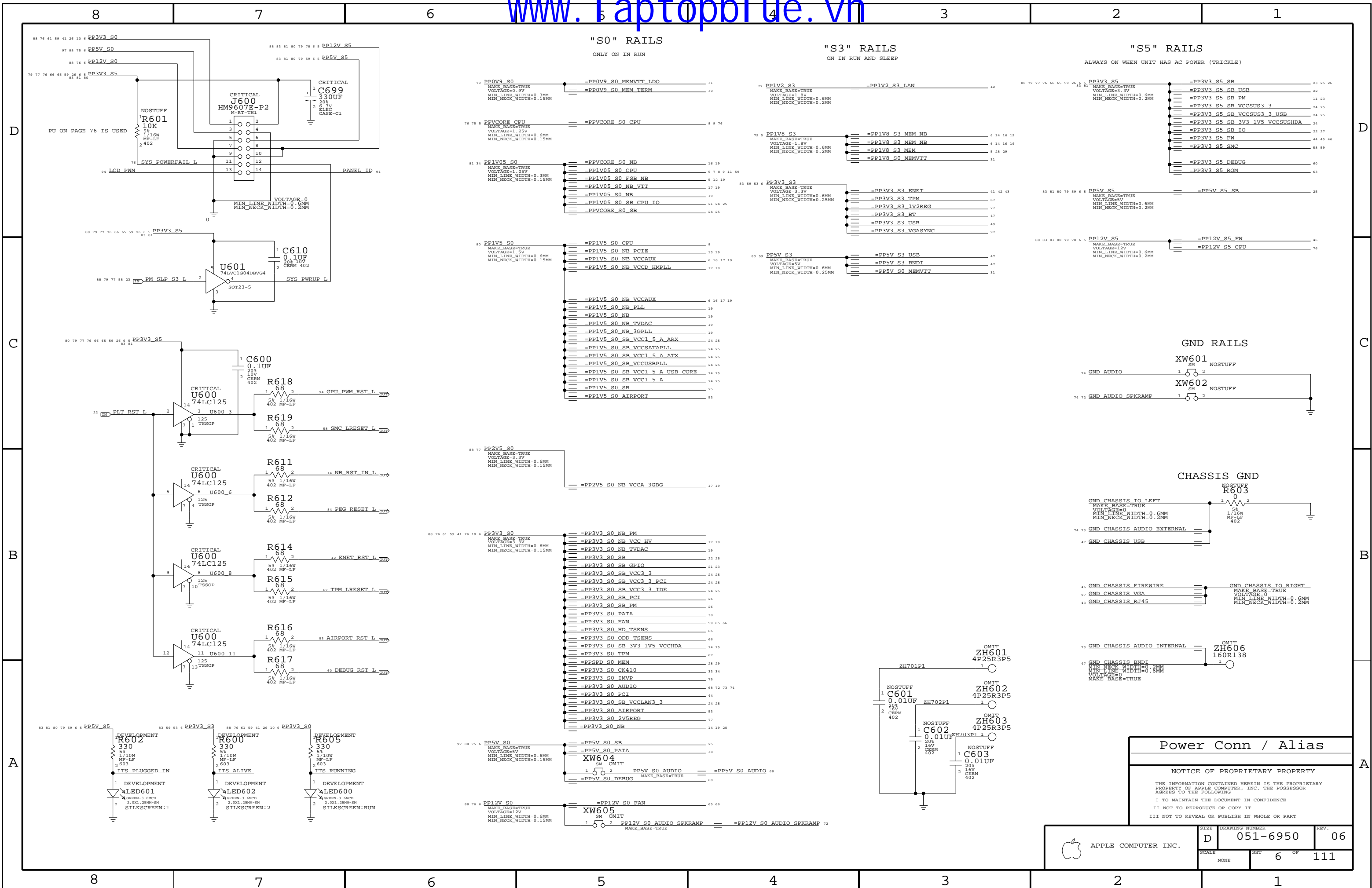
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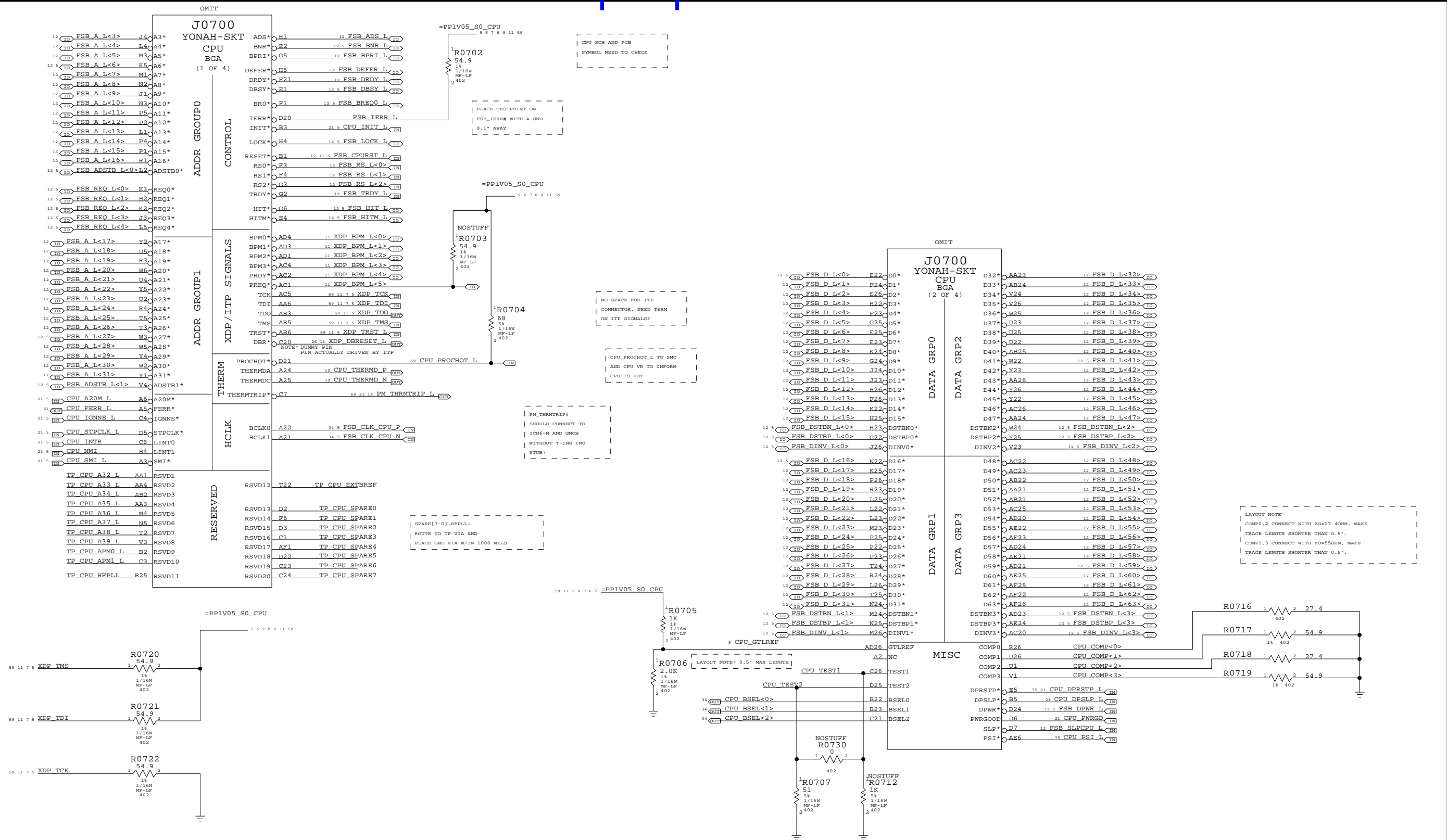
FB B_WE_L<1>

PP9024

OMIT P4MM

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CPU 1 OF 2-FSB

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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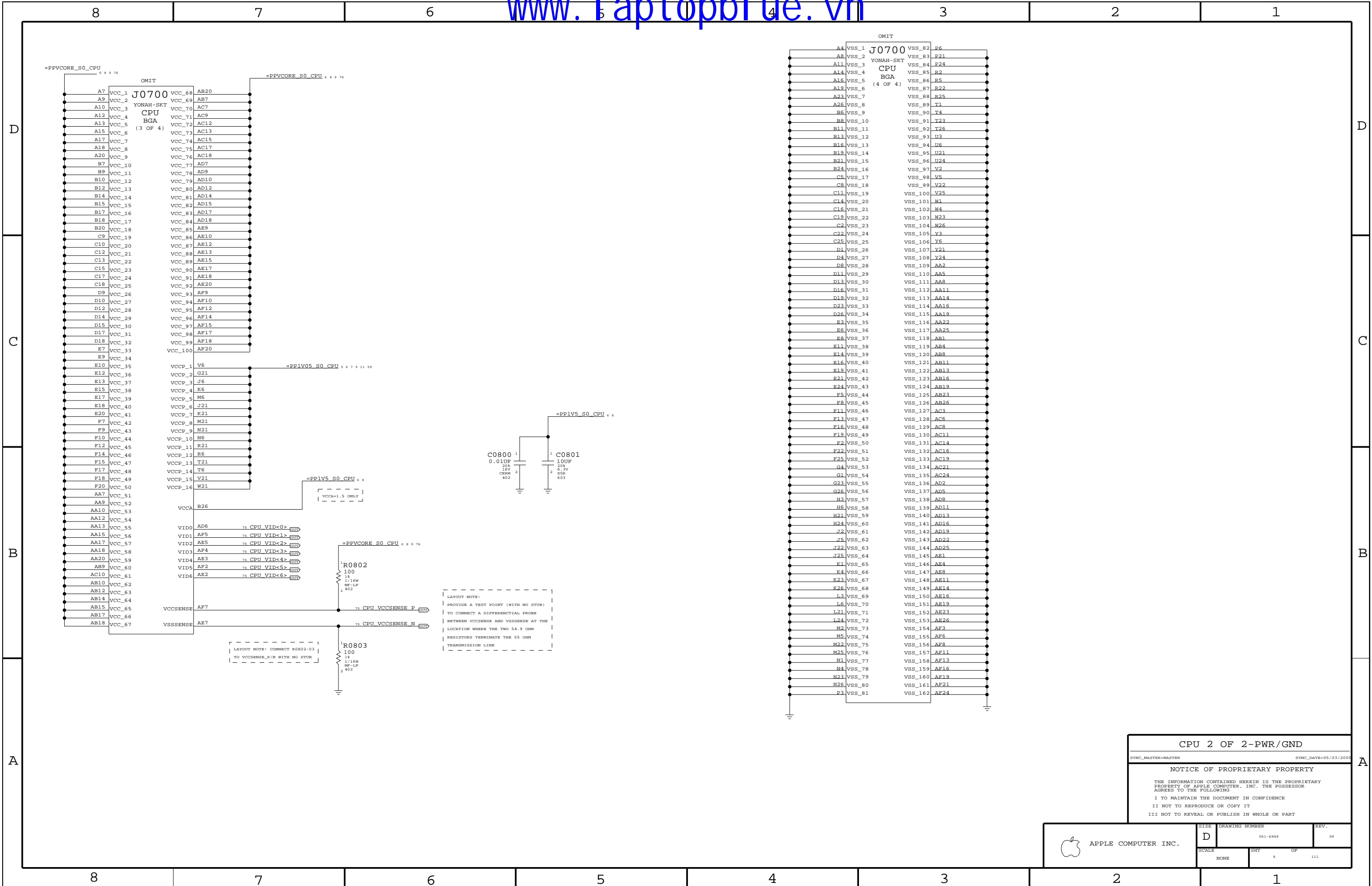
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SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHEET 7 OF 111



CPU 2 OF 2-PWR/GND

SYNC_MASTER=MASTER SYNC_DATE=05/03/2005

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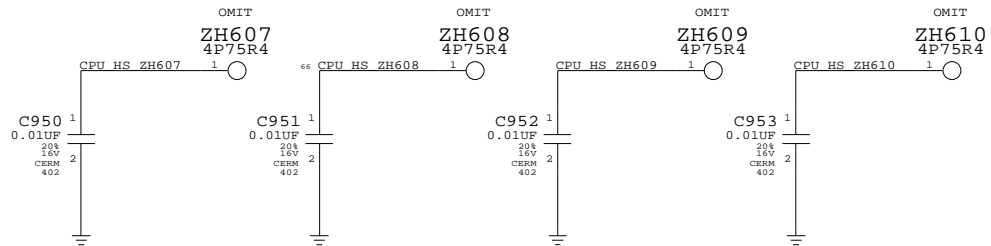
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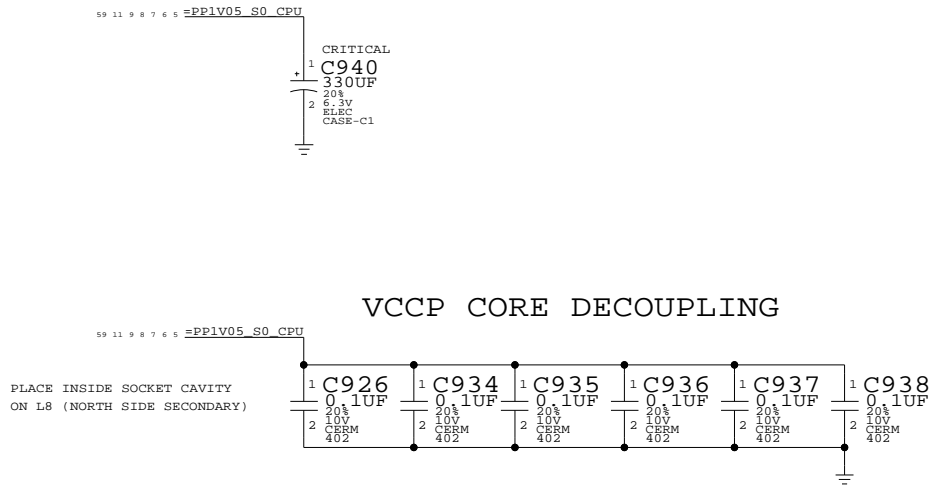
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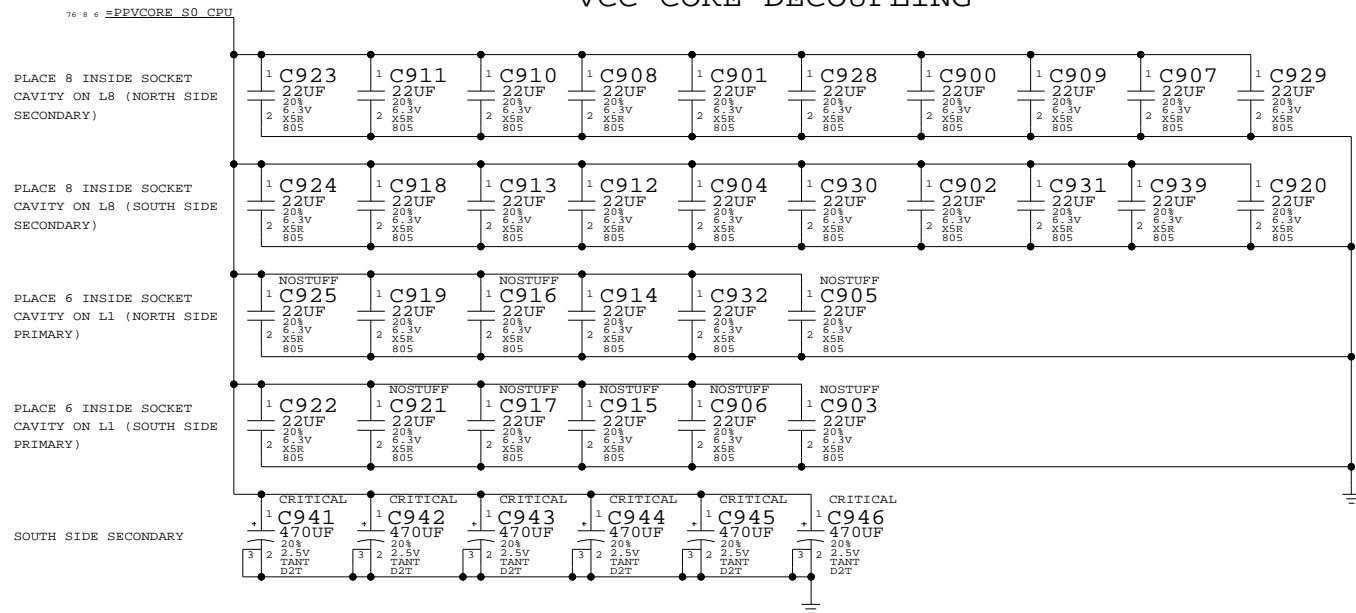
CPU HEATSINK MOUNTING HOLES



VCCP CORE DECOUPLING



VCC CORE DECOUPLING



CPU DECAPS & VID<>

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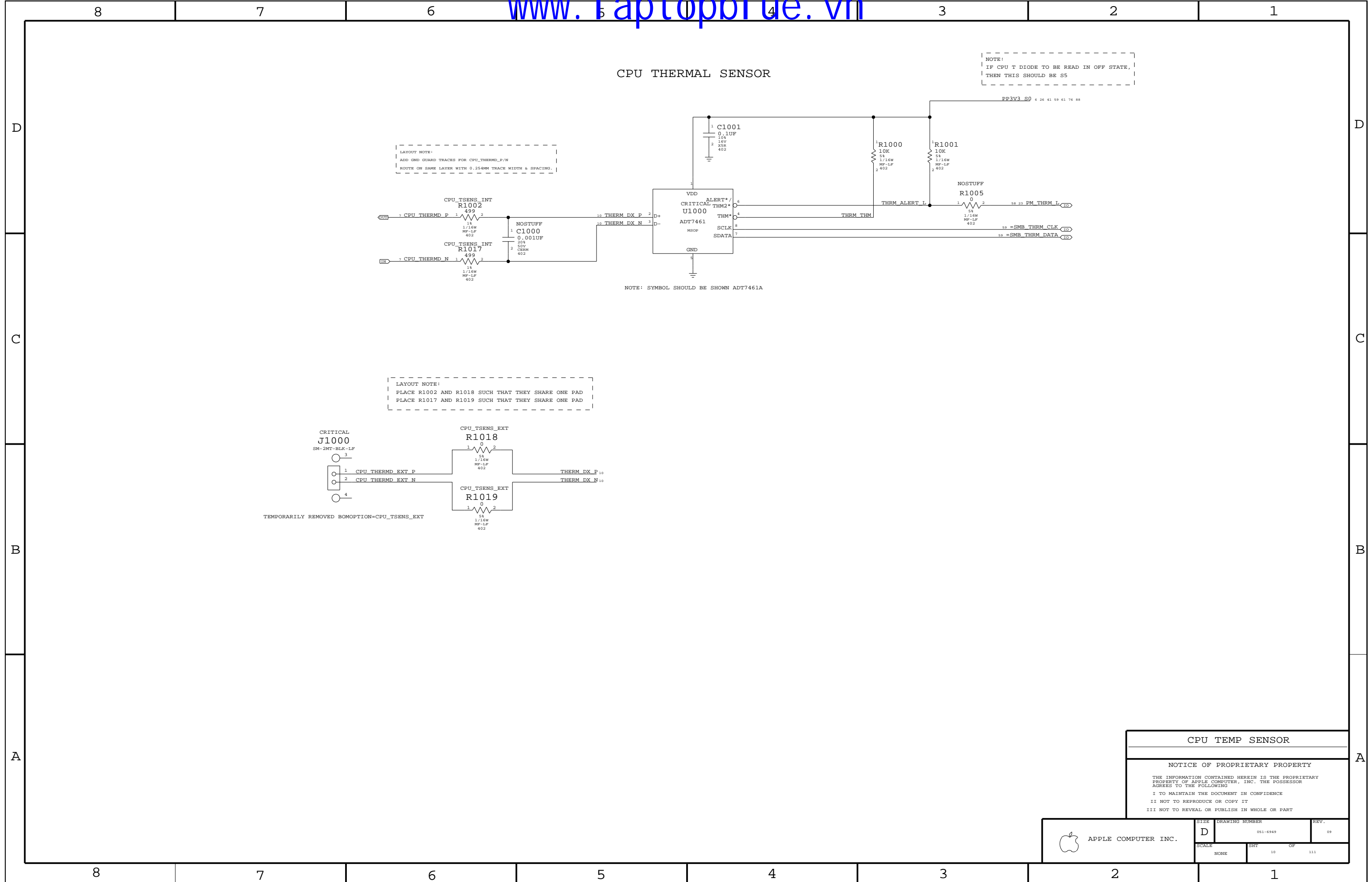
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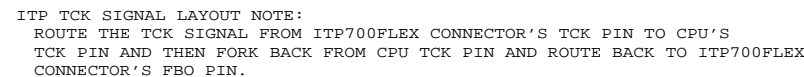
APPLE COMPUTER INC.

SIZE D DRAWING NUMBER 051-6949 REV. 09

SCALE NONE SHEET 9 OF 111



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SIZE D	DRAWING NUMBER 051-6949	REV. 09
SCALE NONE	SHT 11	OF 111

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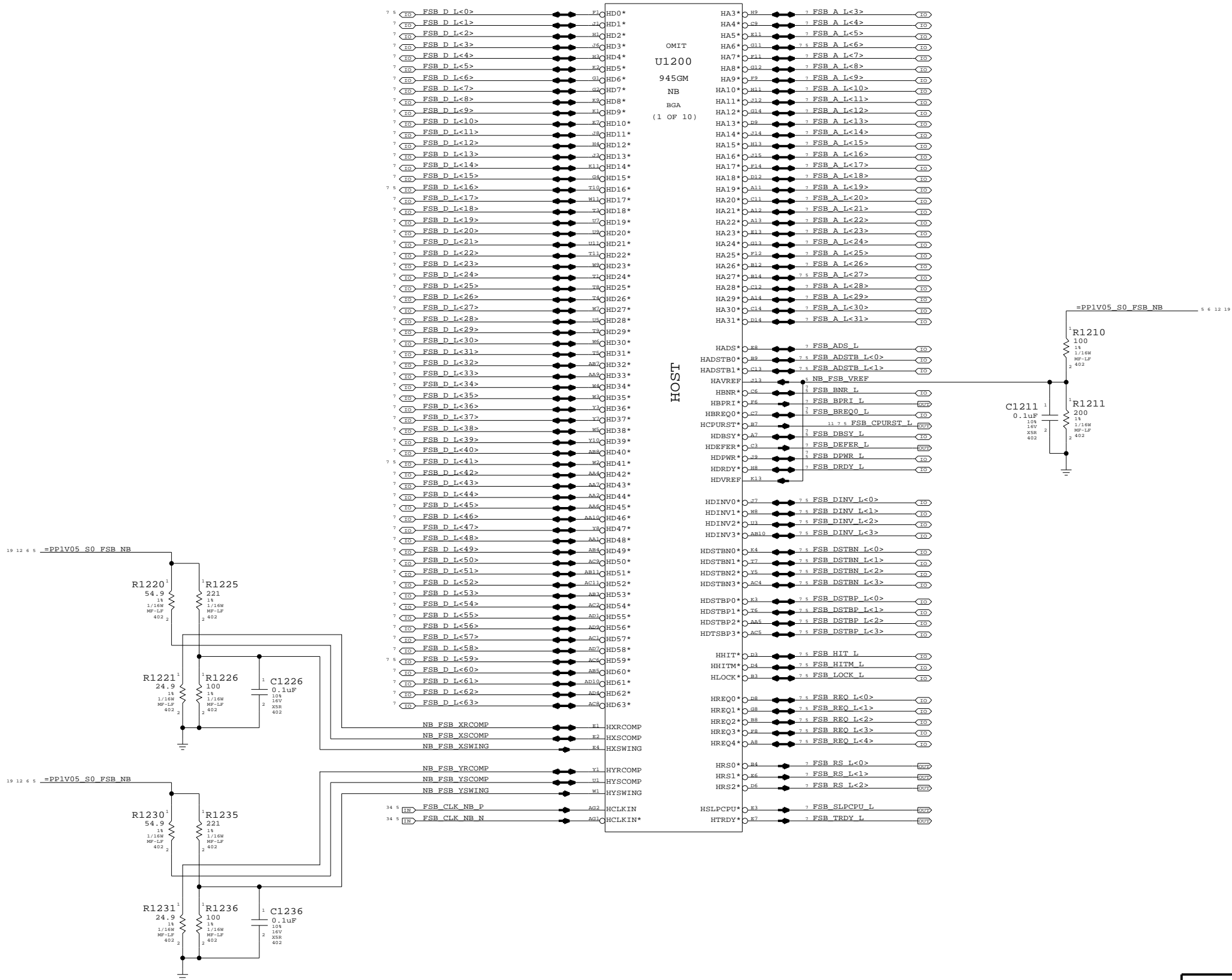
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NB CPU Interface

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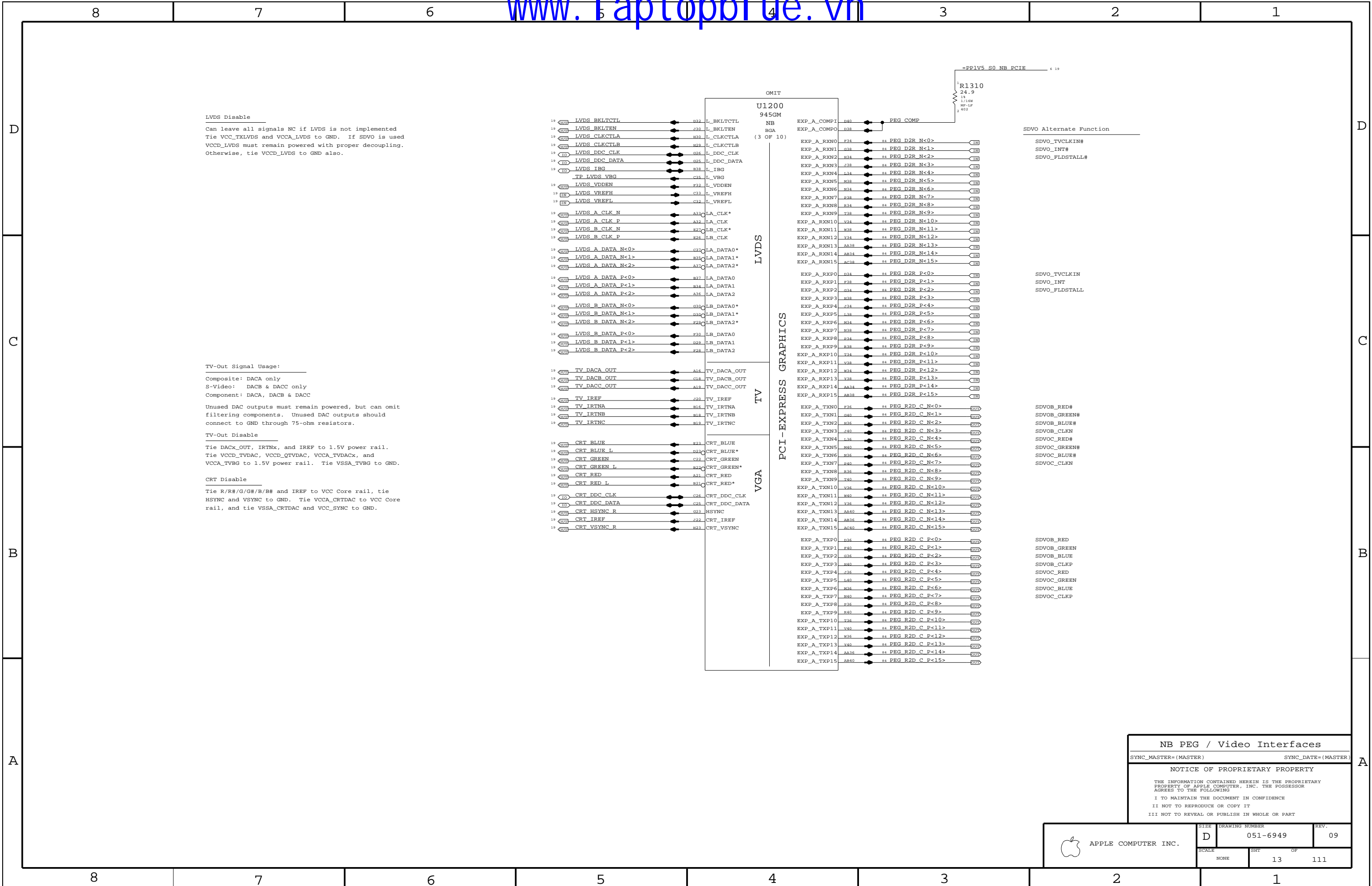
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NB PEG / Video Interfaces

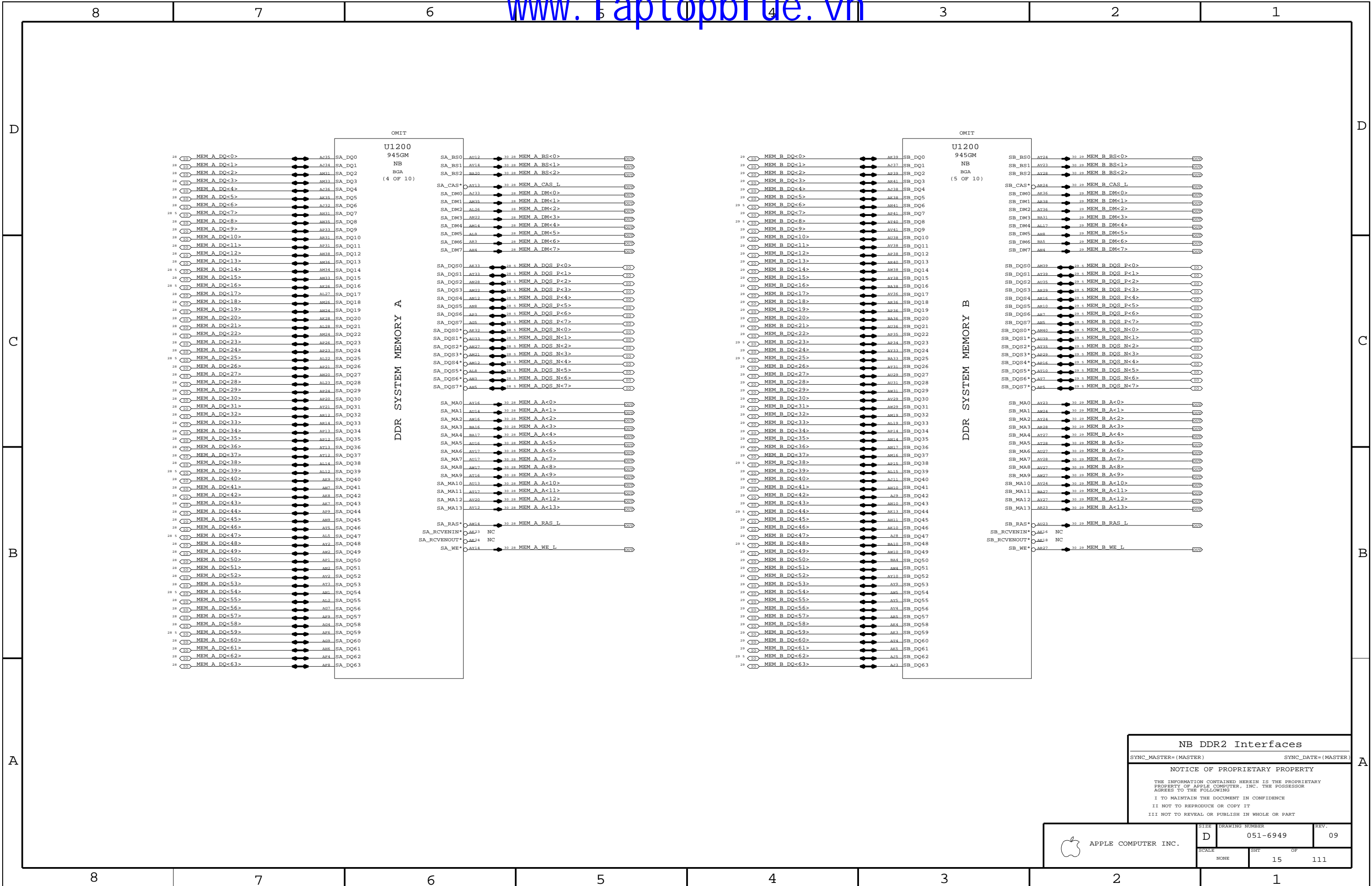
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NONE	14	111
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NB DDR2 Interfaces

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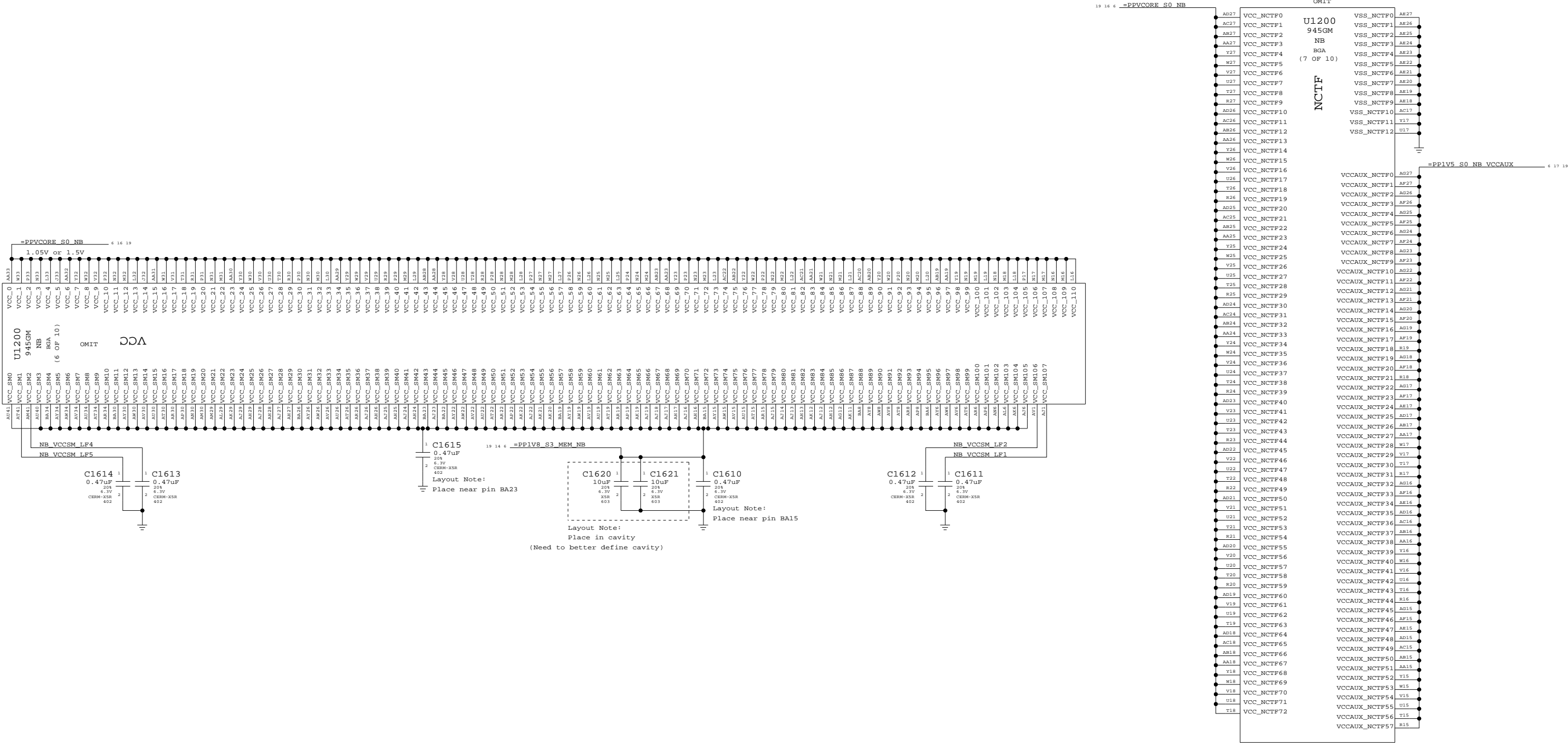
B

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NCTF balls are Not Critical To Function

These connections can break without impacting part performance.

OMIT



NB Power 1

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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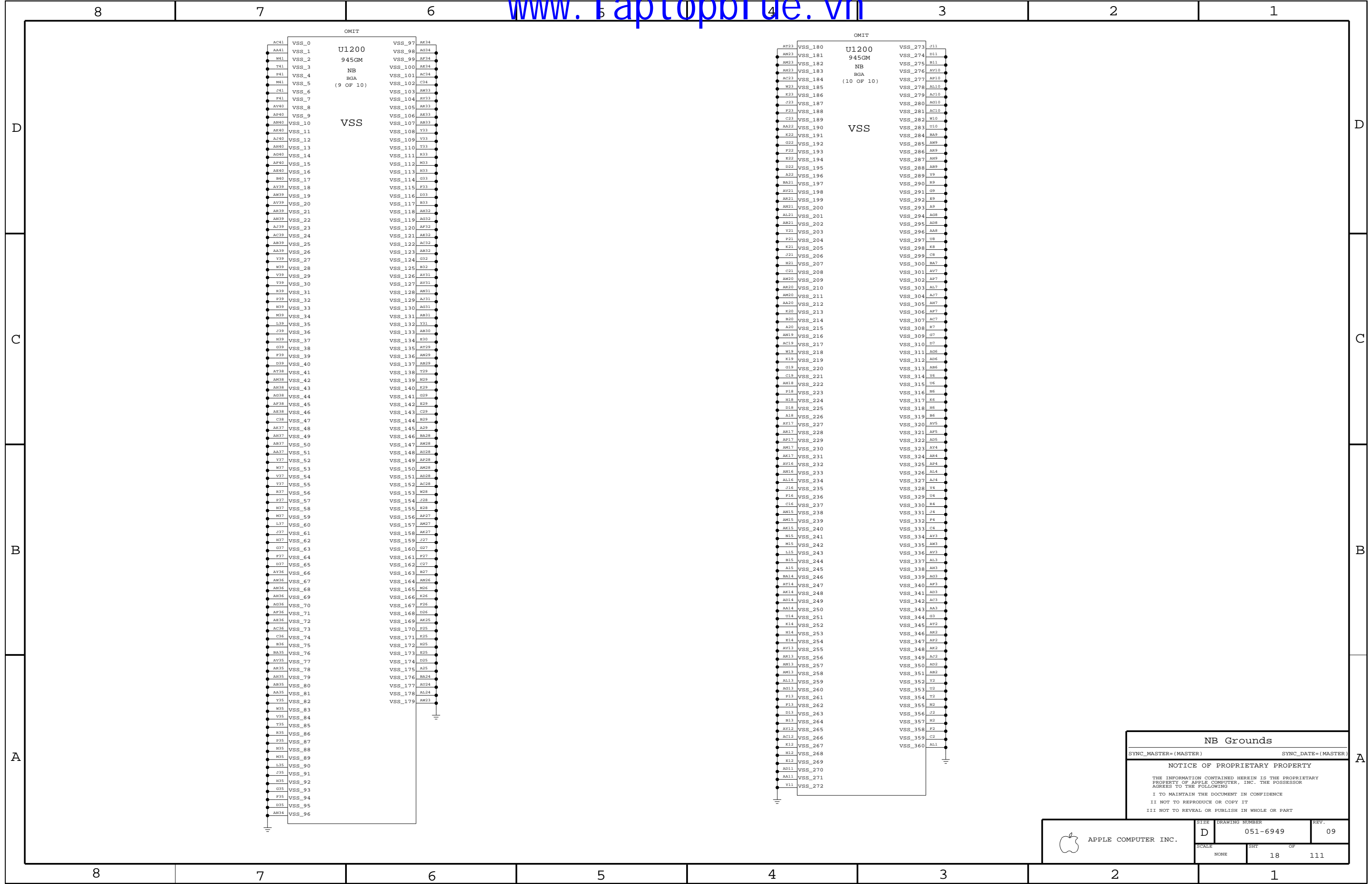
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SIZE DRAWING NUMBER REV.

D 051-6949 09

SCALE SHEET OF

NONE 16 111

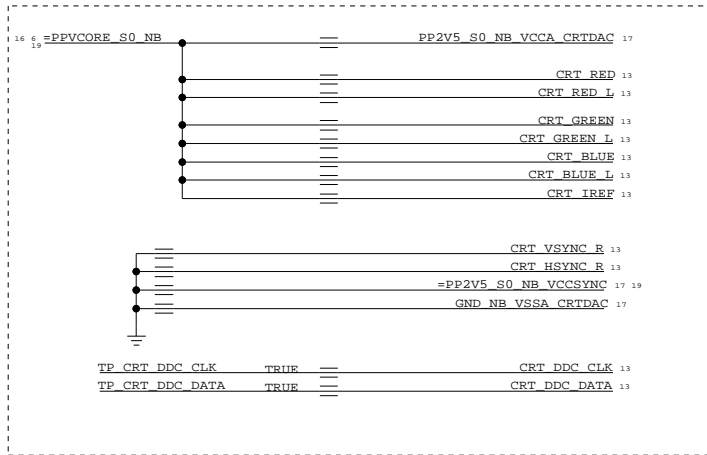


Power Interface

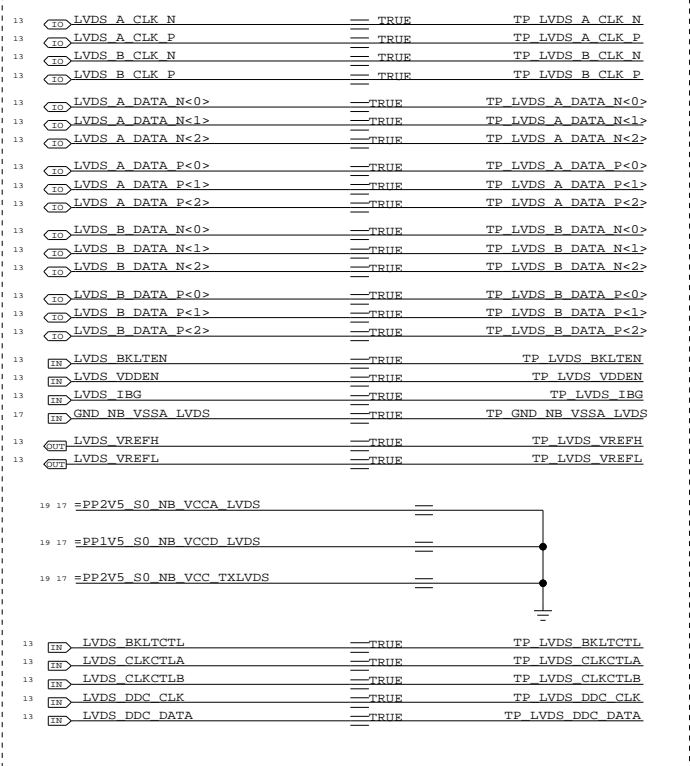
These are the power signals that leave the NB "block"

EN	=PP1V05_S0_FSB_NB	5 6 12
EN	=PPVCORE_S0_NB	6 16 19
EN	=PP1V05_S0_NB	6
EN	=PP1V05_S0_NB_VTT	6 17 19
EN	=PP1V5_S0_NB	6 19
EN	=PP1V5_S0_NB_PCIE	6 13
EN	=PP1V5_S0_NB_PLL	6 19
EN	=PP1V5_S0_NB_TVDAC	6 19
EN	=PP1V5_S0_NB_VCCD_HMPLL	6 17
EN	=PP1V5_S0_NB_VCCD_LVDS	17 19
EN	=PP1V5_S0_NB_VCCAUX	6 16 17 19
EN	=PP1V8_S3_MEM_NB	6 14 16 19
EN	=PP2V5_S0_NB_VCCSYN	17 19
EN	=PP2V5_S0_NB_VCC_TXLVDS	17 19
EN	=PP2V5_S0_NB_VCCA_3GBG	6 17 19
EN	=PP2V5_S0_NB_VCCA_LVDS	17 19
EN	=PP3V3_S0_NB	6 14 20
EN	=PP3V3_S0_NB_TVDAC	6
EN	=PP3V3_S0_NB_VCC_HV	6 17 19

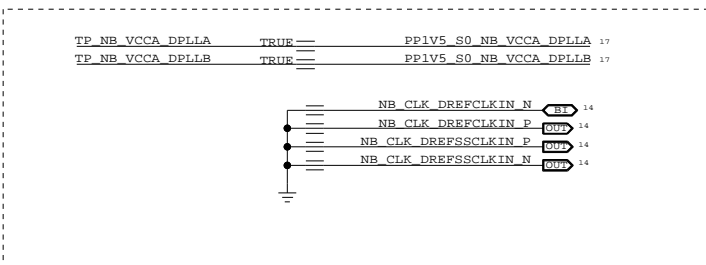
TVOUT DISABLE



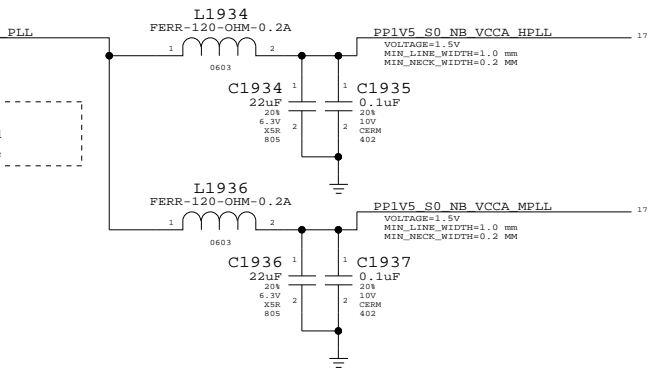
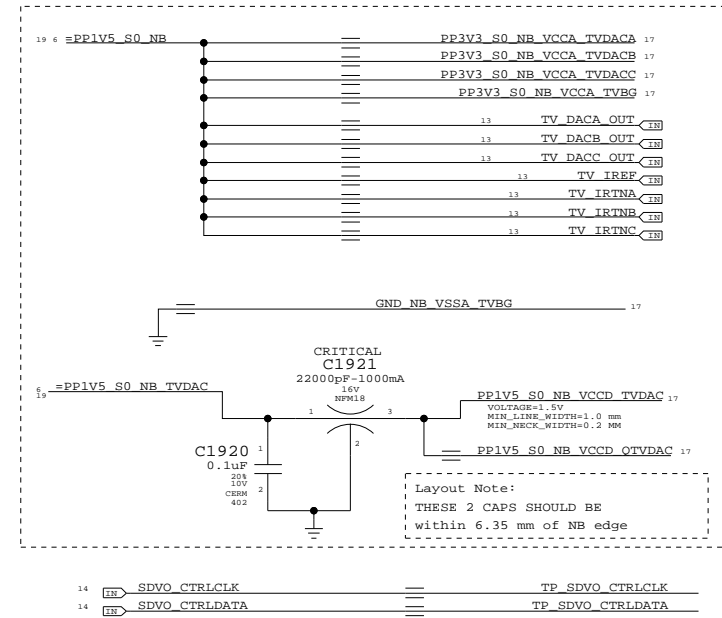
LVDS DISABLE



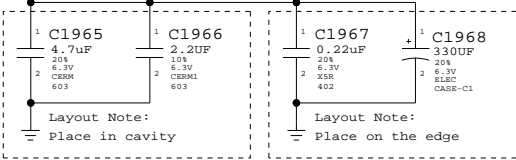
DISPLAY DISABLE



TVOUT DISABLE

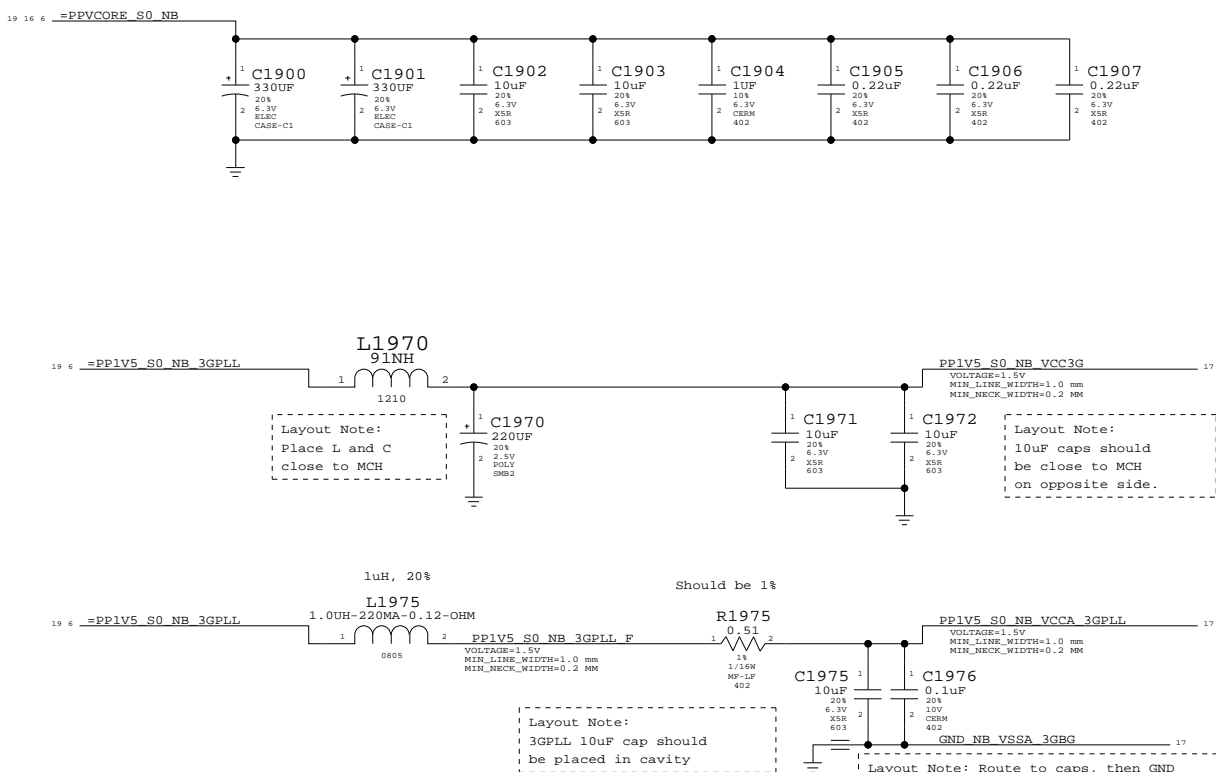


Layout Note:
These 4 0.1uF caps should
be within 5 mm of NB edge



Layout Note:
Place in cavity

Layout Note:
Place on the edge



Layout Note:
Place L and C
close to MCH

Layout Note:
10uF caps should
be close to MCH
on opposite side.

Layout Note:
3GPLL 10uF cap should
be placed in cavity

Layout Note: Route to caps, then GND

NB (GM) Decoupling

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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SIZE: DRAWING NUMBER

D

051-6949

09

SCALE: NONE

SHT

OF

111

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NB_CFG<3>	RESERVED
-----------	----------

NB_CFG<4>	RESERVED
-----------	----------

14_NB_CFG<5> Internal pull-up	
NB_CFG<5> DMI x2 Select	High = DMIx4 Low = DMIx2

PROBABLY NOT NEEDED

NBCFG_DMI_X2

1R2075
2.2K
5%
1/16W
MF-LP
2 402

NB_CFG<6>	RESERVED
-----------	----------

14_NB_CFG<7> Internal pull-up	
NB_CFG<7> CPU Strap	High = Mobile CPU Low = RESERVED

NO STUFF

1R2077
2.2K
5%
1/16W
MF-LP
2 402

NB_CFG<8>	RESERVED
-----------	----------

14_NB_CFG<9> Internal pull-up	
NB_CFG<9> PCIe Graphics Lane Reversal	High = Normal Low = Reversed

NBCFG_PEG_REVERSE

1R2079
2.2K
5%
1/16W
MF-LP
2 402

NB_CFG<10>	RESERVED
------------	----------

NB_CFG<11>	RESERVED
------------	----------

Internal pull-ups	
NB_CFG<13:12>	00 = Partial Clock Gating Disable 01 = XOR Mode Enabled 10 = All-Z Mode Enabled 11 = Normal Operation

NB_CFG<14>	RESERVED
------------	----------

NB_CFG<15>	RESERVED
------------	----------

14_NB_CFG<16> Internal pull-up	
NB_CFG<16> FSB Dynamic ODT	High = Enabled Low = Disabled

NBCFG_DYN_ODT_DISABLE

1R2085
2.2K
5%
1/16W
MF-LP
2 402

NB_CFG<17>	RESERVED
------------	----------

=PP3V3_S0_NB NBCFG_VCC_1V5	
NB_CFG<18> VCC Select	High = 1.5V Low = 1.05V

1R2058
2.2K
5%
1/16W
MF-LP
2 402

14_NB_CFG<18>
Internal pull-down

=PP3V3_S0_NB NBCFG_DMI_REVERSE	
NB_CFG<19> DMI Lane Reversal	High = Reversed Low = Normal

1R2059
2.2K
5%
1/16W
MF-LP
2 402

14_NB_CFG<19>
Internal pull-down


945 External Design Spec says reserved =PP3V3_S0_NB NBCFG_SDVO_AND_PCIE	
NB_CFG<20> PCIe Backward Interop. Mode	High = Both active Low = Only SDVO or PCIe x1

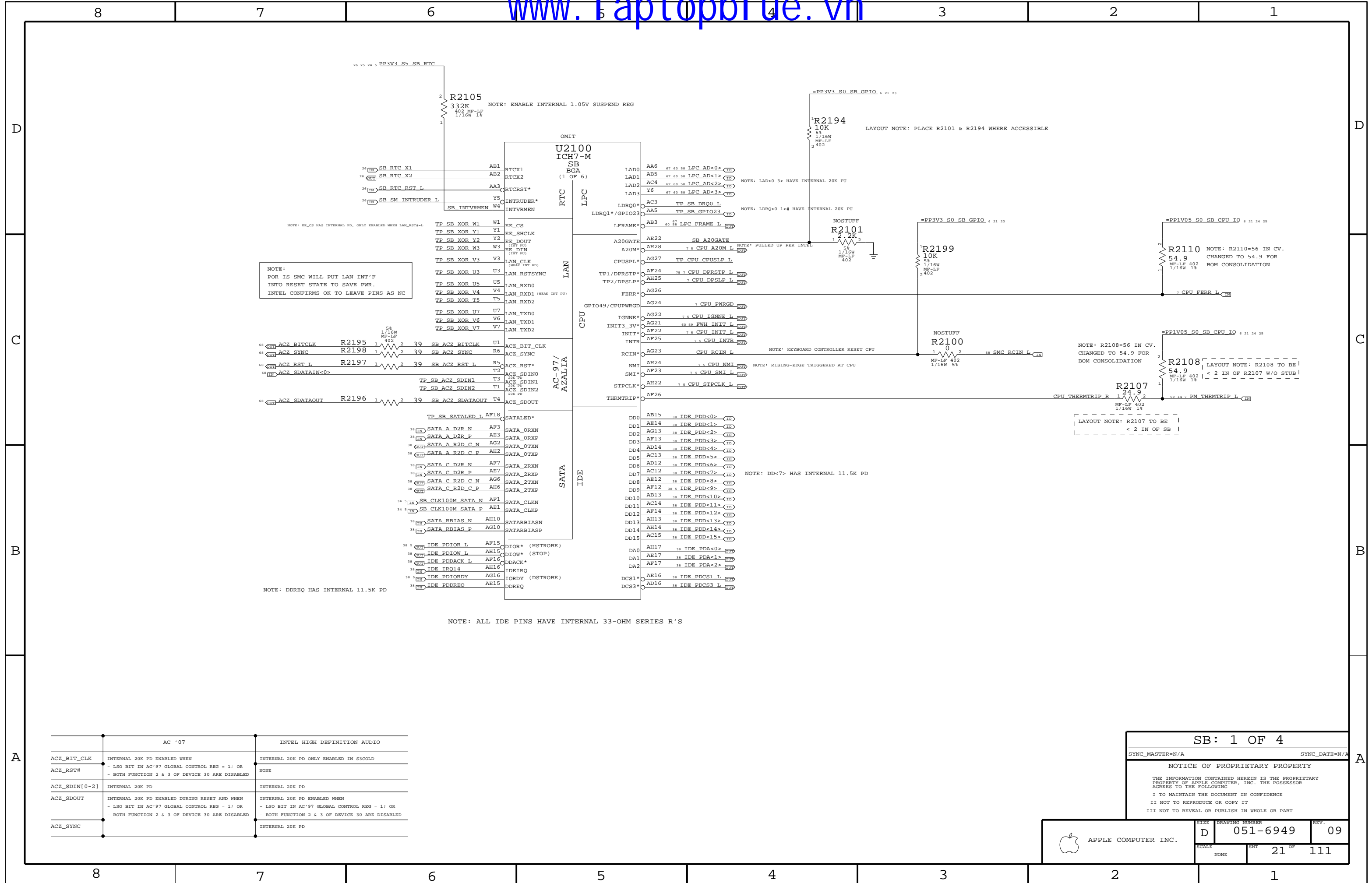
1R2060
2.2K
5%
1/16W
MF-LP
2 402

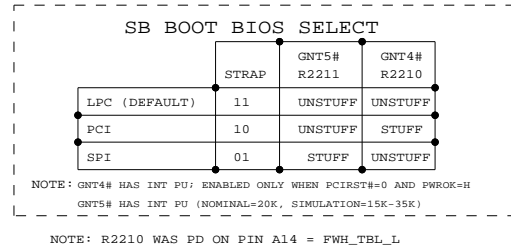
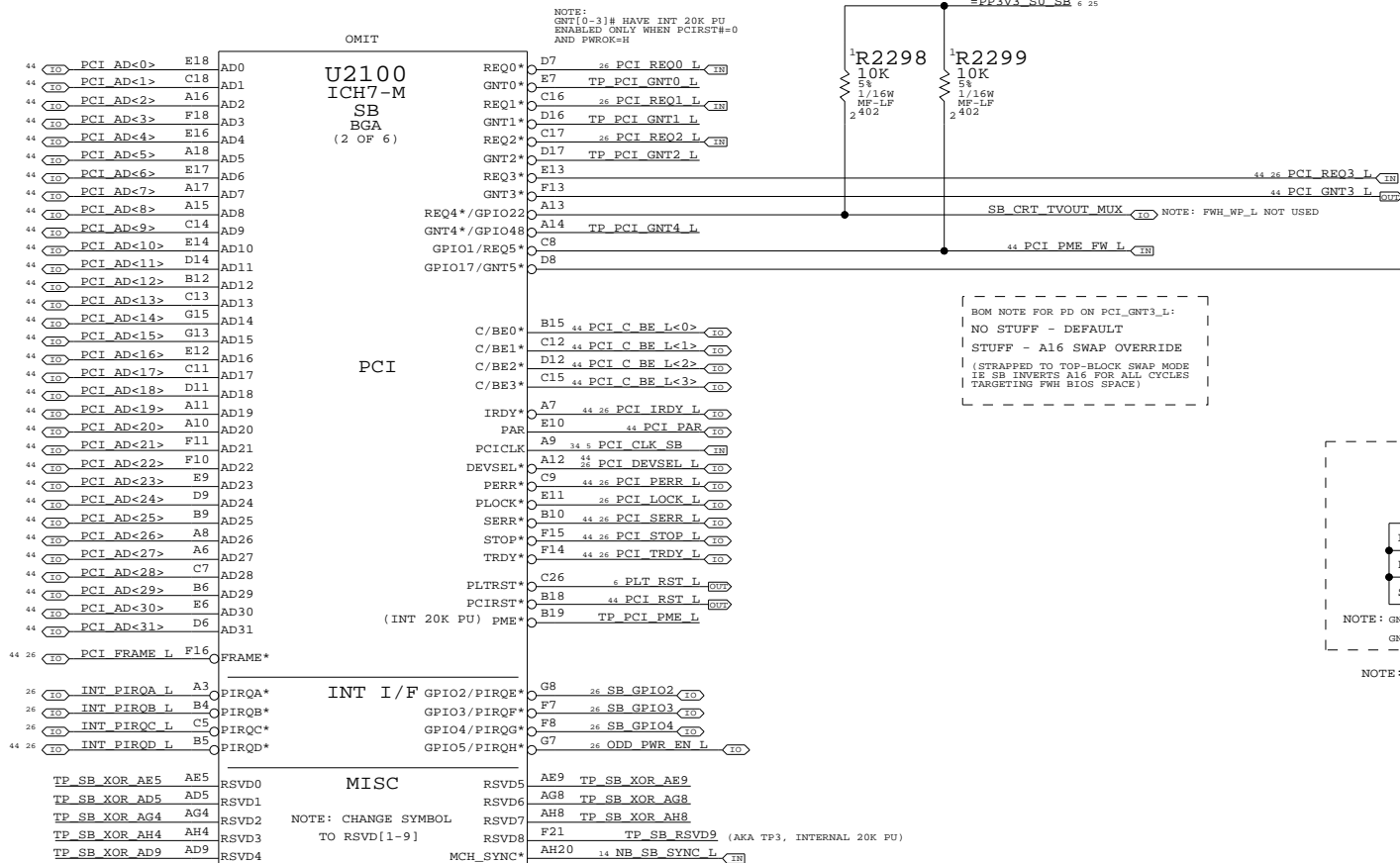
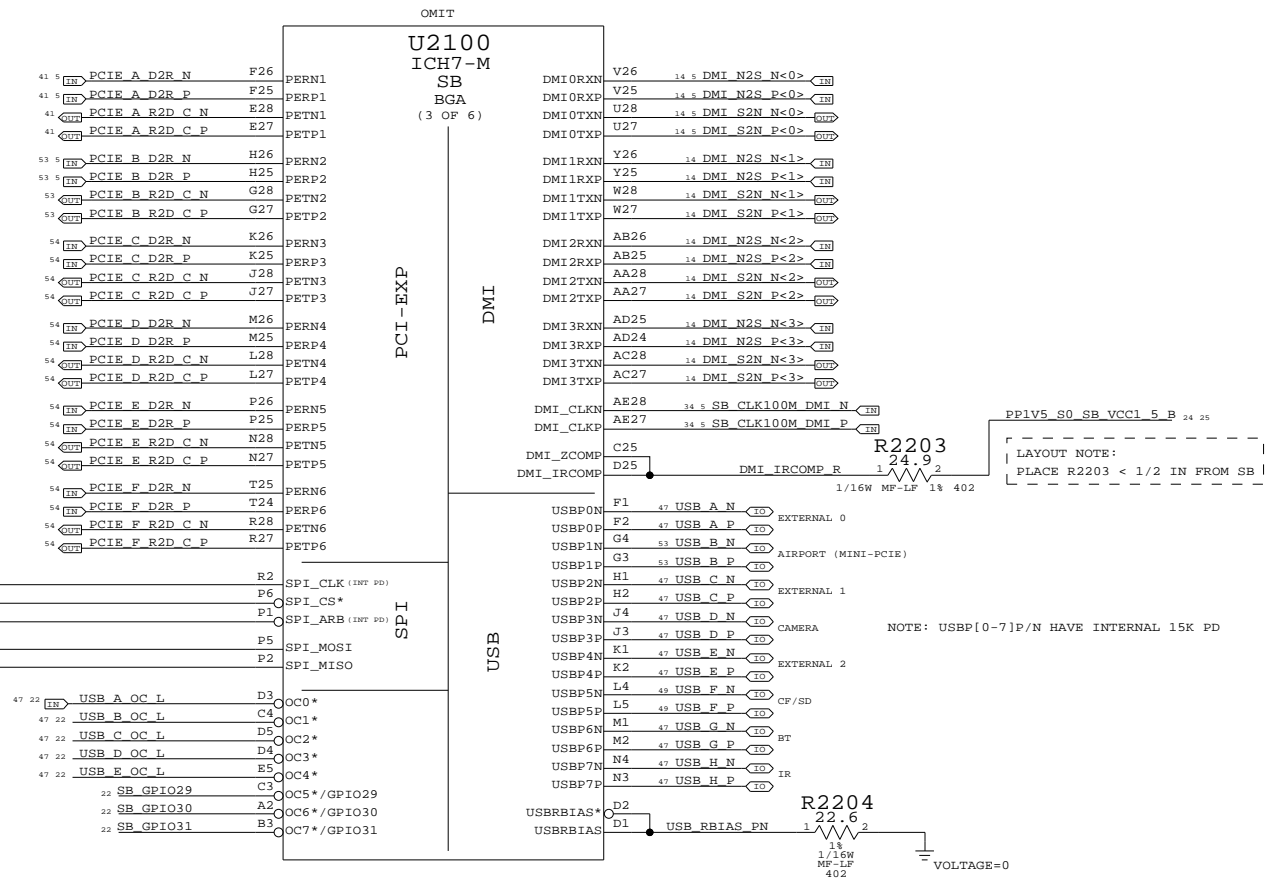
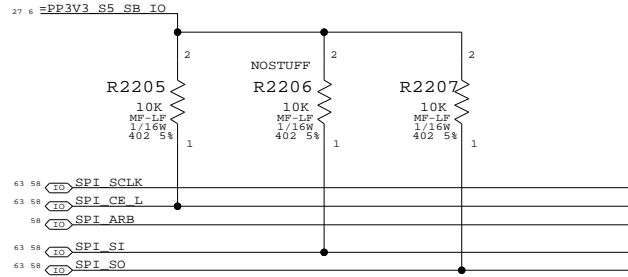
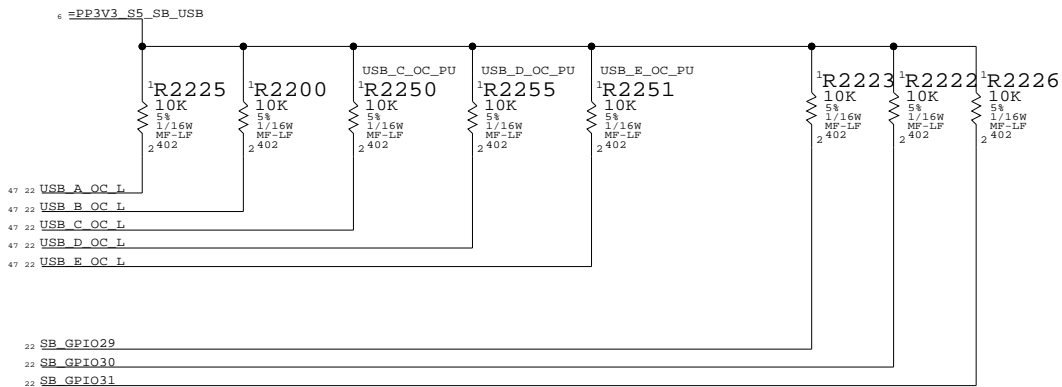
14_NB_CFG<20>
Internal pull-down

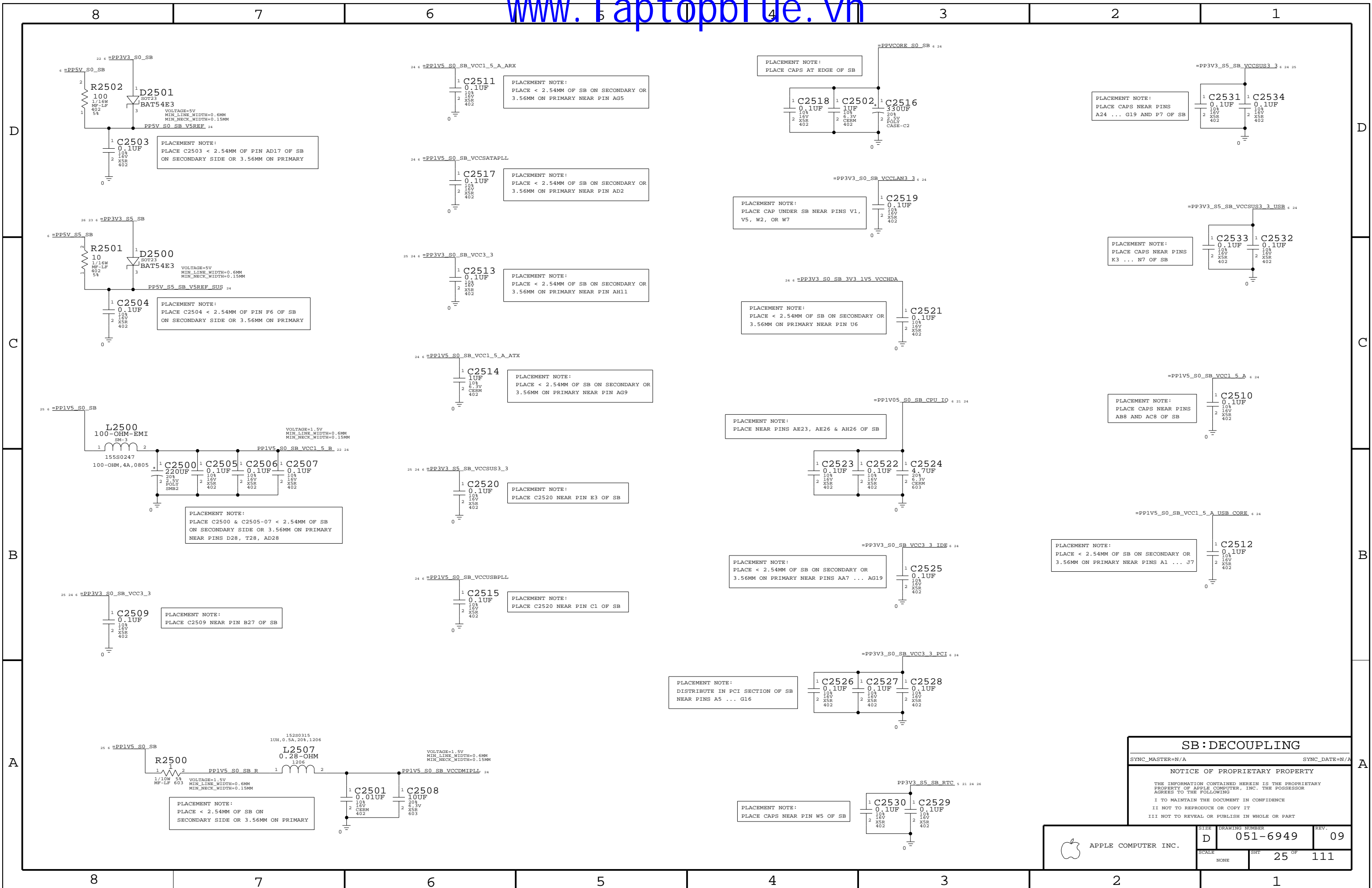
PROBABLY NOT NEEDED

NB Config Straps		
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SB: DECOUPLING

SYNC_MASTER=N/A

SYNC_DATE=N/A

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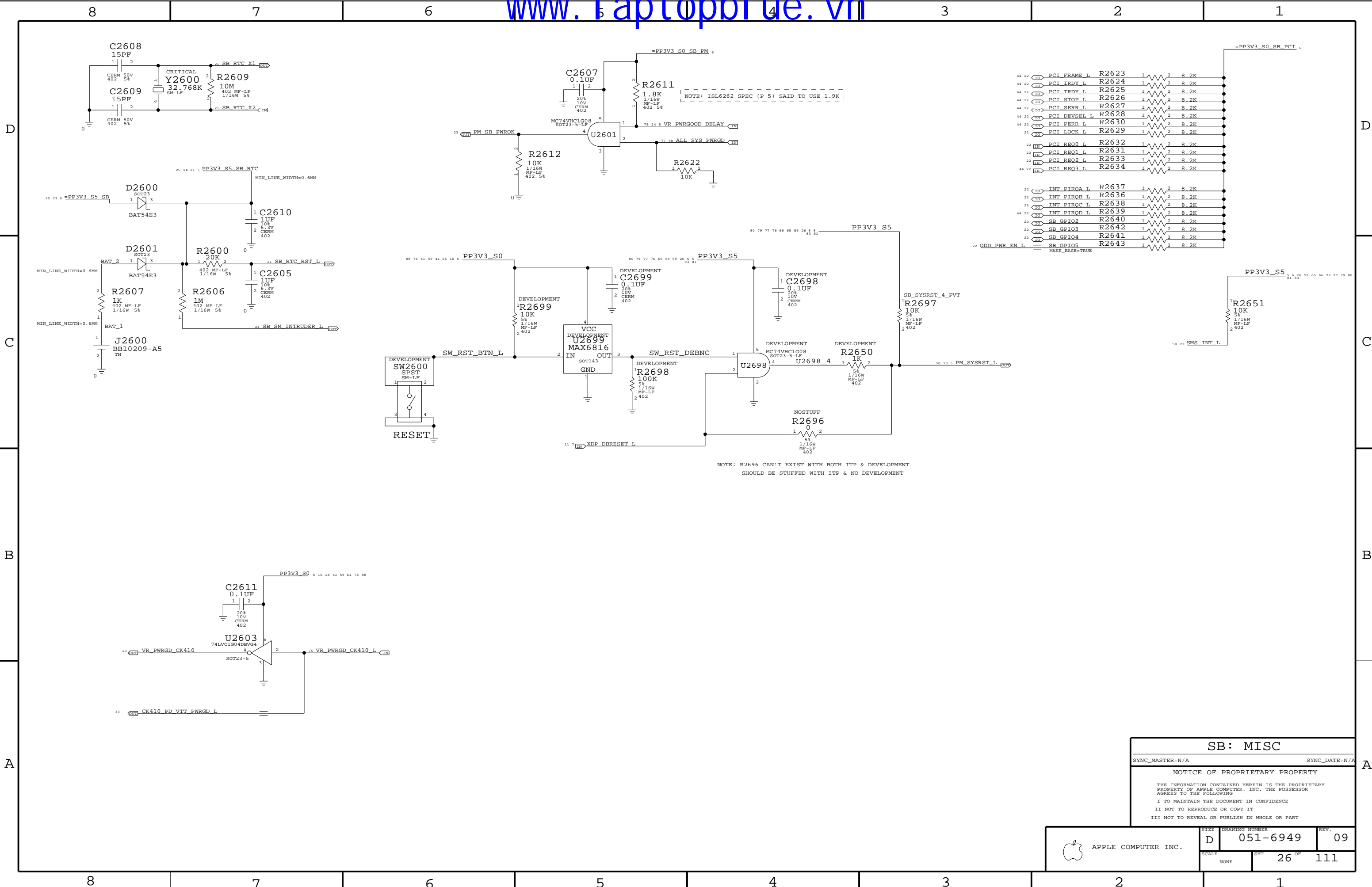
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SHEET: 25 OF 111

SIZE: DRAWING NUMBER: 051-6949

REV.: 09



SB: MISC

SYNC_MASTER=N/A SYNC_DATE=N/A

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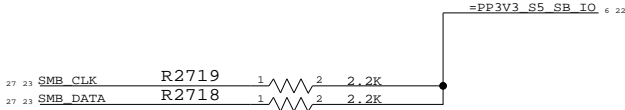
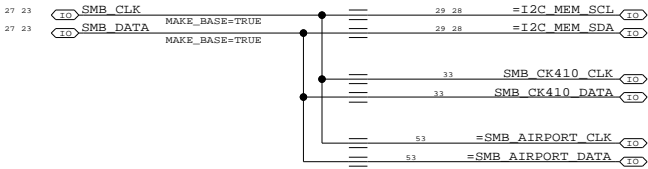
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SB I2C BUSSES



SB: SMB HUB

SYNC_MASTER=N/A SYNC_DATE=N/A

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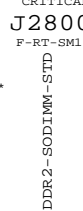
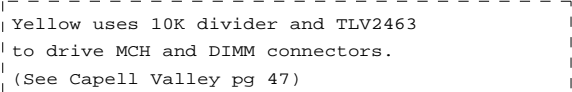
SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	27 OF 111
NONE		

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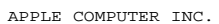
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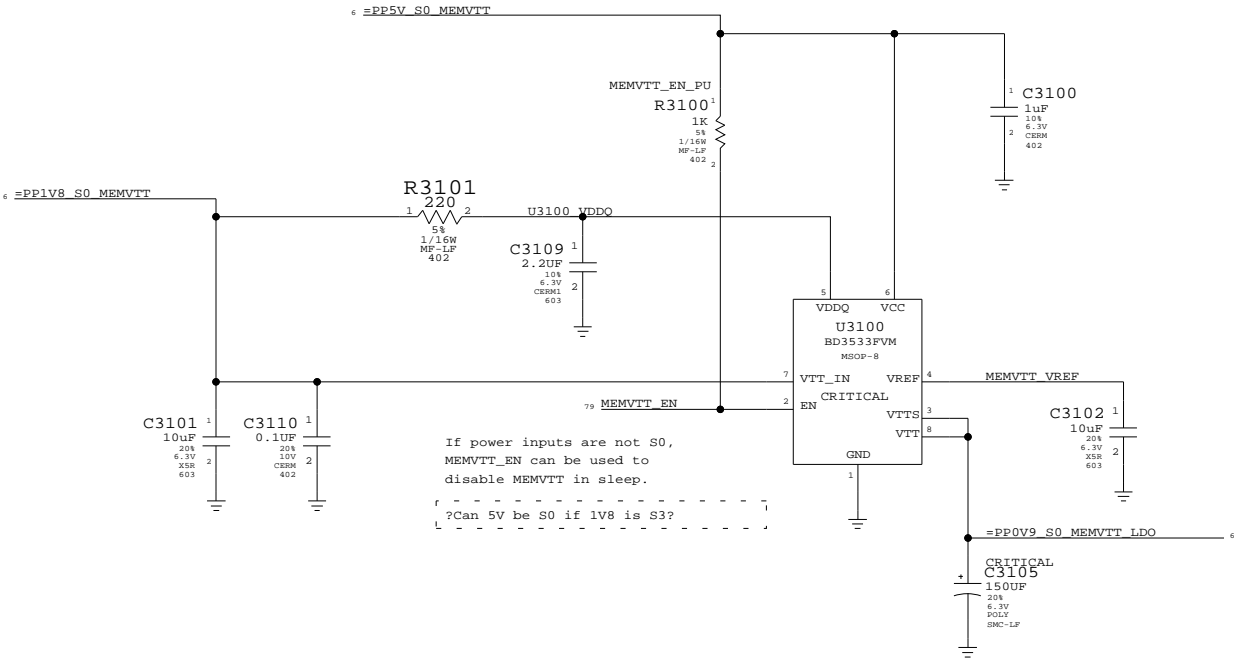
Page Notes

Power aliases required by this page:
- =PP5V_S0_MEMVTT
- =PP1V8_S0_MEMVTT
- =PP0V9_S0_MEMVTT_LDO

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)

DDR2 Vtt Regulator



Memory Vtt Supply

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NONE	31	111



* FOR EXT. GRAPHIC SYSTEM

SIZE D	DRAWING NUMBER 051-6949	REV. 09
SCALE NONE	SHT 33	OF 111

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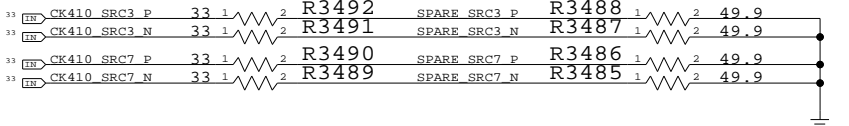
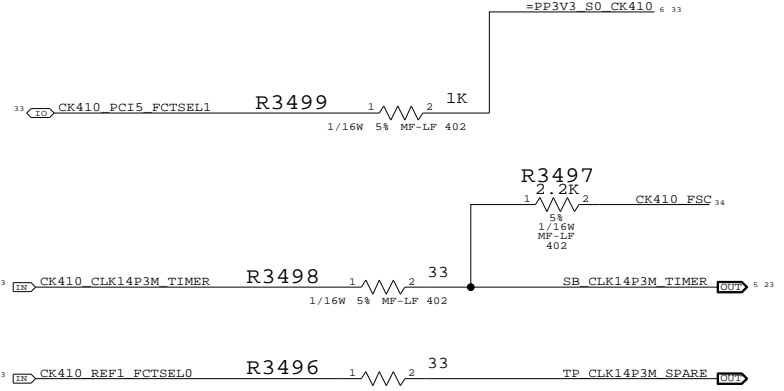
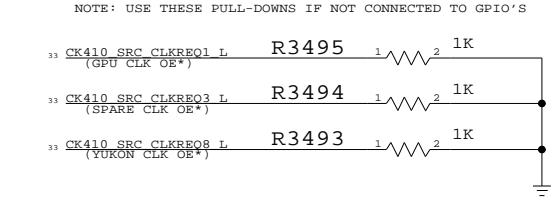
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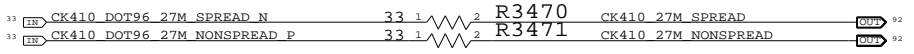
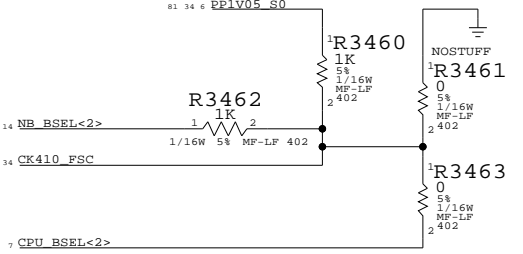
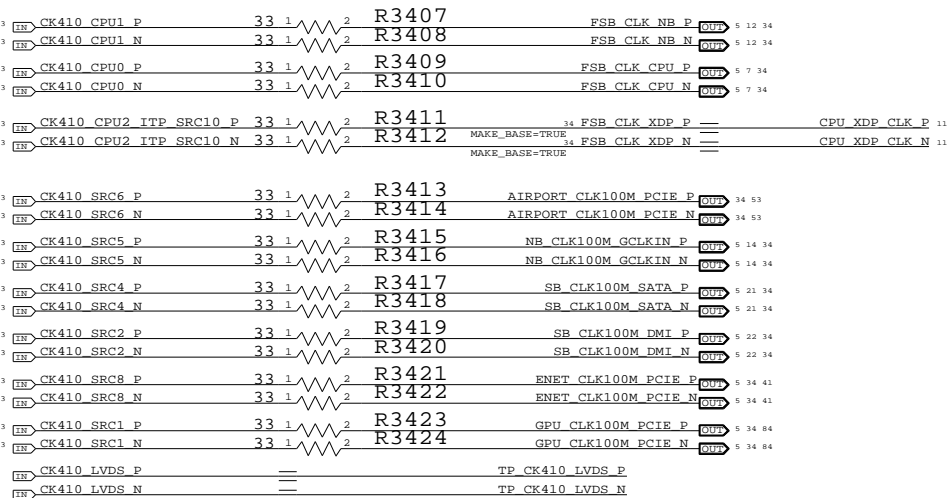
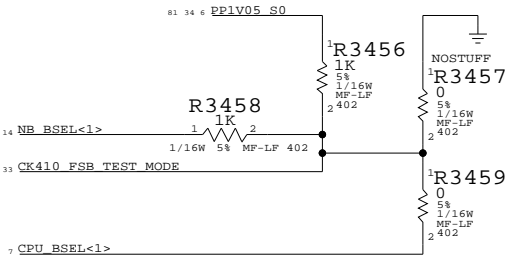
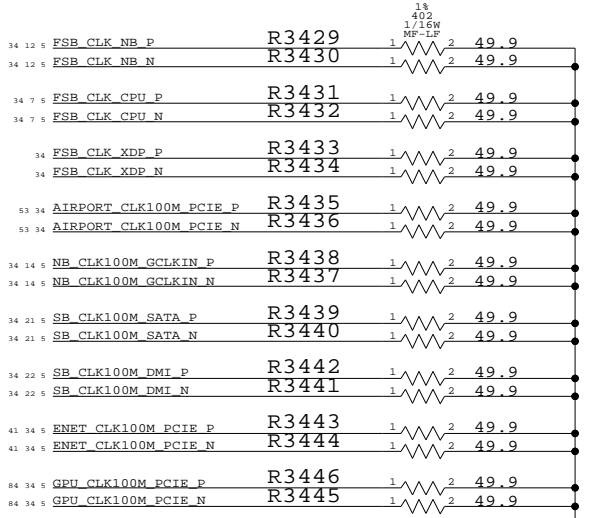
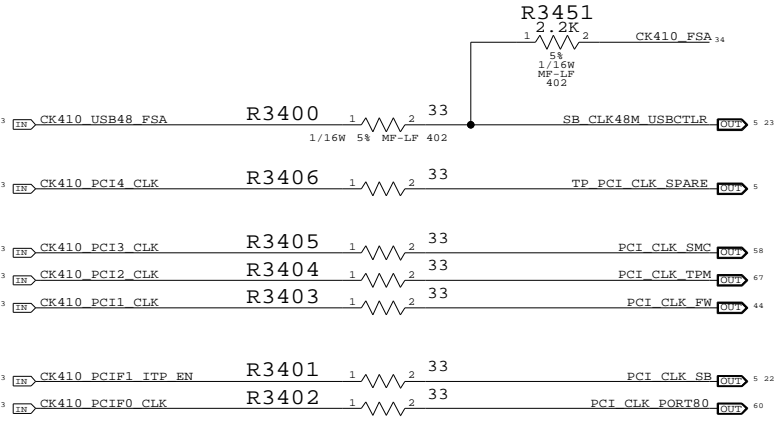
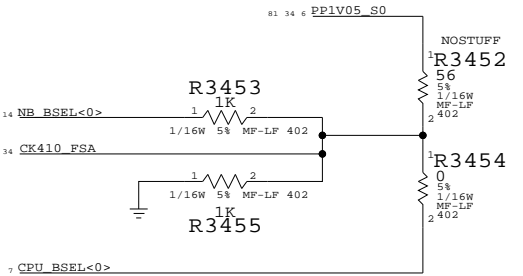
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FSB FREQUENCY SELECT:

	STUFF	NO STUFF
CPU DRIVEN	R3454 R3455 R3456	R3457 R3458 R3459
533MHZ (133MHZ CPU CLK)	R3452 R3453 R3454	R3455 R3456 R3457
667MHZ (166MHZ CPU CLK)	R3452 R3453 R3454	R3455 R3456 R3457



CLOCKS:TERMINATIONS

SYNC_MASTER=N/A

SYNC_DATE=N/A

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SIZE: D

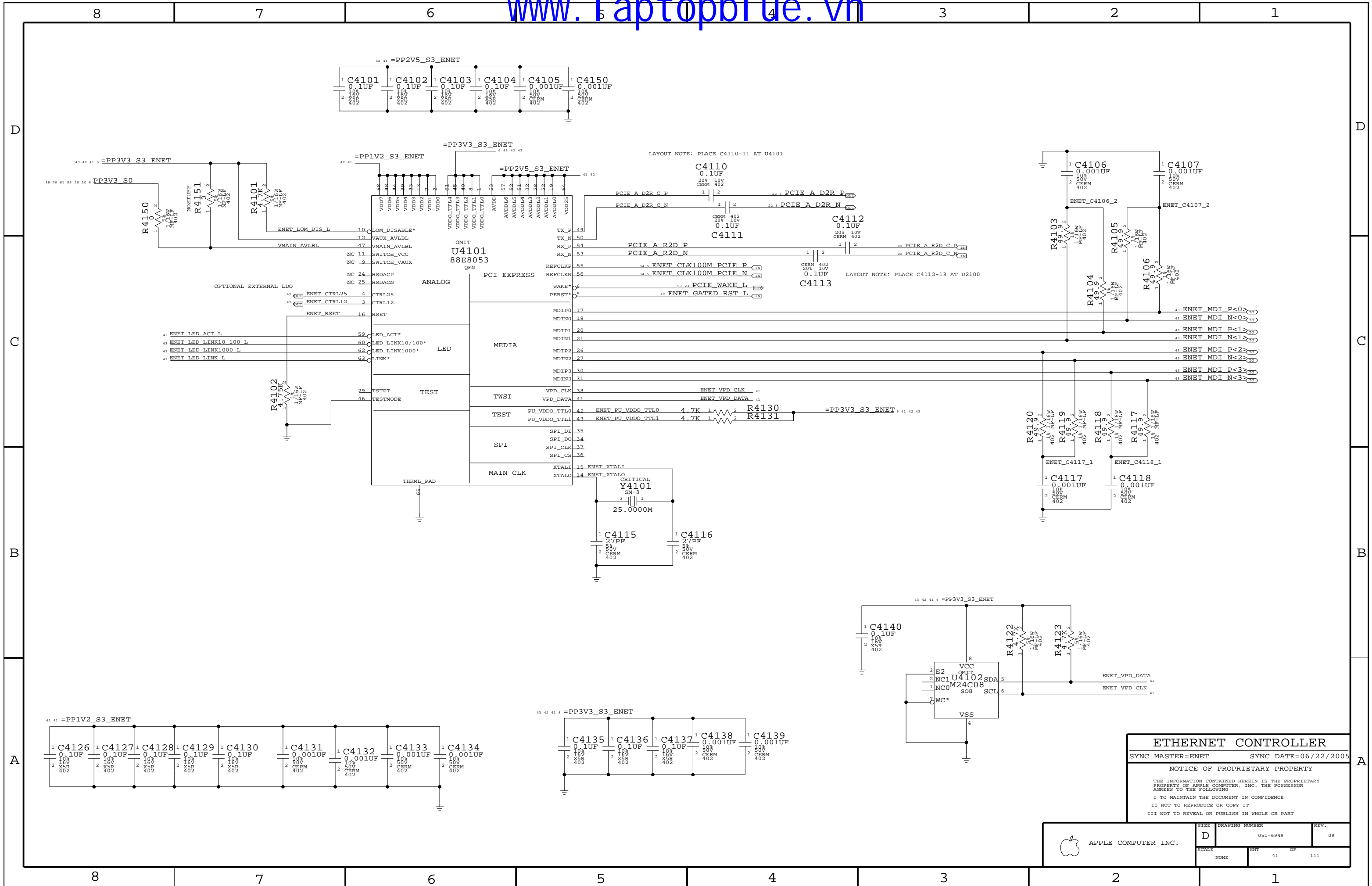
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DRAWING NUMBER: 051-6949

SHT: 34

REV.: 09

OF: 111



D

C

B

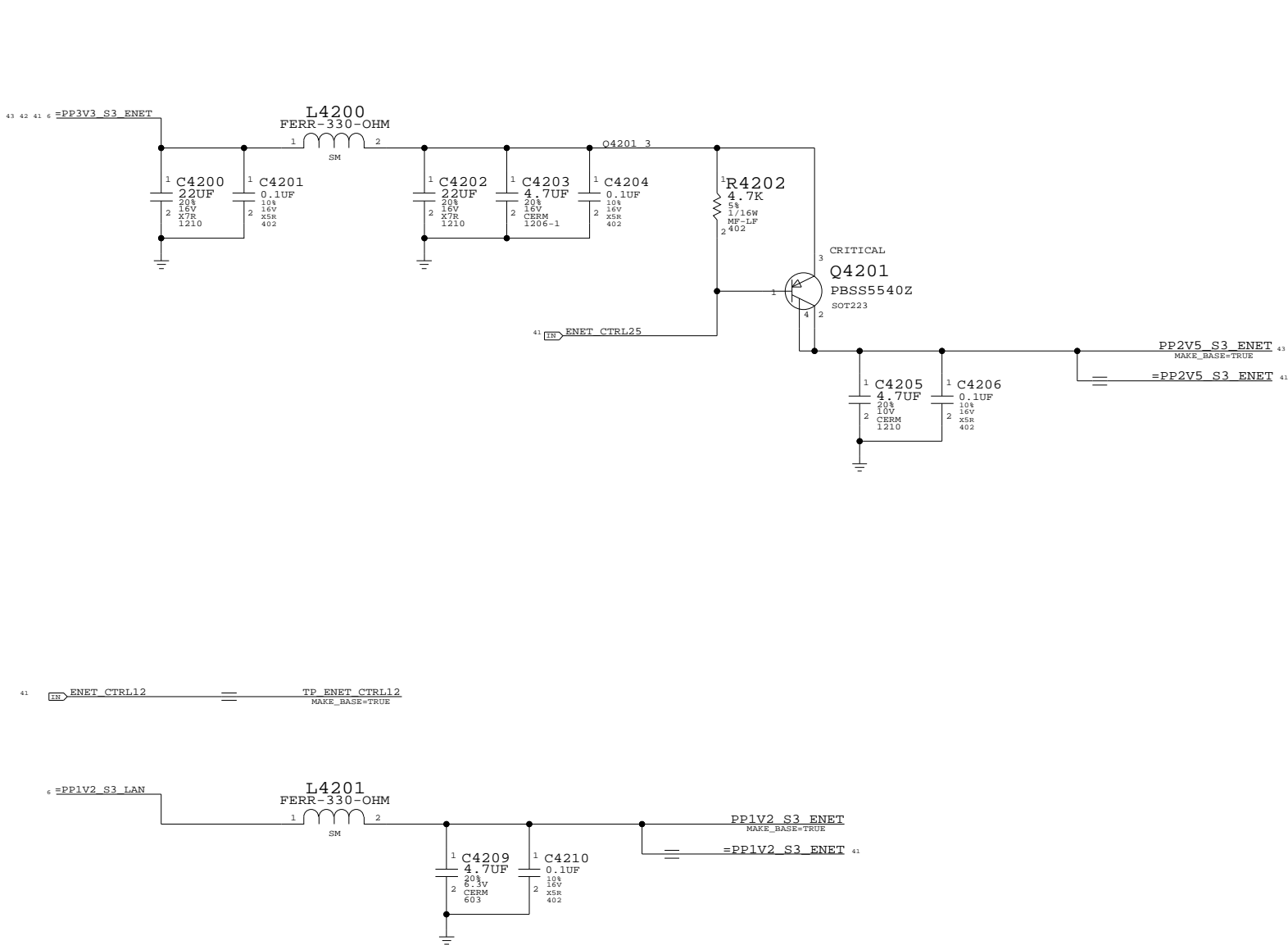
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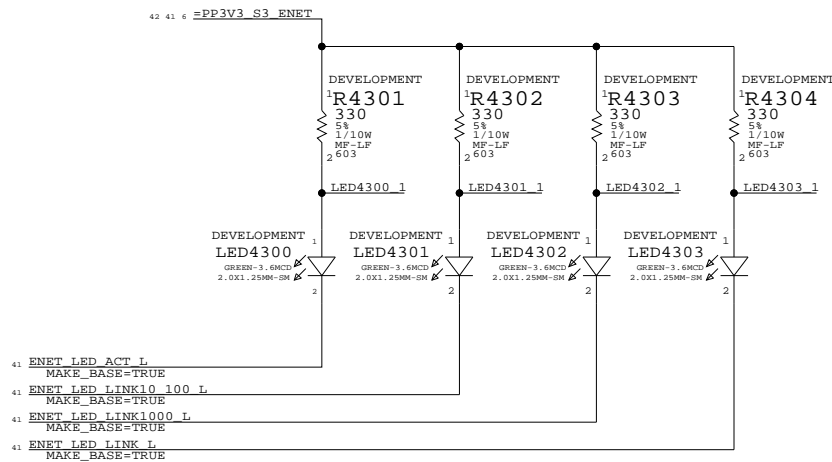
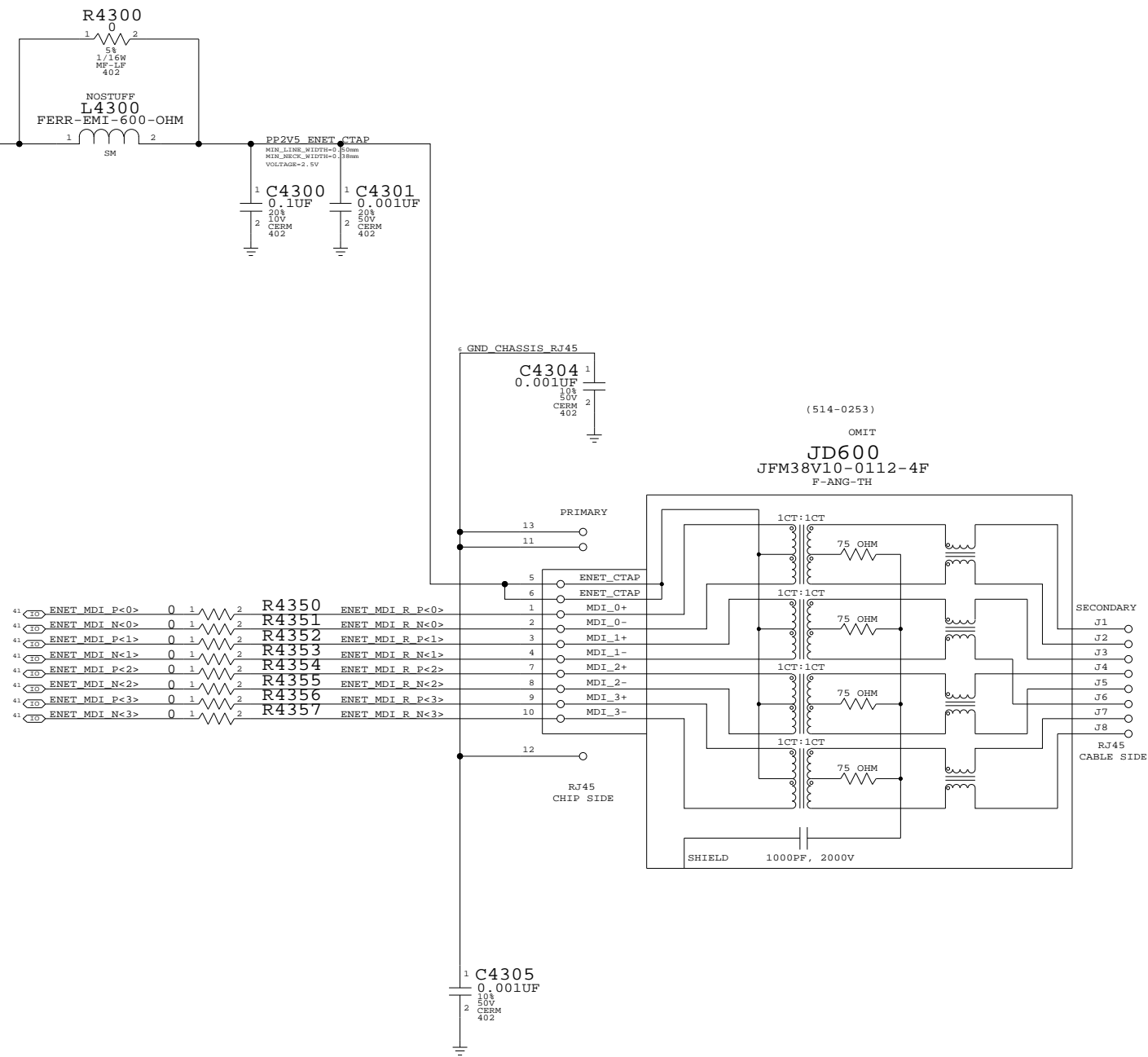
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ETHERNET MISC		
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER
	D	051-6949
SCALE	SHT	OF
NONE	42	111



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0300	1	CON,RJ-45 7 DEGRESS	JD600	CRITICAL	17_INCH_LCD
514-0301	1	CON,RJ-45 7 DEGRESS	JD600	CRITICAL	20_INCH_LCD

ETHERNET CONNECTOR

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APPLE COMPUTER INC.

SIZE DRAWING NUMBER

D

051-6949

REV.

09

SCALE

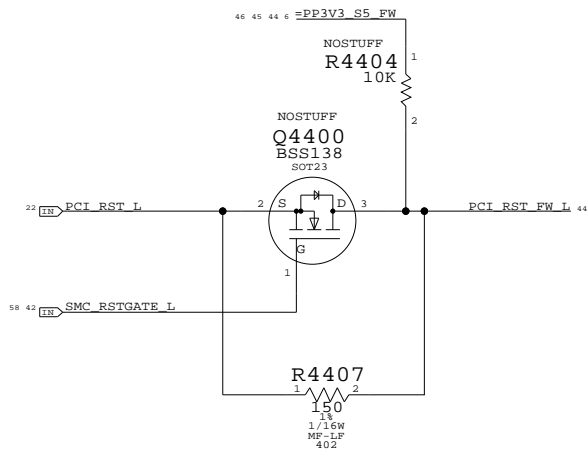
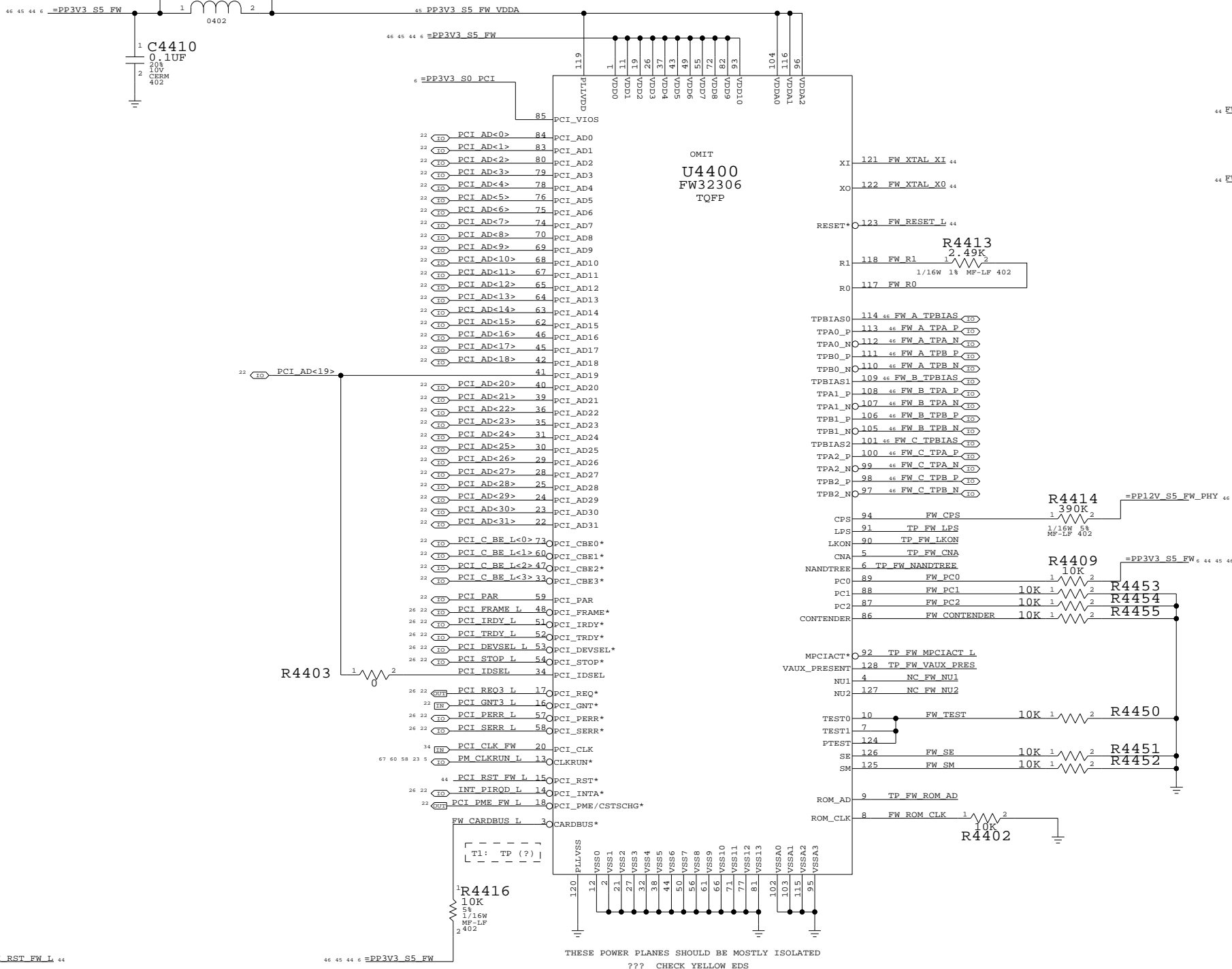
NONE

SHT

43

OF

111



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FW: FW323-06
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SYNC_MASTER=N/A                                SYNC_DATE=N/A
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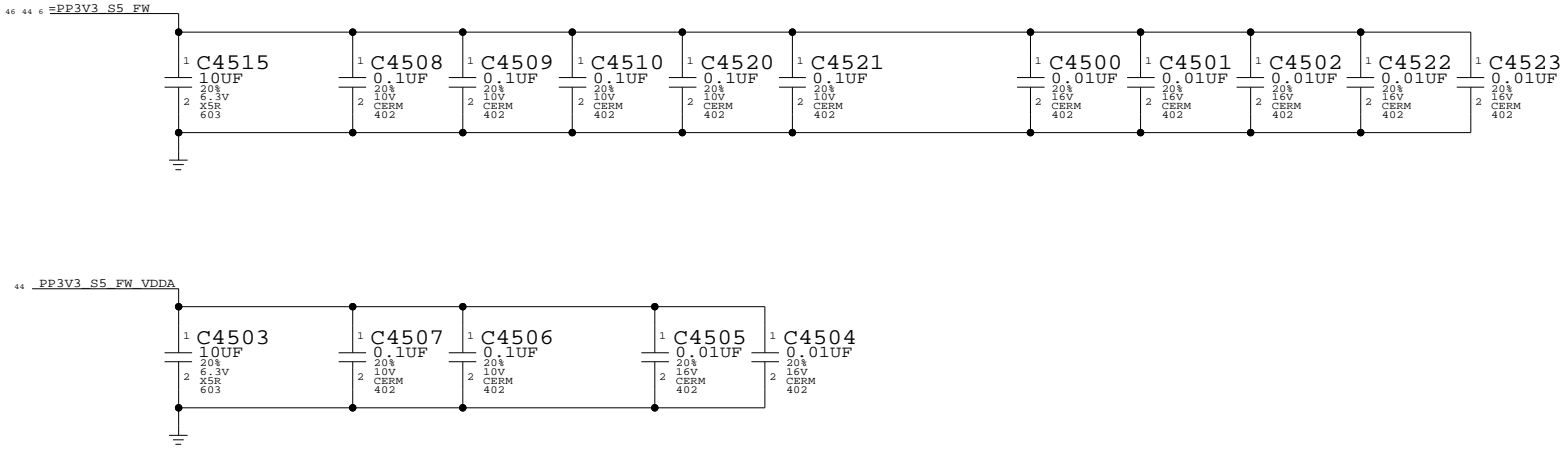
THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY
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    II NOT TO REPRODUCE OR COPY IT

    III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

```



FW: DECAPS

SYNC_MASTER=N/A SYNC_DATE=N/A

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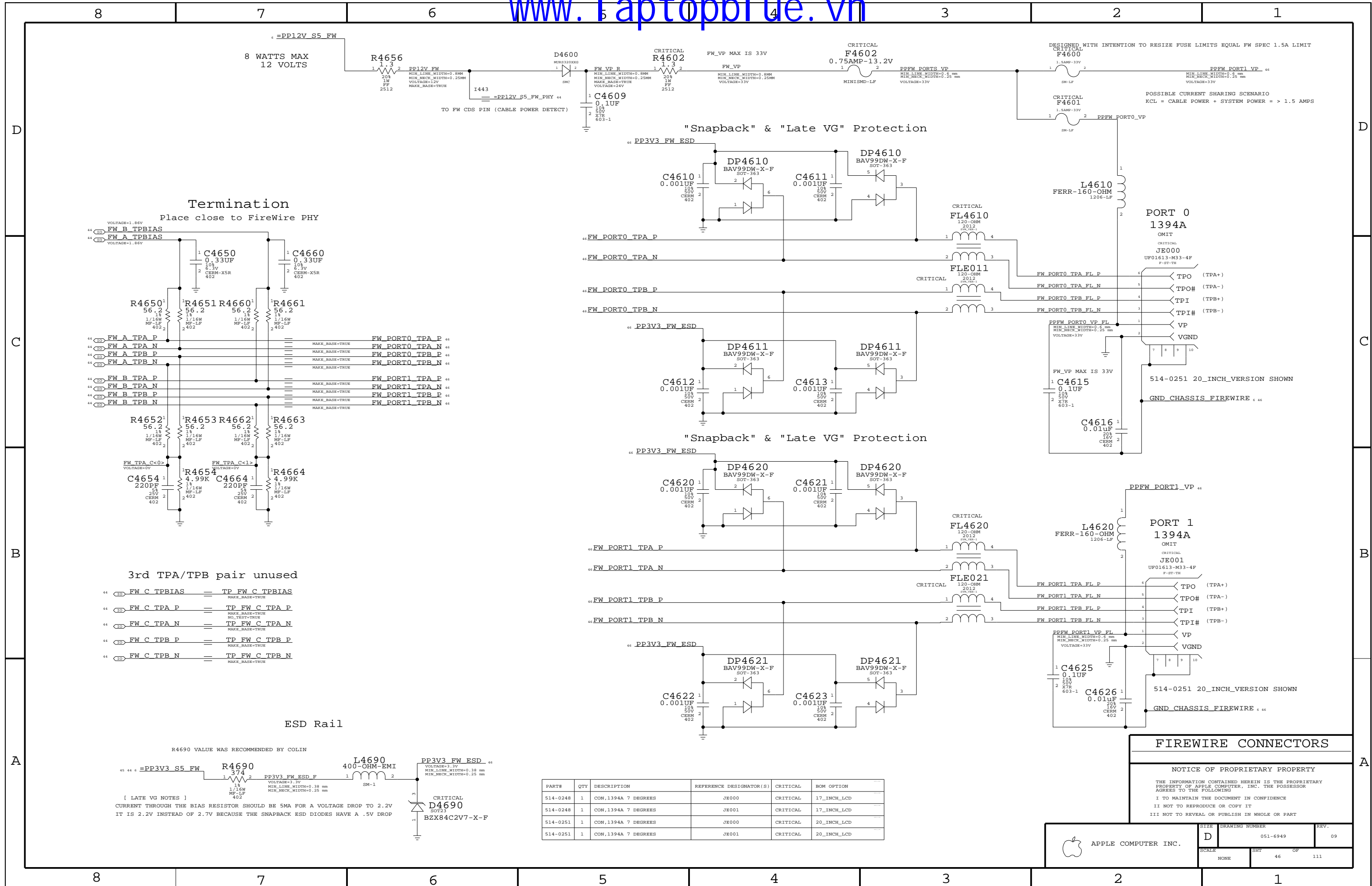
II NOT TO REPRODUCE OR COPY IT

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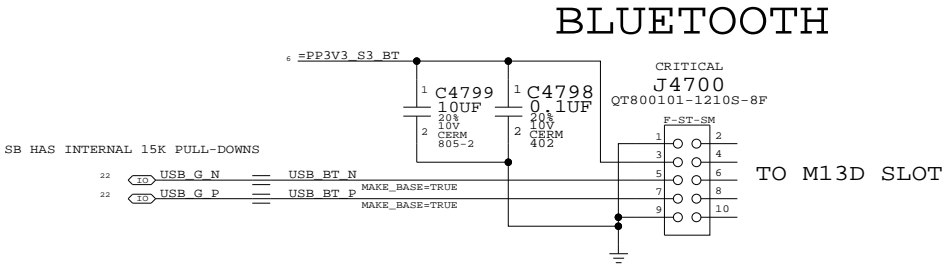
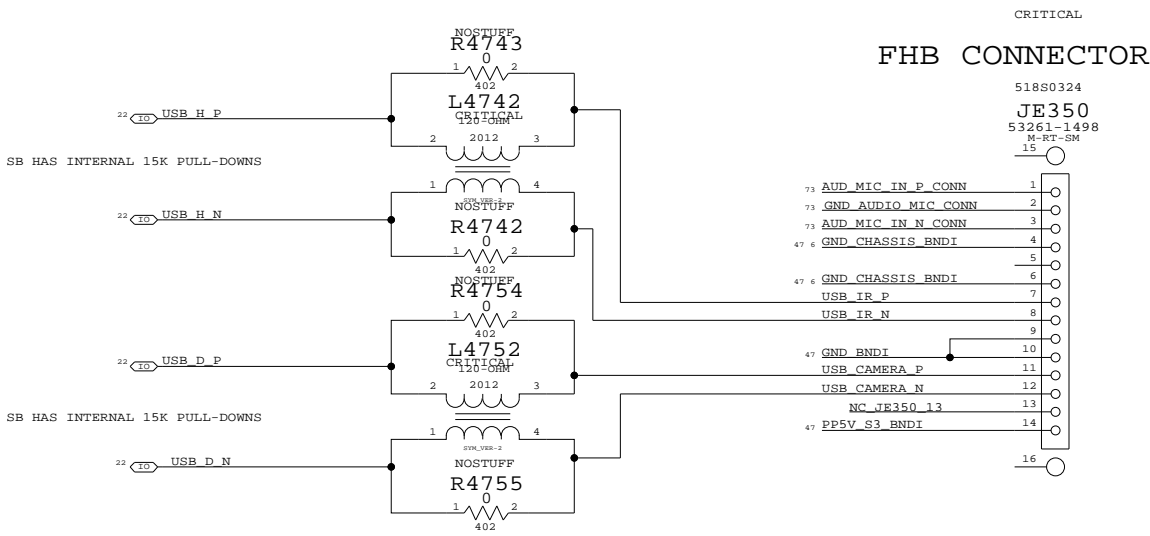
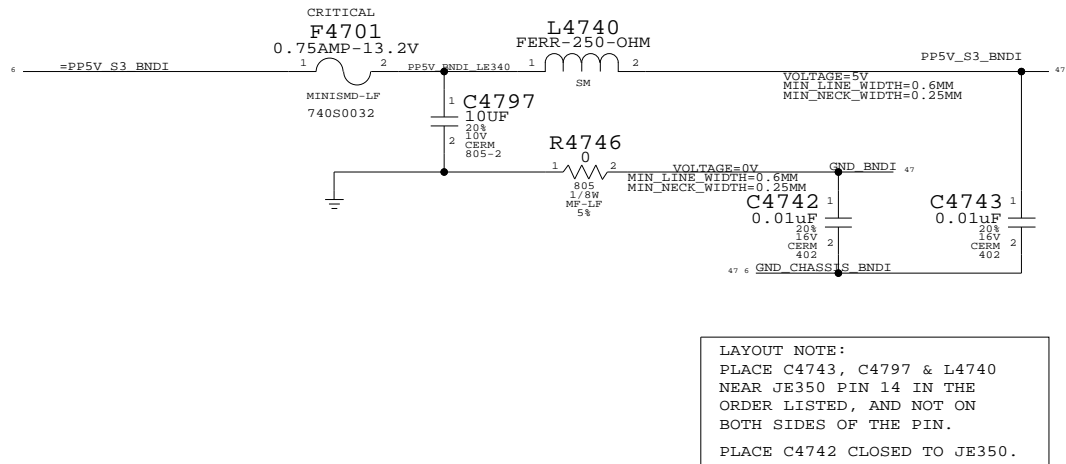
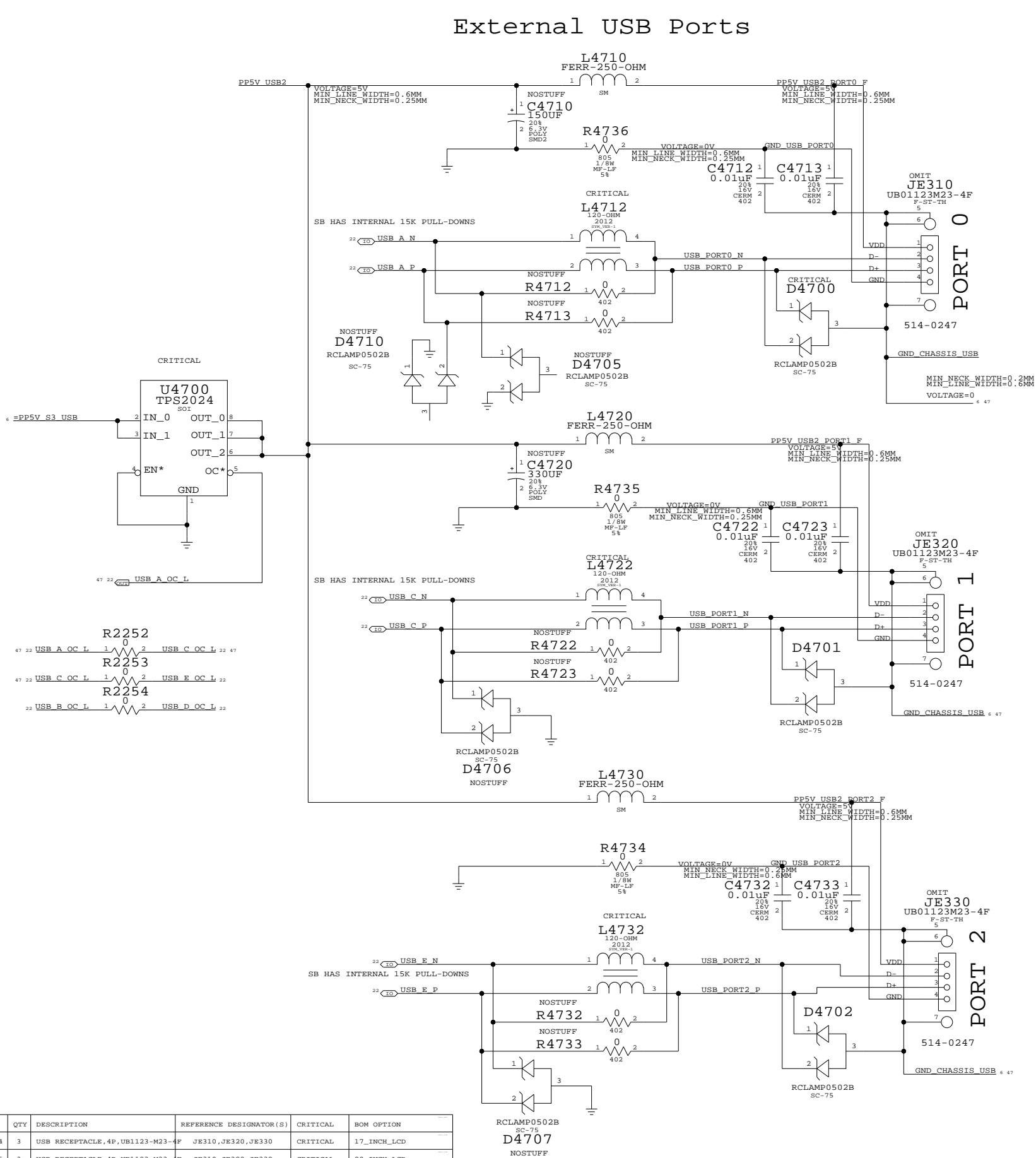


APPLE COMPUTER INC.

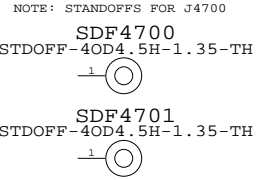
SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	45	111



External USB Ports



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
514-0294	3	USB RECEPTACLE,4P,UB1123-M23-4F	JE310,JE320,JE330	CRITICAL	17_INCH_LCD
514-0295	3	USB RECEPTACLE,4P,UB1123-M33-4F	JE310,JE320,JE330	CRITICAL	20_INCH_LCD



USB Device Interfaces

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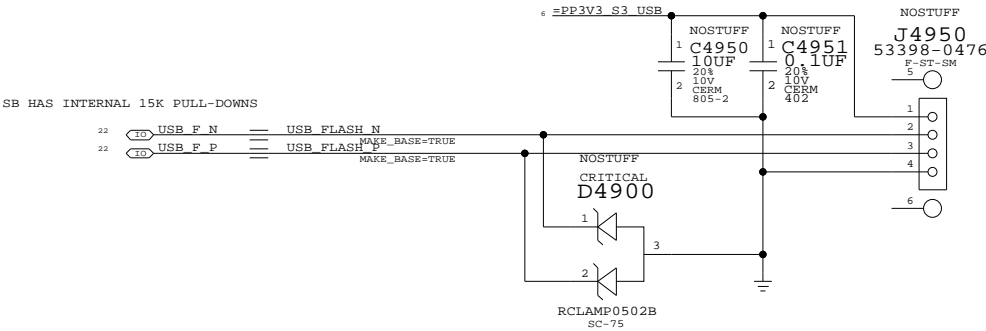
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III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SCALE	DRAWING NUMBER		REV.
	NONE	D	051-6950	06
SHEET		47	OF 111	

CARD-READER USB CONN



Flash Connector


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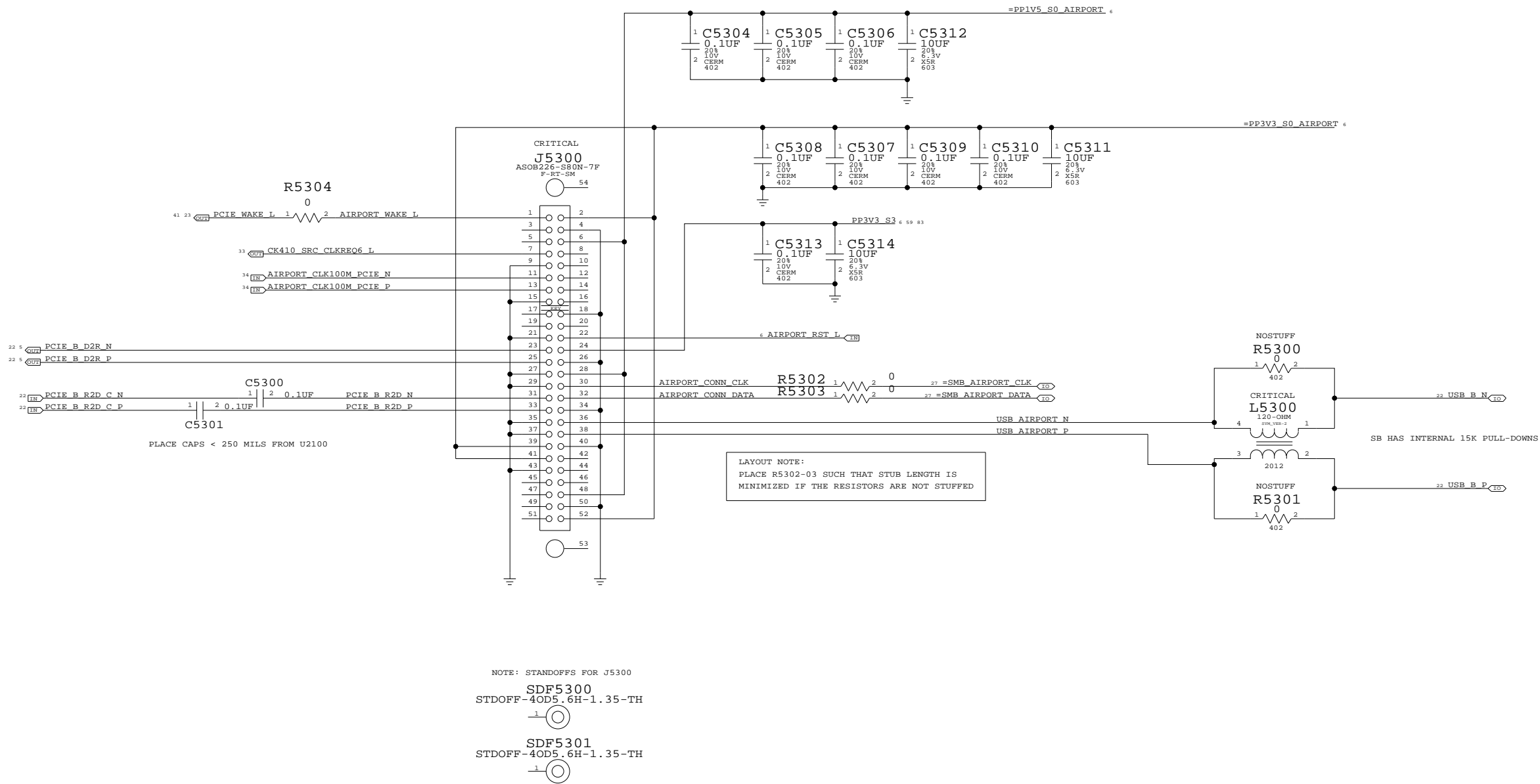
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II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

 APPLE COMPUTER INC.

SIZE D	DRAWING NUMBER 051-6949	REV. 09
SCALE NONE	SHT 49	OF 111



AIRPORT CONN

SYNC_MASTER=N/A SYNC_DATE=N/A

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APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	53	111

D

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22	<div>IN</div> PCIE C R2D C N	==	TP PCIE C R2D C N
			MAKE_BASE=TRUE
22	<div>IN</div> PCIE C R2D C P	==	TP PCIE C R2D C P
			MAKE_BASE=TRUE
22	<div>OUT</div> PCIE C D2R N	==	TP PCIE C D2R N
			MAKE_BASE=TRUE
22	<div>OUT</div> PCIE C D2R P	==	TP PCIE C D2R P
			MAKE_BASE=TRUE
22	<div>IN</div> PCIE D R2D C N	==	TP PCIE D R2D C N
			MAKE_BASE=TRUE
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PCIE UNUSED PORTS

SYNC_MASTER=N/A SYNC_DATE=N/A

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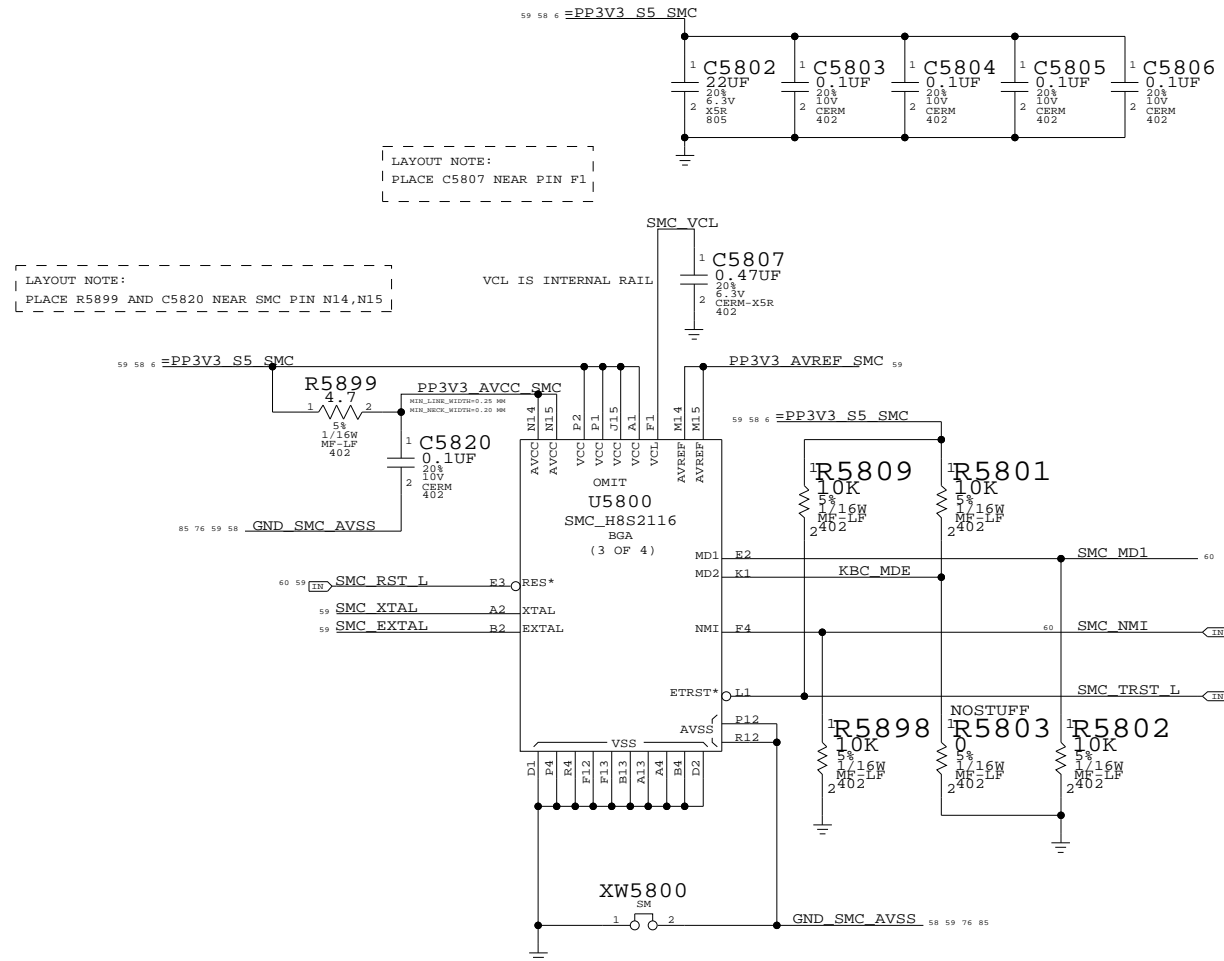
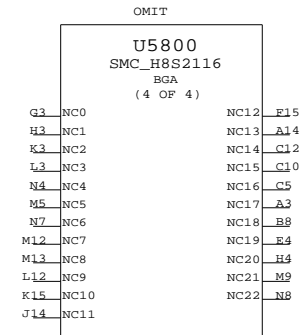
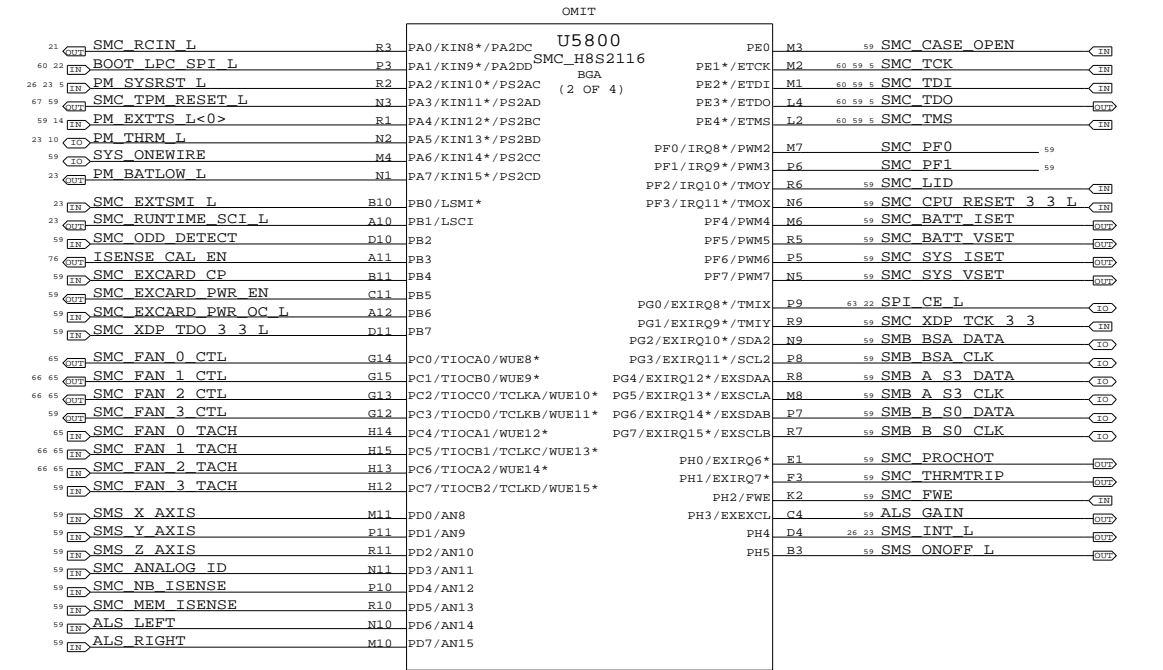
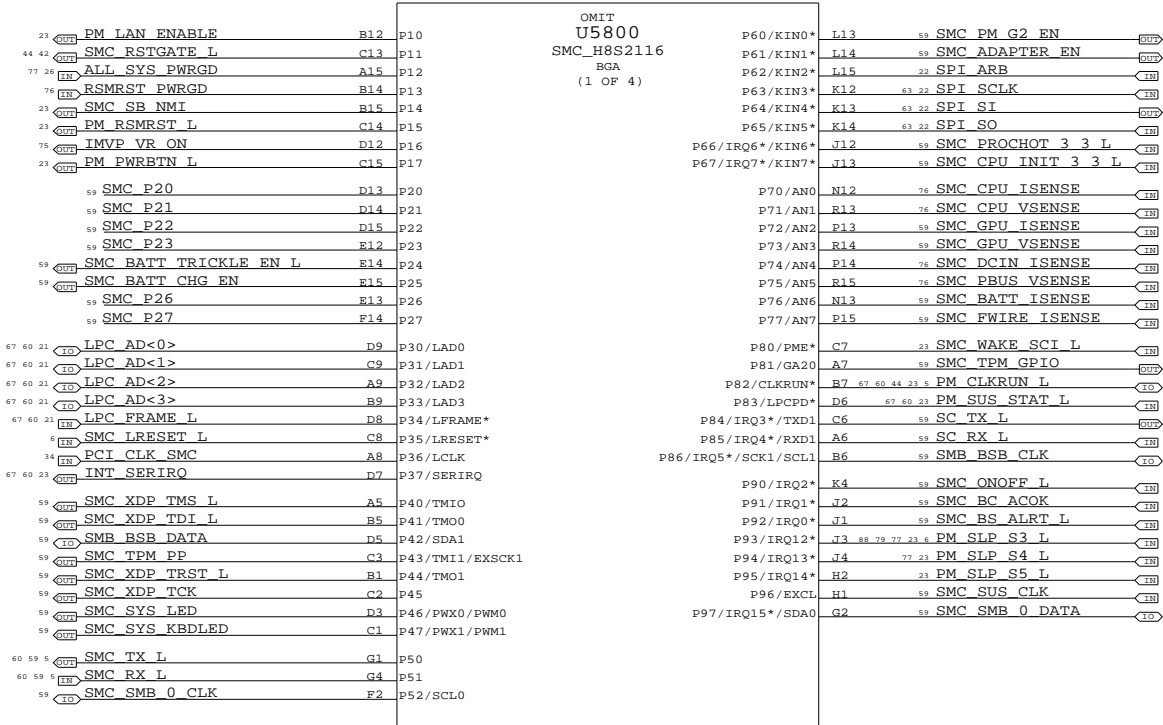
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	54	111

UNUSED PINS HAVE THE FORMAT SMC_XXX WHERE XXX IS THE PORT NUMBER. THEY ARE SET BY SOFTWARE TO BE DRIVEN OUTPUTS ALWAYS SO THEY CAN BE LEFT NO-CONNECTED.



SMC

SYNC_MASTER=N/A SYNC_DATE=N/A

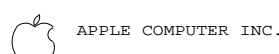
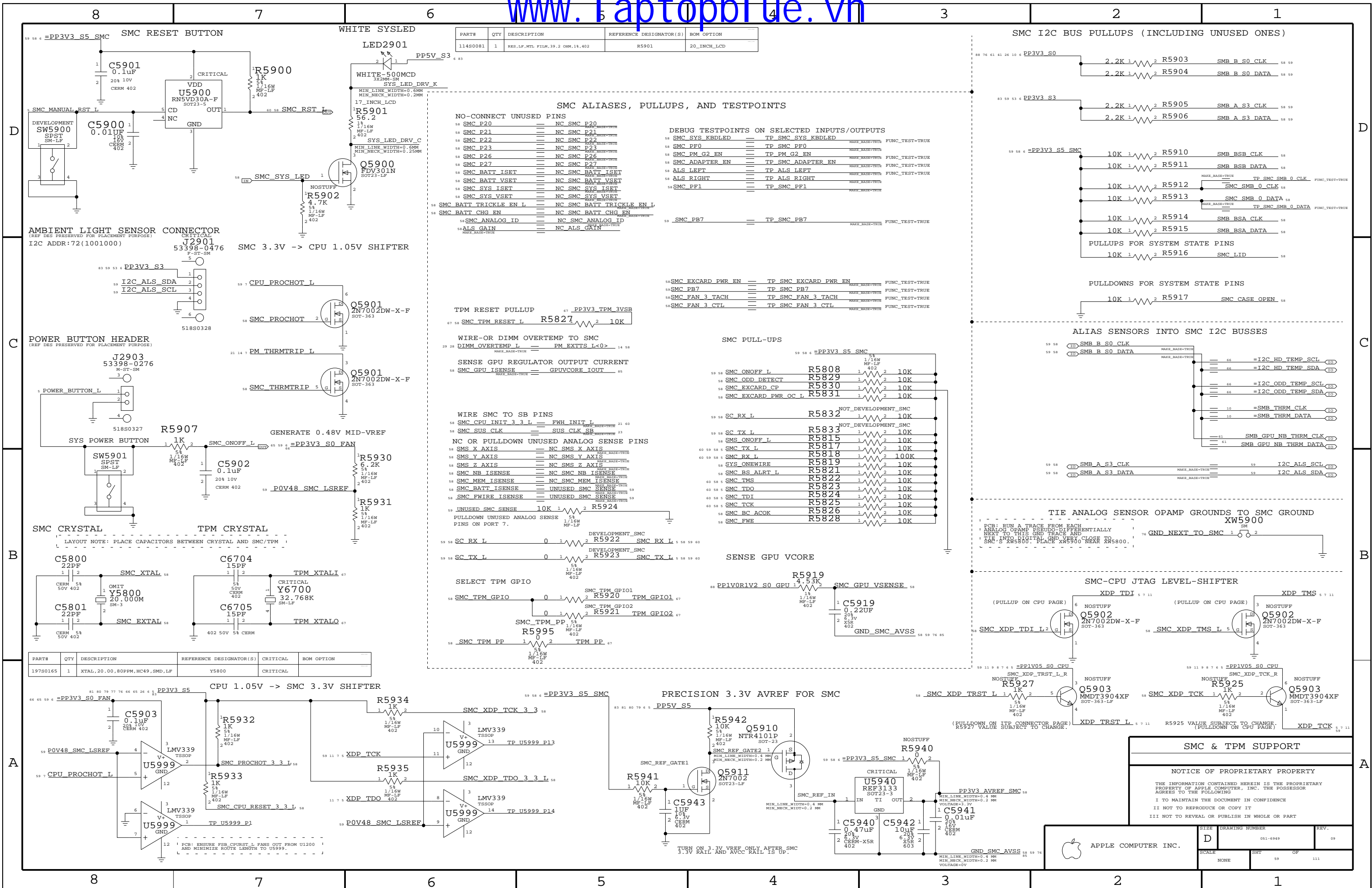
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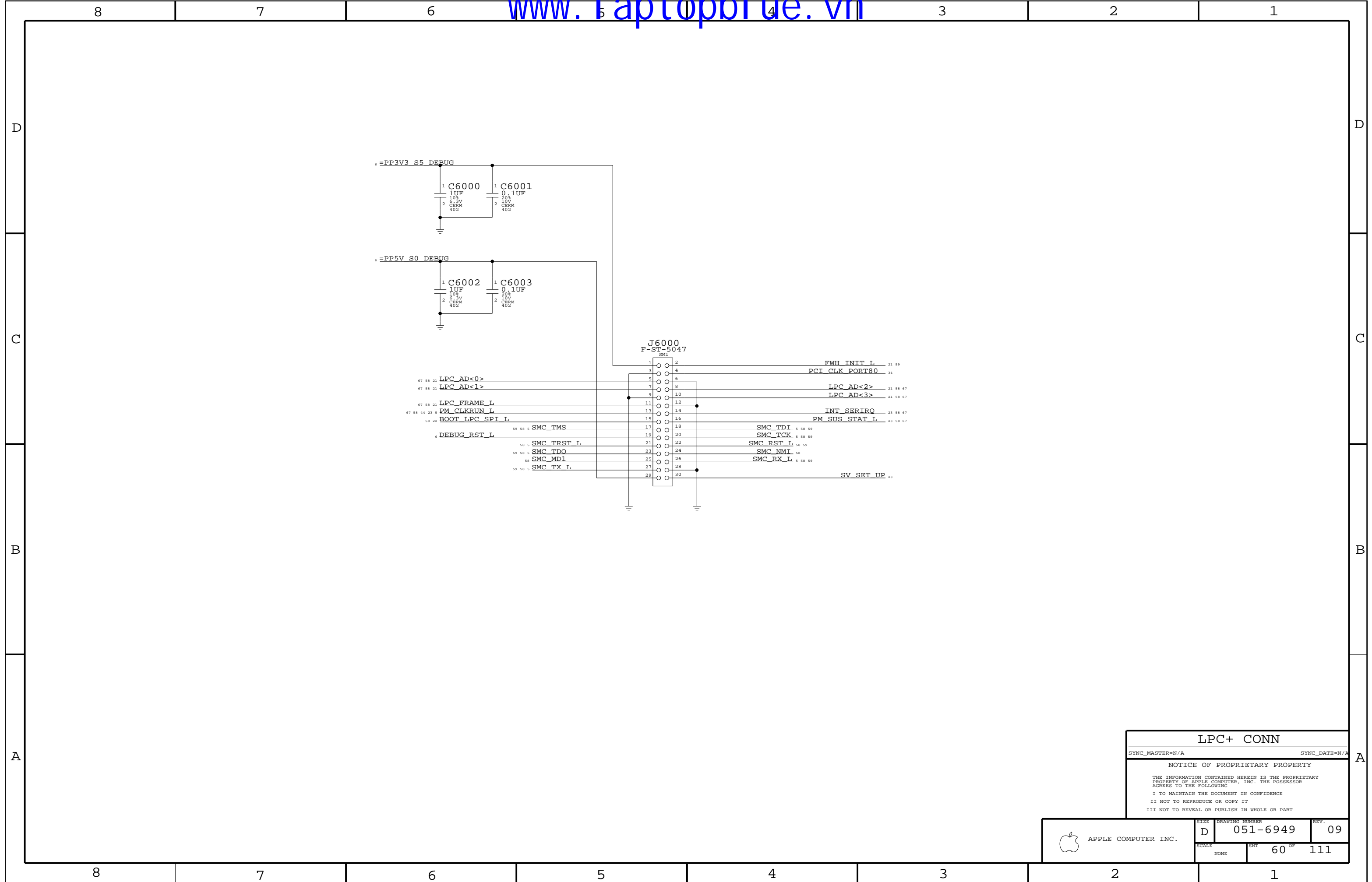
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SMC & TPM SUPPORT		
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III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		
SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	59	111



LPC+ CONN

SYNC_MASTER=N/A SYNC_DATE=N/A

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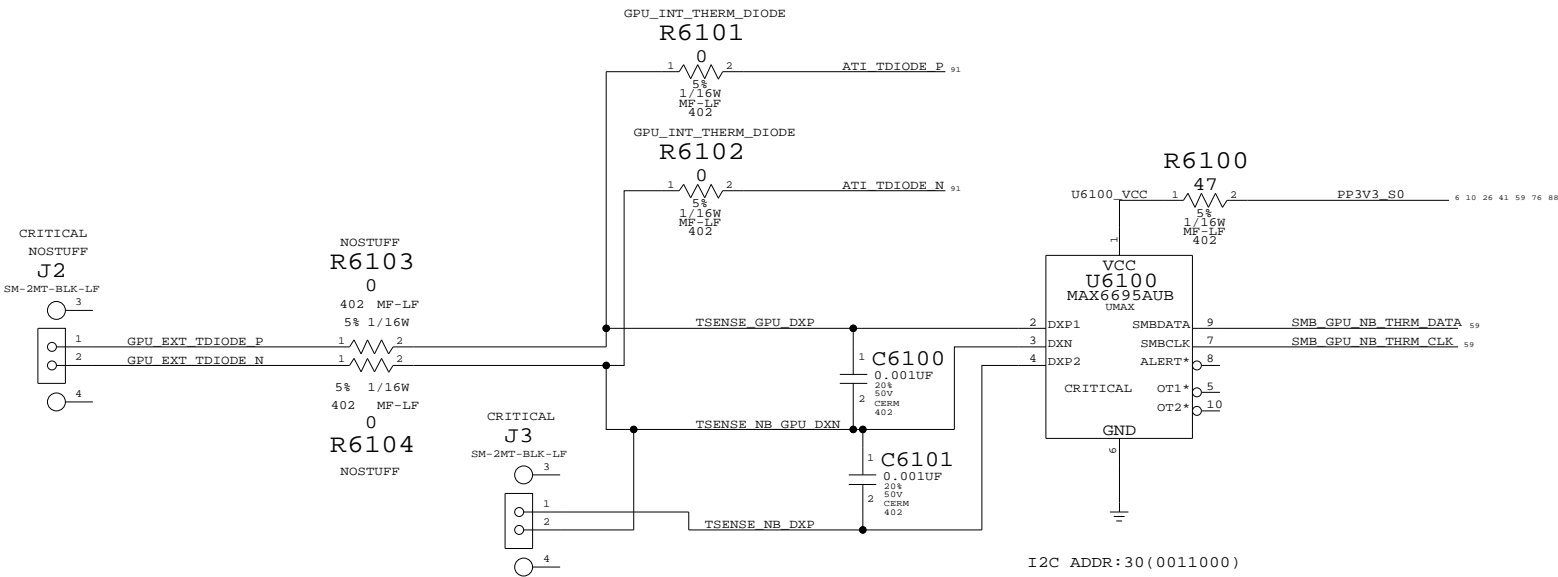
II NOT TO REPRODUCE OR COPY IT

III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART



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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	60	111



GPU+NB THERMAL

SYNC_MASTER=N/A

SYNC_DATE=N/A

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SIZE

D

DRAWING NUMBER

051-6949

REV.

09

SCALE

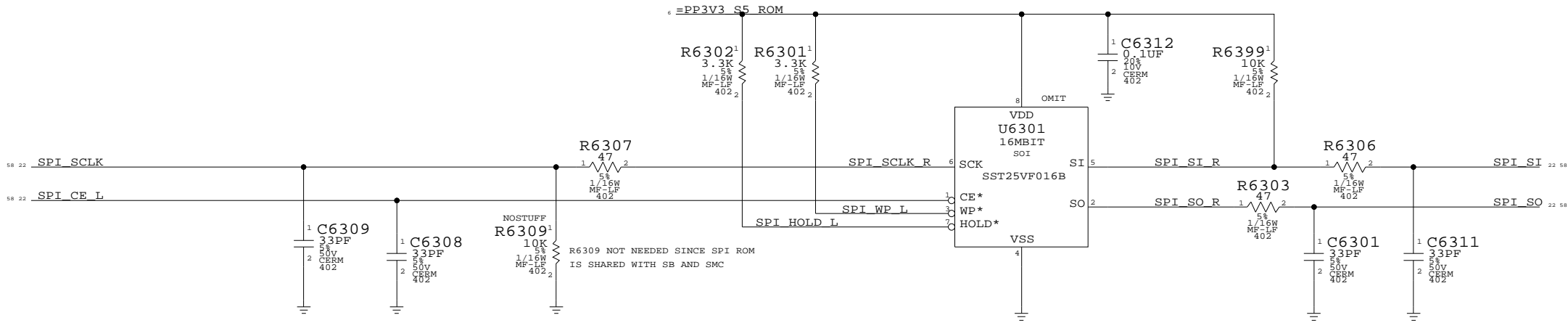
NONE

SHT

61

OF

111



R6306-07 SHOULD BE PLACED LESS THAN 2.54MM FROM U2100
R6303 SHOULD BE PLACED LESS THAN 2.54MM FROM U6301

SPI BOOTROM

SYNC_MASTER=MASTER

SYNC_DATE=5/23/05

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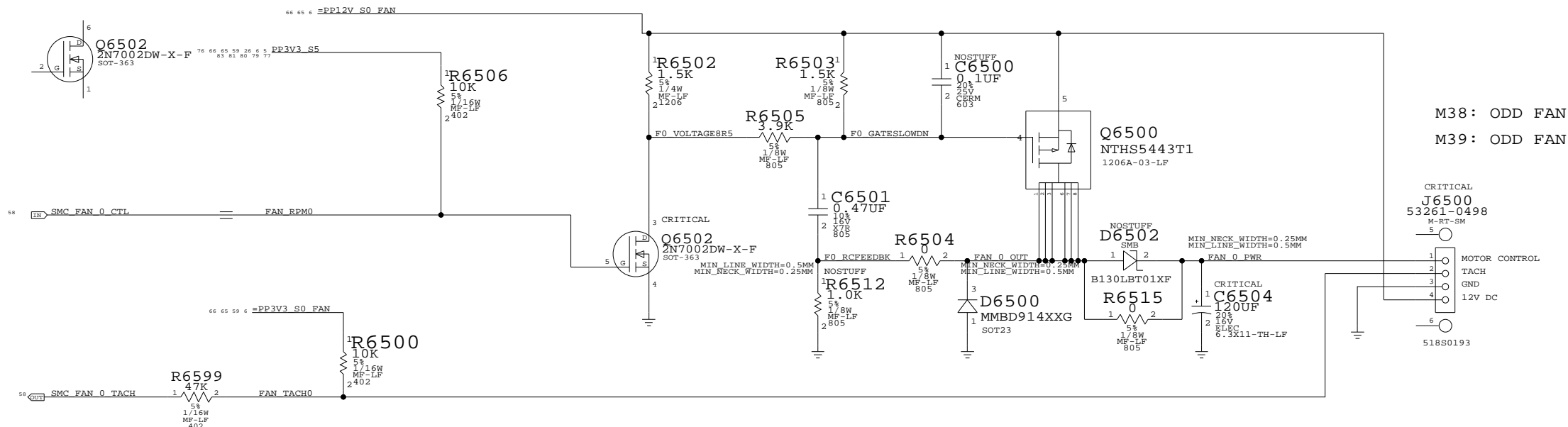
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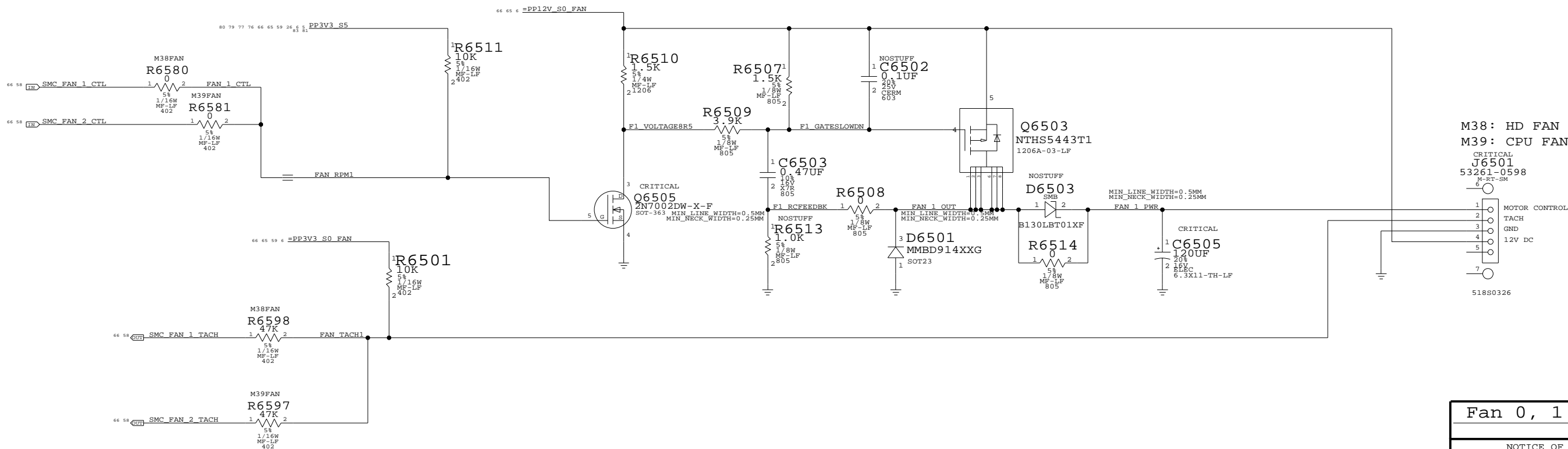
III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER		REV.
	D	051-6949		09
SCALE		SHT	OF	
NONE		63	111	

FAN 0



FAN 1



Fan 0, 1 & System Temp

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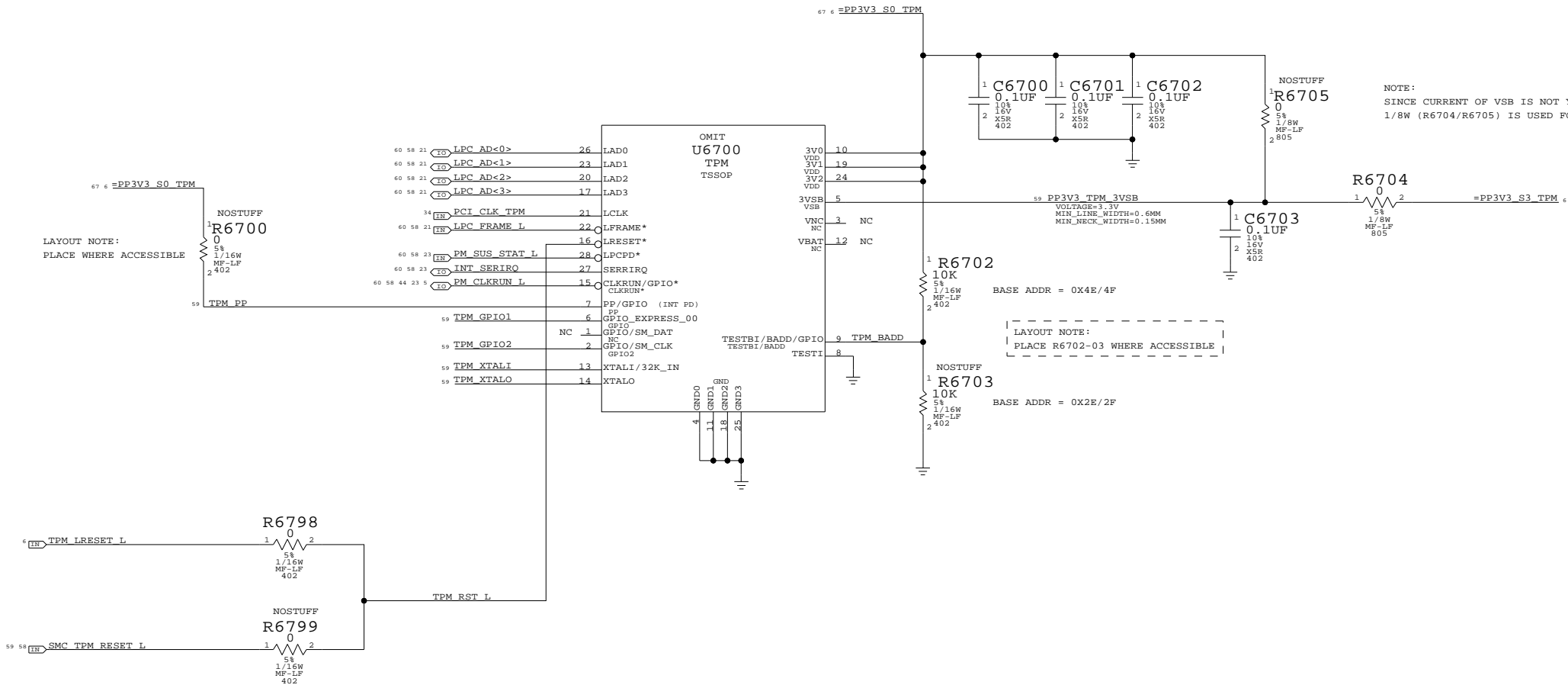


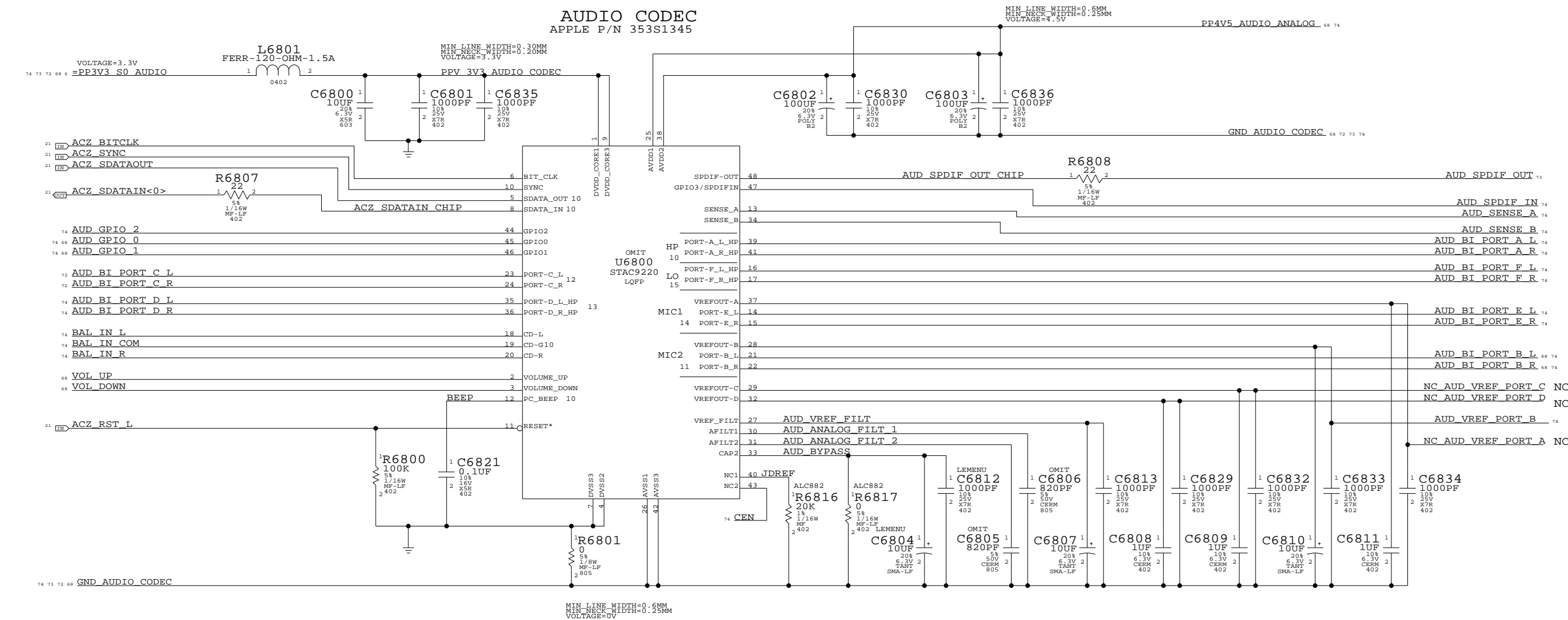
APPLE COMPUTER INC.

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SCALE	SHT	OF
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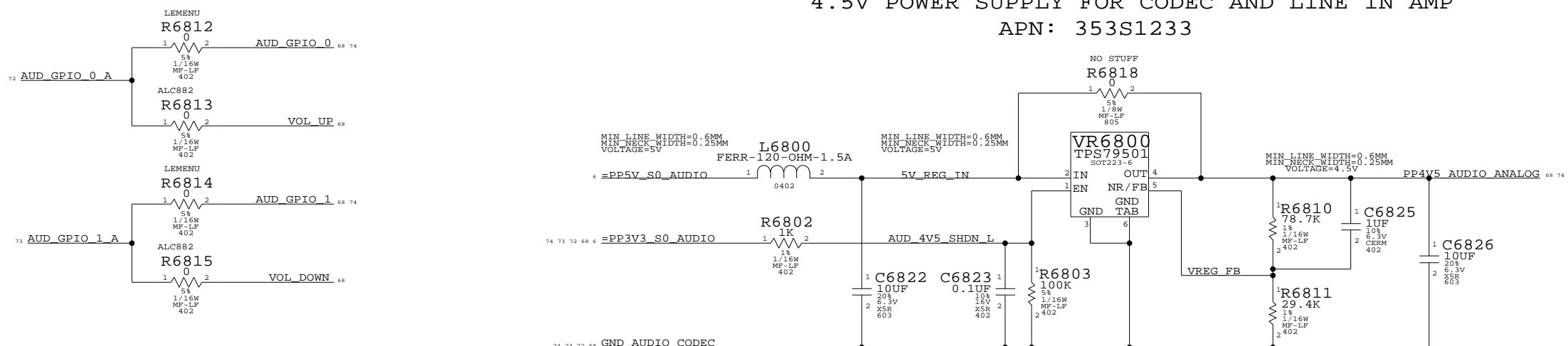


111





4.5V POWER SUPPLY FOR CODEC AND LINE IN AMP
APN: 353S1233



AUDIO: CODEC

SYNC_MASTER=FINO-SO SYNC_DATE=04/28/2005

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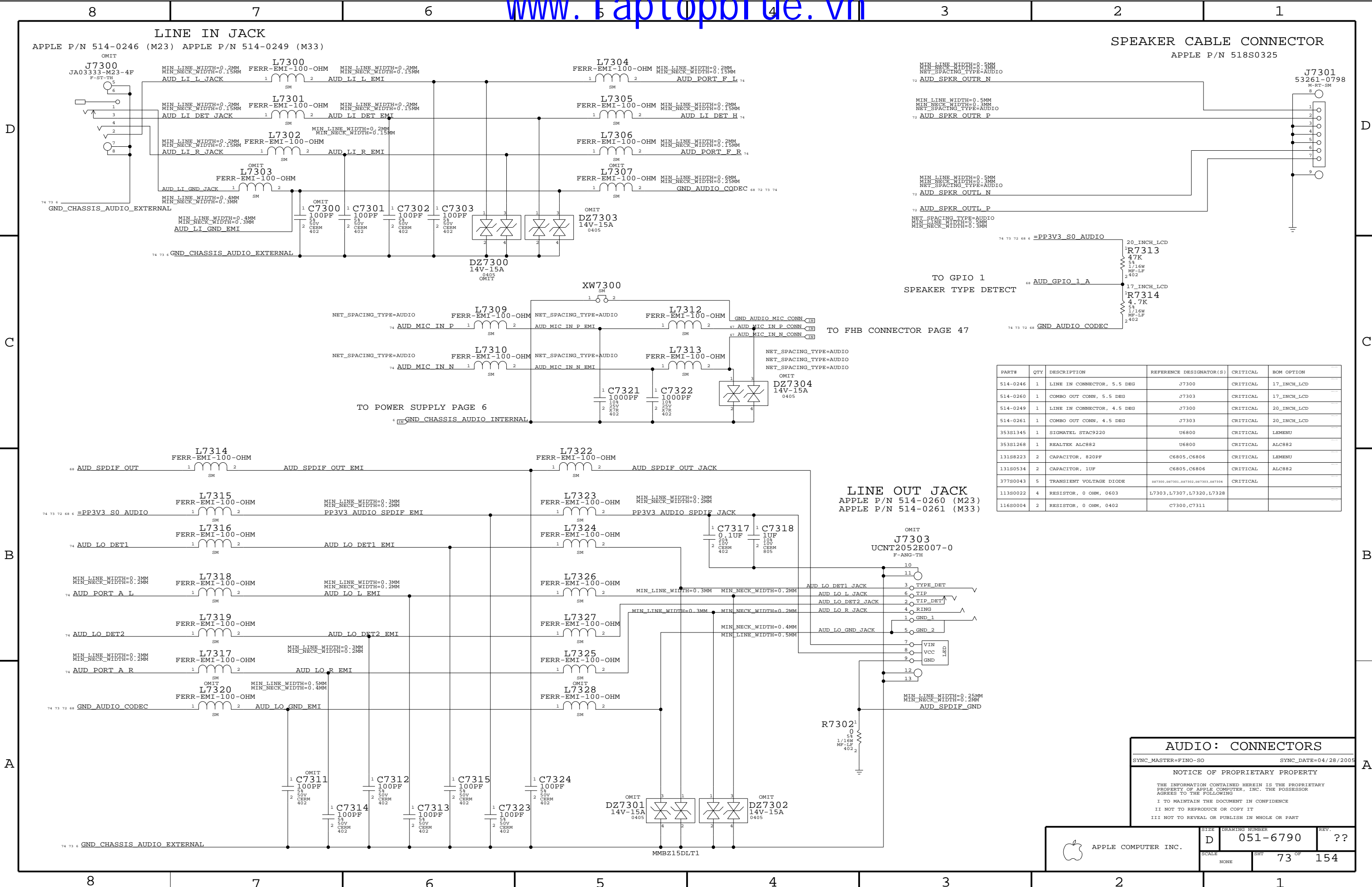
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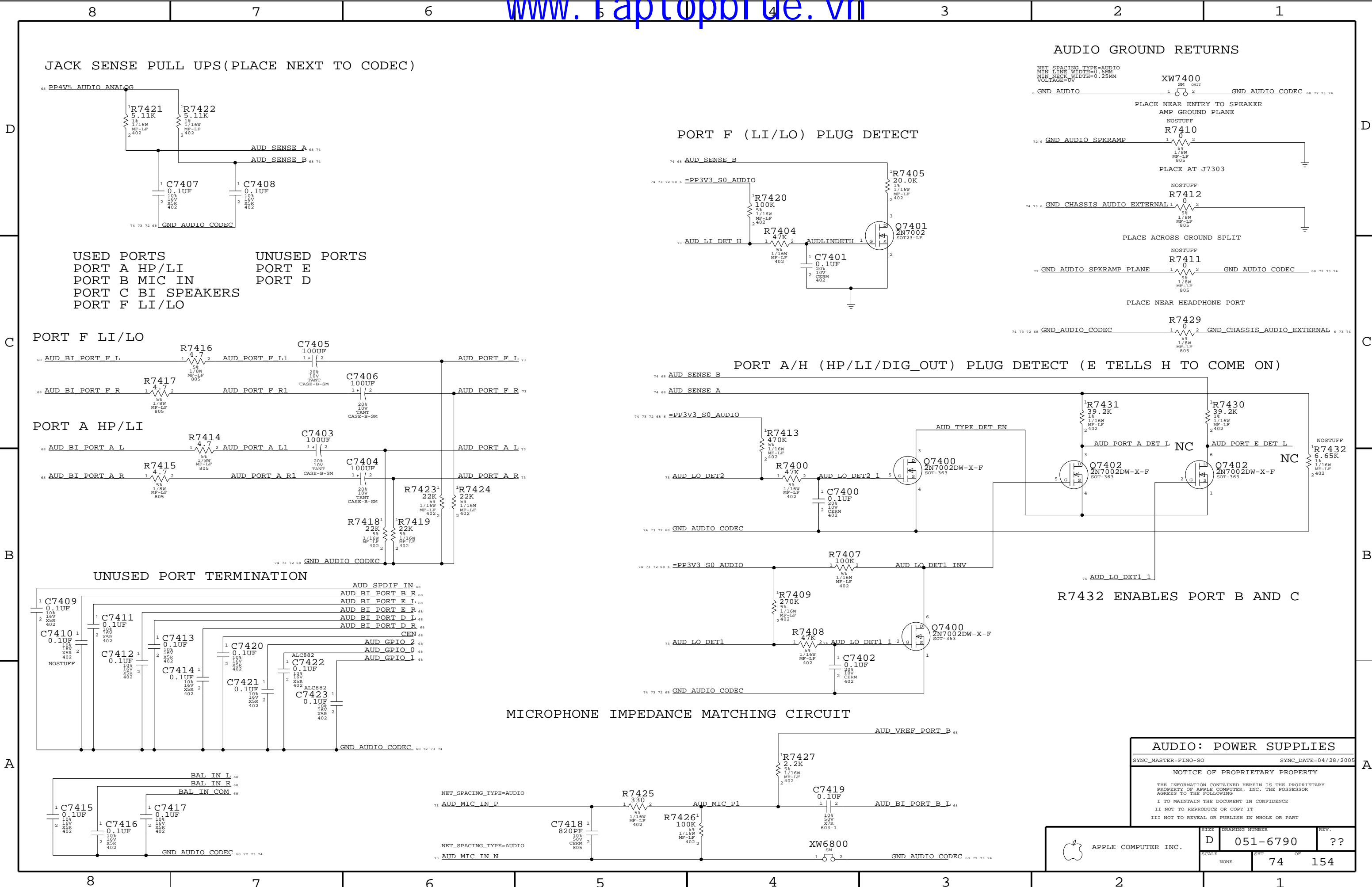
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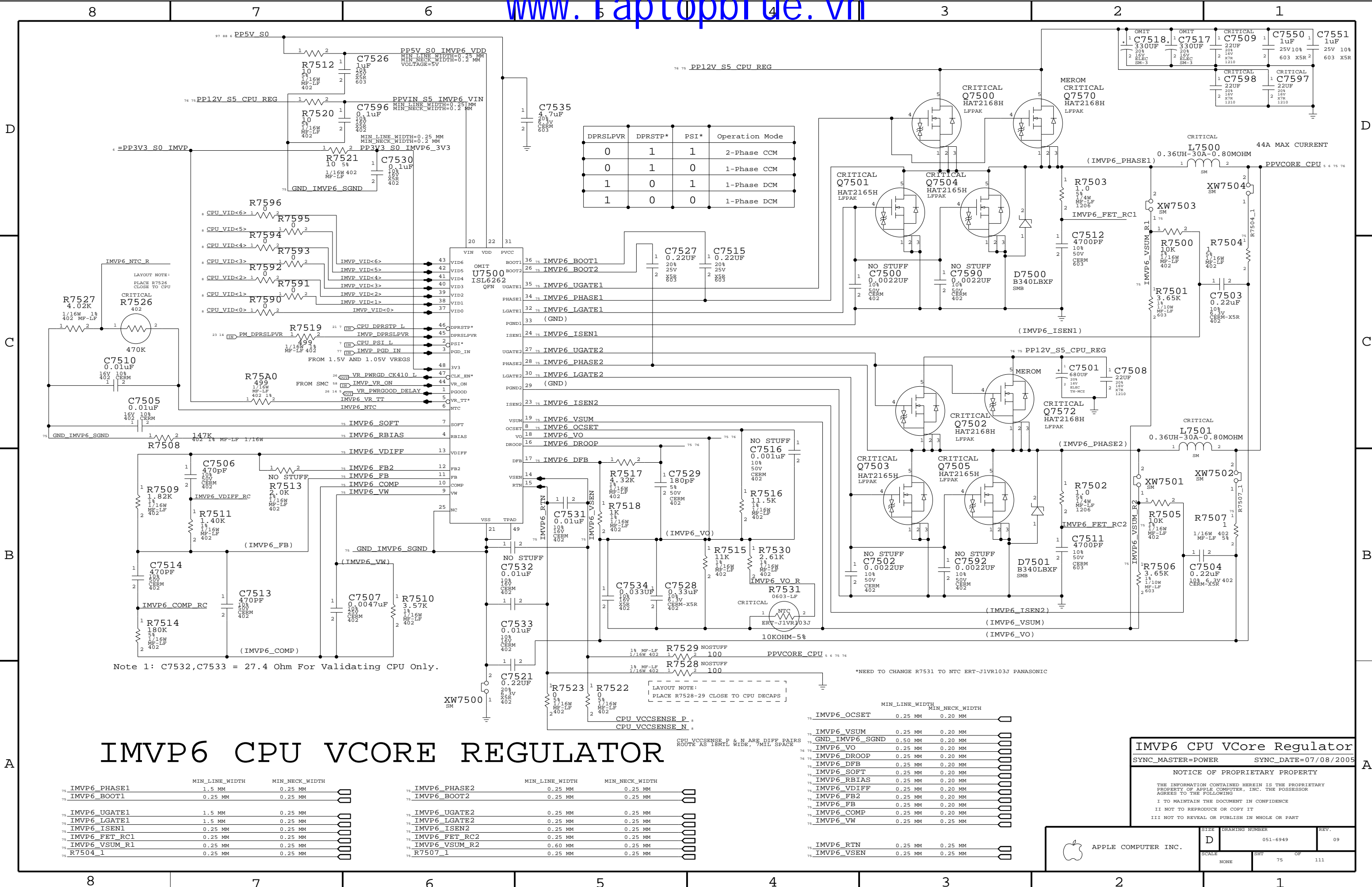
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	D	051-6790	??	
SCALE		NONE	SHT	OF
		68	154	



15







IMVP6 CPU VCore REGULATOR

IMVP6 CPU VCore Regulator

SYNC_MASTER=POWER SYNC_DATE=07/08/2005

NOTICE OF PROPRIETARY PROPERTY

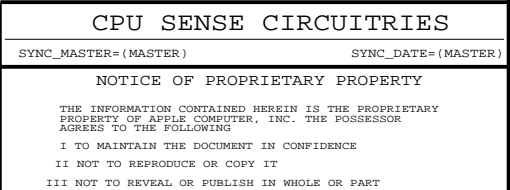
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Switches in fixed load on power supplies to calibrate current sense circuits



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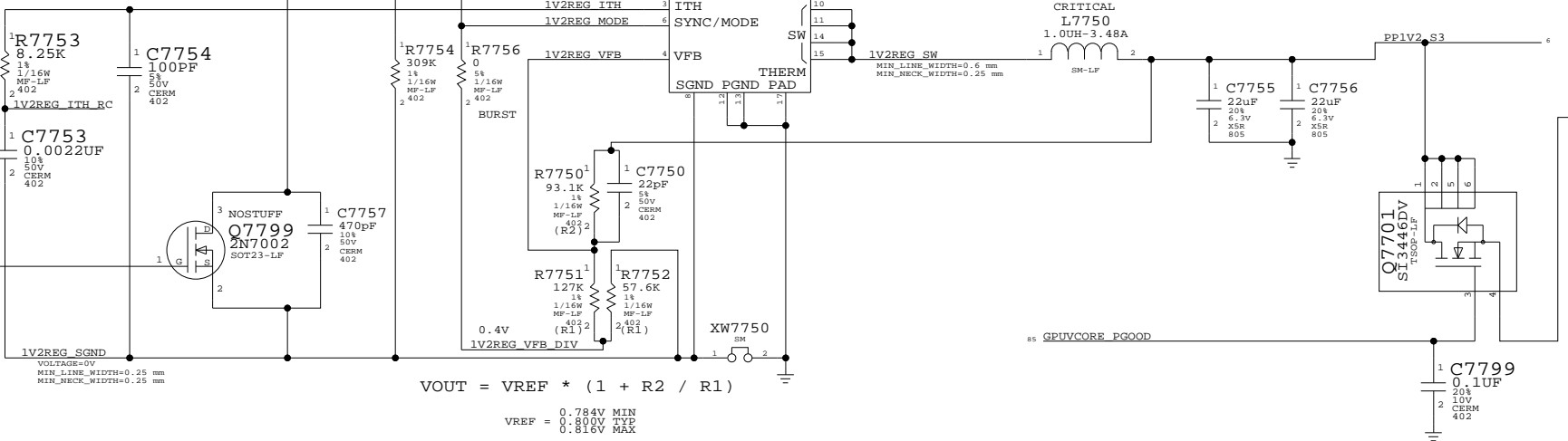
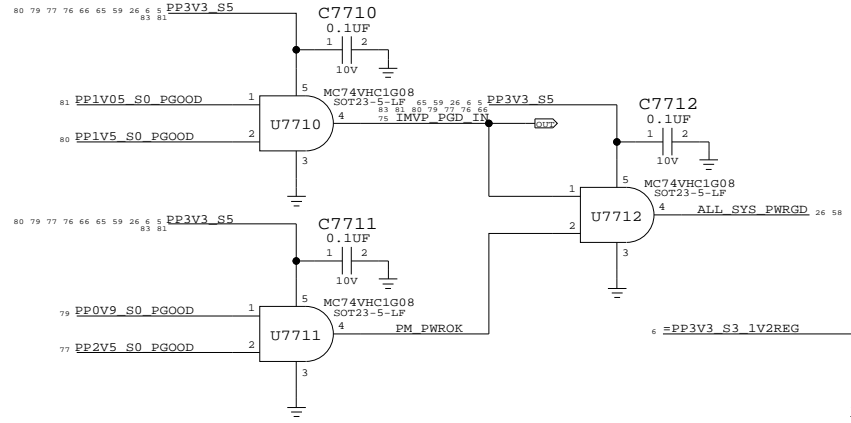
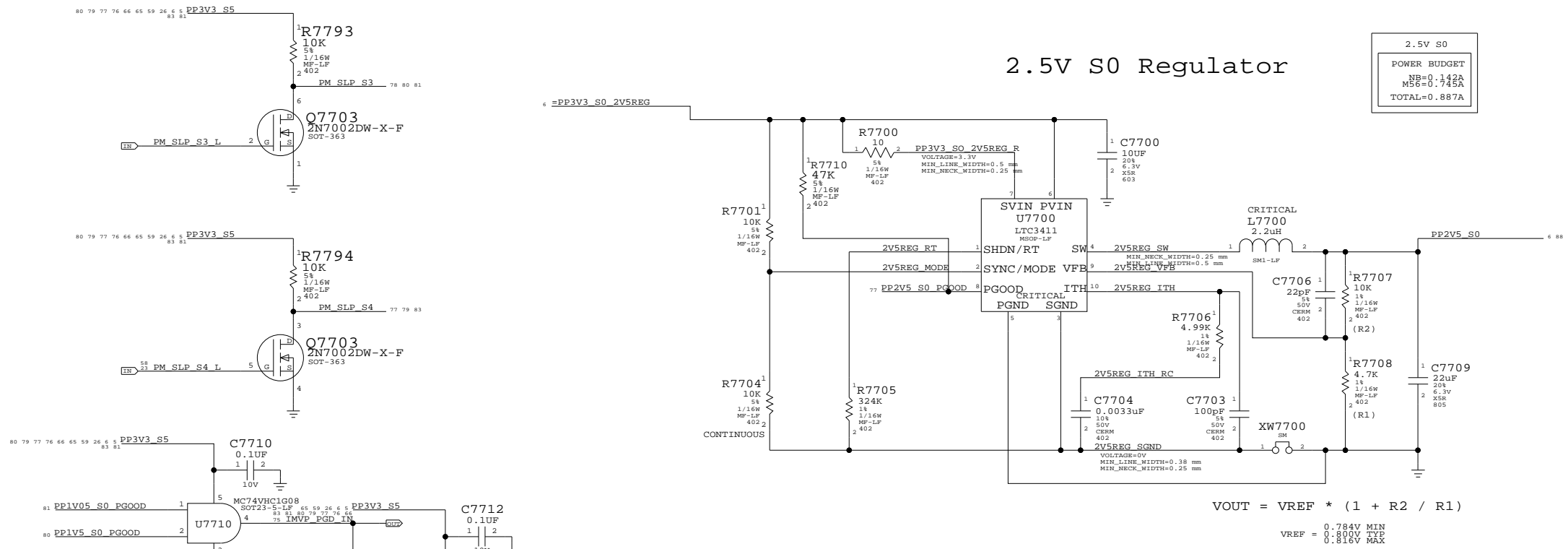
A

D

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2.5V & 1.2V GRAPHICS REGULATORS

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

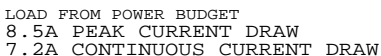
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1.5V S0

POWER BUDGET

M56=2.400A

GDDR=3.320A


IO=1.750A

TOTAL=8.010A

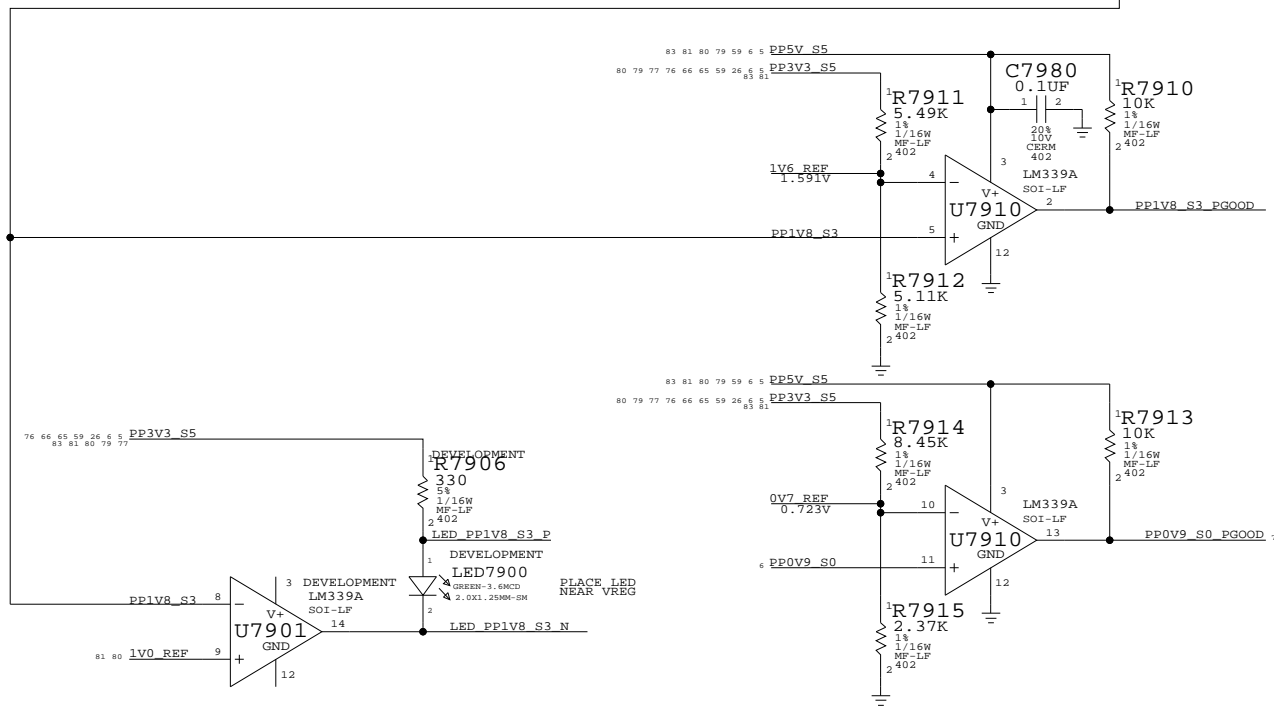
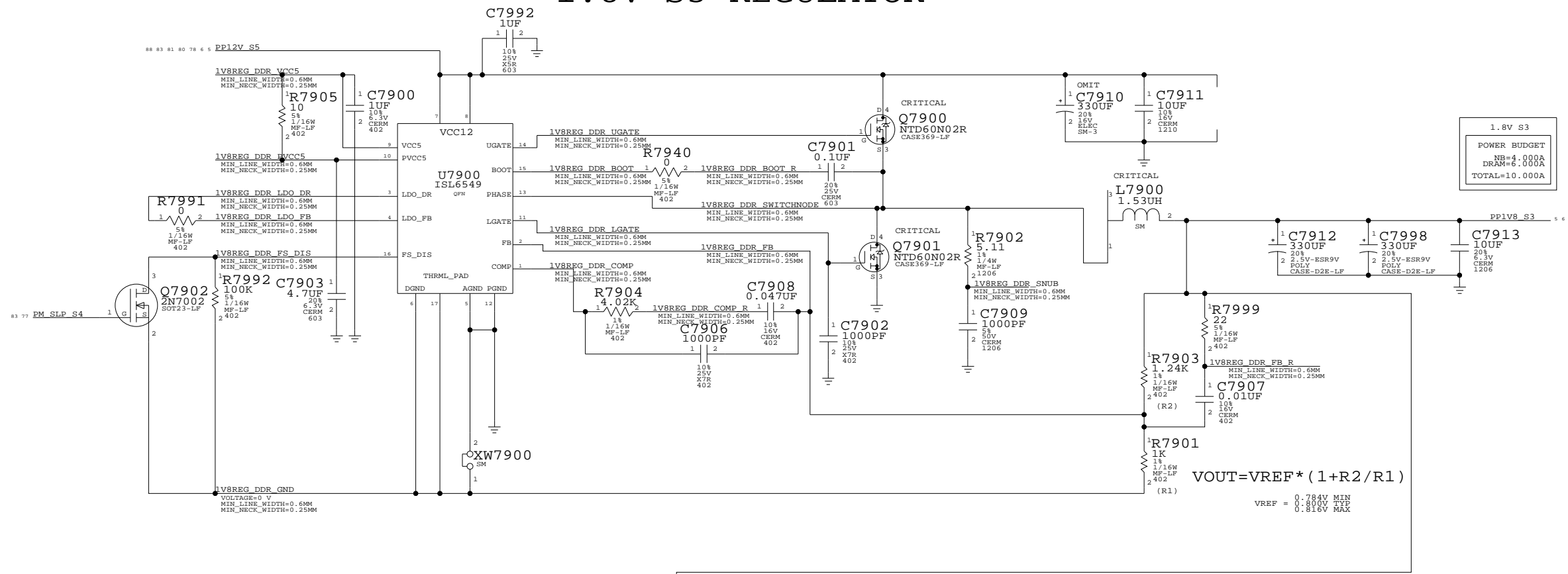
$$V_{OUT} = V_{REF} * (1 + R_2 / R_1)$$

VREF = 0.784V MIN
0.800V TYP
0.816V MAX

1.8V GDDR REGULATOR	
SYNC_MASTER=(MASTER)	SYNC_DATE=(MASTER)
NOTICE OF PROPRIETARY PROPERTY	
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	D	051-6949	09
	SCALE	SHT	
	NONE	78 OF	111

1.8V S3 REGULATOR



1.8V Vreg

SYNC_MASTER=M23-PC SYNC_DATE=04/12/2005

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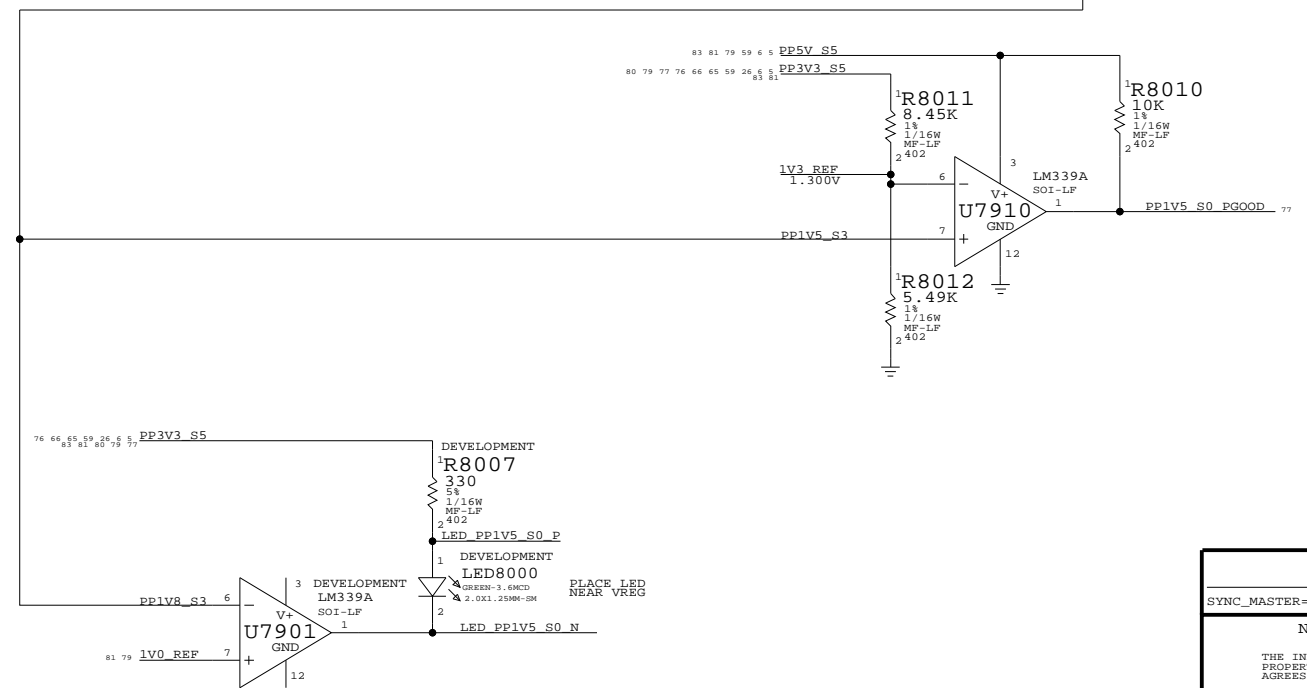
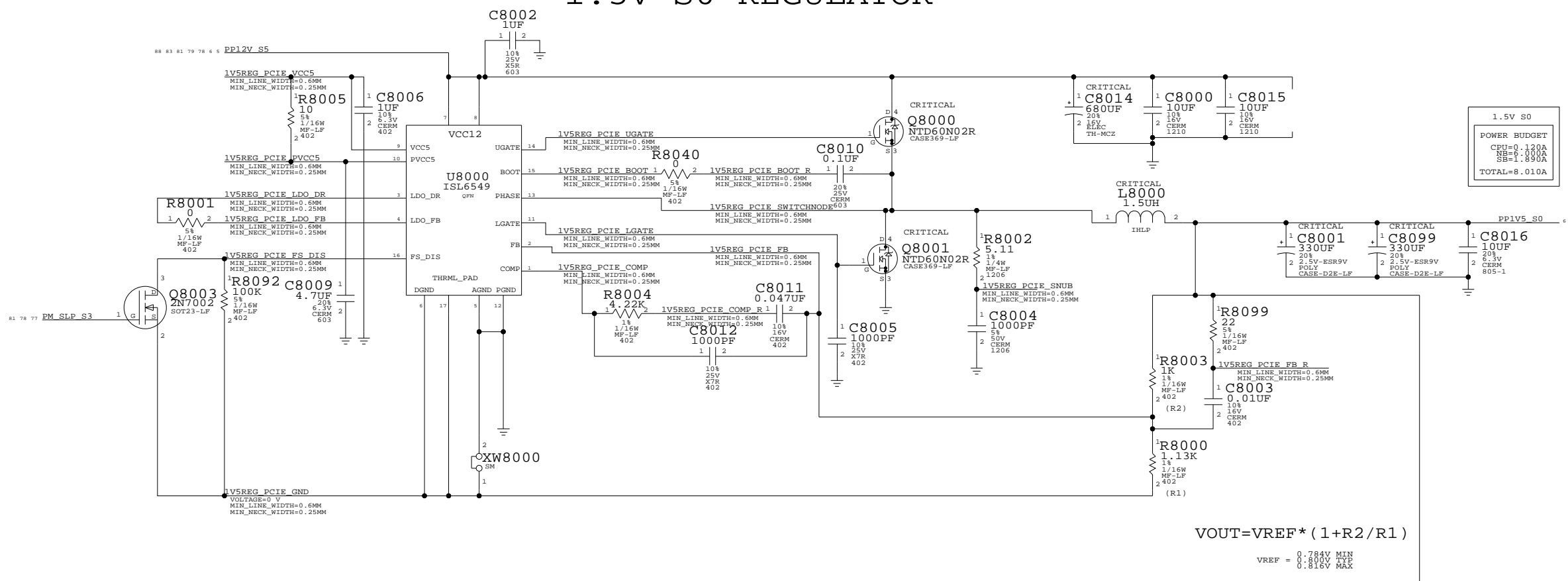
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1.5V S0 REGULATOR



1.5V Vreg

SYNC_MASTER=FINO-PC SYNC_DATE=05/18/2005

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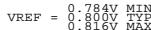
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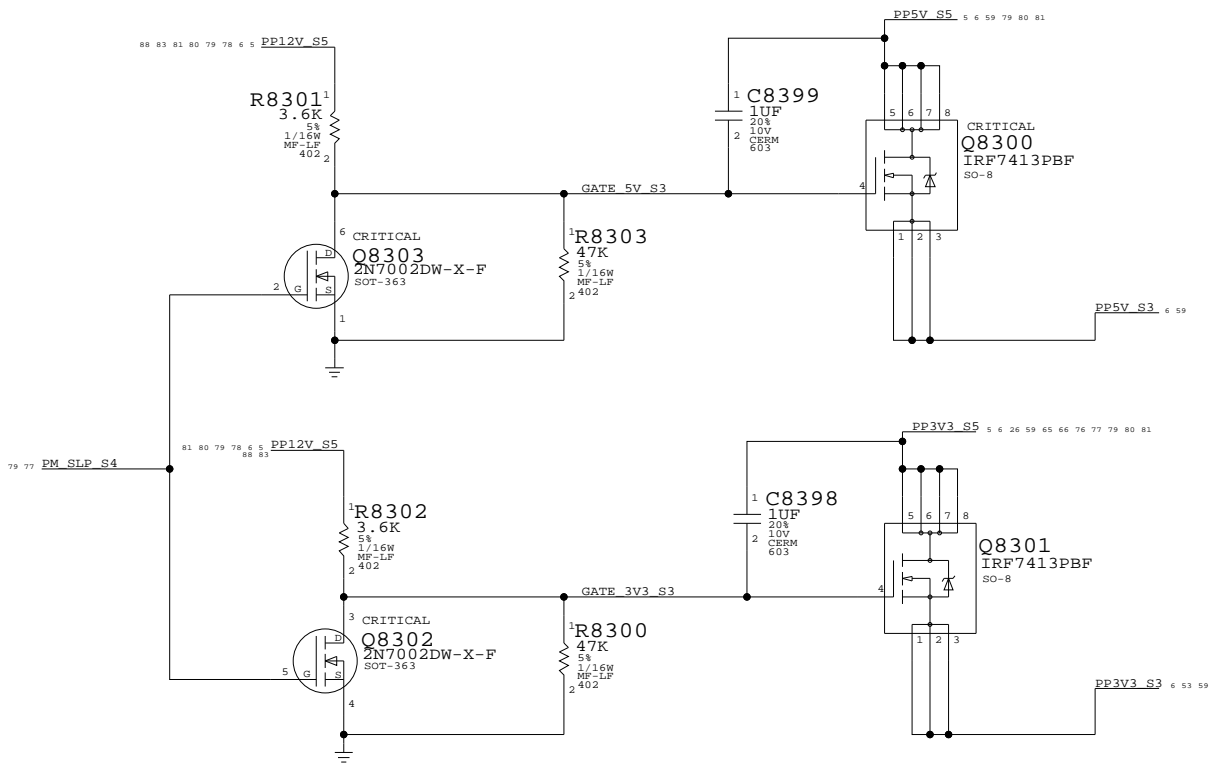
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5V & 3.3V Fets

SYNC_MASTER=FINO-PC SYNC_DATE=04/12/2005

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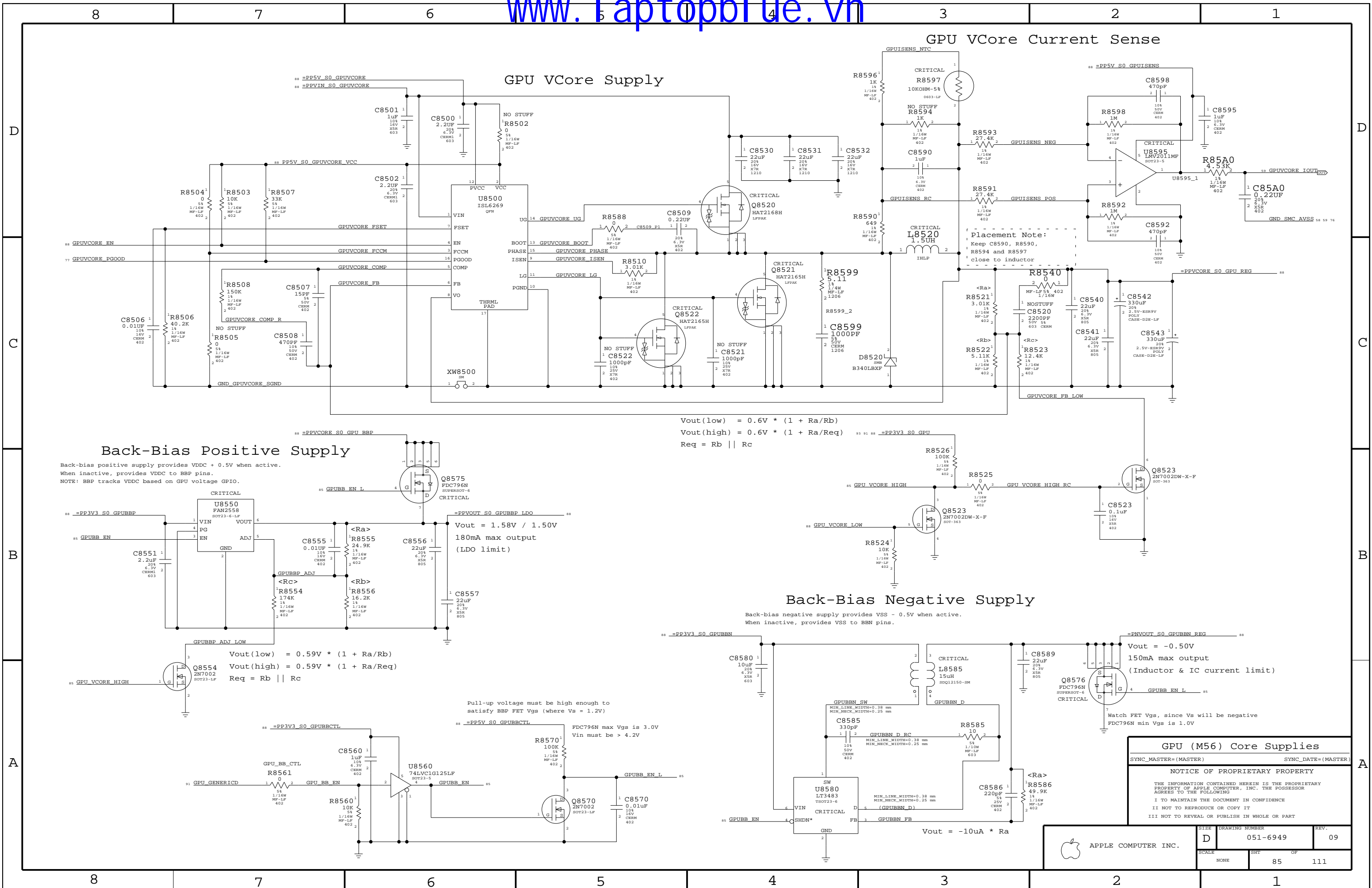
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SCALE	SHT	OF
NONE	83	111

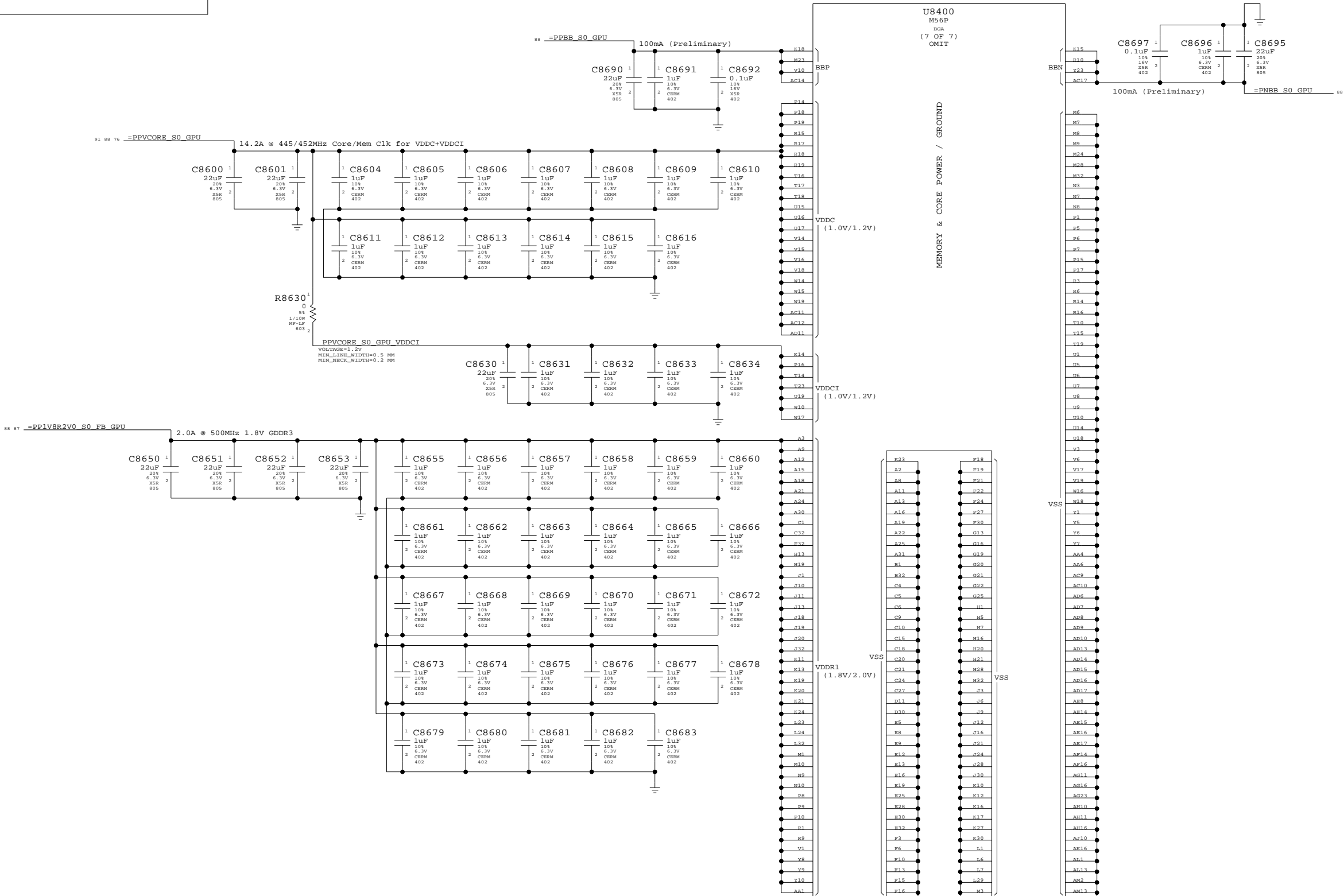


Page Notes

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- =PP1V5_GPU_VDD15
- =PP1VR1V3_GPU_VCORE

Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)



ATI M56 Core Power

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

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SIZE: DRAWING NUMBER

D 051-6949 09

SCALE: NONE SHT OF

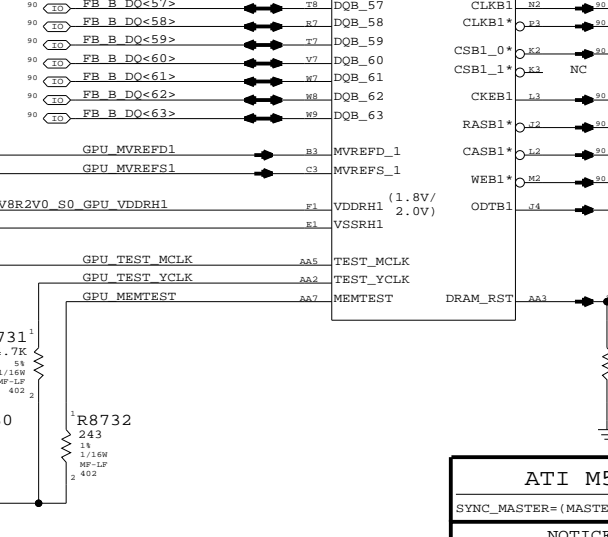
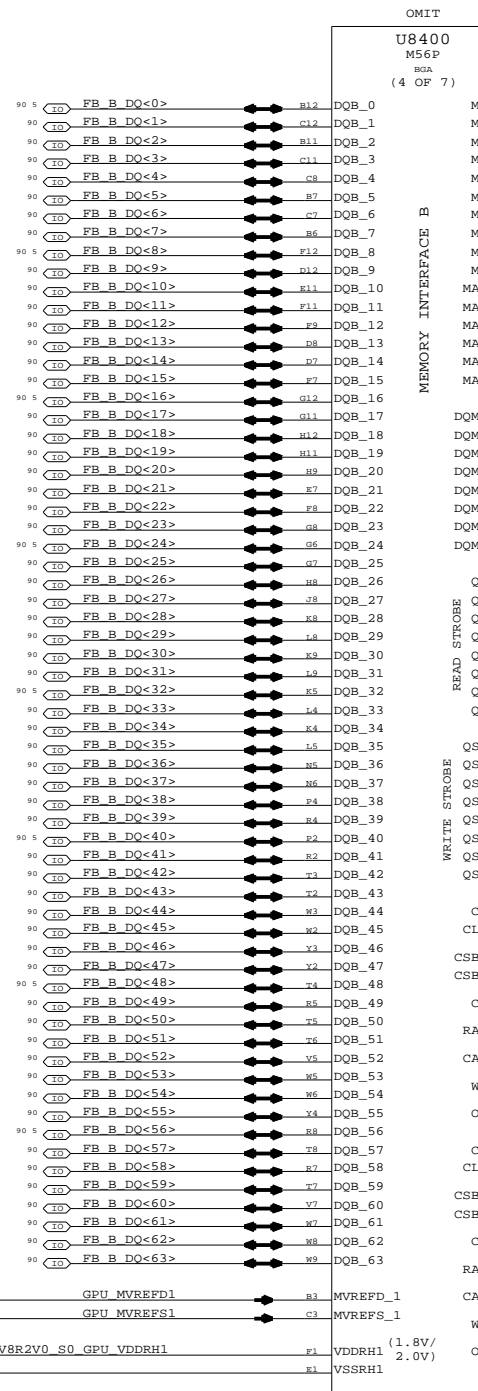
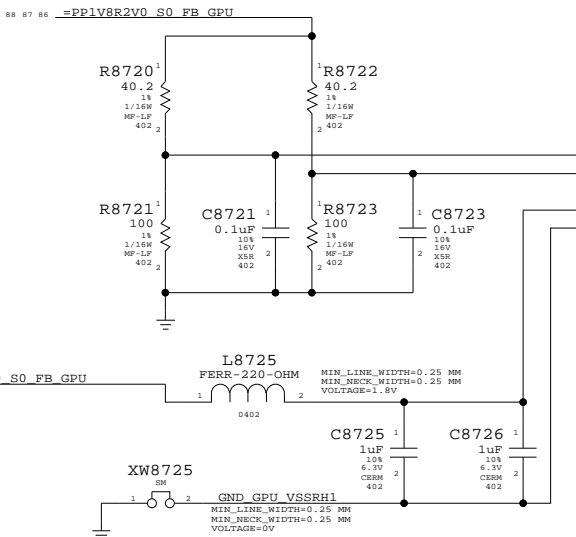
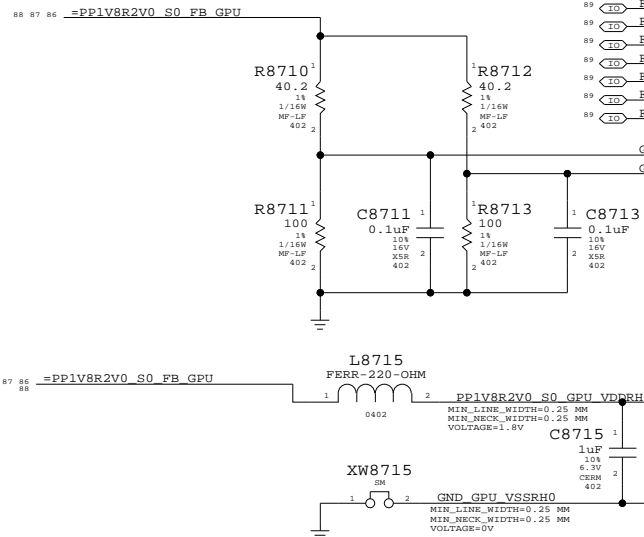
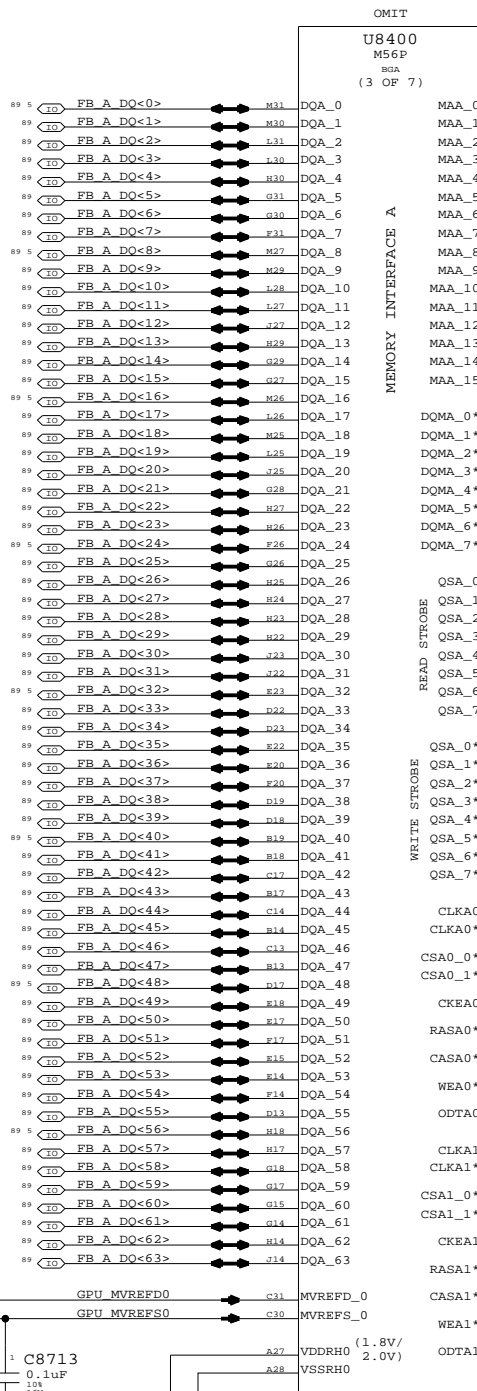
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Page Notes

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Signal aliases required by this page:
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BOM options provided by this page:
(NONE)



ATI M56 Frame Buffer I/F

SYNC_MASTER=(MASTER) SYNC_DATE=(MASTER)

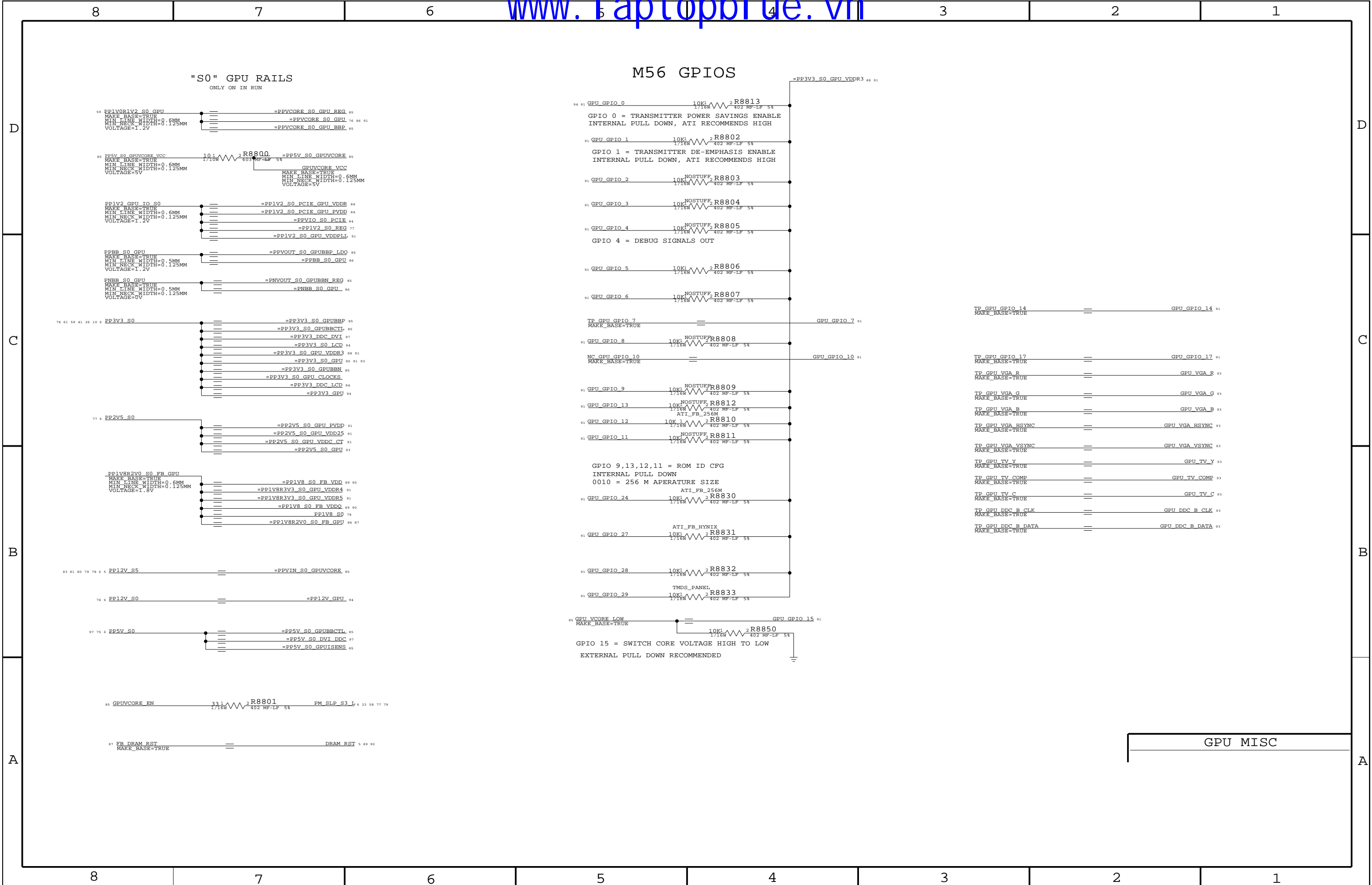
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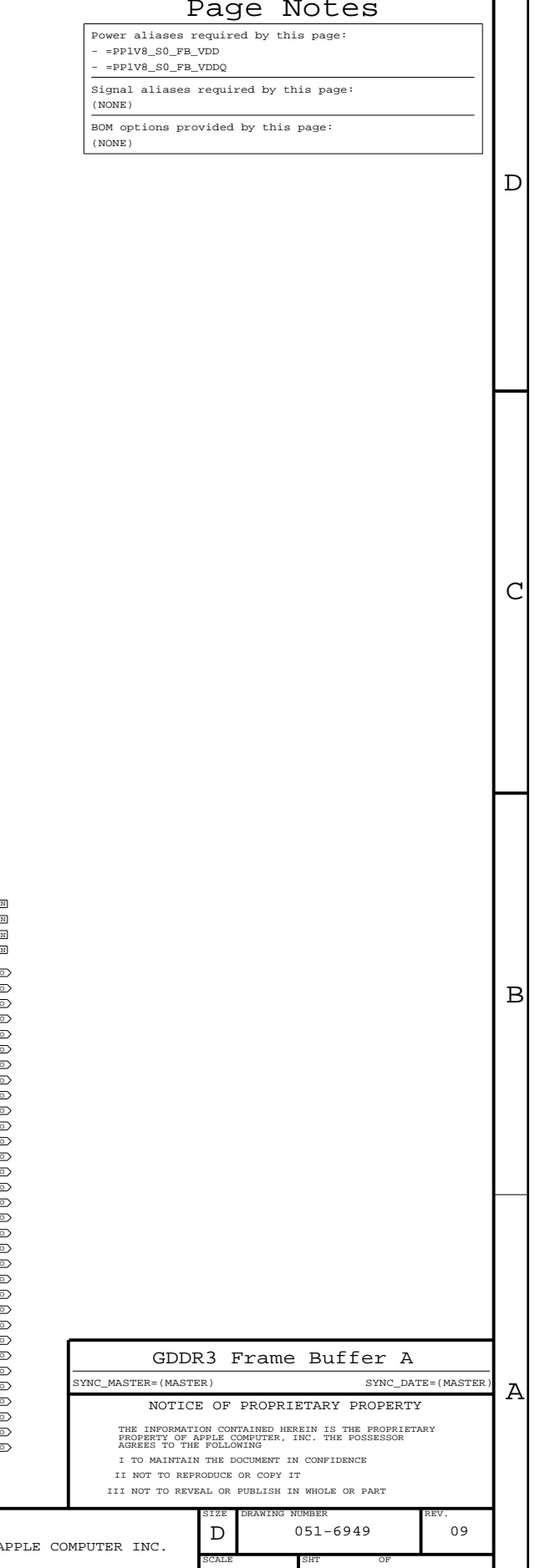
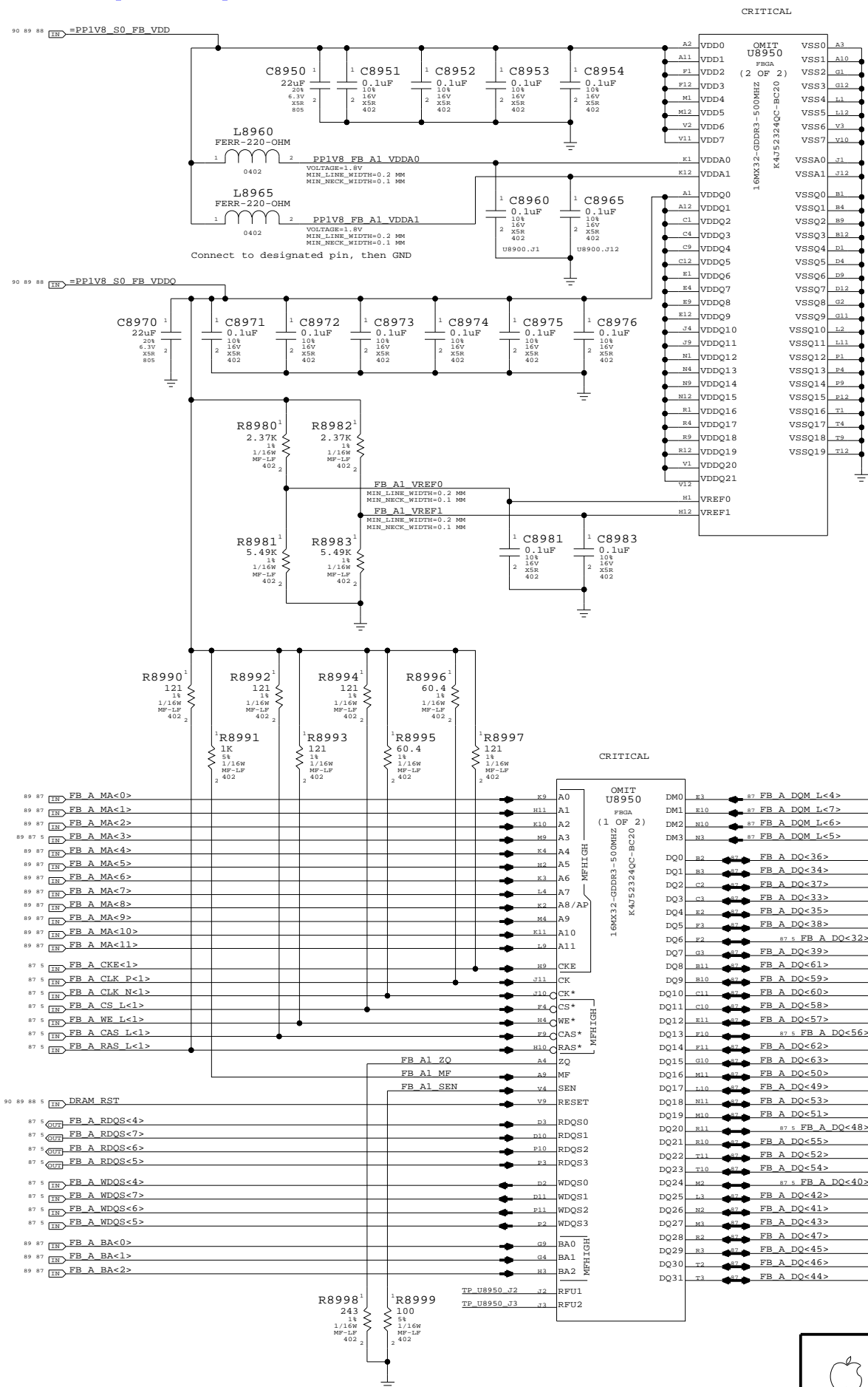
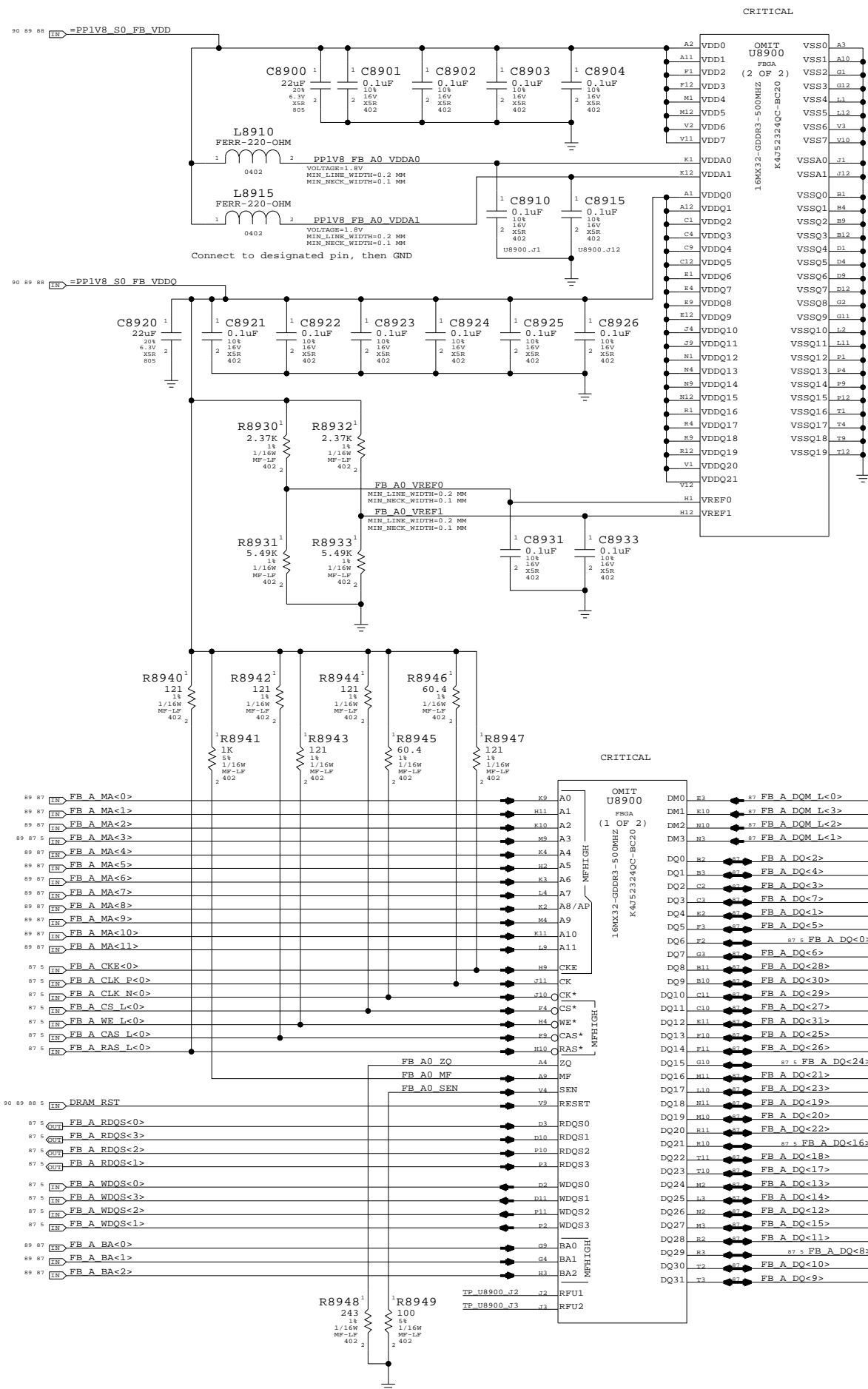


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- =PP1V8_S0_FB_VDDQ
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Signal aliases required by this page:
(NONE)

BOM options provided by this page:
(NONE)



SYNC_MASTER= (MASTER)	SYNC_DATE= (MASTER)
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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT OF	
NONE	89	111

D

- =PP3V3_GPU_GPIOS
- =PP2V5_PVDD
- =PP1V8_GPU_LVDS_PLI

BOM options provided by this page:
(NONE)



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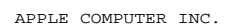
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	SIZE	DRAWING NUMBER	REV.
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D	051-6949	09
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SCALE	SH1	OF
NONE	91	111



D	051-6949	09
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SCALE	SH1	OF
NONE	91	111

87654321

Page Notes

Power aliases required by this page:
- =PP3V3_GPU_CLOCKS - =PP3V3_GPU_PWRSEQ
- =PPVIN_GPU_LVDDR_LDO - =PP2V5_GPU_PWRSEQ
- =PP2V5_GPU_LVDDR_LDO - =PP1V8_GPU_PWRSEQ
- =PP1V5_GPU_PWRSEQ

Signal aliases required by this page:
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BOM options provided by this page:
- GPU_SS - GPU_LVDDR_2V8

D

D

C

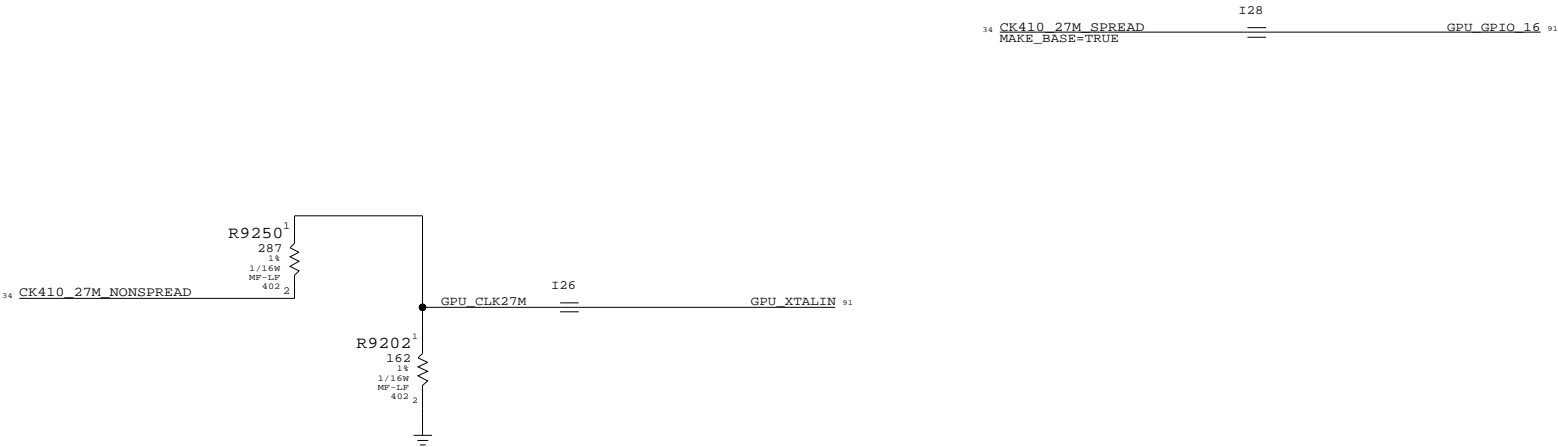
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GPU CLOCKS

SYNC_MASTER=BOZEMAN SYNC_DATE=05/21/2005

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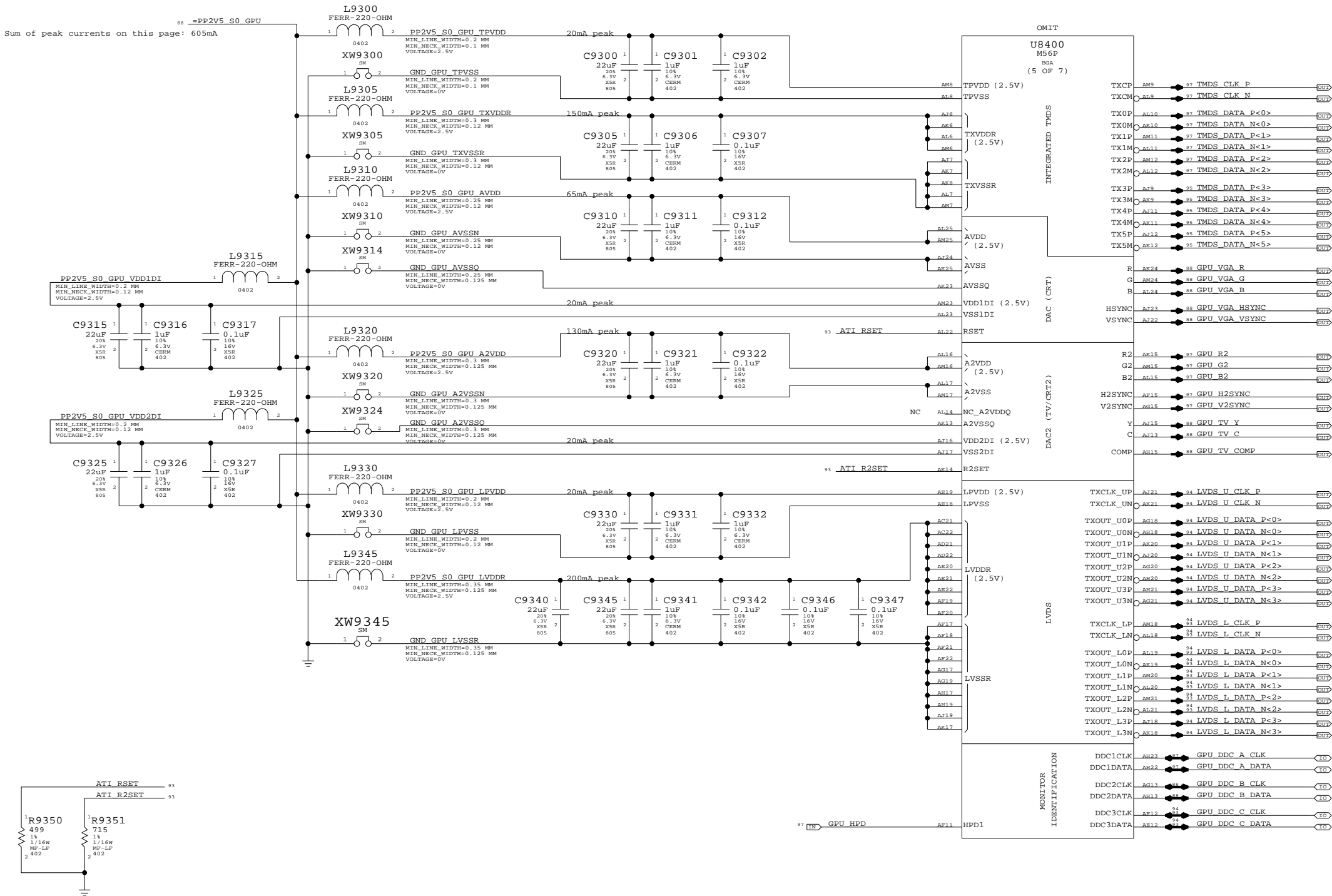
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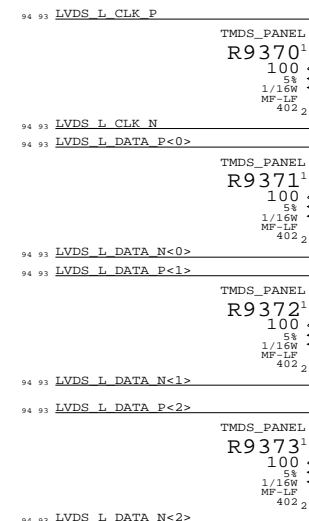
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- =PP1V8R2V5_S0_GPU_LVDDR

Signal aliases required by this page:
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BOM options provided by this page:
(NONE)



TERMINATION FOR TMDS USAGE OF LVDS PINS
PLACE CLOSE TO GPU (U8400)



Composite/S-Video	VGA	Component
Y	G	Y
C	R	Pr
Comp	B	Pb

ATI M56 Video Interfaces

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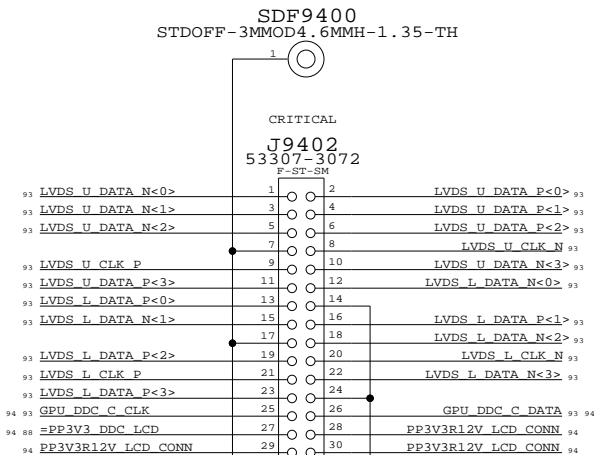
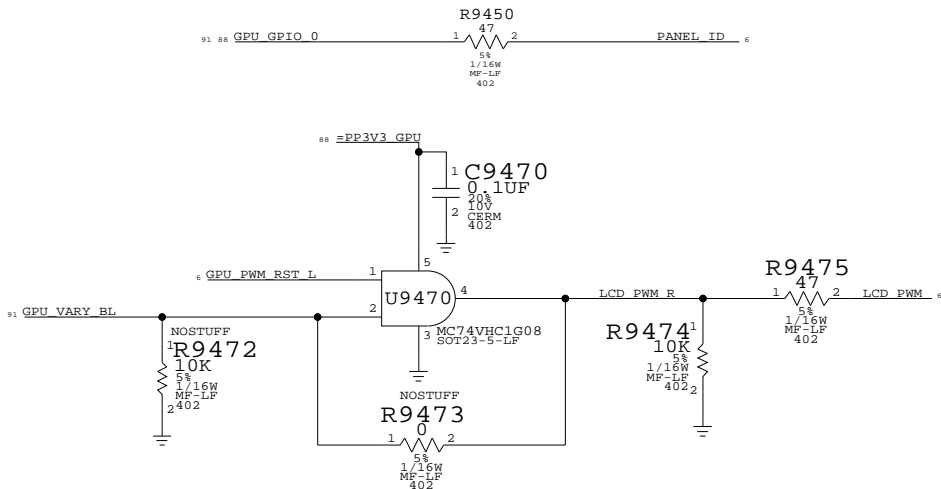
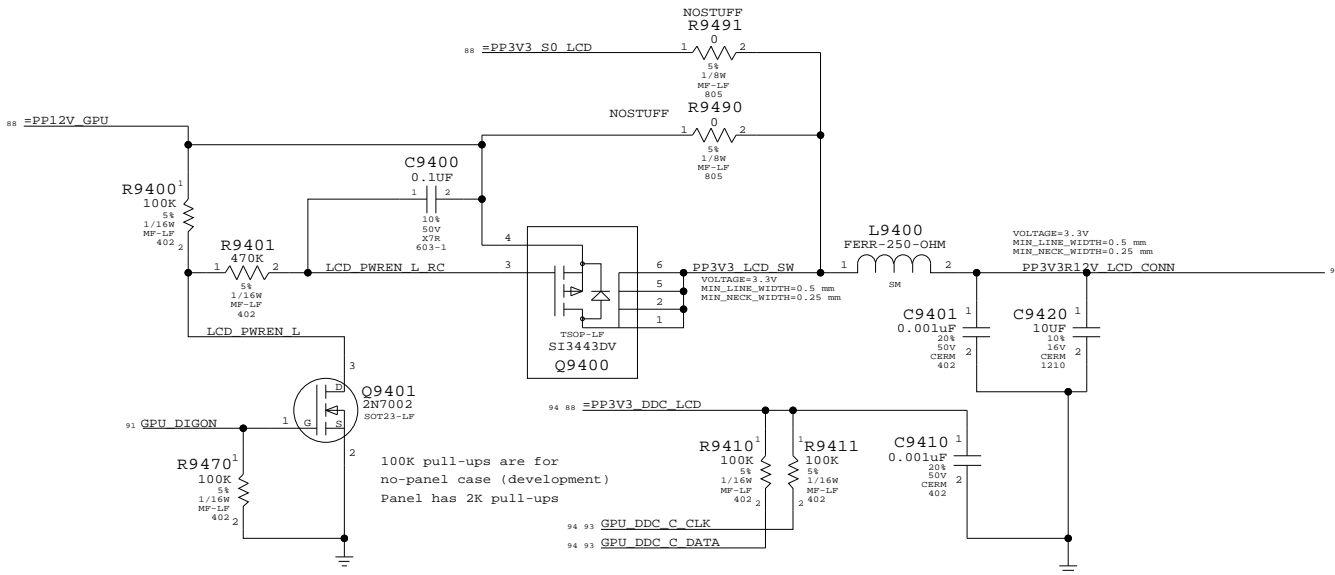


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SIZE	DRAWING NUMBER	REV.
D	051-6949	09
SCALE	SHT	OF
NONE	93	111

LCD (LVDS) INTERFACE

INVERTER INTERFACE



Internal Display Conns		
SYNC_MASTER=BOZEMAN		SYNC_DATE=04/27/2005
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SCALE		SHT	OF
NONE		94	111

D

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M56 TPS

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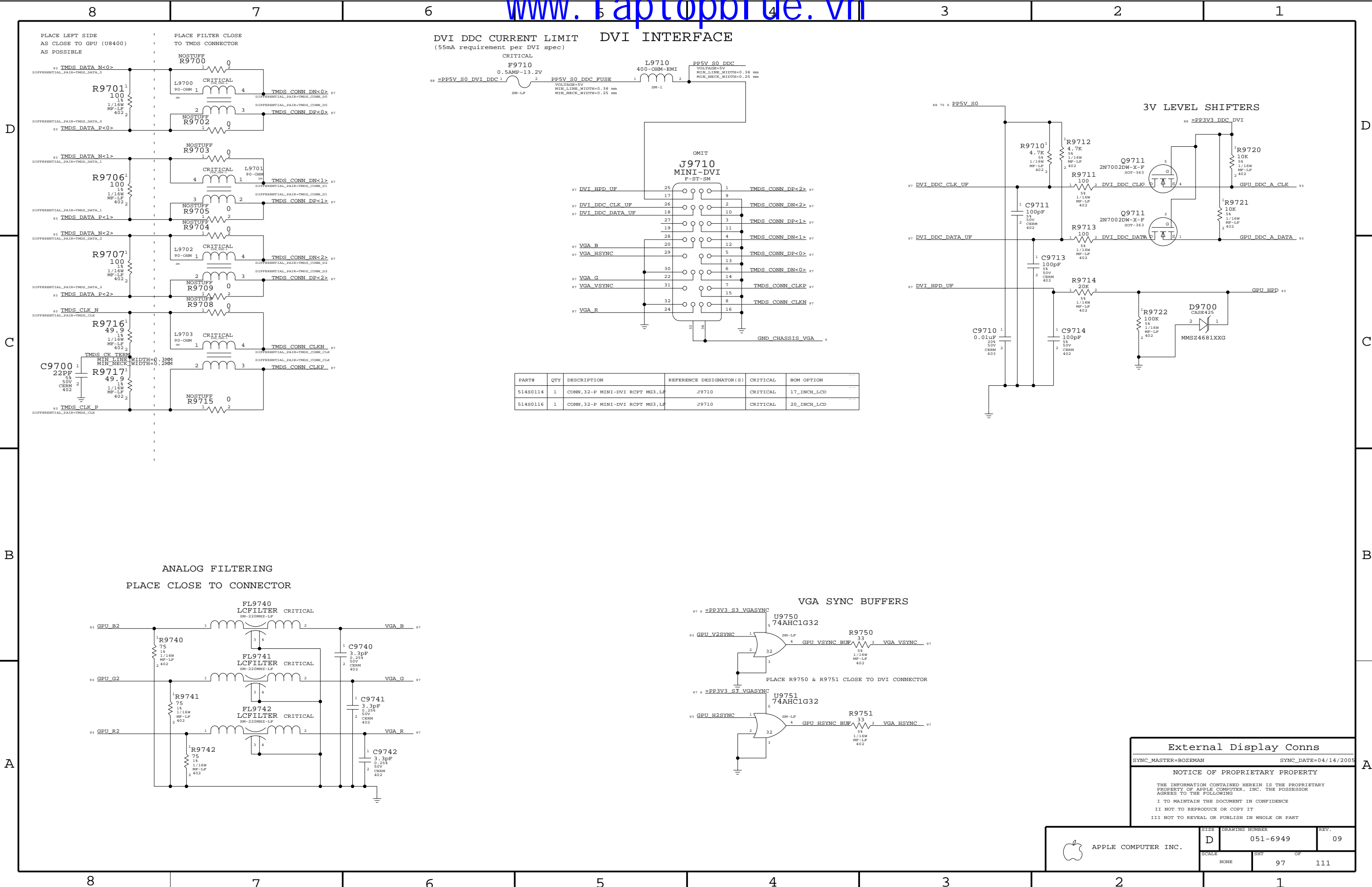
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SCALE	SHT	OF
NONE	95	111



[illegible]

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C	AUD_LO_DET1_1	AUD_LO_DET1_1 - @m39_11b.M39	74A4 74B2			CLK_NB_OE_L	CLK_NB_OE_L - @m39_11b.M39	14B6 33B4				ENET_C4118_1	ENET_C4118_1 - @m39_11b.M39	41B2				FB_A_DQ<42>	FB_A_DQ<42> - @m39_11b.M39	87C7 89A3			
	AUD_LO_DET1_EMI	AUD_LO_DET1_EMI - @m39_11b.M39	73B7			CPUVCORE_ISENSE_CAL	CPUVCORE_ISENSE_CAL - @m39_11b.M39	76A5				ENET_CLK100M_PCIE_N	ENET_CLK100M_PCIE_N - @m39_11b.M39	5D5 34A4 34B2 41C5				FB_A_DQ<43>	FB_A_DQ<43> - @m39_11b.M39	87C7 89A3			
	AUD_LO_DET1_INV	AUD_LO_DET1_INV - @m39_11b.M39	74B3			CPU_A20M_L	CPU_A20M_L - @m39_11b.M39	5C8 7C7 21C4				ENET_CLK100M_PCIE_P	ENET_CLK100M_PCIE_P - @m39_11b.M39	5D5 34A4 34B2 41C5				FB_A_DQ<44>	FB_A_DQ<44> - @m39_11b.M39	87B7 89A3			
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	AUD_LO_DET2_1	AUD_LO_DET2_1 - @m39_11b.M39	74B4			CPU_BSEL<2>	CPU_BSEL<2> - @m39_11b.M39	7B4 34A8				ENET_CTRL25	ENET_CTRL25 - @m39_11b.M39	41C7 42C6				FB_A_DQ<47>	FB_A_DQ<47> - @m39_11b.M39	87B7 89A3			
	AUD_LO_DET2_EMI	AUD_LO_DET2_EMI - @m39_11b.M39	73B7			CPU_COMP<0>	CPU_COMP<0> - @m39_11b.M39	7B2				ENET_GATED_RST_L	ENET_GATED_RST_L - @m39_11b.M39	41C5 42D2				FB_A_DQ<48>	FB_A_DQ<48> - @m39_11b.M39	5A6 5D6 87B7 89A2			
	AUD_LO_DET2_JACK	AUD_LO_DET2_JACK - @m39_11b.M39	73B4			CPU_COMP<1>	CPU_COMP<1> - @m39_11b.M39	7B2				ENET_LED_ACT_L	ENET_LED_ACT_L - @m39_11b.M39	41C8 43C3				FB_A_DQ<49>	FB_A_DQ<49> - @m39_11b.M39	87B7 89A3			
	AUD_LO_GND_EMI	AUD_LO_GND_EMI - @m39_11b.M39	73A7			CPU_COMP<2>	CPU_COMP<2> - @m39_11b.M39	7B2				ENET_LED_LINK100_100_L	ENET_LED_LINK100_100_L - @m39_11b.M39	41C8 43C3				FB_A_DQ<50>	FB_A_DQ<50> - @m39_11b.M39	87B7 89A3			
	AUD_LO_GND_JACK	AUD_LO_GND_JACK - @m39_11b.M39	73B4			CPU_COMP<3>	CPU_COMP<3> - @m39_11b.M39	7B2				ENET_LED_LINK1000_L	ENET_LED_LINK1000_L - @m39_11b.M39	41C8 43C3				FB_A_DQ<51>	FB_A_DQ<51> - @m39_11b.M39	87B7 89A3			
B	AUD_LO_L_EMI	AUD_LO_L_EMI - @m39_11b.M39	73B7			CPU_DCIN_SENSE	CPU_DCIN_SENSE - @m39_11b.M39	76D7				ENET_LED_LINK_L	ENET_LED_LINK_L - @m39_11b.M39	41C8 43C3				FB_A_DQ<52>	FB_A_DQ<52> - @m39_11b.M39	87B7 89A3			
	AUD_LO_L_JACK	AUD_LO_L_JACK - @m39_11b.M39	73B4			CPU_DPRSTP_L	CPU_DPRSTP_L - @m39_11b.M39	7B3 21C4 75C6				ENET_LED_LINK_L	ENET_LED_LINK_L - @m39_11b.M39	41C8 43C3				FB_A_DQ<53>	FB_A_DQ<53> - @m39_11b.M39	87B7 89A3			
	AUD_LO_R_EMI	AUD_LO_R_EMI - @m39_11b.M39	73A7			CPU_DPSP_L	CPU_DPSP_L - @m39_11b.M39	7B3 21C4				ENET_LOM_DIS_L	ENET_LOM_DIS_L - @m39_11b.M39	41C7				FB_A_DQ<54>	FB_A_DQ<54> - @m39_11b.M39	87B7 89A3			
	AUD_LO_R_JACK	AUD_LO_R_JACK - @m39_11b.M39	73B4			CPU_FERR_L	CPU_FERR_L - @m39_11b.M39	7C7 21C2				ENET_MDI_N<0>	ENET_MDI_N<0> - @m39_11b.M39	41C2 43C7				FB_A_DQ<55>	FB_A_DQ<55> - @m39_11b.M39	87B7 89A3			
	AUD_MAX9714_CHOLD	AUD_MAX9714_CHOLD - @m39_11b.M39	72C4			CPU_GTLREF	CPU_GTLREF - @m39_11b.M39	5D4 7B4				ENET_MDI_N<1>	ENET_MDI_N<1> - @m39_11b.M39	41C2 43C7				FB_A_DQ<56>	FB_A_DQ<56> - @m39_11b.M39	5A6 5D6 87B7 89A2			
	AUD_MAX9714_VREG	AUD_MAX9714_VREG - @m39_11b.M39	72C5			CPU_HS_ZH607	CPU_HS_ZH607 - @m39_11b.M39	9D4				ENET_MDI_N<2>	ENET_MDI_N<2> - @m39_11b.M39	41C2 43C7				FB_A_DQ<57>	FB_A_DQ<57> - @m39_11b.M39	87B7 89A3			
	AUD_MIC_IN_N	AUD_MIC_IN_N - @m39_11b.M39	73C6 74A6			CPU_HS_ZH608	CPU_HS_ZH608 - @m39_11b.M39	9D3 66A4 66A6 66B4 66B6				ENET_MDI_N<3>	ENET_MDI_N<3> - @m39_11b.M39	41C2 43B7				FB_A_DQ<58>	FB_A_DQ<58> - @m39_11b.M39	87B7 89B3			
	AUD_MIC_IN_N_CONN	AUD_MIC_IN_N_CONN - @m39_11b.M39	47C2 73C4			CPU_HS_ZH609	CPU_HS_ZH609 - @m39_11b.M39	9D3				ENET_MDI_P<0>	ENET_MDI_P<0> - @m39_11b.M39	41C2 43C7				FB_A_DQ<59>	FB_A_DQ<59> - @m39_11b.M39	87B7 89B3			
	AUD_MIC_IN_N_EMI	AUD_MIC_IN_N_EMI - @m39_11b.M39	73C5			CPU_HS_ZH610	CPU_HS_ZH610 - @m39_11b.M39	9D2				ENET_MDI_P<1>	ENET_MDI_P<1> - @m39_11b.M39	41C2 43C7				FB_A_DQ<60>	FB_A_DQ<60> - @m39_11b.M39				

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DMVP6_UGAT1	DMVP6_UGAT1E1 ~ @m39_11b.M39	7548 75C5	MEM_A_A<4>	MEM_A_A<4> ~ @m39_11b.M39	1585 28B3	MEM_B_DM<0>	MEM_B_DM<0> ~ @m39_11b.M39	1502 29D3	NB_CFG<5>	NB_CFG<5> ~ @m39_11b.M39	14C6 20C7
DMVP6_UGATE2	DMVP6_UGATE2E1 ~ @m39_11b.M39	7546 75C5	MEM_A_A<5>	MEM_A_A<5> ~ @m39_11b.M39	1585 28B6	MEM_B_DM<1>	MEM_B_DM<1> ~ @m39_11b.M39	1502 29D3	NB_CFG<6>	NB_CFG<6> ~ @m39_11b.M39	14C6
DMVP6_VDIFFF	DMVP6_VDIFFF ~ @m39_11b.M39	7544 75B6	MEM_A_A<6>	MEM_A_A<6> ~ @m39_11b.M39	1585 28C3	MEM_B_DM<2>	MEM_B_DM<2> ~ @m39_11b.M39	1502 29C3	NB_CFG<7>	NB_CFG<7> ~ @m39_11b.M39	14C6 20C7
DMVP6_VDIFFF_RC	DMVP6_VDIFFF_RC ~ @m39_11b.M39	75B7	MEM_A_A<7>	MEM_A_A<7> ~ @m39_11b.M39	1585 28C3	MEM_B_DM<3>	MEM_B_DM<3> ~ @m39_11b.M39	15C2 29C6	NB_CFG<8>	NB_CFG<8> ~ @m39_11b.M39	14C6
DMVP6_VO	DMVP6_VO ~ @m39_11b.M39	7544 75B4 76D5	MEM_A_A<8>	MEM_A_A<8> ~ @m39_11b.M39	1585 28C6	MEM_B_DM<4>	MEM_B_DM<4> ~ @m39_11b.M39	15C2 29B3	NB_CFG<9>	NB_CFG<9> ~ @m39_11b.M39	14C6 20B7
DMVP6_VO_R	DMVP6_VO_R ~ @m39_11b.M39	75B4	MEM_A_A<9>	MEM_A_A<9> ~ @m39_11b.M39	1585 28C6	MEM_B_DM<5>	MEM_B_DM<5> ~ @m39_11b.M39	15C2 29A6	NB_CFG<10>	NB_CFG<10> ~ @m39_11b.M39	14C6
DMVP6_VO_R_OA	DMVP6_VO_R_OA ~ @m39_11b.M39	76D4	MEM_A_A<10>	MEM_A_A<10> ~ @m39_11b.M39	1585 28B6	MEM_B_DM<6>	MEM_B_DM<6> ~ @m39_11b.M39	15C2 29A3	NB_CFG<11>	NB_CFG<11> ~ @m39_11b.M39	14C6
DMVP6_VR_TT	DMVP6_VR_TT ~ @m39_11b.M39	75C7	MEM_A_A<11>	MEM_A_A<11> ~ @m39_11b.M39	1585 28C3	MEM_B_DM<7>	MEM_B_DM<7> ~ @m39_11b.M39	15C2 29A6	NB_CFG<12>	NB_CFG<12> ~ @m39_11b.M39	14C6
DMVP6_VSUM	DMVP6_VSUM ~ @m39_11b.M39	7548 75B5	MEM_A_A<12>	MEM_A_A<12> ~ @m39_11b.M39	1585 28B3	MEM_B_DM<8>	MEM_B_DM<8> ~ @m39_11b.M39	15C2 29D6	NB_CFG<13>	NB_CFG<13> ~ @m39_11b.M39	14C6
DMVP6_VSUM	DMVP6_VSUM ~ @m39_11b.M39	7544 75C5	MEM_A_A<13>	MEM_A_A<13> ~ @m39_11b.M39	1585 28B3	MEM_B_DM<9>	MEM_B_DM<9> ~ @m39_11b.M39	15D4 29D6	NB_CFG<14>	NB_CFG<14> ~ @m39_11b.M39	14C6
DMVP6_VSUM_R1	DMVP6_VSUM_R1 ~ @m39_11b.M39	7548 75C2	MEM_A_BS<0>	MEM_A_BS<0> ~ @m39_11b.M39	1505 28B6	MEM_B_DM<10>	MEM_B_DM<10> ~ @m39_11b.M39	15D4 29D6	NB_CFG<15>	NB_CFG<15> ~ @m39_11b.M39	14C6
DMVP6_VSUM_R2	DMVP6_VSUM_R2 ~ @m39_11b.M39	7546 75B2	MEM_A_BS<2..0>	MEM_A_BS<2..0> ~ @m39_11b.M39	30C6	MEM_B_DM<11>	MEM_B_DM<11> ~ @m39_11b.M39	15D4 29D6	NB_CFG<16>	NB_CFG<16> ~ @m39_11b.M39	14C6 20C5
DMVP6_VN	DMVP6_VN ~ @m39_11b.M39	7544 75B6	MEM_A_BS<1>	MEM_A_BS<1> ~ @m39_11b.M39	1505 28B3	MEM_B_DM<12>	MEM_B_DM<12> ~ @m39_11b.M39	15D4 29D3	NB_CFG<17>	NB_CFG<17> ~ @m39_11b.M39	14C6
DMVP_DPRS_LPV	DMVP_DPRS_LPV ~ @m39_11b.M39	75C6	MEM_A_BS<2>	MEM_A_BS<2> ~ @m39_11b.M39	1505 28C6	MEM_B_DM<13>	MEM_B_DM<13> ~ @m39_11b.M39	15D4 29D3	NB_CFG<18>	NB_CFG<18> ~ @m39_11b.M39	14C6 20B5
DMVP_PGD_IN	DMVP_PGD_IN ~ @m39_11b.M39	75C6 77C7	MEM_A_CAS_L	MEM_A_CAS_L ~ @m39_11b.M39	1505 28B6 30B6	MEM_B_DM<14>	MEM_B_DM<14> ~ @m39_11b.M39	5A7 15D4 29D3	NB_CFG<19>	NB_CFG<19> ~ @m39_11b.M39	14C6 20B5
DMVP_VID<0>	DMVP_VID<0> ~ @m39_11b.M39	75C6	MEM_A_DM<0>	MEM_A_DM<0> ~ @m39_11b.M39	1505 28D3	MEM_B_DM<15>	MEM_B_DM<15> ~ @m39_11b.M39	15D4 29D3	NB_CFG<20>	NB_CFG<20> ~ @m39_11b.M39	14B6 20A5
DMVP_VID<1>	DMVP_VID<1> ~ @m39_11b.M39	75C7	MEM_A_DM<1>	MEM_A_DM<1> ~ @m39_11b.M39	1505 28D3	MEM_B_DM<16>	MEM_B_DM<16> ~ @m39_11b.M39	5A7 15C4 29D6	NB_CLK100M_GCLKIN_N	NB_CLK100M_GCLKIN_N ~ @m39_11b.M39	5C7 14C4 34B4 34C2
DMVP_VID<2>	DMVP_VID<2> ~ @m39_11b.M39	75C7	MEM_A_DM<2>	MEM_A_DM<2> ~ @m39_11b.M39	1505 28C3	MEM_B_DM<17>	MEM_B_DM<17> ~ @m39_11b.M39	15C4 29D6	NB_CLK100M_GCLKIN_P	NB_CLK100M_GCLKIN_P ~ @m39_11b.M39	5C7 14C4 34B4 34C2
DMVP_VID<3>	DMVP_VID<3> ~ @m39_11b.M39	75C7	MEM_A_DM<3>	MEM_A_DM<3> ~ @m39_11b.M39	1505 28C6	MEM_B_DM<18>	MEM_B_DM<18> ~ @m39_11b.M39	15C4 29D6	NB_FSB_VREF	NB_FSB_VREF ~ @m39_11b.M39	12C4
DMVP_VID<4>	DMVP_VID<4> ~ @m39_11b.M39	75C7	MEM_A_DM<4>	MEM_A_DM<4> ~ @m39_11b.M39	15C5 28B3	MEM_B_DM<19>	MEM_B_DM<19> ~ @m39_11b.M39	15C4 29D6	NB_FSB_XRCOMP	NB_FSB_XRCOMP ~ @m39_11b.M39	12A6
DMVP_VID<5>	DMVP_VID<5> ~ @m39_11b.M39	75C7	MEM_A_DM<5>	MEM_A_DM<5> ~ @m39_11b.M39	15C5 28A6	MEM_B_DM<20>	MEM_B_DM<20> ~ @m39_11b.M39	15C4 29D3	NB_FSB_XSCOMP	NB_FSB_XSCOMP ~ @m39_11b.M39	12A6
DMVP_VID<6>	DMVP_VID<6> ~ @m39_11b.M39	75C7	MEM_A_DM<6>	MEM_A_DM<6> ~ @m39_11b.M39	15C5 28A3	MEM_B_DM<21>	MEM_B_DM<21> ~ @m39_11b.M39	15C4 29D3	NB_FSB_XSWING	NB_FSB_XSWING ~ @m39_11b.M39	12A6
DMVP_VR_ON	DMVP_VR_ON ~ @m39_11b.M39	58D7 75C6	MEM_A_DM<7>	MEM_A_DM<7> ~ @m39_11b.M39	15C5 28A6	MEM_B_DM<22>	MEM_B_DM<22> ~ @m39_11b.M39	15C4 29D3	NB_FSB_YRCOMP	NB_FSB_YRCOMP ~ @m39_11b.M39	12A6
INT_PIRQA_L	INT_PIRQA_L ~ @m39_11b.M39	22A7 26D2	MEM_A_DQ<0>	MEM_A_DQ<0> ~ @m39_11b.M39	15D7 28D6	MEM_B_DM<23>	MEM_B_DM<23> ~ @m39_11b.M39	15C4 29D3	NB_FSB_YSCOMP	NB_FSB_YSCOMP ~ @m39_11b.M39	12A6
INT_PIRQB_L	INT_PIRQB_L ~ @m39_11b.M39	22A7 26D2	MEM_A_DQ<1>	MEM_A_DQ<1> ~ @m39_11b.M39	15D7 28D6	MEM_B_DM<24>	MEM_B_DM<24> ~ @m39_11b.M39	15C4 29C6	NB_FSB_YSWING	NB_FSB_YSWING ~ @m39_11b.M39	12A6
INT_PIRQC_L	INT_PIRQC_L ~ @m39_11b.M39	22A7 26D2	MEM_A_DQ<2>	MEM_A_DQ<2> ~ @m39_11b.M39	15D7 28D6	MEM_B_DM<25>	MEM_B_DM<25> ~ @m39_11b.M39	15C4 29C6	NB_RST_IN_L	NB_RST_IN_L ~ @m39_11b.M39	6B7 14B7
INT_PIRQD_L	INT_PIRQD_L ~ @m39_11b.M39	22A7 26D2 44B5	MEM_A_DQ<3>	MEM_A_DQ<3> ~ @m39_11b.M39	15D7 28D6	MEM_B_DM<26>	MEM_B_DM<26> ~ @m39_11b.M39	15C4 29C6	NB_RST_IN_L_R	NB_RST_IN_L_R ~ @m39_11b.M39	5C7 14B6
INT_SBRPQ	INT_SBRPQ ~ @m39_11b.M39	58C7 60C3 67C6	MEM_A_DQ<4>	MEM_A_DQ<4> ~ @m39_11b.M39	15D7 28D3	MEM_B_DM<27>	MEM_B_DM<27> ~ @m39_11b.M39	15C4 29C6	NB_RST_IN_R	NB_RST_IN_R ~ @m39_11b.M39	14C4 22A6
ISENSE_CAL_EN	ISENSE_CAL_EN ~ @m39_11b.M39	58B7 76A8	MEM_A_DQ<5>	MEM_A_DQ<5> ~ @m39_11b.M39	15D7 28D3	MEM_B_DM<28>	MEM_B_DM<28> ~ @m39_11b.M39	15C4 29C3	NB_TV_DCONSEL0	NB_TV_DCONSEL0 ~ @m39_11b.M39	14C6
ISENSE_CAL_EN_L	ISENSE_CAL_EN_L ~ @m39_11b.M39	76B7	MEM_A_DQ<6>	MEM_A_DQ<6> ~ @m39_11b.M39	15D7 28D3	MEM_B_DM<29>	MEM_B_DM<29> ~ @m39_11b.M39	15C4 29C3	NB_TV_DCONSEL1	NB_TV_DCONSEL1 ~ @m39_11b.M39	14C6
ISENSE_CAL_EN_LS12V	ISENSE_CAL_EN_LS12V ~ @m39_11b.M39	76B6	MEM_A_DQ<7>	MEM_A_DQ<7> ~ @m39_11b.M39	5B7 15D7 28D3	MEM_B_DM<30>	MEM_B_DM<30> ~ @m39_11b.M39	15C4 29C3	NB_VCCSM_LF1	NB_VCCSM_LF1 ~ @m39_11b.M39	16B4
ISENSE_CAL_EN_L_R	ISENSE_CAL_EN_L_R ~ @m39_11b.M39	76A7	MEM_A_DQ<8>	MEM_A_DQ<8> ~ @m39_11b.M39	15C7 28D6	MEM_B_DM<31>	MEM_B_DM<31> ~ @m39_11b.M39	5A7 15C4 29C3	NB_VCCSM_LF2	NB_VCCSM_LF2 ~ @m39_11b.M39	16B4
ITPRESET_L	ITPRESET_L ~ @m39_11b.M39	11B3	MEM_A_DQ<9>	MEM_A_DQ<9> ~ @m39_11b.M39	15C7 28D6	MEM_B_DM<32>	MEM_B_DM<32> ~ @m39_11b.M39	15C4 29C6	NB_VCCSM_LF4	NB_VCCSM_LF4 ~ @m39_11b.M39	16B8
ITP_TDO	ITP_TDO ~ @m39_11b.M39	11B3	MEM_A_DQ<10>	MEM_A_DQ<10> ~ @m39_11b.M39	15C7 28D6	MEM_B_DM<33>	MEM_B_DM<33> ~ @m39_11b.M39	5A7 15C4 29C6	NB_VCCSM_LF5	NB_VCCSM_LF5 ~ @m39_11b.M39	16B8
ITS_ALIVE	ITS_ALIVE ~ @m39_11b.M39	6A7	MEM_A_DQ<11>	MEM_A_DQ<11> ~ @m39_11b.M39	15C7 28D6	MEM_B_DM<34>	MEM_B_DM<34> ~ @m39_11b.M39	15C4 29C6	NB_VTTTLF_CAP1	NB_VTTTLF_CAP1 ~ @m39_11b.M39	17A4
ITS_PLUGGED_IN	ITS_PLUGGED_IN ~ @m39_11b.M39	6A8	MEM_A_DQ<12>	MEM_A_DQ<12> ~ @m39_11b.M39	15C7 28D3	MEM_B_DM<35>	MEM_B_DM<35> ~ @m39_11b.M39	15C4 29C6	NB_VTTTLF_CAP2	NB_VTTTLF_CAP2 ~ @m39_11b.M39	17A4
ITS_RUNNING	ITS_RUNNING ~ @m39_11b.M39	6A6	MEM_A_DQ<13>	MEM_A_DQ<13> ~ @m39_11b.M39	15C7 28B3	MEM_B_DM<36>	MEM_B_DM<36> ~ @m39_11b.M39	15C4 29C3	NB_VTTTLF_CAP3	NB_VTTTLF_CAP3 ~ @m39_11b.M39	17A4
JDRF	JDRF ~ @m39_11b.M39	69C4	MEM_A_DQ<14>	MEM_A_DQ<14> ~ @m39_11b.M39	5B7 15C7 28D3	MEM_B_DM<37>	MEM_B_DM<37> ~ @m39_11b.M39	15C4 29C3	NC_AUD_VREF_PORT_A	NC_AUD_VREF_PORT_A ~ @m39_11b.M39	68C1
KMC_MDE	KMC_MDE ~ @m39_11b.M39	58C2	MEM_A_DQ<15>	MEM_A_DQ<15> ~ @m39_11b.M39	15C7 28D3	MEM_B_DM<38>	MEM_B_DM<38> ~ @m39_11b.M39	15C4 29C3	NC_AUD_VREF_PORT_C	NC_AUD_VREF_PORT_C ~ @m39_11b.M39	68C1
LCD_PWM	LCD_PWM ~ @m39_11b.M39	6D8 94B1	MEM_A_DQ<16>	MEM_A_DQ<16> ~ @m39_11b.M39	5B7 15C7 28C6	MEM_B_DM<39>	MEM_B_DM<39> ~ @m39_11b.M39	15C4 29C3	NC_AUD_VREF_PORT_D	NC_AUD_VREF_PORT_D ~ @m39_11b.M39	68C1
LCD_PWM_R	LCD_PWM_R ~ @m39_11b.M39	94C8	MEM_A_DQ<17>	MEM_A_DQ<17> ~ @m39_11b.M39	15C7 28C6	MEM_B_DM<40>	MEM_B_DM<40> ~ @m39_11b.M39	15C4 29B6	NC_FW_NU1	NC_FW_NU1 ~ @m39_11b.M39	44B3
LCD_PWRN_L	LCD_PWRN_L ~ @m39_11b.M39	94B2	MEM_A_DQ<18>	MEM_A_DQ<18> ~ @m39_11b.M39	15C7 28C6	MEM_B_DM<41>	MEM_B_DM<41> ~ @m39_11b.M39	15C4 29B6	NC_FW_NU2	NC_FW_NU2 ~ @m39_11b.M39	44B3
LCD_PWRN_L_RC	LCD_PWRN_L_RC ~ @m39_11b.M39	94C7	MEM_A_DQ<19>	MEM_A_DQ<19> ~ @m39_11b.M39	15C7 28C6	MEM_B_DM<42>	MEM_B_DM<42> ~ @m39_11b.M39	15B4 29B6	NC_JE350_13	NC_JE350_13 ~ @m39_11b.M39	47B1
LED4300_L	LED4300_L ~ @m39_11b.M39	43D2	MEM_A_DQ<20>	MEM_A_DQ<20> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<43>	MEM_B_DM<43> ~ @m39_11b.M39	15B4 29B6	NC_SMC_P20	NC_SMC_P20 ~ @m39_11b.M39	59D5
LED4301_L	LED4301_L ~ @m39_11b.M39	43D1	MEM_A_DQ<21>	MEM_A_DQ<21> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<44>	MEM_B_DM<44> ~ @m39_11b.M39	15B4 29B3	SMC_P20 ~ @m39_11b.M39	SMC_P20 ~ @m39_11b.M39	59D6
LED4302_L	LED4302_L ~ @m39_11b.M39	43D1	MEM_A_DQ<22>	MEM_A_DQ<22> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<45>	MEM_B_DM<45> ~ @m39_11b.M39	15B4 29B3	NC_SMC_P21	NC_SMC_P21 ~ @m39_11b.M39	59D5
LED4303_L	LED4303_L ~ @m39_11b.M39	43D1	MEM_A_DQ<23>	MEM_A_DQ<23> ~ @m39_11b.M39	5A7 15C7 29B3	MEM_B_DM<46>	MEM_B_DM<46> ~ @m39_11b.M39	15B4 29B3	SMC_P21 ~ @m39_11b.M39	SMC_P21 ~ @m39_11b.M39	59D6
LED_PP1V05_S0_N	LED_PP1V05_S0_N ~ @m39_11b.M39	81A4	MEM_A_DQ<24>	MEM_A_DQ<24> ~ @m39_11b.M39	15C7 28C6	MEM_B_DM<47>	MEM_B_DM<47> ~ @m39_11b.M39	15B4 29B3	NC_SMC_P22	NC_SMC_P22 ~ @m39_11b.M39	59D5
LED_PP1V05_S0_P	LED_PP1V05_S0_P ~ @m39_11b.M39	81A4	MEM_A_DQ<25>	MEM_A_DQ<25> ~ @m39_11b.M39	5B7 15C7 28C6	MEM_B_DM<48>	MEM_B_DM<48> ~ @m39_11b.M39	15B4 29B6	SMC_P22 ~ @m39_11b.M39	SMC_P22 ~ @m39_11b.M39	59D6
LED_PP1V5_S0_N	LED_PP1V5_S0_N ~ @m39_11b.M39	80A3	MEM_A_DQ<26>	MEM_A_DQ<26> ~ @m39_11b.M39	15C7 28C6	MEM_B_DM<49>	MEM_B_DM<49> ~ @m39_11b.M39	15B4 29B6	NC_SMC_P23	NC_SMC_P23 ~ @m39_11b.M39	59D5
LED_PP1V5_S0_P	LED_PP1V5_S0_P ~ @m39_11b.M39	80A4	MEM_A_DQ<27>	MEM_A_DQ<27> ~ @m39_11b.M39	15C7 28C6	MEM_B_DM<50>	MEM_B_DM<50> ~ @m39_11b.M39	15B4 29A6	SMC_P23 ~ @m39_11b.M39	SMC_P23 ~ @m39_11b.M39	58D7 59D6
LED_PP1V8_S3_N	LED_PP1V8_S3_N ~ @m39_11b.M39	79A4	MEM_A_DQ<28>	MEM_A_DQ<28> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<51>	MEM_B_DM<51> ~ @m39_11b.M39	15B4 29A6	NC_SMC_P26	NC_SMC_P26 ~ @m39_11b.M39	59D5
LED_PP1V8_S3_P	LED_PP1V8_S3_P ~ @m39_11b.M39	79A4	MEM_A_DQ<29>	MEM_A_DQ<29> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<52>	MEM_B_DM<52> ~ @m39_11b.M39	5A7 15B4 29B3	SMC_P26 ~ @m39_11b.M39	SMC_P26 ~ @m39_11b.M39	58D7 59D6
LFC_AD<0>	LFC_AD<0> ~ @m39_11b.M39	21D4 58D7 60C6 67C6	MEM_A_DQ<30>	MEM_A_DQ<30> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<53>	MEM_B_DM<53> ~ @m39_11b.M39	15B4 29B3	NC_SMC_P27	NC_SMC_P27 ~ @m39_11b.M39	59D5
LFC_AD<1>	LFC_AD<1> ~ @m39_11b.M39	21D4 58D7 60C6 67C6	MEM_A_DQ<31>	MEM_A_DQ<31> ~ @m39_11b.M39	15C7 28C3	MEM_B_DM<54>	MEM_B_DM<54> ~ @m39_11b.M39	15B4 29A3	ODD_PWR_EN_L	ODD_PWR_EN_L ~ @m39_11b.M39	26C3
LFC_AD<2>	LFC_AD<2> ~ @m39_11b.M39	21D4 58C7 60C3 67C6	MEM_A_DQ<32>	MEM_A_DQ<32> ~ @m39_11b.M39	15C7 28B6	MEM_B_DM<55>	MEM_B_DM<55> ~ @m39_11b.M39	15B4 29A6	POV48_SMC_LSREF	POV48_SMC_LSREF ~ @m39_11b.M39	59A6 59A8 59B7
LFC_AD<3>	LFC_AD<3> ~ @m39_11b.M39	21D4 58C7 60C3 67C6	MEM_A_DQ<33>	MEM_A_DQ<33> ~ @m39_11b.M39	15B7 28B6	MEM_B_DM<56>	MEM_B_DM<56> ~ @m39_11b.M39	15B4 29A6	PANEL_ID	PANEL_ID ~ @m39_11b.M39	6D6 94C2
LFC_FRAME_L	LFC_FRAME_L ~ @m39_11b.M39	21C5 58C7 60C6 67C6	MEM_A_DQ<34>	MEM_A_DQ<34> ~ @m39_11b.M39	15B7 28B6	MEM_B_DM<57>	MEM_B_DM<57> ~ @m39_11b.M39	15B4 29A6	PATA_PWR_EN_L	PATA_PWR_EN_L ~ @m39_11b.M39	23B3 23C3
LVDS_A_CLK_N	LVDS_A_CLK_N ~ @m39_11b.M39	13D5 19D2	MEM_A_DQ<35>	MEM_A_DQ<35> ~ @m39_11b.M39	15B7 28B6	MEM_B_DM<58>	MEM_B_DM<58> ~ @m39_11b.M39	15B4 29A6	PCIE_A_D2R_C_N	PCIE_A_D2R_C_N ~ @m39_11b.M39	41D5
LVDS_A_CLK_P	LVDS_A_CLK_P ~ @m39_11b.M39	19D1	MEM_A_DQ<36>	MEM_A_DQ<36> ~ @m39_11b.M39	15B7 28B3	MEM_B_DM<59>	MEM_B_DM<59> ~ @m39_11b.M39	15B4 29A3	PCIE_A_D2R_C_P	PCIE_A_D2R_C_P ~ @m39_11b.M39	41D5
LVDS_A_CLK_P	LVDS_A_CLK_P ~ @m39_11b.M39	13C5 19D2	MEM_A_DQ<37>	MEM_A_DQ<37> ~ @m39_11b.M39	15B7 28B3	MEM_B_DM<60>	MEM_B_DM<60> ~ @m39_11b.M39	15B4 29A3	PCIE_A_D2R_N	PCIE_A_D2R_N ~ @m39_11b.M39	

	8	7	6	5	4	3	2	1								
	PCI_AD<3>	PCI_AD<3> - @m39_lib.M39	2287 4405	PEG_R2D_C_N<3>	PEG_R2D_C_N<3> - @m39_lib.M39	1383 84B5	PP1V8_FB_A0_VDDA0	PP1V8_FB_A0_VDDA0 - @m39_lib.M39	8907	SB_RTC_X2	SB_RTC_X2 - @m39_lib.M39	21D6 26D7	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	
	PCI_AD<4>	PCI_AD<4> - @m39_lib.M39	2287 4405	PEG_R2D_C_N<4>	PEG_R2D_C_N<4> - @m39_lib.M39	1383 84B5	PP1V8_FB_A0_VDDA1	PP1V8_FB_A0_VDDA1 - @m39_lib.M39	8907	SB_SM_INTRUDER_L	SB_SM_INTRUDER_L - @m39_lib.M39	21D6 26C7	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	
	PCI_AD<5>	PCI_AD<5> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<5>	PEG_R2D_C_N<5> - @m39_lib.M39	1383 84B5	PP1V8_FB_A1_VDDA0	PP1V8_FB_A1_VDDA0 - @m39_lib.M39	8904	SB_SPKR	SB_SPKR - @m39_lib.M39	23C5	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	
	PCI_AD<6>	PCI_AD<6> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<6>	PEG_R2D_C_N<6> - @m39_lib.M39	1383 84C5	PP1V8_FB_A1_VDDA1	PP1V8_FB_A1_VDDA1 - @m39_lib.M39	8904	SC_RX_L	SC_RX_L - @m39_lib.M39	58C5 59B6 59C4	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	
	PCI_AD<7>	PCI_AD<7> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<7>	PEG_R2D_C_N<7> - @m39_lib.M39	1383 84C5	PP1V8_FB_B0_VDDA0	PP1V8_FB_B0_VDDA0 - @m39_lib.M39	9007	SC_TX_L	SC_TX_L - @m39_lib.M39	58C5 59B4 59B6	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	
	PCI_AD<8>	PCI_AD<8> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<8>	PEG_R2D_C_N<8> - @m39_lib.M39	1383 84C5	PP1V8_FB_B0_VDDA1	PP1V8_FB_B0_VDDA1 - @m39_lib.M39	9007	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<9>	PCI_AD<9> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<9>	PEG_R2D_C_N<9> - @m39_lib.M39	1383 84C5	PP1V8_FB_B1_VDDA0	PP1V8_FB_B1_VDDA0 - @m39_lib.M39	9004	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<10>	PCI_AD<10> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<10>	PEG_R2D_C_N<10> - @m39_lib.M39	1383 84C5	PP1V8_FB_B1_VDDA1	PP1V8_FB_B1_VDDA1 - @m39_lib.M39	9004	SDVO_CTRLCLK	SDVO_CTRLCLK - @m39_lib.M39	1486 19A2	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<11>	PCI_AD<11> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<11>	PEG_R2D_C_N<11> - @m39_lib.M39	1383 84C5	PP1V8_S3_PGOOD	PP1V8_S3_PGOOD - @m39_lib.M39	7982	NC_SMC_ANALOG_ID	NC_SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<12>	PCI_AD<12> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<12>	PEG_R2D_C_N<12> - @m39_lib.M39	1383 84D5	PP2V5_S0_PGOOD	PP2V5_S0_PGOOD - @m39_lib.M39	7788 77C5	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<13>	PCI_AD<13> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<13>	PEG_R2D_C_N<13> - @m39_lib.M39	1383 84D5	PP2V5_ENET_CTAP	PP2V5_ENET_CTAP - @m39_lib.M39	4307	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<14>	PCI_AD<14> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<14>	PEG_R2D_C_N<14> - @m39_lib.M39	1383 84D5	PP2V5_S0_GPU_A2VDD	PP2V5_S0_GPU_A2VDD - @m39_lib.M39	9387	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<15>	PCI_AD<15> - @m39_lib.M39	2287 44C5	PEG_R2D_C_N<15>	PEG_R2D_C_N<15> - @m39_lib.M39	1383 84D5	PP2V5_S0_GPU_AVDD	PP2V5_S0_GPU_AVDD - @m39_lib.M39	93C7	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<16>	PCI_AD<16> - @m39_lib.M39	2287 44C5	PEG_R2D_C_P<0>	PEG_R2D_C_P<0> - @m39_lib.M39	1383 84B5	PP2V5_S0_GPU_LPVDV	PP2V5_S0_GPU_LPVDV - @m39_lib.M39	9387	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<17>	PCI_AD<17> - @m39_lib.M39	2287 44C5	PEG_R2D_C_P<1>	PEG_R2D_C_P<1> - @m39_lib.M39	1383 84B5	PP2V5_S0_GPU_LVDDR	PP2V5_S0_GPU_LVDDR - @m39_lib.M39	9387	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<18>	PCI_AD<18> - @m39_lib.M39	2287 44C5	PEG_R2D_C_P<2>	PEG_R2D_C_P<2> - @m39_lib.M39	1383 84B5	PP2V5_S0_GPU_PVDD_F	PP2V5_S0_GPU_PVDD_F - @m39_lib.M39	91A6	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<19>	PCI_AD<19> - @m39_lib.M39	22A7 44C6	PEG_R2D_C_P<3>	PEG_R2D_C_P<3> - @m39_lib.M39	1383 84B5	PP2V5_S0_GPU_TPVDD	PP2V5_S0_GPU_TPVDD - @m39_lib.M39	93C7	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<20>	PCI_AD<20> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<4>	PEG_R2D_C_P<4> - @m39_lib.M39	1383 84B5	PP2V5_S0_GPU_TPVDDR	PP2V5_S0_GPU_TPVDDR - @m39_lib.M39	93C7	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<21>	PCI_AD<21> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<5>	PEG_R2D_C_P<5> - @m39_lib.M39	1383 84B5	PP2V5_S0_GPU_VDD1D1	PP2V5_S0_GPU_VDD1D1 - @m39_lib.M39	93C8	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<22>	PCI_AD<22> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<6>	PEG_R2D_C_P<6> - @m39_lib.M39	1383 84C5	PP2V5_S0_GPU_VDD2D1	PP2V5_S0_GPU_VDD2D1 - @m39_lib.M39	9387	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<23>	PCI_AD<23> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<7>	PEG_R2D_C_P<7> - @m39_lib.M39	1383 84C5	PP3V3R12V_LCD_CONN	PP3V3R12V_LCD_CONN - @m39_lib.M39	94A6 94A6 94A7 94C5	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<24>	PCI_AD<24> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<8>	PEG_R2D_C_P<8> - @m39_lib.M39	1383 84C5	PP3V3_AUDIO_SPDIF_EM	PP3V3_AUDIO_SPDIF_EM - @m39_lib.M39	7387	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<25>	PCI_AD<25> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<9>	PEG_R2D_C_P<9> - @m39_lib.M39	1383 84C5	I	I		SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<26>	PCI_AD<26> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<10>	PEG_R2D_C_P<10> - @m39_lib.M39	1383 84C5	PP3V3_AUDIO_SPDIF_JA	PP3V3_AUDIO_SPDIF_JACK - @m39_lib.M39	7385	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<27>	PCI_AD<27> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<11>	PEG_R2D_C_P<11> - @m39_lib.M39	1383 84D5	CK	CK		SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<28>	PCI_AD<28> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<12>	PEG_R2D_C_P<12> - @m39_lib.M39	13A3 84D5	PP3V3_AVCC_SMC	PP3V3_AVCC_SMC - @m39_lib.M39	58D3	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<29>	PCI_AD<29> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<13>	PEG_R2D_C_P<13> - @m39_lib.M39	13A3 84D5	PP3V3_AVRFP_SMC	PP3V3_AVRFP_SMC - @m39_lib.M39	58D2 59A3	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<30>	PCI_AD<30> - @m39_lib.M39	22A7 44C5	PEG_R2D_C_P<14>	PEG_R2D_C_P<14> - @m39_lib.M39	13A3 84D5	PP3V3_FW_RSD	PP3V3_FW_RSD - @m39_lib.M39	46A4 46A6 46B5 46C5 46D5	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_AD<31>	PCI_AD<31> - @m39_lib.M39	22A7 44B5	PEG_R2D_C_P<15>	PEG_R2D_C_P<15> - @m39_lib.M39	13A3 84D5	PP3V3_FW_ESD_F	PP3V3_FW_ESD_F - @m39_lib.M39	46A7	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_CLK_FW	PCI_CLK_FW - @m39_lib.M39	34C4 44B5	PEG_R2D_N<0>	PEG_R2D_N<0> - @m39_lib.M39	84D4	PP3V3_INTERCON	PP3V3_INTERCON - @m39_lib.M39	72C7	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	
	PCI_CLK_PORT80	PCI_CLK_PORT80 - @m39_lib.M39	34B4 60C3	PEG_R2D_N<1>	PEG_R2D_N<1> - @m39_lib.M39	84D4	PP3V3_LCD_SW	PP3V3_LCD_SW - @m39_lib.M39	94C6	SMC_ANALOG_ID	SMC_ANALOG_ID - @m39_lib.M39	59A7 59C6	TP_SDVO_CTRLCLK	TP_SDVO_CTRLCLK - @m39_lib.M39	19A1	

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D	Title: Cref Part Report Design: m39 Date: Nov 15 18:33:07 2005			C2515 CAP_402 m39[25B6] C2516 CAP_P_CASE-C2 m39[25D3] C2517 CAP_402 m39[25D6] C2518 CAP_402 m39[25D4] C2519 CAP_402 m39[25D3] C2520 CAP_402 m39[25B6] C2521 CAP_402 m39[25C3] C2522 CAP_402 m39[25B3] C2523 CAP_402 m39[25B4] C2524 CAP_603 m39[25B3] C2525 CAP_402 m39[25B3] C2526 CAP_402 m39[25A4] C2527 CAP_402 m39[25A3] C2528 CAP_402 m39[25A3] C2529 CAP_402 m39[25A3] C2530 CAP_402 m39[25A3] C2531 CAP_402 m39[25D1] C2532 CAP_402 m39[25C1] C2533 CAP_402 m39[25C1] C2534 CAP_402 m39[25D1] C2605 CAP_402 m39[26C7] C2607 CAP_402 m39[26D5] C2608 CAP_402 m39[26D8] C2609 CAP_402 m39[26D8] C2610 CAP_402 m39[26C7] C2611 CAP_402 m39[26B7] C2698 CAP_402 m39[26C4] C2699 CAP_402 m39[26C5] C2800 CAP_402 m39[28D6] C2801 CAP_603 m39[28B2] C2802 CAP_603 m39[28B2] C2803 CAP_603 m39[28B1] C2804 CAP_603 m39[28B1] C2810 CAP_402 m39[28B2] C2811 CAP_402 m39[28B2] C2812 CAP_402 m39[28B1] C2813 CAP_402 m39[28B1] C2814 CAP_402 m39[28B2] C2815 CAP_402 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8			7			6			5			4			3			2			1		
D	C7512	CAP_603	m39[75C2]	C	C8420	CAP_402	m39[84D5]	B	C8658	CAP_402	m39[86B5]	A	C9136	CAP_402	m39[91A6]	D							
	C7513	CAP_402	m39[75B7]		C8421	CAP_402	m39[84D5]		C8659	CAP_402	m39[86B5]		C9137	CAP_402	m39[91A5]								
	C7514	CAP_402	m39[75B8]		C8422	CAP_402	m39[84D5]		C8660	CAP_402	m39[86B5]		C9140	CAP_805	m39[91A6]								
	C7515	CAP_603	m39[75C4]		C8423	CAP_402	m39[84D5]		C8661	CAP_402	m39[86B6]		C9141	CAP_402	m39[91A6]								
	C7516	CAP_402	m39[75B4]		C8424	CAP_402	m39[84D5]		C8662	CAP_402	m39[86B6]		C9142	CAP_402	m39[91A5]								
	C7517	CAP_P_SM-3	m39[75D2]		C8425	CAP_402	m39[84D5]		C8663	CAP_402	m39[86B6]		C9191	CAP_402	m39[91D2]								
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	C7526	CAP_603	m39[75D6]		C8428	CAP_402	m39[84D5]		C8666	CAP_402	m39[86B5]		C9302	CAP_402	m39[93C5]								
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	C7529	CAP_402	m39[75B5]	C8431	CAP_402	m39[84C5]	C8669	CAP_402	m39[86B6]	C9307	CAP_402	m39[93C5]											
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	C7531	CAP_402	m39[75B5]	C8433	CAP_402	m39[84C5]	C8671	CAP_402	m39[86B5]	C9311	CAP_402	m39[93C6]											
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	C7533	CAP_402	m39[75B6]	C8435	CAP_402	m39[84C5]	C8673	CAP_402	m39[86B6]	C9315	CAP_805	m39[93B8]											
	C7534	CAP_402	m39[75B5]	C8436	CAP_402	m39[84C5]	C8674	CAP_402	m39[86B6]	C9316	CAP_402	m39[93B8]											
	C7535	CAP_603	m39[75D5]	C8437	CAP_402	m39[84C5]	C8675	CAP_402	m39[86B6]	C9317	CAP_402	m39[93B7]											
	C7550	CAP_603	m39[75D1]	C8438	CAP_402	m39[84C5]	C8676	CAP_402	m39[86B5]	C9320	CAP_805	m39[93B6]											
	C7551	CAP_603	m39[75D1]	C8439	CAP_402	m39[84C5]	C8677	CAP_402	m39[86B5]	C9321	CAP_402	m39[93B6]											
B	C7590	CAP_402	m39[75C3]	C8440	CAP_402	m39[84B5]	C8678	CAP_402	m39[86B5]	C9322	CAP_402	m39[93B5]											
	C7592	CAP_402	m39[75B3]	C8441	CAP_402	m39[84B5]	C8679	CAP_402	m39[86A6]	C9325	CAP_805	m39[93B8]											
	C7596	CAP_402	m39[75D6]	C8442	CAP_402	m39[84B5]	C8680	CAP_402	m39[86A6]	C9326	CAP_402	m39[93B8]											
	C7597	CAP_1210	m39[75D1]	C8443	CAP_402	m39[84B5]	C8681	CAP_402	m39[86A6]	C9327	CAP_402	m39[93B7]											
	C7598	CAP_1210	m39[75D1]	C8444	CAP_402	m39[84B5]	C8682	CAP_402	m39[86A5]	C9330	CAP_805	m39[93B6]											
	C7599	CAP_402	m39[76D6]	C8445	CAP_402	m39[84B5]	C8683	CAP_402	m39[86A5]	C9331	CAP_402	m39[93B6]											
	C7600	CAP_402	m39[76D3]	C8446	CAP_402	m39[84B5]	C8690	CAP_805	m39[86D5]	C9332	CAP_402	m39[93B5]											
	C7601	CAP_402	m39[76D3]	C8447	CAP_402	m39[84B5]	C8691	CAP_402	m39[86D5]	C9340	CAP_805	m39[93A6]											
	C7602	CAP_402	m39[76D2]	C8448	CAP_402	m39[84B5]	C8692	CAP_402	m39[86D5]	C9341	CAP_402	m39[93A6]											
	C7603	CAP_402	m39[76C4]	C8449	CAP_402	m39[84B5]	C8695	CAP_805	m39[86D2]	C9342	CAP_402	m39[93A5]											
A	C7612	CAP_402	m39[76B2]	C8450	CAP_402	m39[84B5]	C8696	CAP_402	m39[86D3]	C9345	CAP_805	m39[93A6]											
	C7633	CAP_402	m39[76C7]	C8451	CAP_402	m39[84B5]	C8697	CAP_402	m39[86D3]	C9346	CAP_402	m39[93A5]											
	C7659	CAP_402	m39[76D4]	C8455	CAP_402	m39[84D2]	C8711	CAP_402	m39[87B7]	C9347	CAP_402	m39[93A5]											
	C7669	CAP_402	m39[76D4]	C8456	CAP_402	m39[84D2]	C8713	CAP_402	m39[87B7]	C9400	CAP_603-1	m39[94C7]											
	C7700	CAP_603	m39[77D4]	C8457	CAP_402	m39[84D2]	C8715	CAP_402	m39[87A7]	C9401	CAP_402	m39[94C6]											
	C7703	CAP_402	m39[77C4]	C8458	CAP_402	m39[84D2]	C8716	CAP_402	m39[87A6]	C9410	CAP_402	m39[94C6]											
	C7704	CAP_402	m39[77C4]	C8459	CAP_402	m39[84D2]	C8721	CAP_402	m39[87B4]	C9411	CAP_1210	m39[94C5]											
	C7706	CAP_402	m39[77C4]	C8460	CAP_402	m39[84D2]	C8723	CAP_402	m39[87B4]	C9470	CAP_402	m39[94B2]											
	C7709	CAP_805	m39[77C3]	C8461	CAP_402	m39[84D2]	C8725	CAP_402	m39[87A4]	C9700	CAP_402	m39[97C8]											
	C7710	CAP_402	m39[77C7]	C8462	CAP_402	m39[84D2]	C8726	CAP_402	m39[87A3]	C9710	CAP_603	m39[97C3]											
D	C7711	CAP_402	m39[77B7]	C8463	CAP_402	m39[84D2]	C8900	CAP_805	m39[89D7]	C9711	CAP_402	m39[97D3]											
	C7712	CAP_402	m39[77C7]	C8464	CAP_402	m39[84C2]	C8901	CAP_402	m39[89D7]	C9713	CAP_402	m39[97C2]											
	C7750	CAP_402	m39[77A5]	C8465	CAP_402	m39[84C2]	C8902	CAP_402	m39[89D7]	C9714	CAP_402	m39[97C2]											
	C7751	CAP_805	m39[77B4]	C8466	CAP_402	m39[84C2]	C8903	CAP_402	m39[89D7]	C9740	CAP_402	m39[97A7]											
	C7752	CAP_805	m39[77B4]	C8467	CAP_402	m39[84C2]	C8904	CAP_402	m39[89D6]	C9741	CAP_402	m39[97A6]											
	C7753	CAP_402	m39[77A7]	C8468	CAP_402	m39[84C2]	C8910	CAP_402	m39[89D7]	C9742	CAP_402	m39[97A6]											
	C7754	CAP_402	m39[77B6]	C8469	CAP_402	m39[84C2]	C8915	CAP_402	m39[89D6]	D2500	DIODE_SCHOT_SOT23	m39[25C8]											
	C7755	CAP_805	m39[77B3]	C8470	CAP_402	m39[84C2]	C8920	CAP_805	m39[89C8]	D2501	DIODE_SCHOT_SOT23	m39[25D8]											
	C7756	CAP_805	m39[77B3]	C8471	CAP_402	m39[84C2]	C8921	CAP_402	m39[89C8]	D2600	DIODE_SCHOT_SOT23	m39[26D8]											
	C7757	CAP_402	m39[77A6]	C8472	CAP_402	m39[84C2]	C8922	CAP_402	m39[89C7]	D2601	DIODE_SCHOT_SOT23	m39[26C8]											
C	C7799	CAP_402	m39[77A3]	C8473	CAP_402	m39[84C2]	C8923	CAP_402	m39[89C7]	D4600	DIODE_SMC	m39[46D5]											
	C7800	CAP_1210	m39[78C3]	C8474	CAP_402	m39[84C2]	C8924	CAP_402	m39[89C7]	D4690	ZENER_SOT23	m39[46A6]											
	C7801	CAP_P_SM-LF	m39[78C4]	C8475	CAP_402	m39[84B2]	C8925	CAP_402	m39[89C7]	D4700	DIODE_SCHOT_3P_A_SC-	m39[47C5]											
	C7802	CAP_603	m39[78C6]	C8476	CAP_402	m39[84B2]	C8926	CAP_402	m39[89C6]	D4701	DIODE_SCHOT_3P_A_SC-	m39[47B5]											
	C7803	CAP_402	m39[78B6]	C8477	CAP_402	m39[84B2]	C8931	CAP_402	m39[89C7]	75													
	C7804	CAP_402	m39[78C6]	C8478	CAP_402	m39[84B2]	C8933	CAP_402	m39[89C6]	D4702	DIODE_SCHOT_3P_A_SC-	m39[47A5]											
	C7805	CAP_603	m39[78C4]	C8479	CAP_402	m39[84B2]	C8939	CAP_805	m39[89D4]	75													
	C7806	CAP_805-1	m39[78B2]	C8480	CAP_402	m39[84B2]	C8951	CAP_402	m39[89D4]	D4705	DIODE_SCHOT_3P_A_SC-	m39[47C6]											
	C7807	CAP_P_CASE-D2E-LF	m39[78B3]	C8481	CAP_402	m39[84B2]	C8952	CAP_402	m39[89D4]	75													
	C7809	CAP_402	m39[78B3]	C8482	CAP_402	m39[84B2]	C8953	CAP_402	m39[89D3]	D4706	DIODE_SCHOT_3P_A_SC-	m39[47B6]											
B	C7810	CAP_402	m39[78B4]	C8483	CAP_402	m39[84B2]	C8954	CAP_402	m39[89D3]	75													
	C7811	CAP_402	m39[78B5]	C8484	CAP_402	m39[84B2]	C8960	CAP_402	m39[89D3]	D4707	DIODE_SCHOT_3P_A_SC-	m39[47A6]											
	C7813	CAP_1206	m39[78B4]	C8485	CAP_402	m39[84B2]	C8965	CAP_402	m39[89D3]	75													
	C7814	CAP_402	m39[78B5]	C8486	CAP_402	m39[84B2]	C8970	CAP_805	m39[89C5]	D4710	DIODE_SCHOT_3P_A_SC-	m39[47C6]											
	C7817	CAP_P_CASE-D2E-LF	m39[78B2]	C8500	CAP_603	m39[85D6]	C8971	CAP_402	m39[89C4]	75													
	C7900	CAP_402	m39[79D6]	C8501	CAP_603	m39[85D6]	C8972	CAP_402	m39[89C4]	D4900	DIODE_SCHOT_3P_A_SC-	m39[49B4]											
	C7901	CAP_603	m39[79D5]	C8502	CAP_603	m39[85D6]	C8973	CAP_402	m39[89C4]	75													
	C7902	CAP_402	m39[79C5]	C8506	CAP_402	m39[85C8]	C8974	CAP_402	m39[89C4]	D6500	DIODE_SOT23	m39[65C4]											
	C7903	CAP_603	m39[79C7]	C8507	CAP_402	m39[85C7]	C8975	CAP_402	m39[89C3]	D6501	DIODE_SOT23	m39[65B4]											
	C7906	CAP_402	m39[79C5]	C8508	CAP_402	m39[85C7]	C8976	CAP_402	m39[89C3]	D6502	DIODE_SCHOT_SMB	m39[65C4]											
A	C7907	CAP_402	m39[79C3]	C8509	CAP_402	m39[85C5]	C8981	CAP_402	m39[89C3]	D6503	DIODE_SCHOT_SMB	m39[65B4]											
	C7908	CAP_402	m39[79C5]	C8520	CAP_603	m39[85C3]	C8983	CAP_402	m39[89C3]	D6600	DIODE_SOT23	m39[66C4]											
	C7909	CAP_1206	m39[79C4]	C8521	CAP_402	m39[85C4]	C9000	CAP_805	m39[90D7]	D6601	DIODE_SCHOT_SMB	m39[66C3]											
	C7910	CAP_P_SM-3	m39[79D4]	C8522	CAP_402	m39[85C5]	C9001	CAP_402	m39[90D7]	D7500	DIODE_SCHOT_SMB	m39[75C3]											
	C7911	CAP_1210	m39[79D3]	C8523	CAP_402	m39[85D2]	C9002	CAP_402	m39[90D7]	D7501	DIODE_SCHOT_SMB	m39[75B2]											
	C7912	CAP_P_CASE-D2E-LF	m39[79C3]	C8530	CAP_1210	m39[85D4]	C9003	CAP_402	m39[90D7]	D7599	DIODE_SOT23	m39[76D6]											
	C7913	CAP_1206	m39[79C2]	C8531	CAP_1210	m39[85D4]	C9004	CAP_402	m39[90D6]	D8520	DIODE_SCHOT_SMB	m39[85C3]											
	C7980	CAP_402	m39[79B3]	C8532	CAP_1210	m39[85D4]	C9010	CAP_402	m39[90D7]	D9700	ZENER_CASE425	m39[97C1]											
	C7992	CAP_603	m39[79D6]	C8540	CAP_805	m39[85C2]	C9015	CAP_402	m39[90D6]	DP4610	DIODE_DUAL_6P_SOT-36	m39[46D4 46D3]											
	C7998	CAP_P_CASE-D2E-LF	m39[79C3]	C8541	CAP_805	m39[85C2]	C9020	CAP_805	m39[90C8]	3													
D	C8000	CAP_1210	m39[80D3]	C8542	CAP_P_CASE-D2E-LF	m39[85C2]	C9021	CAP_402	m39[90C8]	DP4611	DIODE_DUAL_6P_SOT-36	m39[46C4 46C3]											
	C8001	CAP_P_CASE-D2E-LF	m39[80C3]	C8543	CAP_P_CASE-D2E-LF	m39[85C2]	C9022	CAP_402	m39[90C7]	3													
	C8002	CAP_603	m39[80D6]	C8551	CAP_603	m39[85B8]	C9023	CAP_402	m39[90C7]	DP4620	DIODE_DUAL_6P_SOT-36	m39[46B4 46B3]											
	C8003	CAP_402	m39[80C3]	C8555	CAP_402	m39[85B7]	C9024	CAP_402	m39[90C7]	3													
	C8004	CAP_1206	m39[80C4]	C8556	CAP_805	m39[85B6]	C9025	CAP_402	m39[90C7]	DP4621	DIODE_DUAL_6P_SOT-36	m39[46A4 46A3]											
	C8005	CAP_402	m39[80C4]	C8557	CAP_805	m39[85B6]	C9026	CAP_402	m39[90C6]	3													
	C8006	CAP_402	m39[80D6]	C8560	CAP_402	m39[85A6]	C9031	CAP_402	m39[90C7]	D27300	SUPPR_TRANSIENT_4P1_	m39[73C6]											
	C8009	CAP_603	m39[80C6]	C8570	CAP_402	m39[85A5]	C9033	CAP_402	m39[90C6]	0405													
	C8010	CAP_603	m39[80C4]	C8580	CAP_603	m39[85A4]	C9050	CAP_805	m39[90D4]	D27301	SUPPR_TRANSIENT_4P1_	m39[73A5]											
	C8011	CAP_402	m39[80C5]	C8585	CAP_402	m39[85A4]	C9051	CAP_402	m39[90D4]	0405													
C	C8012	CAP_402	m39[80C5]	C8586	CAP_402																		

D	J600	CON_M14RT_D_THA_M-RT	m39[6D7]	-TH1				L7500	IND_SM	m39[75D1]		PP627	PROBEPOINT_SM	m39[5C8]		PP8908	PROBEPOINT_SM	m39[5B5]		D	PP8908	PROBEPOINT_SM	m39[5B5]		C	PP8908	PROBEPOINT_SM	m39[5B5]		B	A					
	J0700	CPU_YONAH_SKT_BGA	m39[7C3 7D7]					L7501	IND_SM	m39[75B2]		PP628	PROBEPOINT_SM	m39[5C8]		PP8909	PROBEPOINT_SM	m39[5B5]			PP8909	PROBEPOINT_SM	m39[5B5]			PP8909	PROBEPOINT_SM	m39[5B5]				PP8909	PROBEPOINT_SM	m39[5B5]		
	J0700	CPU_YONAH_SKT_BGA	m39[8D4 8D8]					L7502	IND_TH-VERT-LF	m39[76D8]		PP629	PROBEPOINT_SM	m39[5C8]		PP8910	PROBEPOINT_SM	m39[5B5]			PP8910	PROBEPOINT_SM	m39[5B5]			PP8910	PROBEPOINT_SM	m39[5B5]				PP8910	PROBEPOINT_SM	m39[5B5]		
	J1000	CON_2RTSM_125_SM-2MT	m39[10B7]	-BLK-LF				L7700	IND_SM-LF	m39[77C4]		PP630	PROBEPOINT_SM	m39[5C8]		PP8911	PROBEPOINT_SM	m39[5B5]			PP8911	PROBEPOINT_SM	m39[5B5]			PP8911	PROBEPOINT_SM	m39[5B5]				PP8911	PROBEPOINT_SM	m39[5B5]		
	J1101	CON_F28RT_S2MT_SM_F-	m39[11C2]	RT-SM				L7750	IND_SM-LF	m39[77B4]		PP631	PROBEPOINT_SM	m39[5D6]		PP8912	PROBEPOINT_SM	m39[5B5]			PP8912	PROBEPOINT_SM	m39[5B5]			PP8912	PROBEPOINT_SM	m39[5B5]				PP8912	PROBEPOINT_SM	m39[5B5]		
	J2600	BATTERY_2P_TH	m39[26C8]					L7800	IND_3P_SM	m39[78B3]		PP632	PROBEPOINT_SM	m39[5D6]		PP8913	PROBEPOINT_SM	m39[5B5]			PP8913	PROBEPOINT_SM	m39[5B5]			PP8913	PROBEPOINT_SM	m39[5B5]				PP8913	PROBEPOINT_SM	m39[5B5]		
	J2800	CON_F200RT_DDR2DIMM_	m39[28D5]	5MT_SM_F-RT-SM1				L7900	IND_3P_SM	m39[79C3]		PP633	PROBEPOINT_SM	m39[5D6]		PP8914	PROBEPOINT_SM	m39[5A5]			PP8914	PROBEPOINT_SM	m39[5A5]			PP8914	PROBEPOINT_SM	m39[5A5]				PP8914	PROBEPOINT_SM	m39[5A5]		
	J2900	CON_F200RT_DDR2DIMM_	m39[29D5]	5MT_SM_F-RT-SM1				L8000	IND_IHLP	m39[80C3]		PP634	PROBEPOINT_SM	m39[5D6]		PP8915	PROBEPOINT_SM	m39[5A5]			PP8915	PROBEPOINT_SM	m39[5A5]			PP8915	PROBEPOINT_SM	m39[5A5]				PP8915	PROBEPOINT_SM	m39[5A5]		
	J2901	CON_F45T_S2MT_SM_F-S	m39[59C7]	T-SM				L8100	IND_IHLP	m39[81C3]		PP635	PROBEPOINT_SM	m39[5D6]		PP8916	PROBEPOINT_SM	m39[5A5]			PP8916	PROBEPOINT_SM	m39[5A5]			PP8916	PROBEPOINT_SM	m39[5A5]				PP8916	PROBEPOINT_SM	m39[5A5]		
	J2903	CON_M2ST_S2MT_SM_M-S	m39[59C8]	T-SM				L8400	IND_0402	m39[84B7]		PP636	PROBEPOINT_SM	m39[5D6]		PP8920	PROBEPOINT_SM	m39[5A5]			PP8920	PROBEPOINT_SM	m39[5A5]			PP8920	PROBEPOINT_SM	m39[5A5]				PP8920	PROBEPOINT_SM	m39[5A5]		
C	J4700	CON_F10ST_D_SMA_F-ST	m39[47A2]	-SM				L8520	IND_IHLP	m39[85C3]		PP637	PROBEPOINT_SM	m39[5D6]		PP8921	PROBEPOINT_SM	m39[5A5]		C	PP8921	PROBEPOINT_SM	m39[5A5]			PP8921	PROBEPOINT_SM	m39[5A5]			PP8921	PROBEPOINT_SM	m39[5A5]		B	A
	J4950	CON_F45T_S2MT_SM_F-S	m39[49C4]	T-SM				L8585	IND_4P_2COIL_SDQ1215	m39[85A3]		PP638	PROBEPOINT_SM	m39[5D6]		PP8922	PROBEPOINT_SM	m39[5A5]			PP8922	PROBEPOINT_SM	m39[5A5]			PP8922	PROBEPOINT_SM	m39[5A5]			PP8922	PROBEPOINT_SM	m39[5A5]			
	J5300	CON_F52RT_D2MT_SM_F-	m39[53C5]	RT-SM				L8715	IND_0402	m39[87A7]		PP639	PROBEPOINT_SM	m39[5D6]		PP8923	PROBEPOINT_SM	m39[5A5]			PP8923	PROBEPOINT_SM	m39[5A5]			PP8923	PROBEPOINT_SM	m39[5A5]			PP8923	PROBEPOINT_SM	m39[5A5]			
	J6000	CON_F30STSM_5047_SM1	m39[60B5]					L8725	IND_0402	m39[87A4]		PP641	PROBEPOINT_SM	m39[5D6]		PP8925	PROBEPOINT_SM	m39[5A5]			PP8925	PROBEPOINT_SM	m39[5A5]			PP8925	PROBEPOINT_SM	m39[5A5]			PP8925	PROBEPOINT_SM	m39[5A5]			
	J6500	CON_M4RT_S2MT_SM_M-R	m39[65D3]	T-SM				L8910	IND_0402	m39[89D7]		PP642	PROBEPOINT_SM	m39[5D6]		PP8926	PROBEPOINT_SM	m39[5A5]			PP8926	PROBEPOINT_SM	m39[5A5]			PP8926	PROBEPOINT_SM	m39[5A5]			PP8926	PROBEPOINT_SM	m39[5A5]			
	J6501	CON_M5RT_S2MT_SMA_M-	m39[65B2]	RT-SM				L8960	IND_0402	m39[89D4]		PP643	PROBEPOINT_SM	m39[5D6]		PP8927	PROBEPOINT_SM	m39[5A5]			PP8927	PROBEPOINT_SM	m39[5A5]			PP8927	PROBEPOINT_SM	m39[5A5]			PP8927	PROBEPOINT_SM	m39[5A5]			
	J6600	CON_F45T_S2MT_SM_F-S	m39[66C2]	T-SM				L8965	IND_0402	m39[89D4]		PP644	PROBEPOINT_SM	m39[5D6]		PP9000	PROBEPOINT_SM	m39[5B4]			PP9000	PROBEPOINT_SM	m39[5B4]			PP9000	PROBEPOINT_SM	m39[5B4]			PP9000	PROBEPOINT_SM	m39[5B4]			
	J6601	CON_M4RT_S2MT_SM_M-R	m39[66B5]	T-SM				L9010	IND_0402	m39[90D7]		PP645	PROBEPOINT_SM	m39[5D6]		PP9001	PROBEPOINT_SM	m39[5B4]			PP9001	PROBEPOINT_SM	m39[5B4]			PP9001	PROBEPOINT_SM	m39[5B4]			PP9001	PROBEPOINT_SM	m39[5B4]			
	J6602	CON_M4RT_S2MT_SM_M-R	m39[66B3]	T-SM				L9015	IND_0402	m39[90D7]		PP646	PROBEPOINT_SM	m39[5D6]		PP9002	PROBEPOINT_SM	m39[5B4]			PP9002	PROBEPOINT_SM	m39[5B4]			PP9002	PROBEPOINT_SM	m39[5B4]			PP9002	PROBEPOINT_SM	m39[5B4]			
	J7300	CON_F45T_S4MT_TH1_F-	m39[73D8]	ST-TH				L9060	IND_0402	m39[90D4]		PP647	PROBEPOINT_SM	m39[5D6]		PP9003	PROBEPOINT_SM	m39[5A5]			PP9003	PROBEPOINT_SM	m39[5A5]			PP9003	PROBEPOINT_SM	m39[5A5]			PP9003	PROBEPOINT_SM	m39[5A5]			
B	J7301	CON_M7RT_S2MT_SM_M-R	m39[73D1]	T-SM				L9065	IND_0402	m39[90D4]		PP648	PROBEPOINT_SM	m39[5D6]		PP9032	PROBEPOINT_SM	m39[5A5]		B	PP9032	PROBEPOINT_SM	m39[5A5]			PP9032	PROBEPOINT_SM	m39[5A5]			PP9032	PROBEPOINT_SM	m39[5A5]		A	A
	J7303	CON_F9ANG_S4MT_TH1_F	m39[73B3]	-ANG-TH				L9120	IND_0402	m39[91B6]		PP649	PROBEPOINT_SM	m39[5D6]		PP9033	PROBEPOINT_SM	m39[5A5]			PP9033	PROBEPOINT_SM	m39[5A5]			PP9033	PROBEPOINT_SM	m39[5A5]			PP9033	PROBEPOINT_SM	m39[5A5]			
	J9402	CON_F30ST_D_SMA_F-ST-	m39[94B6]	SM				L9125	IND_0402	m39[91B6]		PP650	PROBEPOINT_SM	m39[5D6]		PP9034	PROBEPOINT_SM	m39[5A4]			PP9034	PROBEPOINT_SM	m39[5A4]			PP9034	PROBEPOINT_SM	m39[5A4]			PP9034	PROBEPOINT_SM	m39[5A4]			
	J9710	CON_DVI_F32ST_Q2MT_S	m39[97D5]	M_F-ST-SM				L9135	IND_0402	m39[91A7]		PP651	PROBEPOINT_SM	m39[5C6]		PP9035	PROBEPOINT_SM	m39[5A4]			PP9035	PROBEPOINT_SM	m39[5A4]			PP9035	PROBEPOINT_SM	m39[5A4]			PP9035	PROBEPOINT_SM	m39[5A4]			
	JC900	CON_M7ST_SATA_SM_M-S	m39[38B8]	T-SM				L9140	IND_0402	m39[91A7]		PP652	PROBEPOINT_SM	m39[5C6]		Q4200	TRA_2N7002_SOT23-LF	m39[42D2]			Q4200	TRA_2N7002_SOT23-LF	m39[42D2]			Q4200	TRA_2N7002_SOT23-LF	m39[42D2]			Q4200	TRA_2N7002_SOT23-LF	m39[42D2]			
	JC901	CON_M50SM_5MM_M-ST-S	m39[38C2]	M2-LF				L9300	IND_0402	m39[93C7]		PP653	PROBEPOINT_SM	m39[5C6]		Q4201	TRA_PBSS5540Z_SOT223	m39[42C6]			Q4201	TRA_PBSS5540Z_SOT223	m39[42C6]			Q4201	TRA_PBSS5540Z_SOT223	m39[42C6]			Q4201	TRA_PBSS5540Z_SOT223	m39[42C6]			
	JD600	CON_RJ45_10ANG_S3MT_	m39[43C6]	TH1_F-ANG-TH				L9305	IND_0402	m39[93C7]		PP654	PROBEPOINT_SM	m39[5C6]		Q4400	TRA_SINGLE_MOSFET_NC	m39[44A7]			Q4400	TRA_SINGLE_MOSFET_NC	m39[44A7]			Q4400	TRA_SINGLE_MOSFET_NC	m39[44A7]			Q4400	TRA_SINGLE_MOSFET_NC	m39[44A7]			
	JE000	CON_F6ST_S4MT_TH1_F-	m39[46C2]	ST-TH				L9310	IND_0402	m39[93C7]		PP655	PROBEPOINT_SM	m39[5C6]		Q5900	TRA_FDV301N_SOT23-LF	m39[59D7]			Q5900	TRA_FDV301N_SOT23-LF	m39[59D7]			Q5900	TRA_FDV301N_SOT23-LF	m39[59D7]			Q5900	TRA_FDV301N_SOT23-LF	m39[59D7]			
	JE001	CON_F6ST_S4MT_TH1_F-	m39[46B2]	ST-TH				L9315	IND_0402	m39[93C7]		PP656	PROBEPOINT_SM	m39[5C6]		Q5901	TRA_2N7002DW_SOT-363	m39[59C7 59C7]			Q5901	TRA_2N7002DW_SOT-363	m39[59C7 59C7]			Q5901	TRA_2N7002DW_SOT-363	m39[59C7 59C7]			Q5901	TRA_2N7002DW_SOT-363	m39[59C7 59C7]			
	JE310	CON_F45T_USB_S3MT_TH	m39[47D4]	_F-ST-TH				L9320	IND_0402	m39[93B7]		PP657	PROBEPOINT_SM	m39[5C6]		Q5902	TRA_2N7002DW_SOT-363	m39[59B1 59B2]			Q5902	TRA_2N7002DW_SOT-363	m39[59B1 59B2]			Q5902	TRA_2N7002DW_SOT-363	m39[59B1 59B2]			Q5902	TRA_2N7002DW_SOT-363	m39[59B1 59B2]			
A	JE320	CON_F45T_USB_S3MT_TH	m39[47B4]	_F-ST-TH				PP5E1	PROBEPOINT_SM	m39[5B8]		PP658	PROBEPOINT_SM	m39[5C6]		Q5903	TRA_DUAL_MMDT3904_SO	m39[59A2 59A1]		A	Q5903	TRA_DUAL_MMDT3904_SO	m39[59A2 59A1]			Q5903	TRA_DUAL_MMDT3904_SO	m39[59A2 59A1]			Q5903	TRA_D				

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