

- LAYER 1 : TOP
- LAYER 2 : GND1
- LAYER 3 : IN1
- LAYER 4 : VCC
- LAYER 5 : IN2
- LAYER 6 : IN3
- LAYER 7 : GND2
- LAYER 8 : BOT

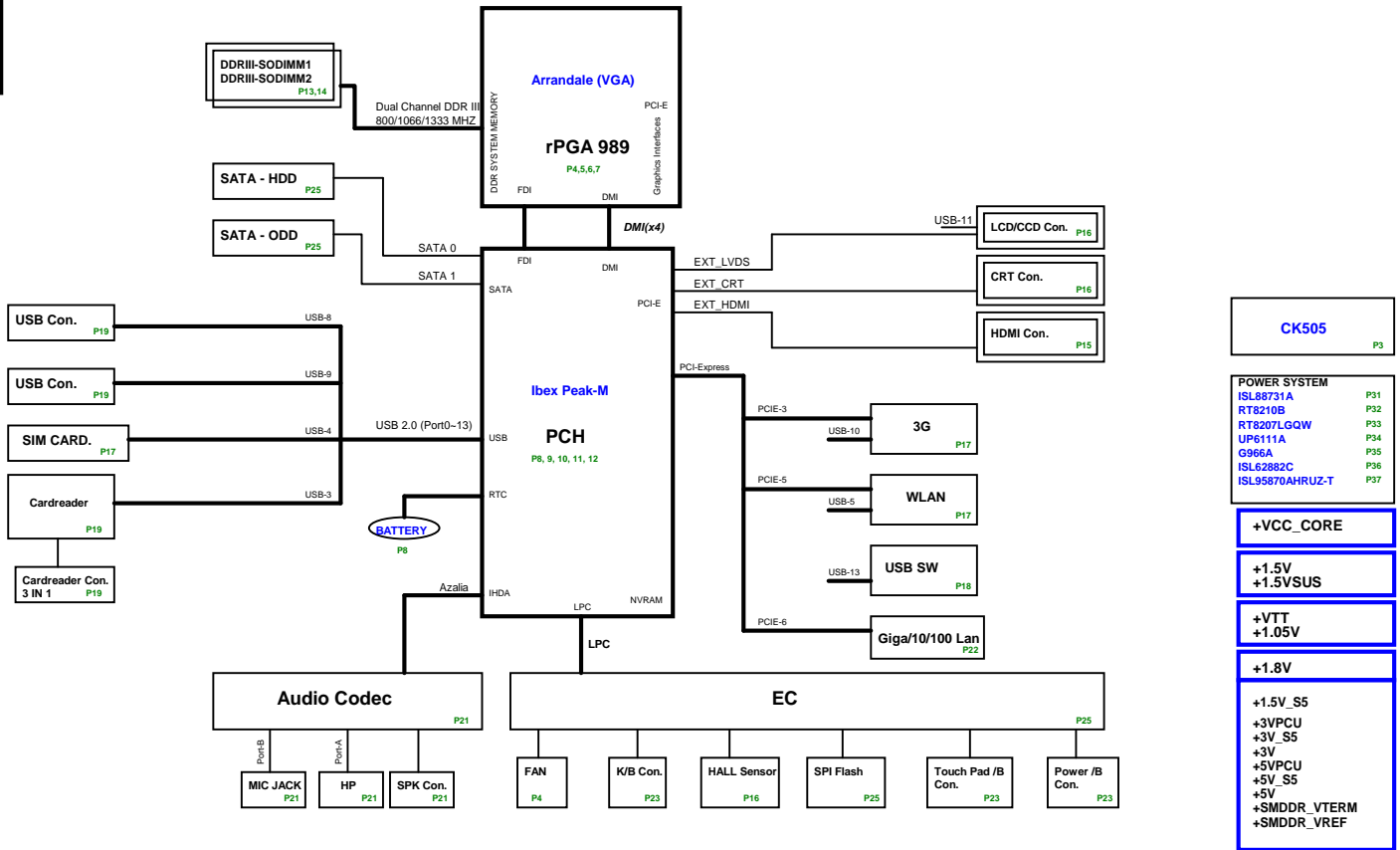






Table of Contents


PAGE	DESCRIPTION	BOI-FUNCTIONS
1	Schematic Block Diagram	
2	Front Page	
3-6	Processor	CPU
7-12	PCH	CLG
8	RTC	RTC
13-14	DDRIII SO-DIMM	DDR
17	VGA Connector	VGA
16	LCD Panel	LDS
	CRT & CRT BUS SWITCH	CRT
	CCD	CCD
	HALL SENSOR&BACK LIGHT SWITCH	HSR
19	Display Port	DPP
20	HDMI comm part	HDM
	HDMI for GM	HMG
21	SATA ODD	ODD
	Main SATA HDD & 2nd SATA HDD	HDD
	G-Sensor	H3D
22	5 IN 1 Card reader	MMC
	IEEE1394	FIW
23	MINI Card (Wi-Fi & WIMAX)	WLN
	MINI Card 2nd	MNC
	MINI Card 3rd	MNC
	TMA Connector	TMA
24	INT KeyBoard & K/B LED Power	KBC
	LED Board	LED
	TP&FP board	TPD,FPD
	Bluetooth Connector	BTM
	Felica Connector	FEC
	MMB Connector	MMB
	Power SW	PSW
	B-CAS Connector	BCS
25	New Card (Express Card)	EXC
	E-SATA comb USB	ESA
	USB Connector	USB
	Audio & USB Board	USB,ADO
	Light Sensor	LSN
	Satellite LED	LED
	RF LED / WIMAX LED / Kill SW	KSW
26	EC WP8763LDG/WPC8769L(O)	KBC
	CIR	CIR
27	Codec (CX20583)	ADO
28	FM Tunner	FMM
	Modem Connector	MDM
	HOLE	
29	Atheros LAN	LAN
30	NVRAM Connecyoy	NVR
31	Charger (ISL6251A)	PWM
32	System 5V/3V (ISL6237)	PWM
33	CPU CORE (ISL62882)	PWM

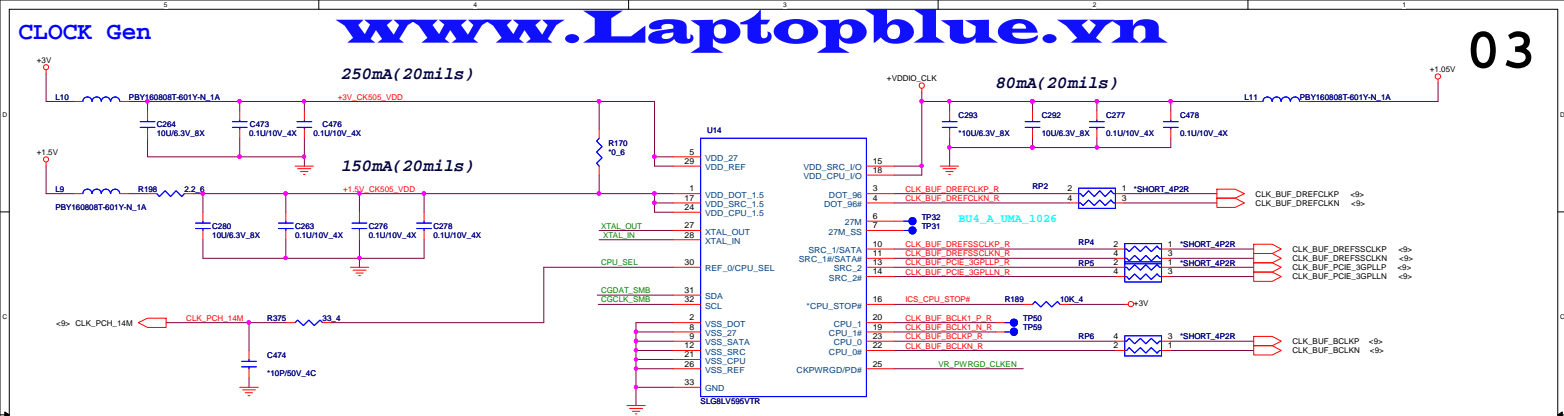
POWER PLANE	VOLTAGE	CONTROL SIGNAL	Power States ACTIVE IN
VIN	10V~+19V		S0-S5
+VCCRTC	+3.0V~+3.3V		S0-S5
+3V	+3.3V	MAIN_ON	S0
+3V_S5	+3.3V	S5_ON	S0-S5
+3V_HDP	+3.3V	MAIN_ON	S0
+3VPCU	+3.3V	AC/DC Insert enable	S0
+5V	+5V	MAIN_ON	S0
+5V_S5	+5V	S5_ON	S0-S5
+5VPCU	+5V	AC/DC Insert enable	S0-S5
+5V_TMA	+5V	MAIN_ON	S0
WIMAX_P	+3.3V	WMAX_P for EC	
+1.8V	+1.8V	MAIN_ON	S0
+1.5V	+1.5V	MAIN_ON	S0
+1.5V_S5	+1.5V	S5_ON	S0-S5
+1.5V_SUS	+1.5V	SUSON	S0-S3
+VCC_CORE		VRON	S0
+VTT	+1.05V~+1.1V	MAIN_ON	S0
+1.05V	+1.05V	MAIN_ON	S0
+VAXG		GFXVR_EN	S0

GND PLANE	PAGE
 GND_SIGNAL	32
 CARD_GND	21
 AGND_DC/DC	31
 GND	ALL

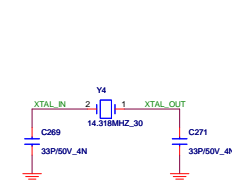
PAGE	DESCRIPTION	BOI-FUNCTIONS
34	VAXG (ISL62881)	PWM
35	+VTT (UP6111A)	PWM
36	+1.05V (UP6111AQDD)	PWM
37	DDR 1.5V (TPS51116)	PWM
38	Discharge (1.5V_S5/1.8V)	PWM
39	Power Tree Table	
40	PCH Power Plane	
41	Power Management	
42	Change List	

ITEM	Value Code	FUNCTIONS
1	EV@	DISCRETE
2	IV@	UMA
3	U3@	USB 3.0
4	U2@	USB 2.0 (colay W USB 3.0)
5	HM@	HDMI
6	IHM@	Internal HDMI
7	EHM@	External HDMI
8	3G@	3G
9	C@	Cost issue
10	MDC@	Modem
11	S3@	S3 Power Reduction
12	NS3@	No S3 Power Reduction
13	KBP@	K/B LED power
14	51@	1G LAN
15	52@	10/100 LAN
16	HDR@	SATA HDD Re-drive IC
17	HDO@	W/O SATA HDD Re-drive IC

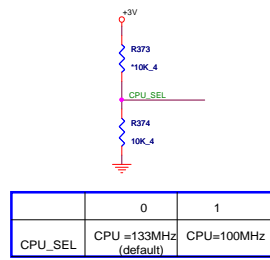
		<b>Quanta Computer Inc.</b>	
		<b>PROJECT : BU4</b>	
Size	Document Number	POWER STAGE AND BOI-FUNCTION	
Date: Wednesday, December 23, 2010		Sheet	2 of 32
			Rev 1A



CLK CRYSTAL

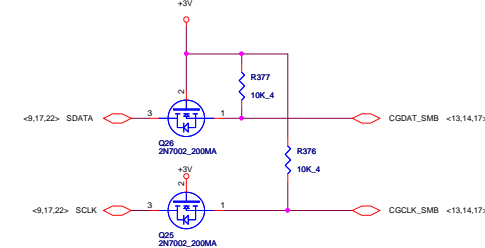


CLK CPU\_SEL

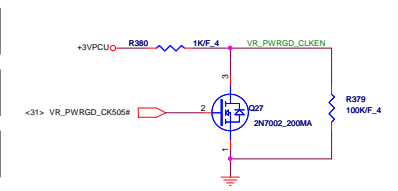



CPU_SEL	0	1
CPU_SEL	CPU =133MHz (default)	CPU=100MHz

CLK I2C



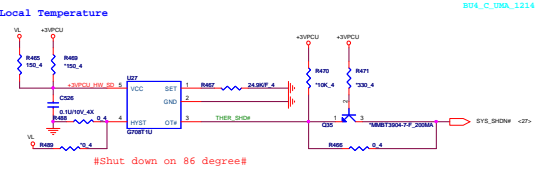
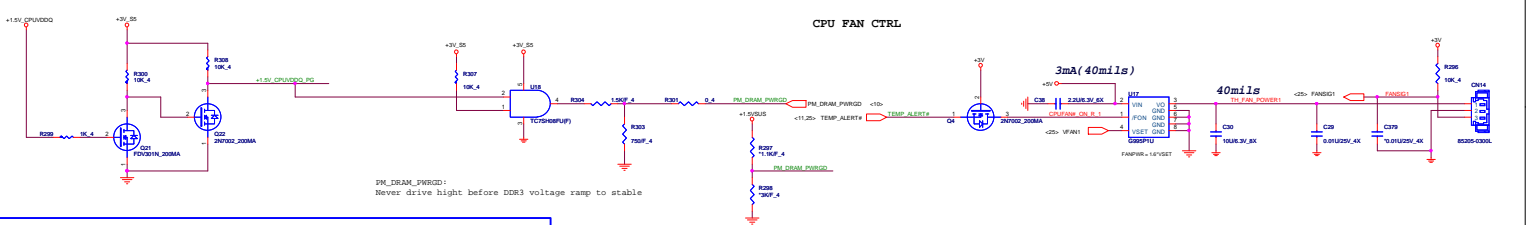
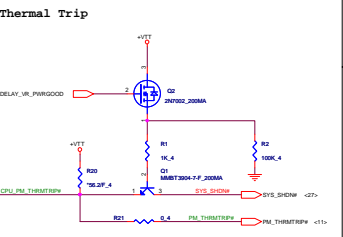
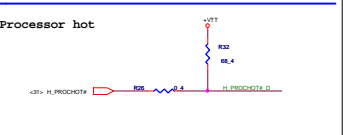
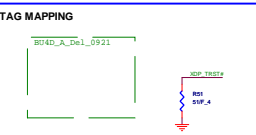
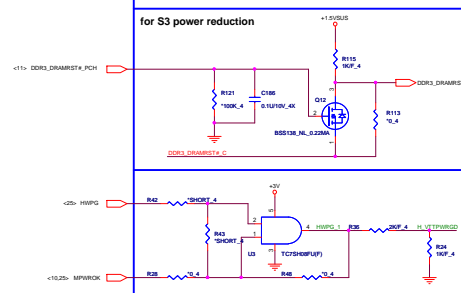
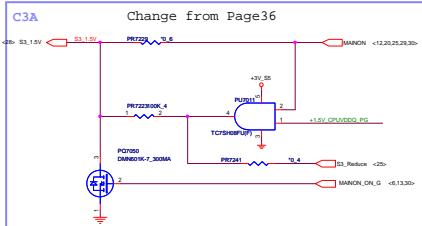
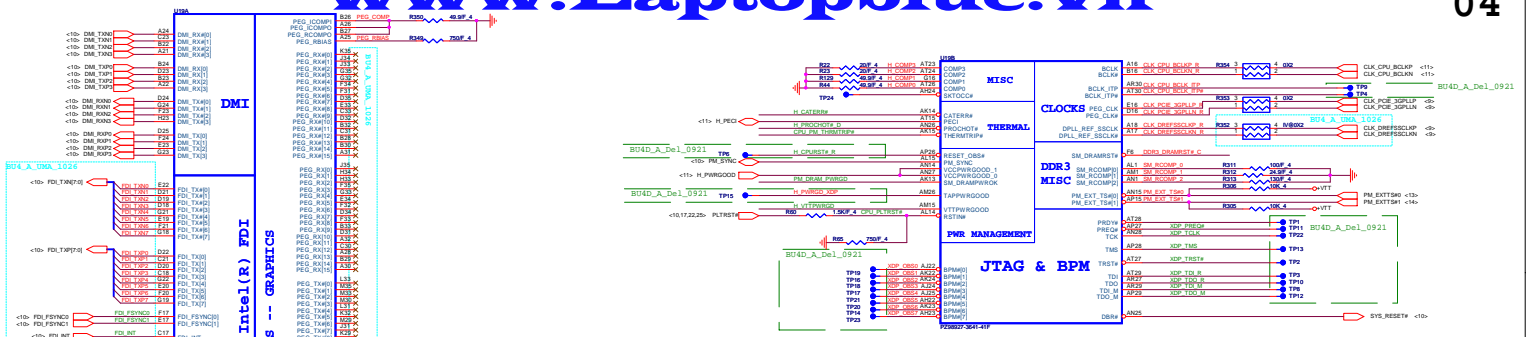
CLK POWERGOOD



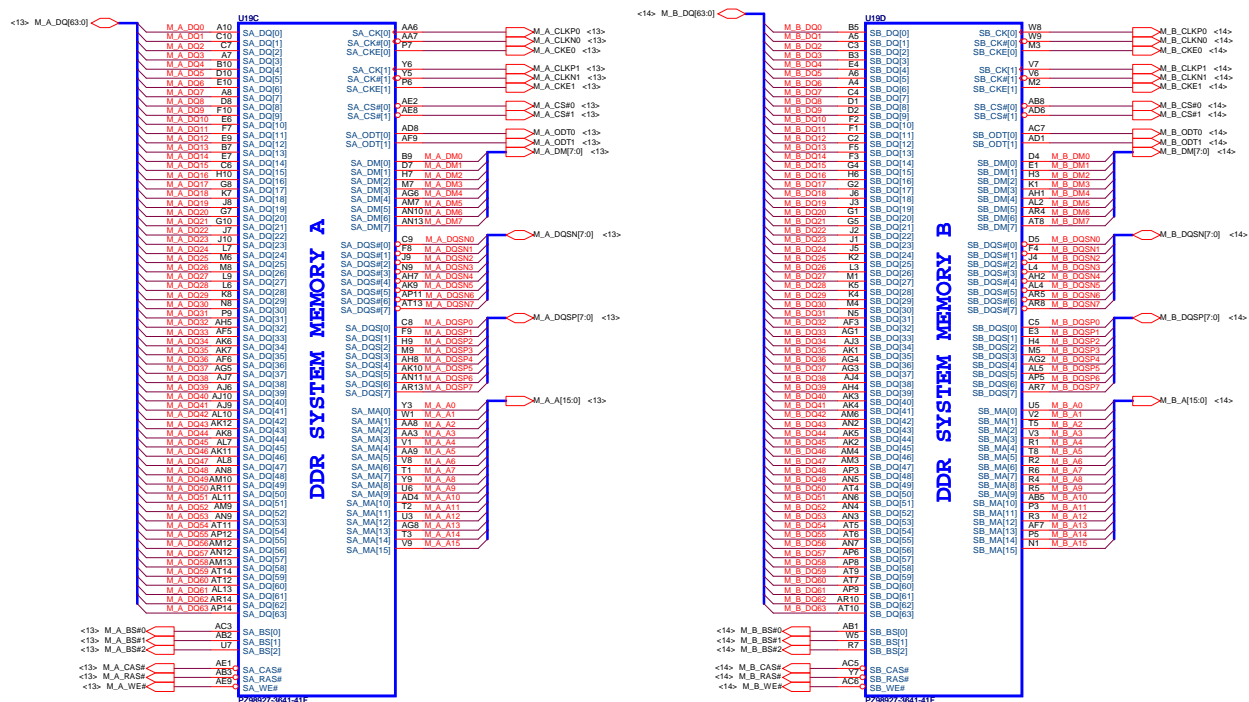


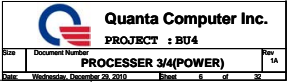
**Quanta Computer Inc.**  
**PROJECT : BU4**

Size	Document Number	Rev
	<b>CLOCK GENERATOR</b>	1A
Date:	Wednesday, December 29, 2010	Sheet 3 of 32

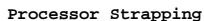


## AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)





## AUBURNDALE/CLARKSFIELD PROCESSOR( RESERVED, CFG)

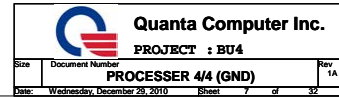


CFG0 R62 \*3.01K/F\_4

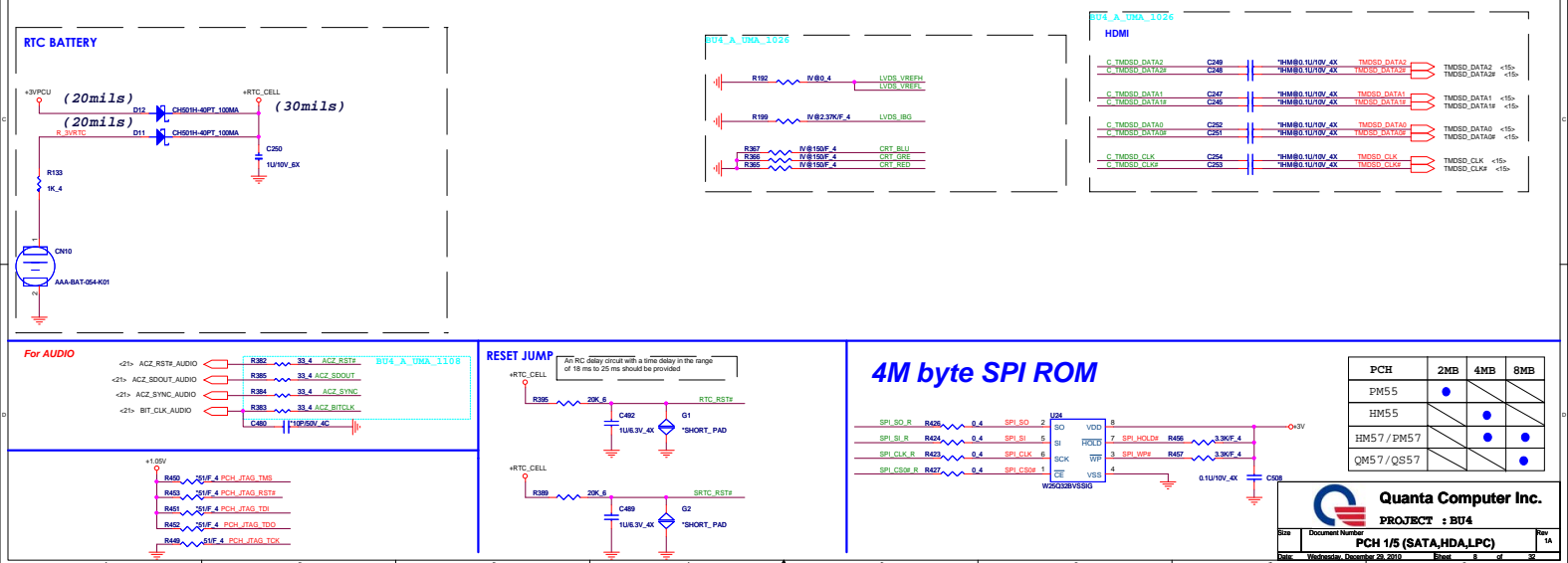
CFG3 R66 3.01K/F\_4

CFG4 R67 \*3.01K/F\_4

CFG7 R61 \*3.01K/F\_4



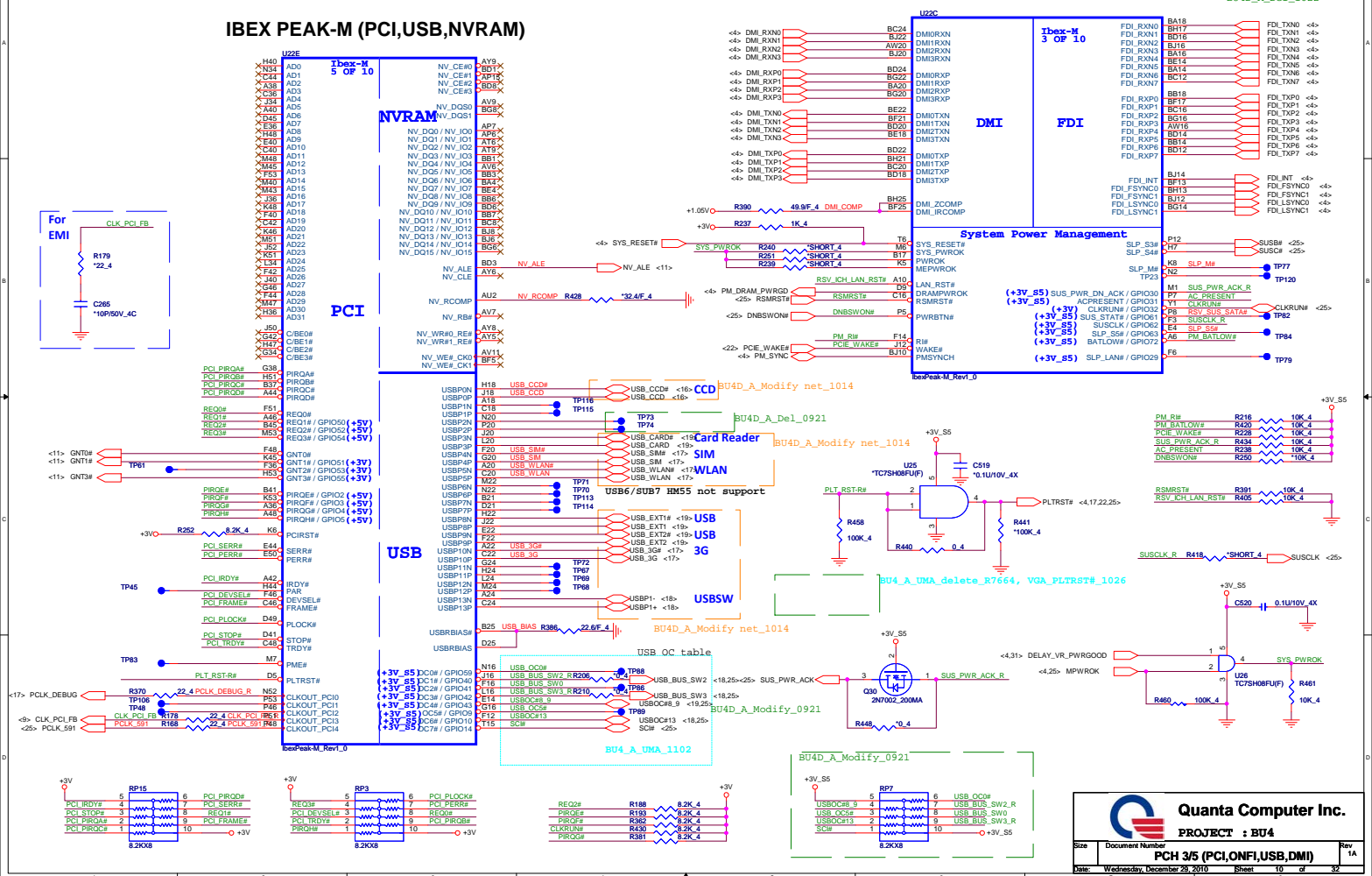
The Clarkfield processor's PCI Express interface may not meet PCI Express 2.0 jitter specifications. Intel recommends placing a 3.01K +/- 5% pull down resistor to VSS on CFG[7] pin for both rPGA and BGA components. This pull down resistor should be removed when this issue is fixed.




## IBEX PEAK-M (GND)

AY7	VSS159	VSS260	H49
B11	VSS160	VSS261	H5
B12	VSS161	VSS262	K11
B19	VSS162	VSS263	K43
B20	VSS163	VSS264	K47
B31	VSS164	VSS265	K7
B35	VSS165	VSS266	L14
B39	VSS166	VSS267	L18
B43	VSS167	VSS268	L22
B47	VSS168	VSS269	L26
B50	VSS169	VSS270	L30
B512	VSS170	VSS271	L34
B516	VSS171	VSS272	L38
B517	VSS172	VSS273	L42
B524	VSS173	VSS274	L46
B530	VSS174	VSS275	L50
B534	VSS175	VSS276	L54
B538	VSS176	VSS277	L58
B542	VSS177	VSS278	L62
B546	VSS178	VSS279	L66
B550	VSS179	VSS280	L70
B554	VSS180	VSS281	L74
B558	VSS181	VSS282	L78
B562	VSS182	VSS283	L82
B566	VSS183	VSS284	L86
B570	VSS184	VSS285	L90
B574	VSS185	VSS286	L94
B578	VSS186	VSS287	L98
B582	VSS187	VSS288	M02
B586	VSS188	VSS289	M06
B590	VSS189	VSS290	M10
B594	VSS190	VSS291	M14
B598	VSS191	VSS292	M18
B602	VSS192	VSS293	M22
B606	VSS193	VSS294	M26
B610	VSS194	VSS295	M30
B614	VSS195	VSS296	M34
B618	VSS196	VSS297	M38
B622	VSS197	VSS298	M42
B626	VSS198	VSS299	M46
B630	VSS199	VSS300	M50
B634	VSS200	VSS301	M54
B638	VSS201	VSS302	M58
B642	VSS202	VSS303	M62
B646	VSS203	VSS304	M66
B650	VSS204	VSS305	M70
B654	VSS205	VSS306	M74
B658	VSS206	VSS307	M78
B662	VSS207	VSS308	M82
B666	VSS208	VSS309	M86
B670	VSS209	VSS310	M90
B674	VSS210	VSS311	M94
B678	VSS211	VSS312	M98
B682	VSS212	VSS313	N02
B686	VSS213	VSS314	N06
B690	VSS214	VSS315	N10
B694	VSS215	VSS316	N14
B698	VSS216	VSS317	N18
B702	VSS217	VSS318	N22
B706	VSS218	VSS319	N26
B710	VSS219	VSS320	N30
B714	VSS220	VSS321	N34
B718	VSS221	VSS322	N38
B722	VSS222	VSS323	N42
B726	VSS223	VSS324	N46
B730	VSS224	VSS325	N50
B734	VSS225	VSS326	N54
B738	VSS226	VSS327	N58
B742	VSS227	VSS328	N62
B746	VSS228	VSS329	N66
B750	VSS229	VSS330	N70
B754	VSS230	VSS331	N74
B758	VSS231	VSS332	N78
B762	VSS232	VSS333	N82
B766	VSS233	VSS334	N86
B770	VSS234	VSS335	N90
B774	VSS235	VSS336	N94
B778	VSS236	VSS337	N98
B782	VSS237	VSS338	P02
B786	VSS238	VSS339	P06
B790	VSS239	VSS340	P10
B794	VSS240	VSS341	P14
B798	VSS241	VSS342	P18
B802	VSS242	VSS343	P22
B806	VSS243	VSS344	P26
B810	VSS244	VSS345	P30
B814	VSS245	VSS346	P34
B818	VSS246	VSS347	P38
B822	VSS247	VSS348	P42
B826	VSS248	VSS349	P46
B830	VSS249	VSS350	P50
B834	VSS250	VSS351	P54
B838	VSS251	VSS352	P58
B842	VSS252	VSS353	P62
B846	VSS253	VSS354	P66
B850	VSS254	VSS355	P70
B854	VSS255	VSS356	P74
B858	VSS256	VSS357	P78
B862	VSS257	VSS358	P82
B866	VSS258	VSS359	P86
B870	VSS259	VSS360	P90
B874	VSS260	VSS361	P94
B878	VSS261	VSS362	P98
B882	VSS262	VSS363	Q02
B886	VSS263	VSS364	Q06
B890	VSS264	VSS365	Q10
B894	VSS265	VSS366	Q14
B898	VSS266	VSS367	Q18
B902	VSS267	VSS368	Q22
B906	VSS268	VSS369	Q26
B910	VSS269	VSS370	Q30
B914	VSS270	VSS371	Q34
B918	VSS271	VSS372	Q38
B922	VSS272	VSS373	Q42
B926	VSS273	VSS374	Q46
B930	VSS274	VSS375	Q50
B934	VSS275	VSS376	Q54
B938	VSS276	VSS377	Q58
B942	VSS277	VSS378	Q62
B946	VSS278	VSS379	Q66
B950	VSS279	VSS380	Q70
B954	VSS280	VSS381	Q74
B958	VSS281	VSS382	Q78
B962	VSS282	VSS383	Q82
B966	VSS283	VSS384	Q86
B970	VSS284	VSS385	Q90
B974	VSS285	VSS386	Q94
B978	VSS286	VSS387	Q98
B982	VSS287	VSS388	R02
B986	VSS288	VSS389	R06
B990	VSS289	VSS390	R10
B994	VSS290	VSS391	R14
B998	VSS291	VSS392	R18
C002	VSS292	VSS393	R22
C006	VSS293	VSS394	R26
C010	VSS294	VSS395	R30
C014	VSS295	VSS396	R34
C018	VSS296	VSS397	R38
C022	VSS297	VSS398	R42
C026	VSS298	VSS399	R46
C030	VSS299	VSS400	R50
C034	VSS300	VSS401	R54
C038	VSS301	VSS402	R58
C042	VSS302	VSS403	R62
C046	VSS303	VSS404	R66
C050	VSS304	VSS405	R70
C054	VSS305	VSS406	R74
C058	VSS306	VSS407	R78
C062	VSS307	VSS408	R82
C066	VSS308	VSS409	R86
C070	VSS309	VSS410	R90
C074	VSS310	VSS411	R94
C078	VSS311	VSS412	R98
C082	VSS312	VSS413	S02
C086	VSS313	VSS414	S06
C090	VSS314	VSS415	S10
C094	VSS315	VSS416	S14
C098	VSS316	VSS417	S18
C102	VSS317	VSS418	S22
C106	VSS318	VSS419	S26
C110	VSS319	VSS420	S30
C114	VSS320	VSS421	S34
C118	VSS321	VSS422	S38
C122	VSS322	VSS423	S42
C126	VSS323	VSS424	S46
C130	VSS324	VSS425	S50
C134	VSS325	VSS426	S54
C138	VSS326	VSS427	S58
C142	VSS327	VSS428	S62
C146	VSS328	VSS429	S66
C150	VSS329	VSS430	S70
C154	VSS330	VSS431	S74
C158	VSS331	VSS432	S78
C162	VSS332	VSS433	S82
C166	VSS333	VSS434	S86
C170	VSS334	VSS435	S90
C174	VSS335	VSS436	S94
C178	VSS336	VSS437	S98
C182	VSS337	VSS438	T02
C186	VSS338	VSS439	T06
C190	VSS339	VSS440	T10
C194	VSS340	VSS441	T14
C198	VSS341	VSS442	T18
C202	VSS342	VSS443	T22
C206	VSS343	VSS444	T26
C210	VSS344	VSS445	T30
C214	VSS345	VSS446	T34
C218	VSS346	VSS447	T38
C222	VSS347	VSS448	T42
C226	VSS348	VSS449	T46
C230	VSS349	VSS450	T50
C234	VSS350	VSS451	T54
C238	VSS351	VSS452	T58
C242	VSS352	VSS453	T62
C246	VSS353	VSS454	T66
C250	VSS354	VSS455	T70
C254	VSS355	VSS456	T74
C258	VSS356	VSS457	T78
C262	VSS357	VSS458	T82
C266	VSS358	VSS459	T86
C270	VSS359	VSS460	T90
C274	VSS360	VSS461	T94
C278	VSS361	VSS462	T98
C282	VSS362	VSS463	U02
C286	VSS363	VSS464	U06
C290	VSS364	VSS465	U10
C294	VSS365	VSS466	U14
C298	VSS366	VSS467	U18
C302	VSS367	VSS468	U22
C306	VSS368	VSS469	U26
C310	VSS369	VSS470	U30
C314	VSS370	VSS471	U34
C318	VSS371	VSS472	U38
C322	VSS372	VSS473	U42
C326	VSS373	VSS474	U46
C330	VSS374	VSS475	U50
C334	VSS375	VSS476	U54
C338	VSS376	VSS477	U58
C342	VSS377	VSS478	U62
C346	VSS378	VSS479	U66
C350	VSS379	VSS480	U70
C354	VSS380	VSS481	U74
C358	VSS381	VSS482	U78
C362	VSS382	VSS483	U82
C366	VSS383	VSS484	U86
C370	VSS384	VSS485	U90
C374	VSS385	VSS486	U94
C378	VSS386	VSS487	U98
C382	VSS387	VSS488	V02
C386	VSS388	VSS489	V06
C390	VSS389	VSS490	V10
C394	VSS390	VSS491	V14
C398	VSS391	VSS492	V18
C402	VSS392	VSS493	V22
C406	VSS393	VSS494	V26
C410	VSS394	VSS495	V30
C414	VSS395	VSS496	V34
C418	VSS396	VSS497	V38
C422	VSS397	VSS498	V42
C426	VSS398	VSS499	V46
C430	VSS399	VSS500	V50
C434	VSS400	VSS501	V54
C438	VSS401	VSS502	V58
C442	VSS402	VSS503	V62
C446	VSS403	VSS504	V66
C450	VSS404	VSS505	V70
C454	VSS405	VSS506	V74
C458	VSS406	VSS507	V78
C462	VSS407	VSS508	V82
C466	VSS408	VSS509	V86
C470	VSS409	VSS510	V90
C474	VSS410	VSS511	V94
C478	VSS411	VSS512	V98
C482	VSS412	VSS513	W02
C486	VSS413	VSS514	W06
C490	VSS414	VSS515	W10
C494	VSS415	VSS516	W14
C498	VSS416	VSS517	W18
C502	VSS417	VSS518	W22
C506	VSS418	VSS519	W26
C510	VSS419	VSS520	W30
C514	VSS420	VSS521	W34
C518	VSS421	VSS522	W38
C522	VSS422	VSS523	W42
C526	VSS423	VSS524	W46
C530	VSS424	VSS525	W50
C534	VSS425	VSS526	W54
C538	VSS426	VSS527	W58
C542	VSS427	VSS528	W62
C546	VSS428	VSS529	W66
C550	VSS429	VSS530	W70
C554	VSS430	VSS531	W74
C558	VSS431	VSS532	W78
C562	VSS432	VSS533	W82
C566	VSS433	VSS534	W86
C570	VSS434	VSS535	W90
C574	VSS435	VSS536	W94
C578	VSS436	VSS537	W98
C582	VSS437	VSS538	X02
C586	VSS438	VSS539	X06
C590	VSS439	VSS540	X10
C594	VSS440	VSS541	X14
C598	VSS441	VSS542	X18
C602	VSS442	VSS543	X22
C606	VSS443	VSS544	X26
C610	VSS444	VSS545	X30
C614	VSS445	VSS546	X34
C618	VSS446	VSS547	X38
C622	VSS447	VSS548	X42
C626	VSS448	VSS549	X46
C630	VSS449	VSS550	X50
C634	VSS450	VSS551	X54
C638	VSS451	VSS552	X58
C642	VSS452	VSS553	X62
C646	VSS453	VSS554	X66
C650	VSS454	VSS555	X70
C654	VSS455	VSS556	X74
C658	VSS456	VSS557	X78
C662	VSS457	VSS558	X82
C666	VSS458	VSS559	X86
C670	VSS459	VSS560	X90
C674	VSS460	VSS561	X94
C678	VSS461	VSS562	X98
C682	VSS462	VSS563	Y02
C686	VSS463	VSS564	Y06
C690	VSS464	VSS565	Y10
C694	VSS465	VSS566	Y14
C698	VSS466	VSS567	Y18
C702	VSS467	VSS568	Y22
C706	VSS468	VSS569	Y26
C710	VSS469	VSS570	Y30
C714	VSS470	VSS571	Y34
C718	VSS471	VSS572	Y38
C722	VSS472	VSS573	Y42
C726	VSS473	VSS574	Y46
C730	VSS474	VSS575	Y50
C734	VSS475	VSS576	Y54
C738	VSS476	VSS577	Y58
C742	VSS477	VSS578	Y62
C746	VSS478	VSS579	Y66
C750	VSS4		




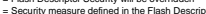






## IBEX PEAK-M (PCI,USB,NVRAM)




**Quanta Computer Inc.**  
**PROJECT : BU4**  

Size	Document Number
	<b>PCH 3/5 (PCI,ONFI,USB,DMI)</b>
Date	Wednesday, December 26, 2010

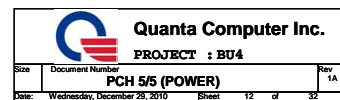


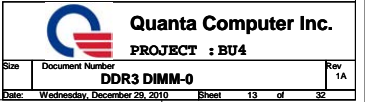
<b>SPKR</b>  Reboot option at power-up	 <p>0 = Default Mode (Internal weak Pull-down) 1 = No Reboot Mode with TCO Disabled</p>																		
<b>GNT3#/ GPIO55</b>  Top-Block Swap Override	 <p>0 = Top Block Swap Mode 1 = Default Mode (Internal pull-up)</p>																		
<b>HDA_DOCK_EN #/GPIO33</b>  Flash Descriptor Security Override	 <p>0 = Flash Descriptor Security will be overridden 1 = Security measure defined in the Flash Descriptor will be enabled.</p>																		
<b>GNT0#, GNT1#</b>  																			
	<table border="1"> <thead> <tr> <th colspan="3">Boot BIOS Strap</th> </tr> <tr> <th>PCR_GNT#s</th> <th>GNT#s</th> <th>Boot BIOS Location</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>LPC</td> </tr> <tr> <td>0</td> <td>1</td> <td>PCI</td> </tr> <tr> <td>1</td> <td>0</td> <td>Reserved (NAND)</td> </tr> <tr> <td>1</td> <td>1</td> <td>SPi</td> </tr> </tbody> </table>	Boot BIOS Strap			PCR_GNT#s	GNT#s	Boot BIOS Location	0	0	LPC	0	1	PCI	1	0	Reserved (NAND)	1	1	SPi
Boot BIOS Strap																			
PCR_GNT#s	GNT#s	Boot BIOS Location																	
0	0	LPC																	
0	1	PCI																	
1	0	Reserved (NAND)																	
1	1	SPi																	
<b>SPI_MOSI</b>  TPM Functionality Disable	 <p>1 = Enabled 0 = Disable</p>																		
<b>NV_ALE</b>  Intel® Anti-Theft Technology HDD Data Protection (Intel AT-d) Enable	 <p>1 = Enabled 0 = Disabled (Default)</p>																		
<b>GPIO8</b>  Reserved	 <p>This signal has a weak internal pull up. NOTE: This signal should not be pulled low</p>																		
<b>GPIO15</b>  Reserved	 <p>0 = Intel ME Crypto Transport Layer Security (TLS) cipher suite with no confidentiality 1 = Intel ME Crypto Transport Layer Security (TLS) cipher suite with confidentiality</p>																		
<b>GPIO27</b>  On-Die PLL Voltage Regulator	 <p>0 = Disables the VccVrm. Need to use on-board filter capacitors for analog rails. 1 = Enables the internal VccVrm to have a clean supply for analog rails. No need to use on-board filter circuit. This signal has a weak internal pull-up.</p>																		
																			
	<div>INVRMEN - Integrated SUS 1.1V Vrm Enable High - Enable Internal Vrm</div>																		

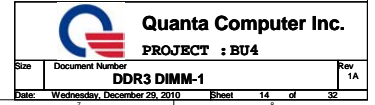


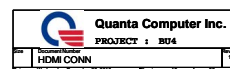
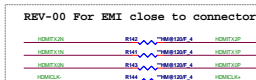
**PROJECT : BU4**

<b>PCH 4/5 (GPIO &amp; Strap)</b>			<b>1A</b>
Date:	Wednesday, December 29, 2010	Sheet	11 of 32





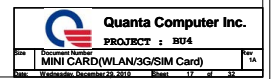
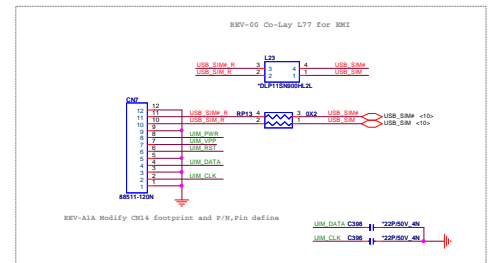
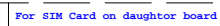
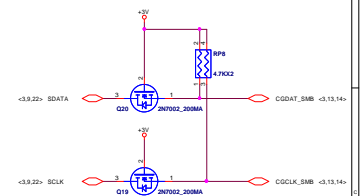


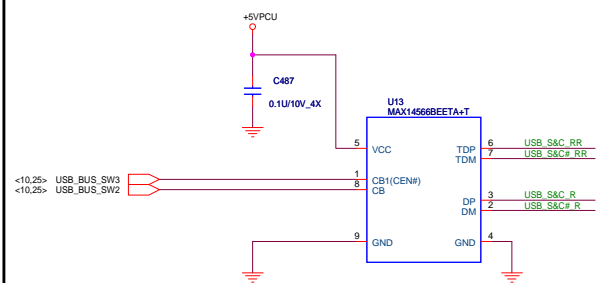


## 16



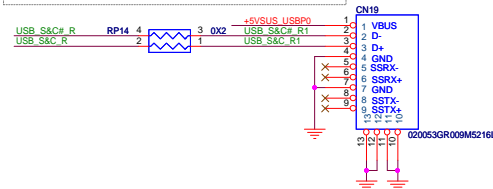
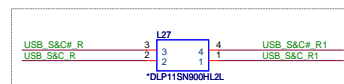
The schematic diagram illustrates the power supply and video input section of the AD9548 evaluation board. The power supply section includes a 5V regulator (U1) and a 1.8V regulator (U2). The video input section includes a video input buffer (U3) and a video input multiplexer (U4). The AD9548 IC is connected to the power supply and video input. The diagram also shows the connection of the AD9548 IC to the video output and the video input multiplexer.





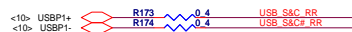
CB0	CB1	Status
0	0	Auto mode
0	1	Force dedicated charger mode
1	X	Pass-Through(USB) mode: Connect DP/DM to TDP/TDM

## USB 3.0 CONN <USB>

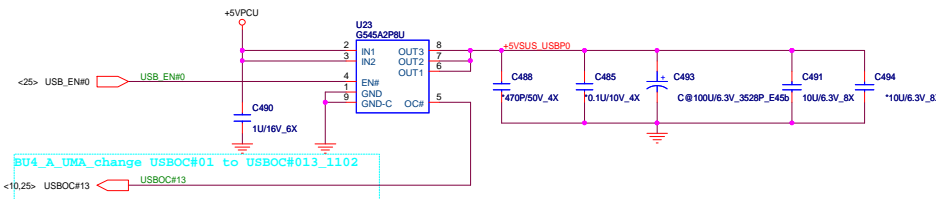


only USB2.0: DFHS04FR163  
USB3.0: DFHD09MR002

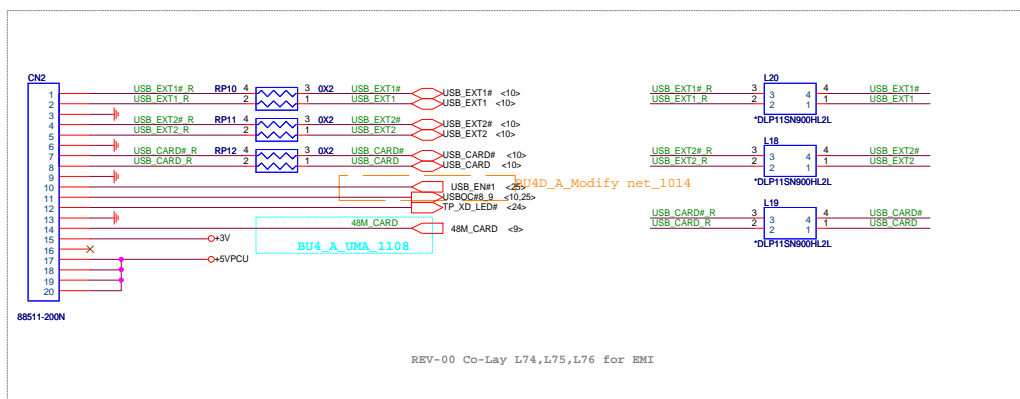
## <U2B>

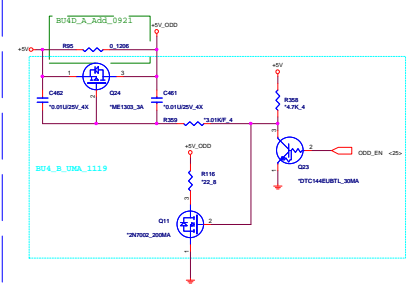
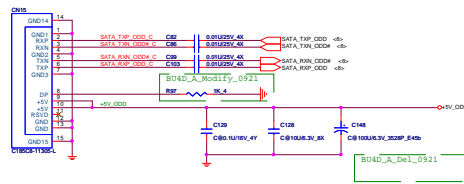
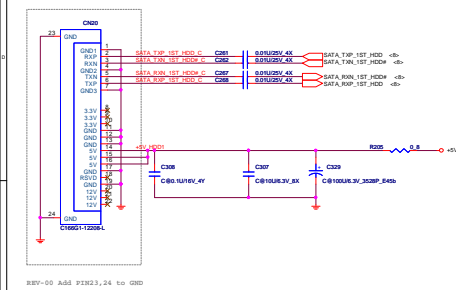


## USB 3.0 Power switch <USB> <U2B>

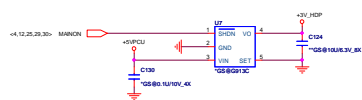


USB2.0 Left 1  
USB2.0 Left 2 <U2B> <MMC> <EMI>

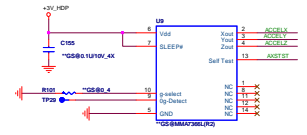
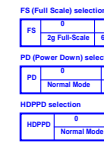
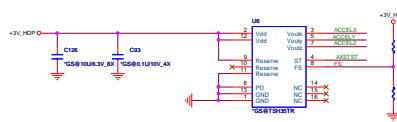




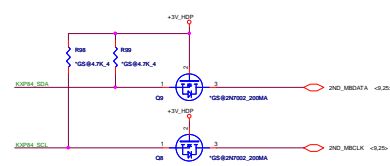
3D-LDO Power <GSR>



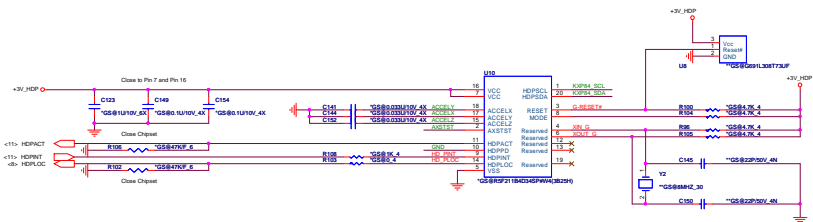
### 3D-Sensor IC <GSR>

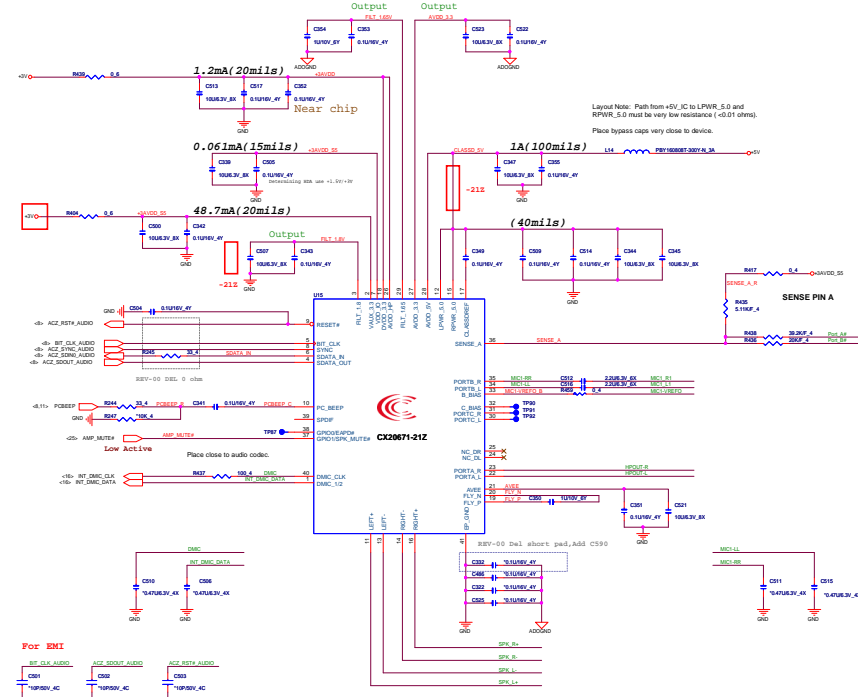


## 3D-SMBus &lt;GSR&gt;

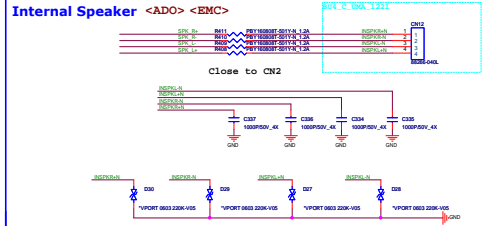
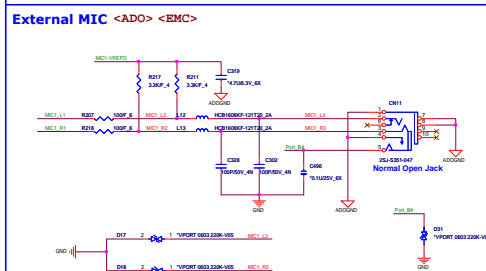
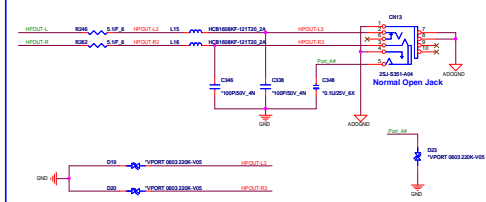


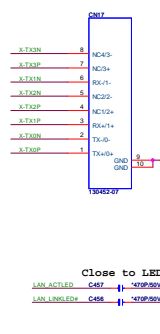
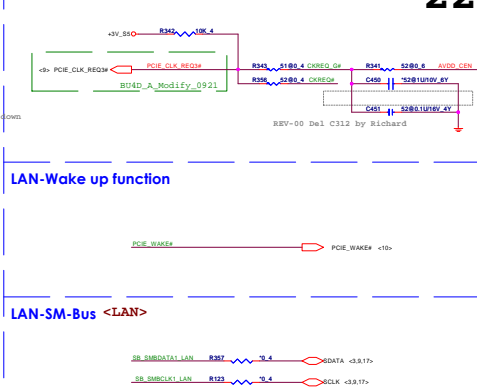
## 3D-u-micro P &lt;GSR&gt;





Need to change 20671-21Z footprint





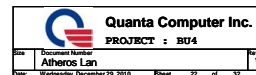
LED0 = LAN_ACTLED	1	Over-clocking enable (default = 1)
	0	Over-clocking disable
LED1 = LAN_LINKLED	1	SNR switch-mode regulator select Giga LAN pull High (default = 1)
	0	LDO linear regulator select 10/100M LAN pull Low
CKREQ# or CKREQ_G#	1	Normal function
	0	ATE test mode

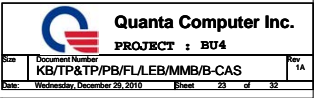
Power on Strapping pin

LAN\_ACTLED# R355 5.1K

LAN\_LINKED# 5.1K

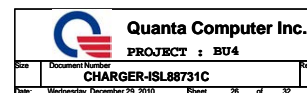
+3V\_SS



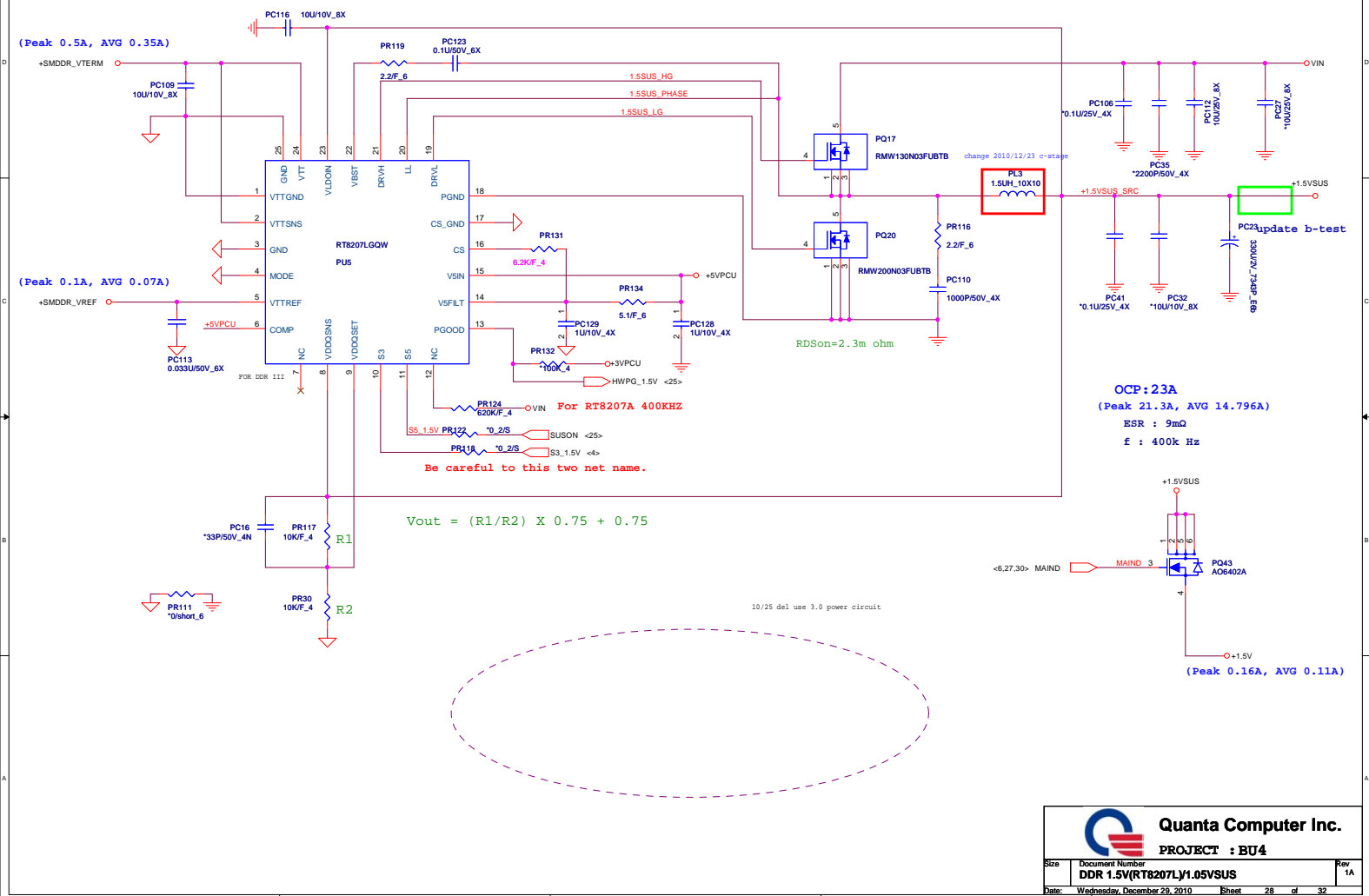
















**Quanta Computer Inc.**  
**PROJECT : BU4**  
 Size: Document Number  
 +VTT /+1.05V(RT8238A)  
 Date: Wednesday, December 29, 2010 Sheet 29 of 32

