

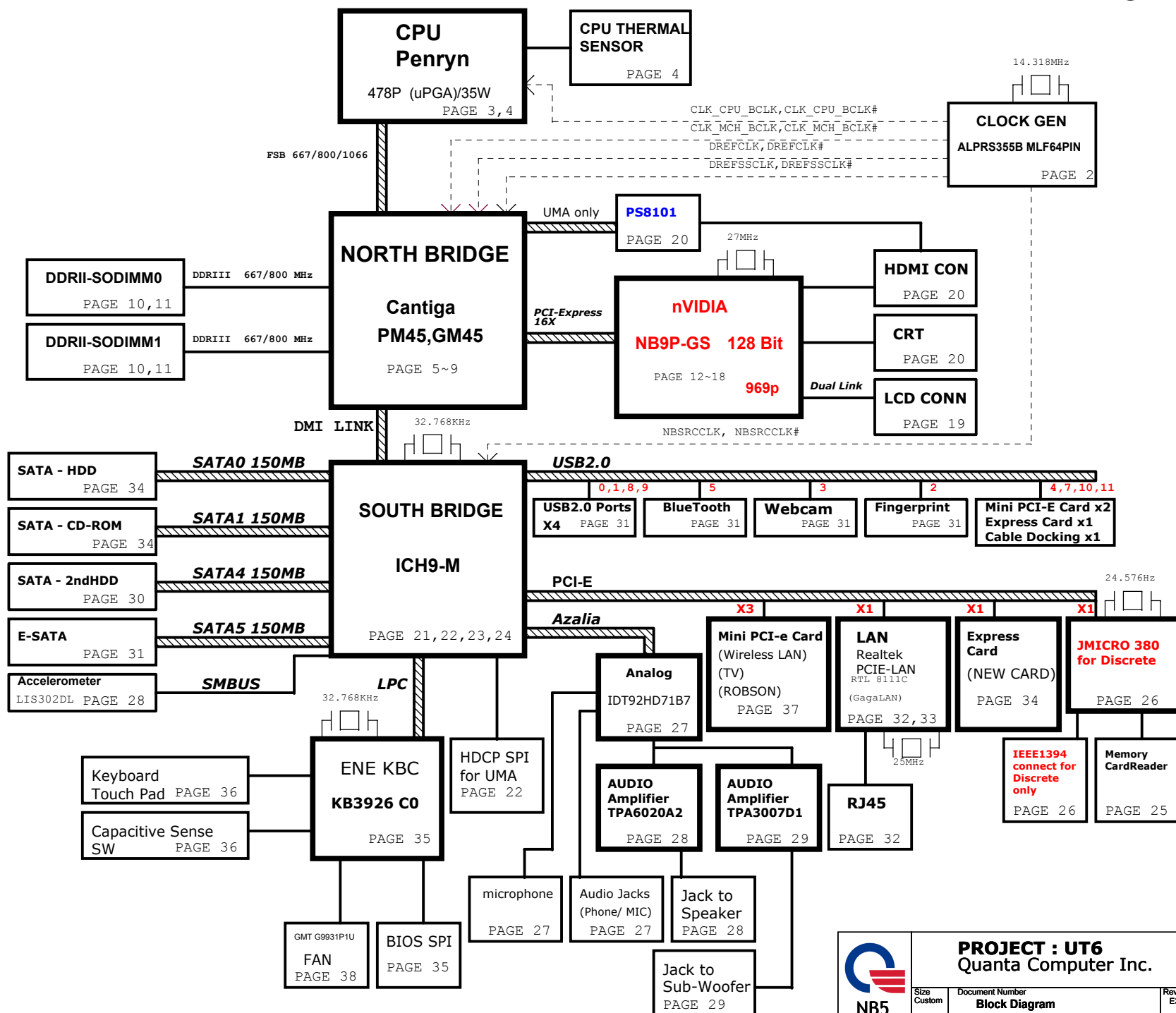
8L

Cable Docking

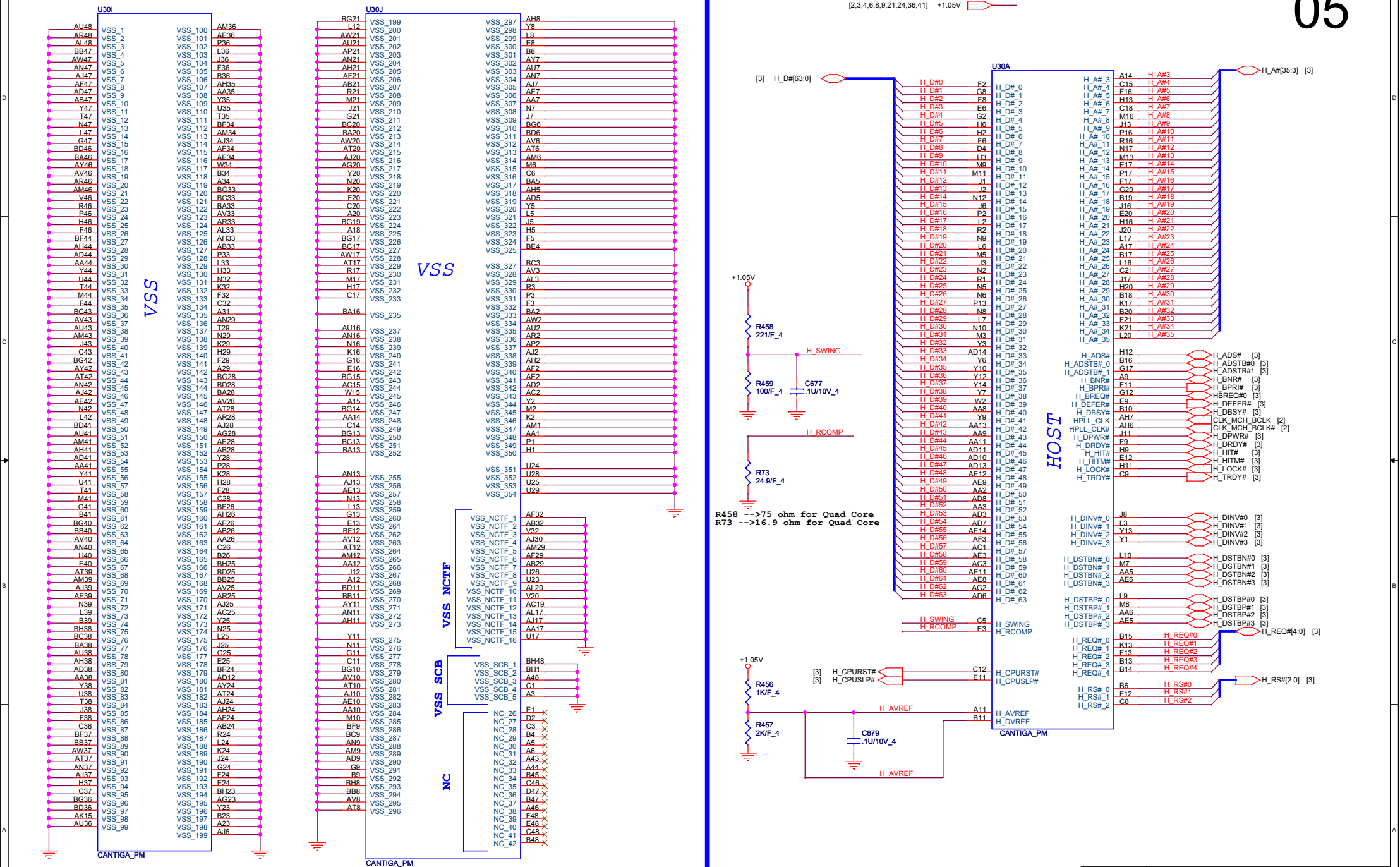
VGA
RJ-45
CIR/Pwr btn
SPDIF Out
Stereo MIC
Headphone Jack
USB Port
VOL Cntr

PAGE 38

CPU CORE ISL6266A PAGE 42







PROJECT : UT6
Quanta Computer Inc.

MCH_CFG_5 DMIx2 selection
Low = DMI X2
High = DMI X4 (Default)
MCH_CFG_16 FSB Dynamic ODT
Low = Dynamic ODT disabled (default)
High = Dynamic ODT enabled (default)

MCH_CFG_9 PCI Express Graphic Lane
Low: Reverse Lane
High: Normal operation(Default)
MCH_CFG_19 DMI Lane Reversal
Low = Normal operation (Default)
High = Reverse Lanes

MCH_CFG_6 ITPM Host Interface
Low = The ITPM Host Interface is enabled2
High = The ITPM Host Interface is disabled (default)
MCH_CFG_7 Intel (R) Management Engine Crypto
Low: Intel (R) Management Engine Crypto
High: Intel (R) Management Engine Crypto

TLS cipher suite with no confidentiality
High: Intel (R) Management Engine Crypto
MCH_CFG_10 PCIe Lookback Enable
Low = Enabled3
High: Disabled (Default)

MCH_CFG_12/13 XOR/ALLZ/CLOCK Un-gating

MCH_CFG_13 MCH_CFG_12 Configuration

0	0	Reserved
1	0	XOR Mode enabled
0	1	All-Z Mode enabled
1	1	Normal operation (Default)

Digital Display Port (SDVO/DP/HDMI) Concurrent with PCIE
Low = Only digital display port (SDVO/DP/HDMI) or PCIE is operational (default)
High = Digital display port (SDVO/DP/HDMI) and PCIE are operating simultaneously via the PEG port

MCH_CFG2:0

000 = FSB1066
010 = FSB800
011 = FSB667
Others = Reserved

MCH_CFG_3 MCH_CFG_4 MCH_CFG_5 MCH_CFG_6 MCH_CFG_7 MCH_CFG_8 MCH_CFG_9 MCH_CFG_10 MCH_CFG_11 MCH_CFG_12 MCH_CFG_13 MCH_CFG_14 MCH_CFG_15 MCH_CFG_16 MCH_CFG_17 MCH_CFG_18 MCH_CFG_19 MCH_CFG_20

[2,3] MCH_BSEL0
[2,3] MCH_BSEL1
[2,3] MCH_BSEL2

[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

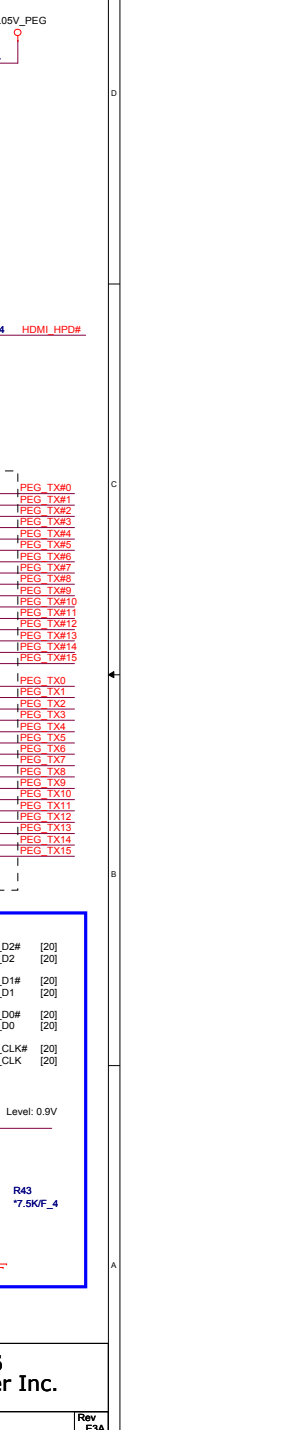
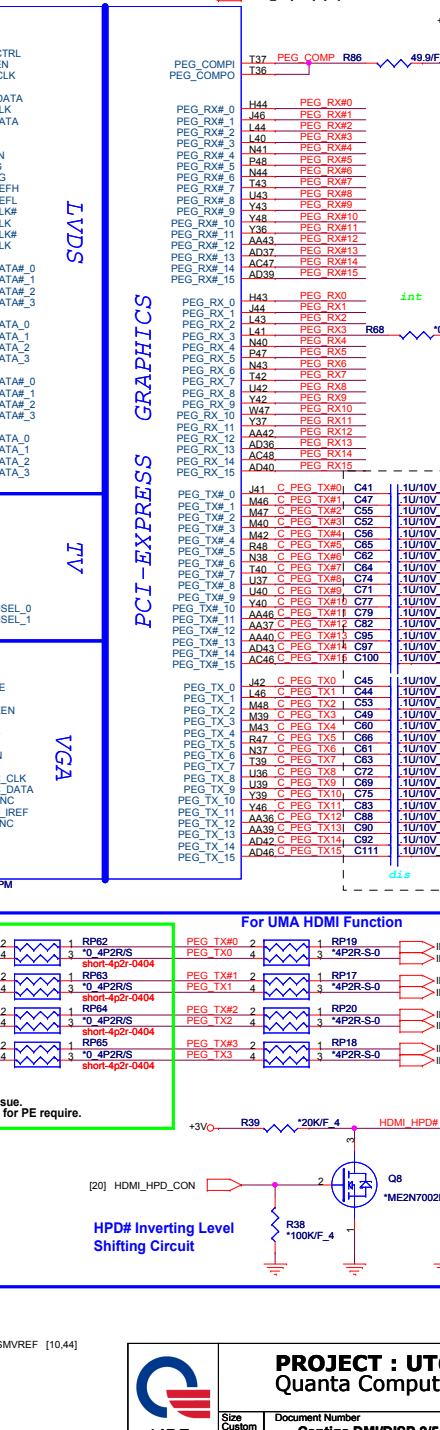
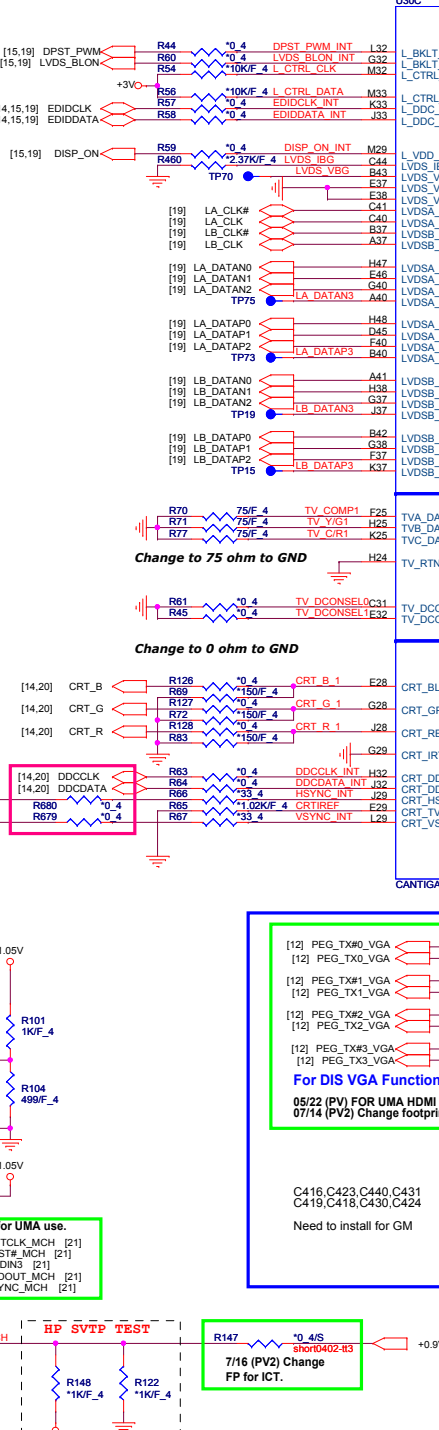
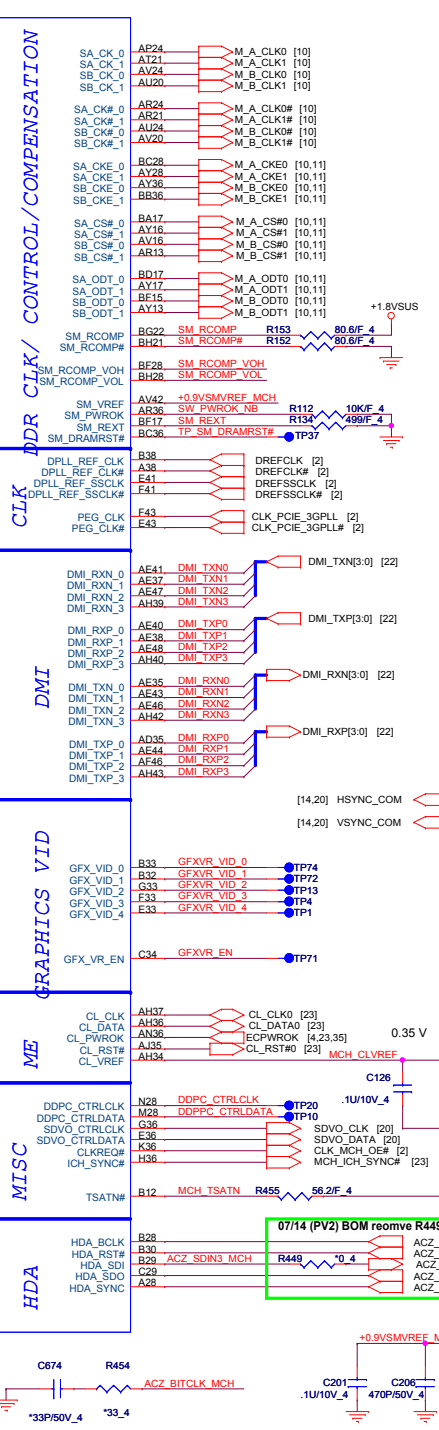
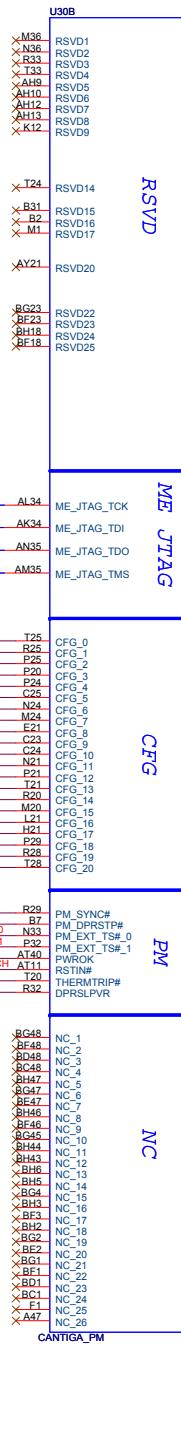
[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

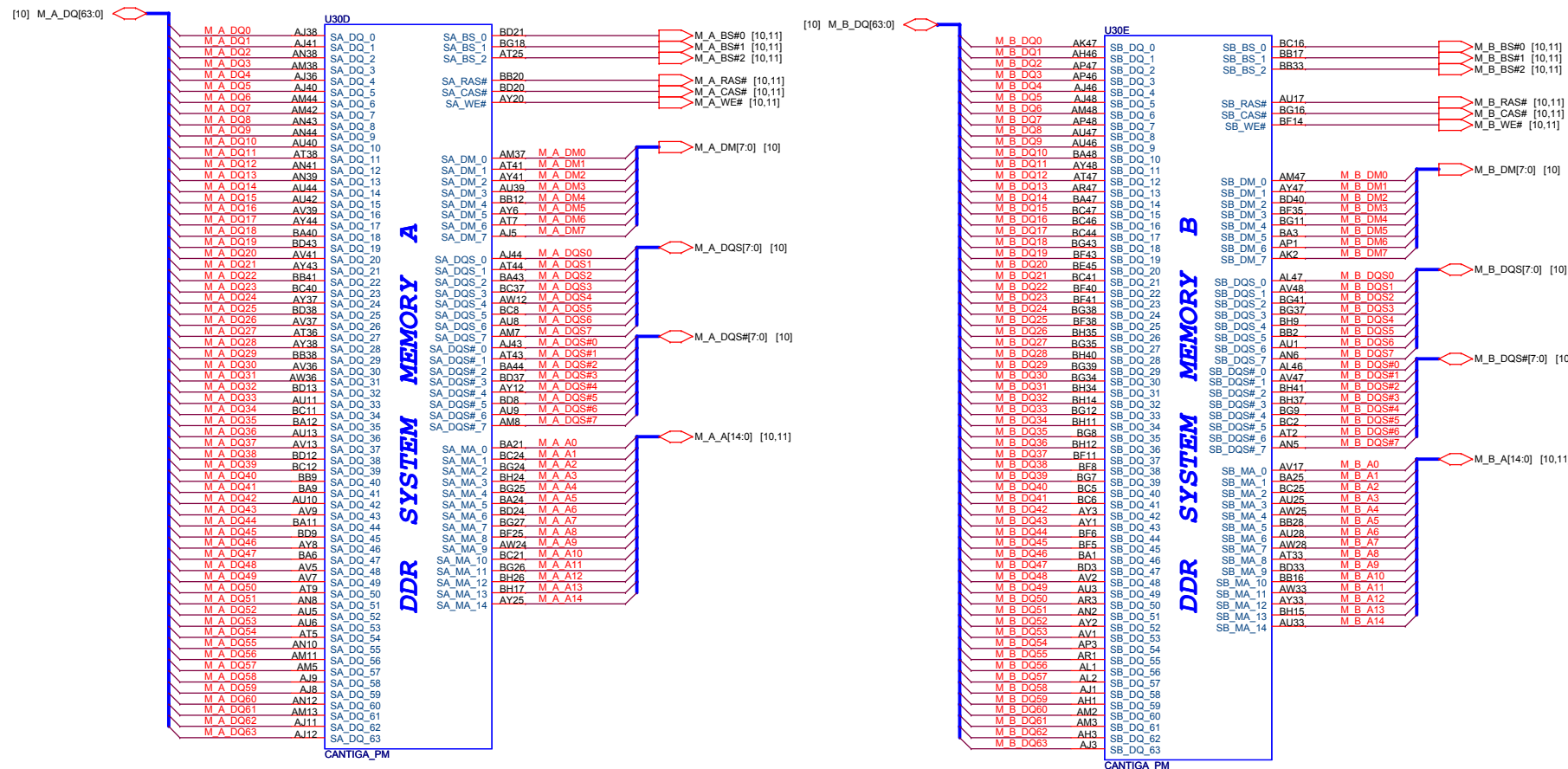
[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

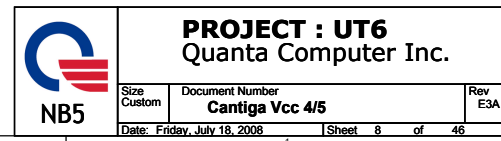
[23] PM_SYNC#
[3,21,42] H_DPRSTP#
[10,11] PM_EXTTS#0
[11] PM_EXTTS#1
[12,22,26,32,34,35,37] PLT_RST-R#
[3,21] PM.THRMTRIP#
[23,42] DPRSLPVR

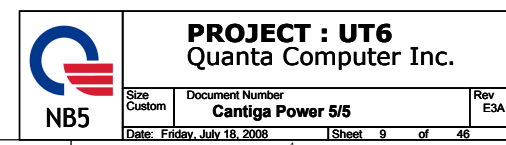


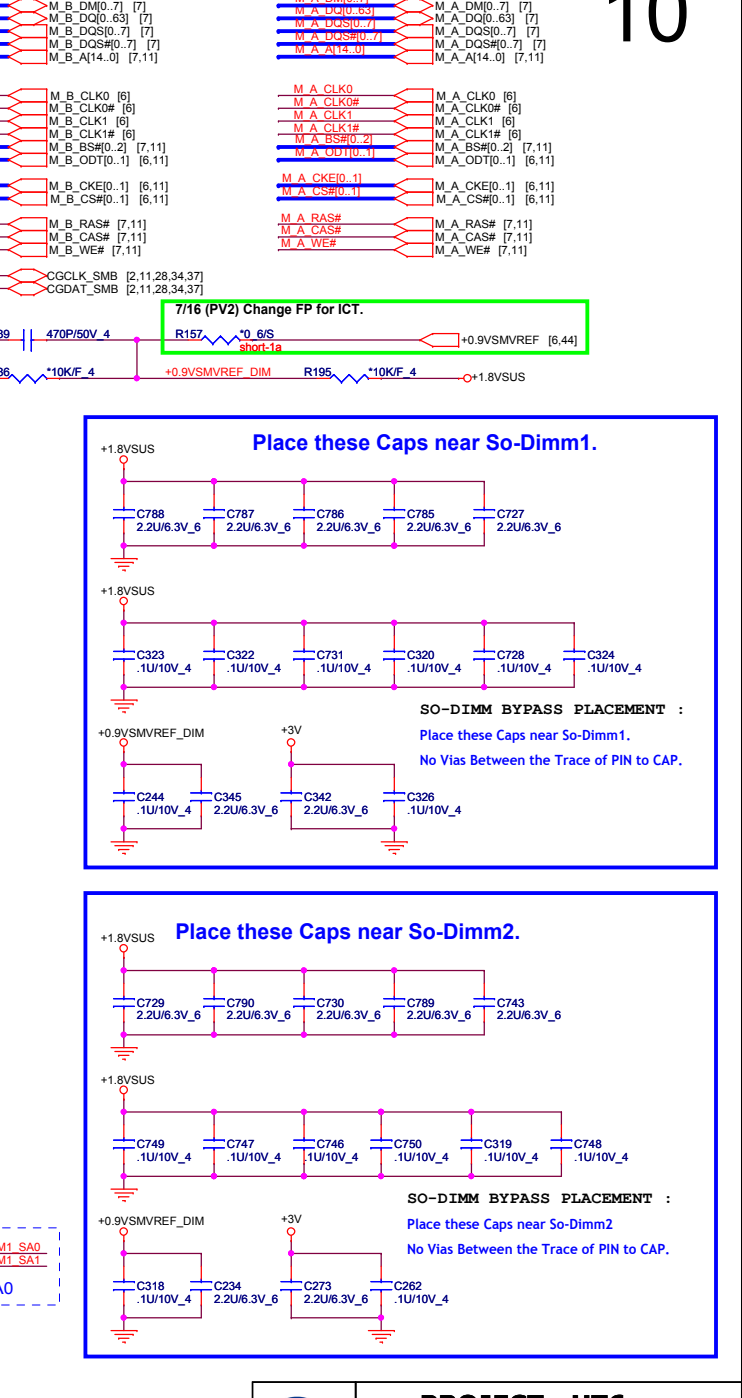
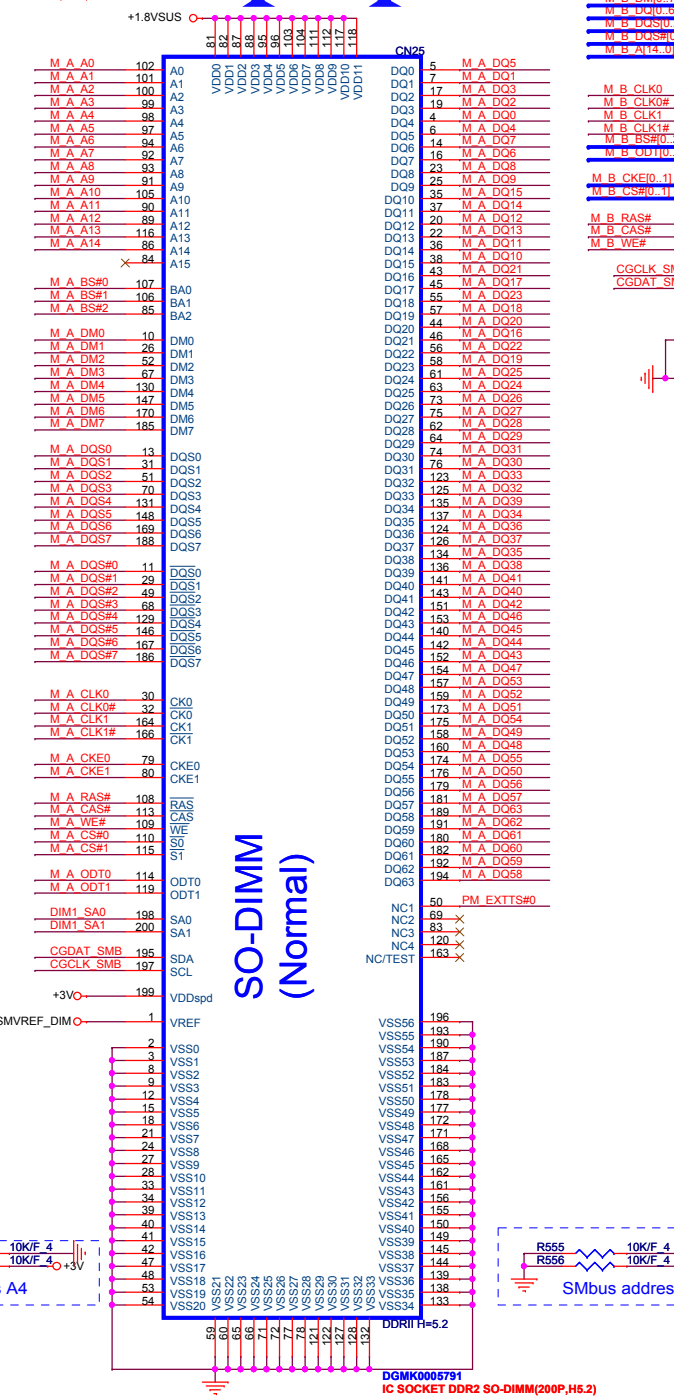
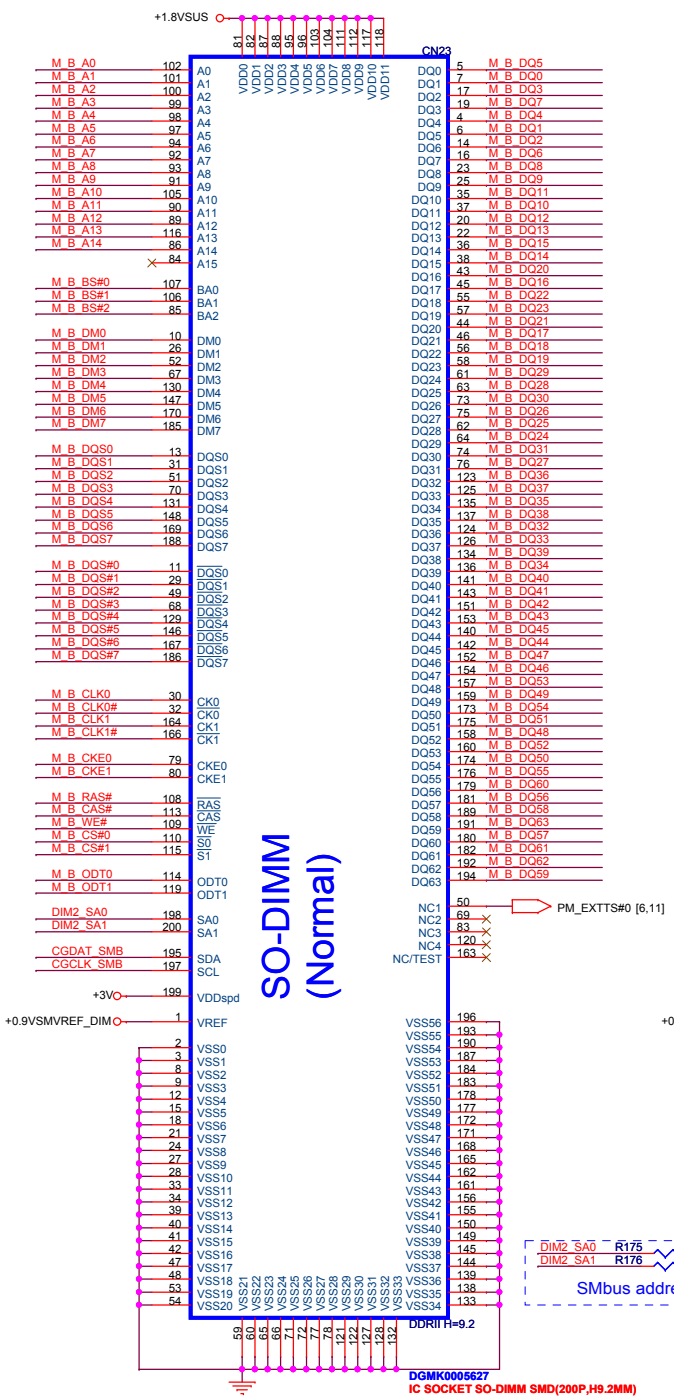


PROJECT : UT6
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Size Custom	Document Number Cantiga DDR2 3/5	Rev E3A
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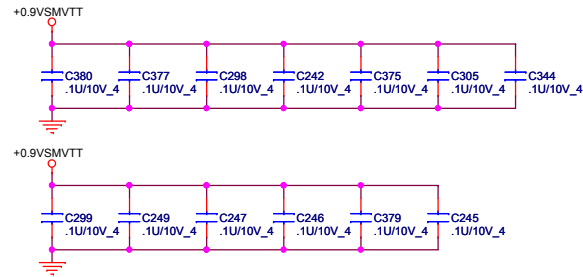






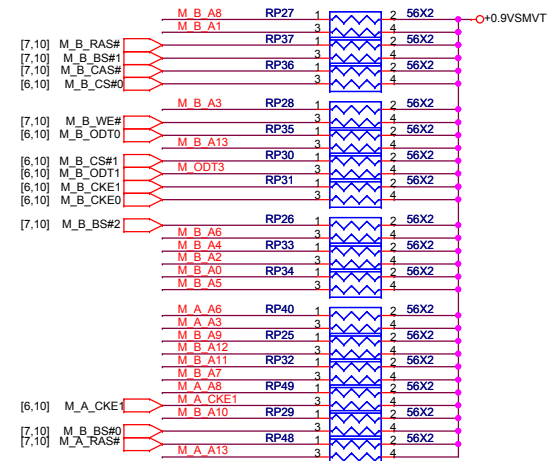
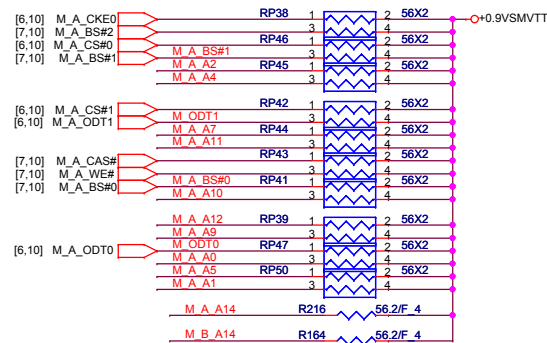
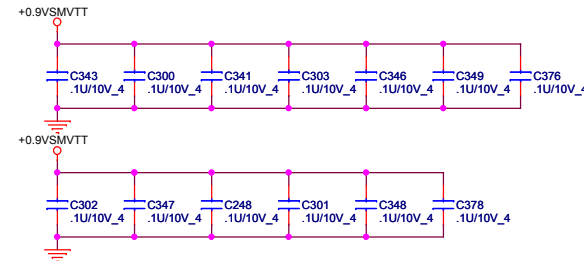
DDRII DUAL CHANNEL A,B.

DDRII A CHANNEL

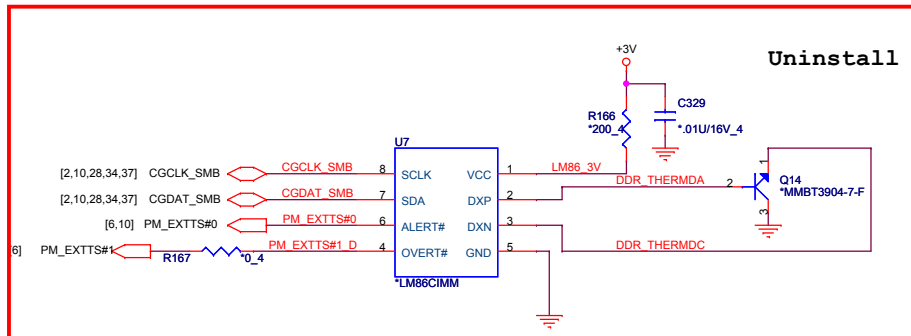


Layout note: Place one cap close to every 2 pullup resistors terminated to SMDR_VTERM

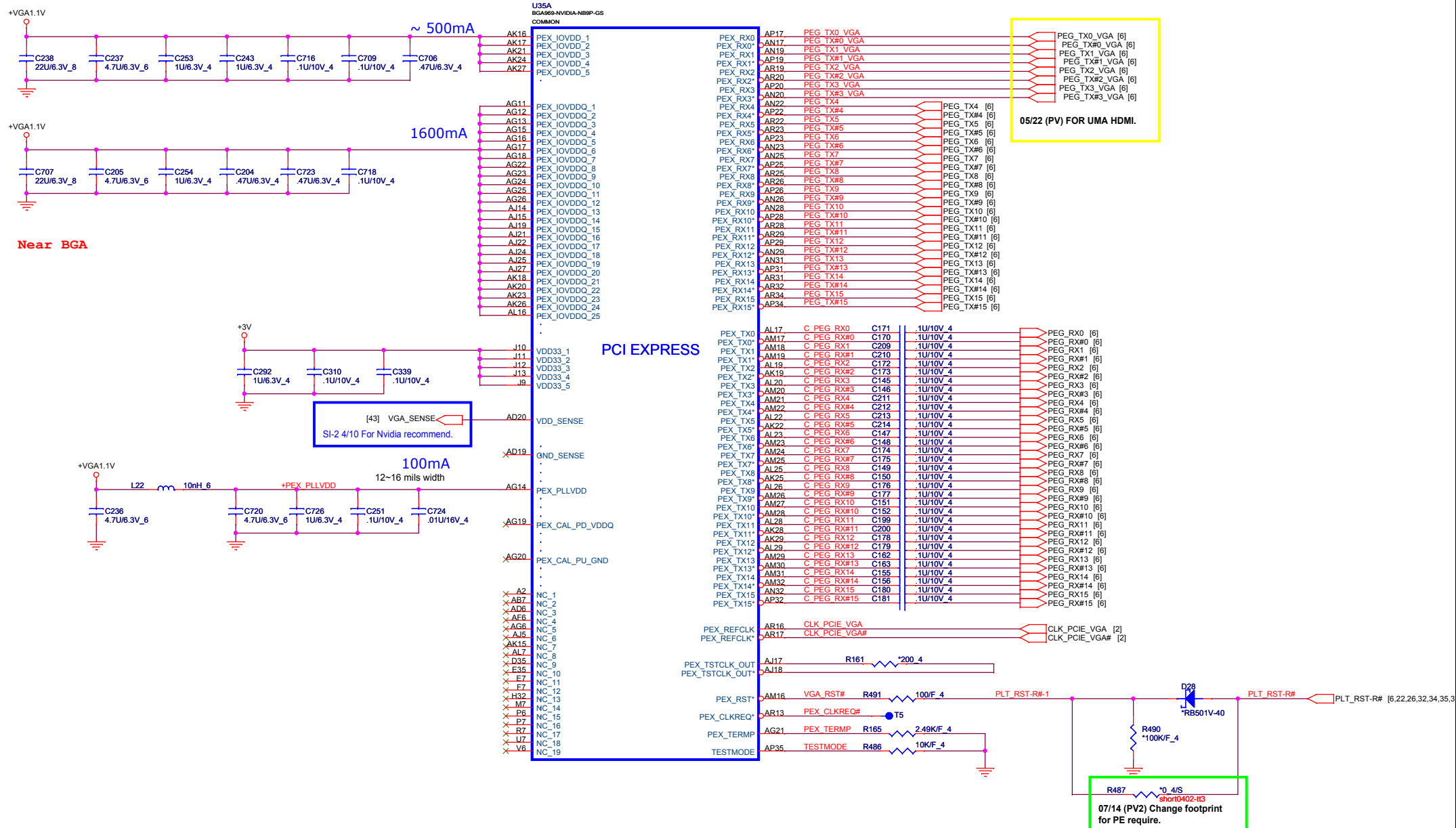
DDRII B CHANNEL

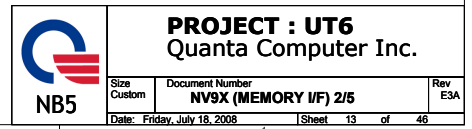


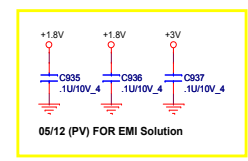
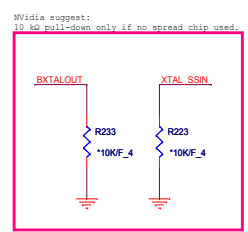
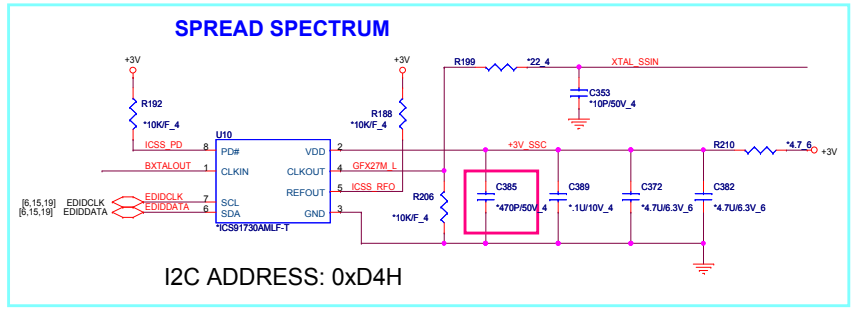
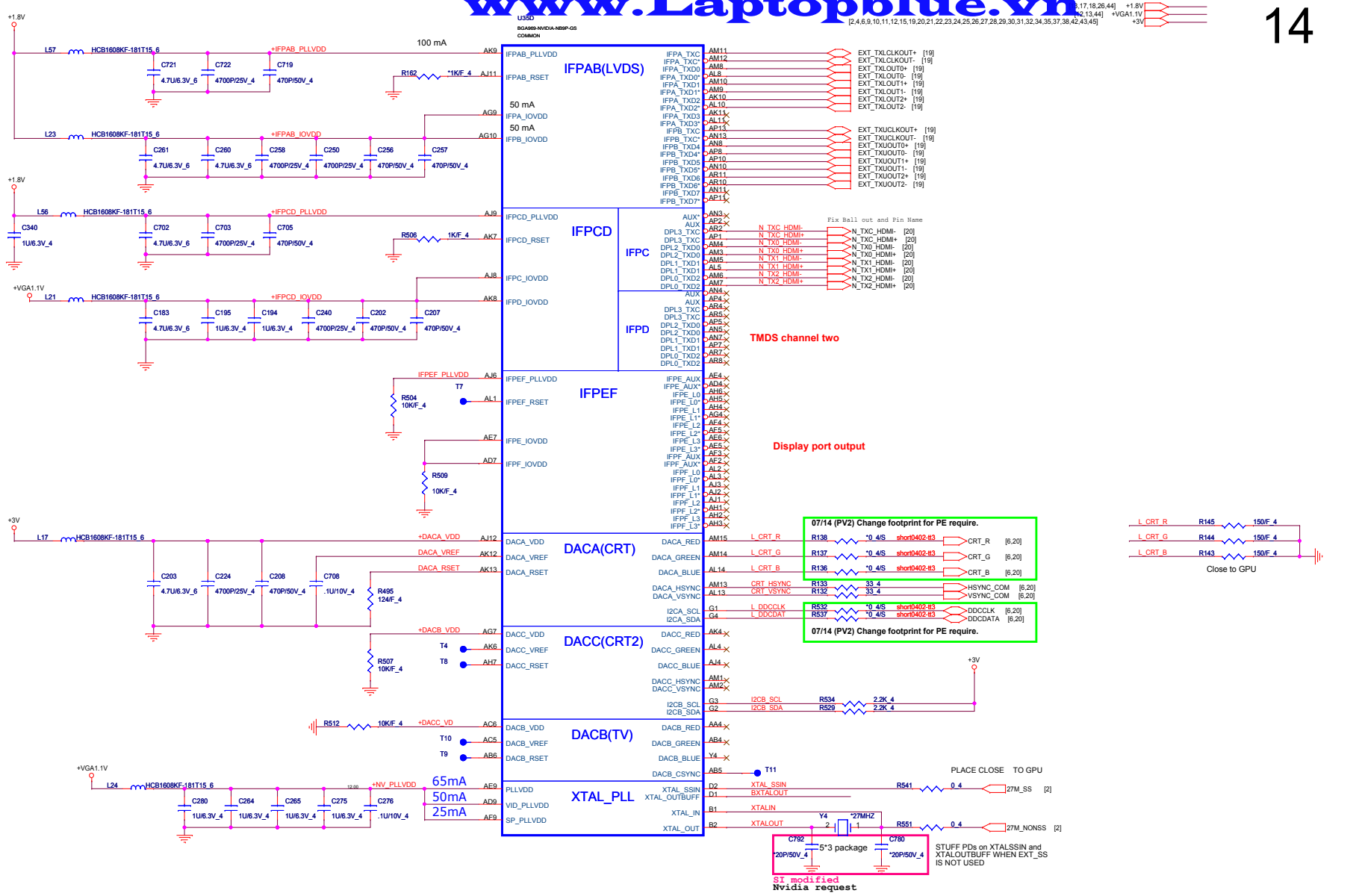
M_B_A[14..0] M_B_A[14..0] [7..10]
M_A_A[14..0] M_A_A[14..0] [7..10]

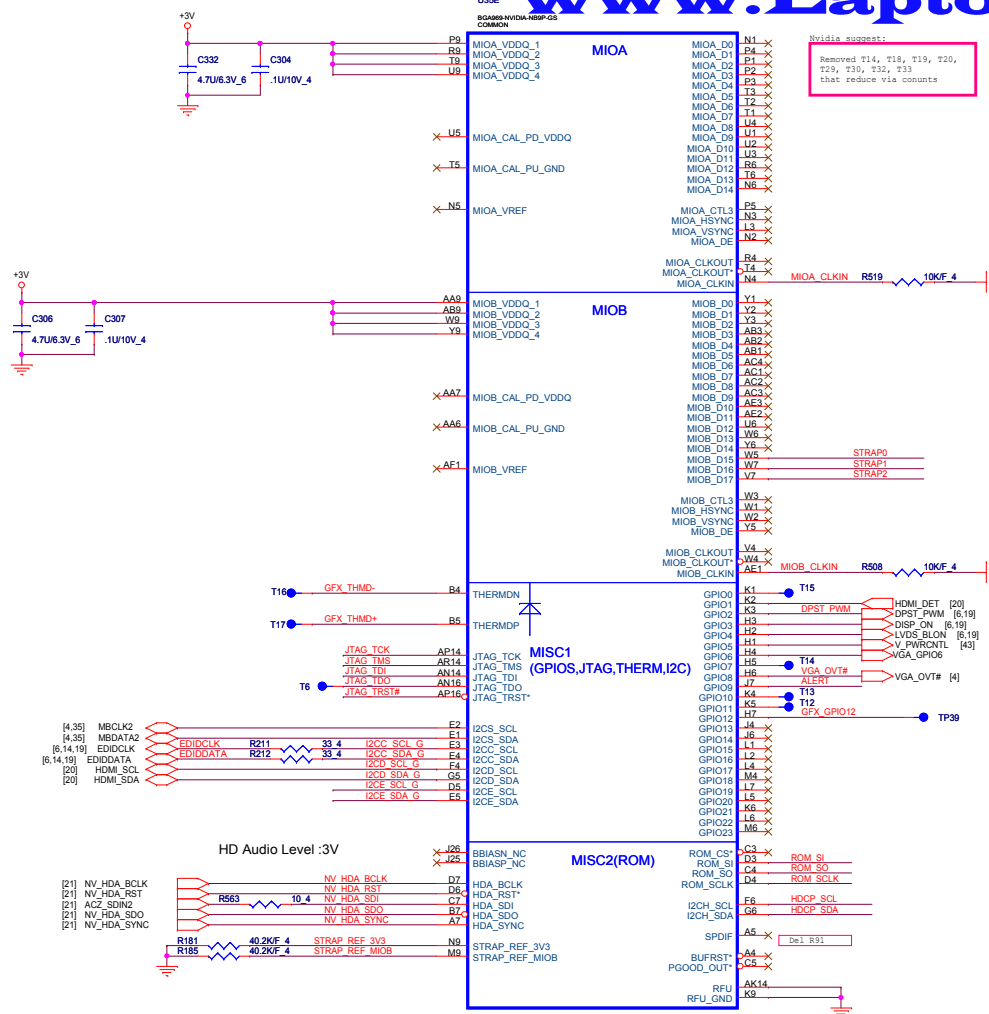


PROJECT : QT6
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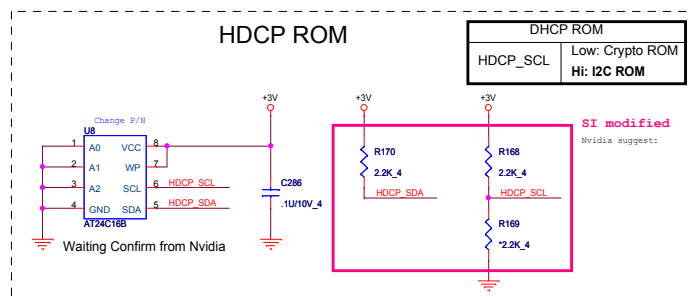






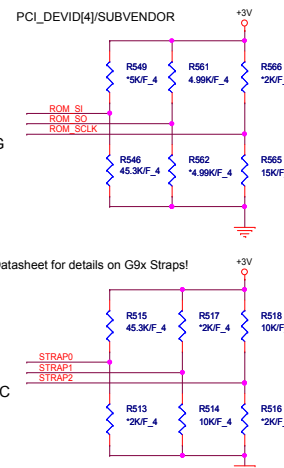


Delete VGA thermal circuit



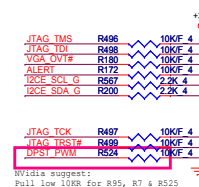
NB9P-GS (G96) Straps
NB9M-GE (G98) Straps
GPIO ASSIGNMENTS

GPIO	I/O	ACTIVE	USAGE
0	IN	N/A	PRIMARY DVI HOTPLUG
1	IN	N/A	SECONDARY DVI HOTPLUG
2	OUT	HIGH	PANEL BACKLIGHT PWM
3	OUT	HIGH	PANEL POWER ENABLE
4	OUT	HIGH	PANEL BACKLIGHT ENABLE
5	OUT	N/A	NVDD VID0
6	OUT	N/A	NVDD VID1
7	OUT	N/A	FBVDD VID0
8	IN	LOW	THERMAL ALERT
9	OUT	LOW	FAN PWM
10	OUT	N/A	FBVREF SELECT
11	OUT	N/A	SLI SYNC0
12	IN	N/A	AC DETECT
13	OUT	LOW	PS CONTROL OR HDMI_CEC
14	OUT	HIGH	PS CONTROL



Logical Strap Bit Mapping

	PU-VDD	PD
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111



	Logical Strapping Bit3	Logical Strapping Bit2	Logical Strapping Bit1	Logical Strapping Bit0
ROM_SO	XCLK_277	TVMODE[2]	TVMODE[1]	TVMODE[0]
ROM_SCLK	PCI_DEVIDE[4]	SUB_VENDOR	SLOT_CLK_CFG	PEX_PLL_EN_TERM100
ROM_SI	RAMCFG[3]	RAMCFG[2]	RAMCFG[1]	RAMCFG[0]
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
STRAP1	3GIO_PADCFG[3]	3GIO_PADCFG[2]	3GIO_PADCFG[1]	3GIO_PADCFG[0]
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]

1000
0010
XXXX
XXXX
0001
1111

PCI_DEVID: STRAP2

NB9M-GE	0x06E	8	1000 default
NB9M-GS	0x06E	9	1001
NB9P-GE2	0x064	8	1000
NB9P-GS	0x064	9	1001 default

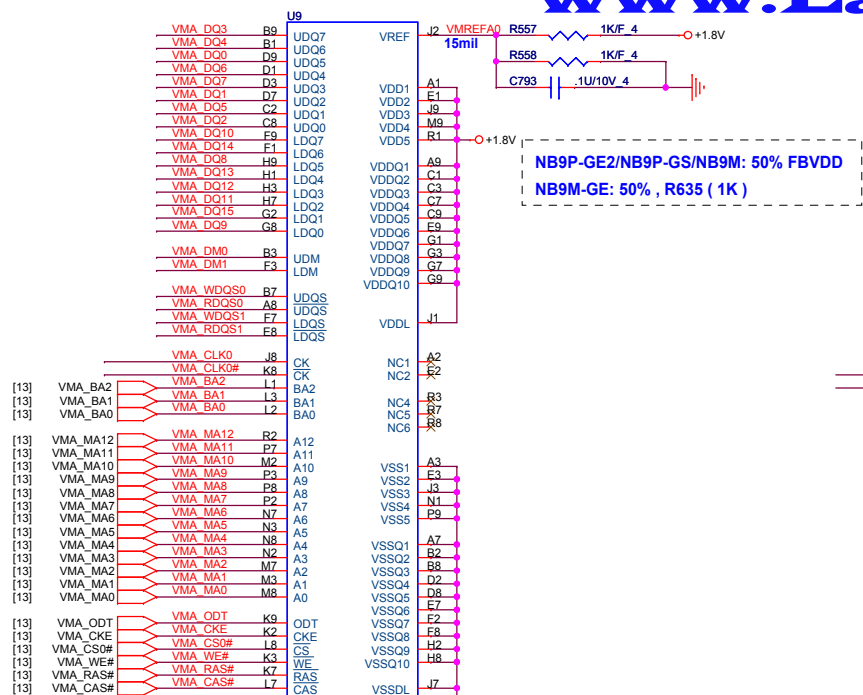
NB9X VRAM Configuration Table

RAM_CFG[3:0]	DESCRIPTION	Vendor
0111	DDR2 32Mx16x8, 128bit, 512MB	Hynix HY5PS121621CFP-25
0110	DDR2 32Mx16x8, 128bit, 512MB	Qimonda HYB187512161B2F-25
0101	DDR2 32Mx16x8, 128bit, 512MB	Samsung K4N51163QC-ZC25
0100	DDR2 32Mx16x8, 128bit, 512MB	Nanya/Elipda
0000	DDR2 64Mx16x8, 128bit, 1GB	Hynix
0001	DDR2 64Mx16x8, 128bit, 1GB	Samsung
0010	DDR2 64Mx16x8, 128bit, 1GB	Qimonda

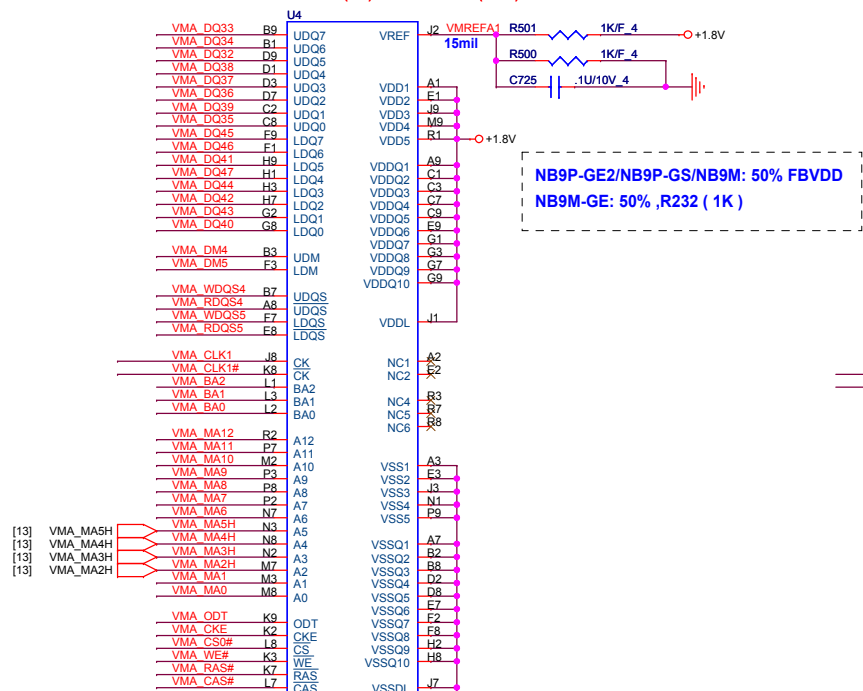
CS33572FB13 RES CHIP 35.7K 1/16W +-1% (0402)

GROUND



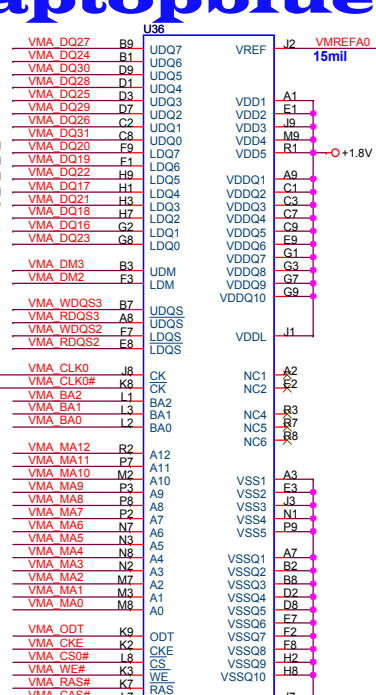


H5PS5162FFR-25C
AKD5FG-TW03
IC SDRAM(84P) H5PS5162FFR-25C(FBGA)



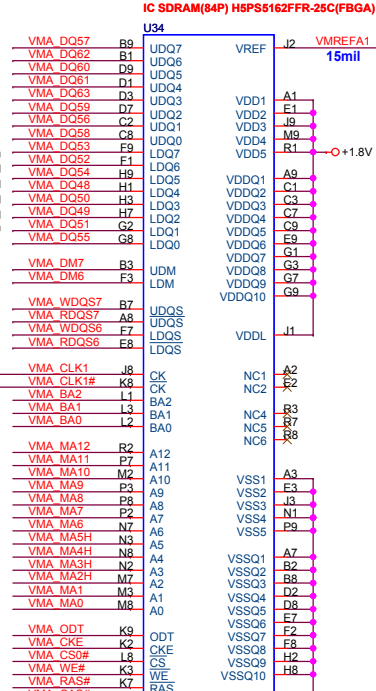


 H5PS5162FFR-25C
 AKD5FG-TW03
 IC SDRAM (84P) H5PS5162FFR-25C (FBGA)

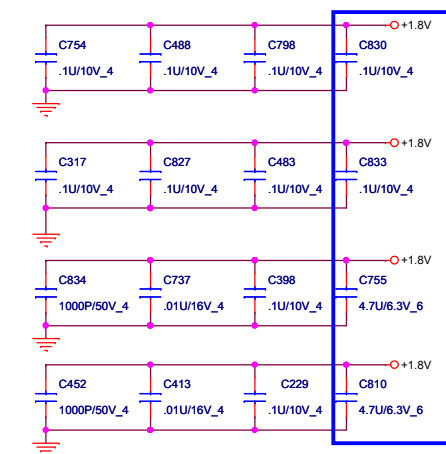
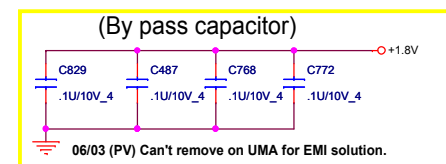
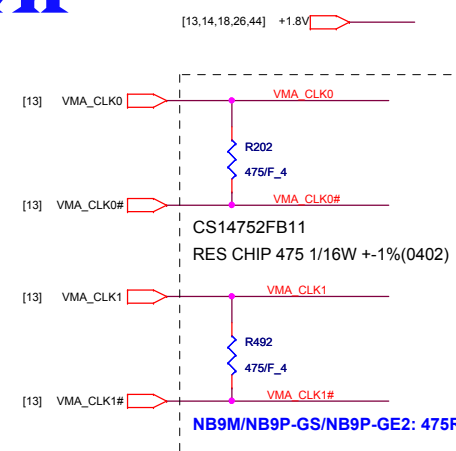


VIMA CAS# 17 CAS VSSDL 37

H5PS5162FFR-25C
AKD5FG-TW03



VMA_CAS# L7 CAS VSSDL J7



For DB:

NB9P : AKD59G-T502(Samsung,32M*16)

NB9M : AKD5FG-TW31(Hynix,32M*16)

AKD5FG-T^03(Qimonda 32M*16)

256Mb : AKD5JGAT^05
512Mb : AKD59G-T^01

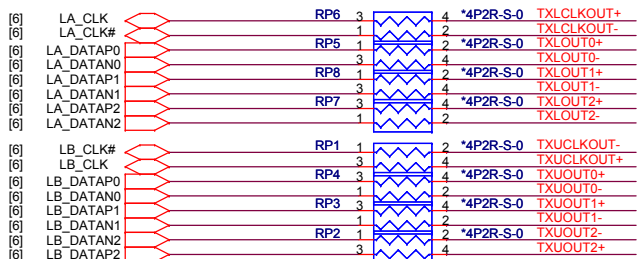


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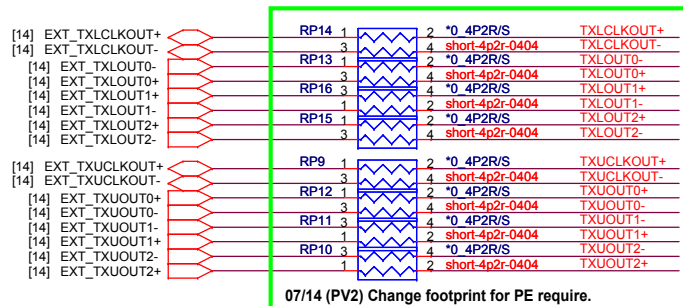
Size Custom	Document Number NV9X VRAM-1(GDDR2 BGA84)	Rev E3A
Date: Friday, July 18, 2008	Sheet 17 of 46	

1. If LCD connector near GPU, then place these series Resistors near GPU
2. If LCD connector near N/B, then place these series Resistors near N/B

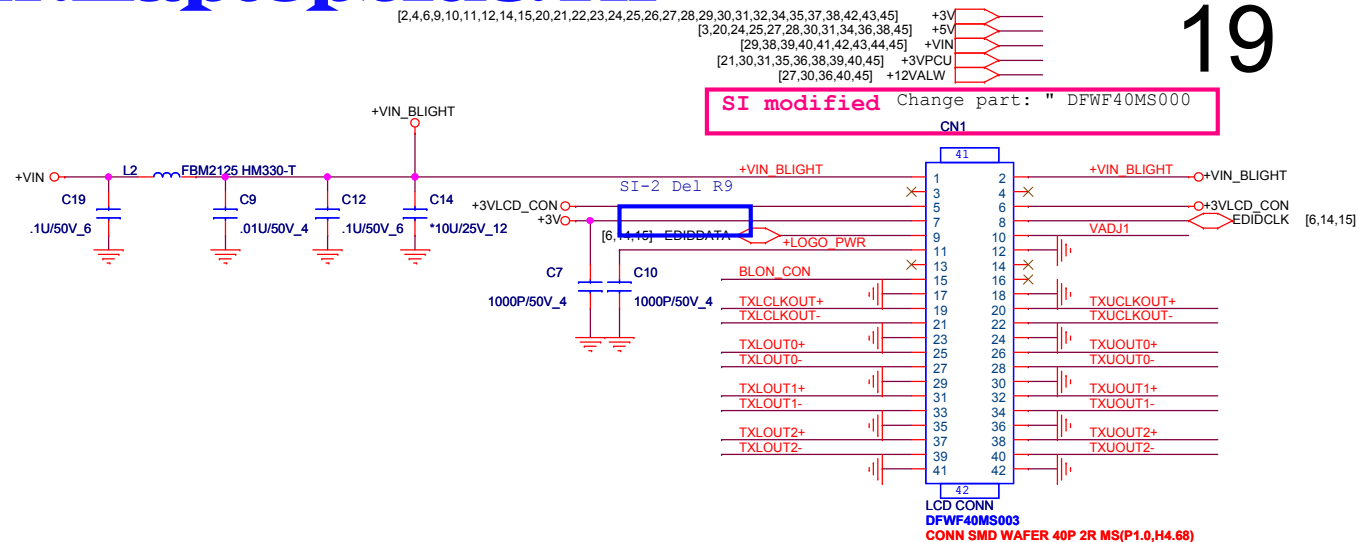
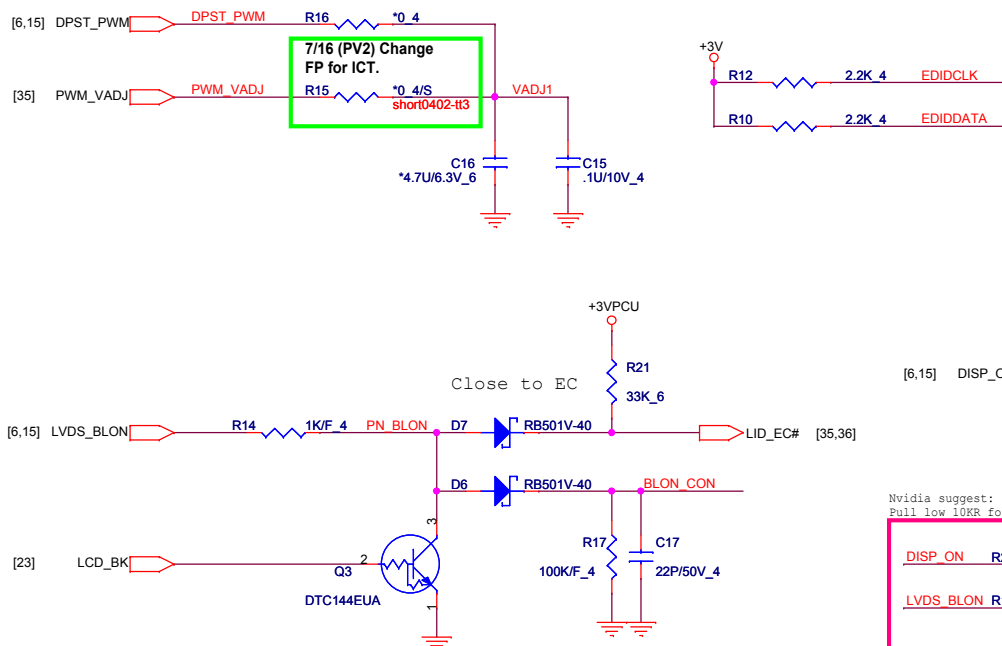
OPTION SIGNAL FROM NB FOR UMA VGA



OPTION SIGNAL FROM Nvidia to VGA

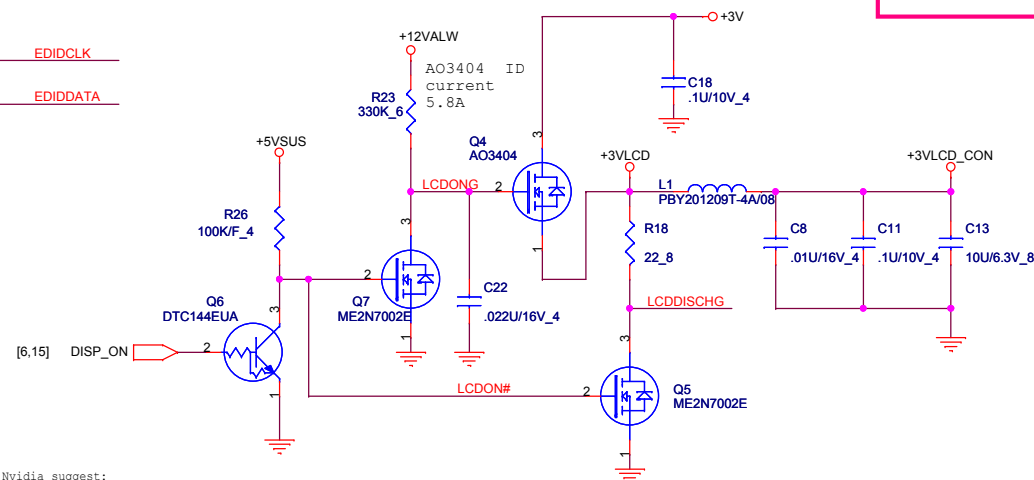
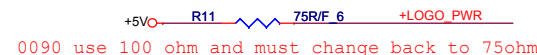


07/14 (PV2) Change footprint for PE require.

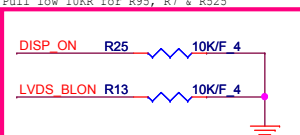


SI modified

Del CN7,R88,C115
Remove Logo light2

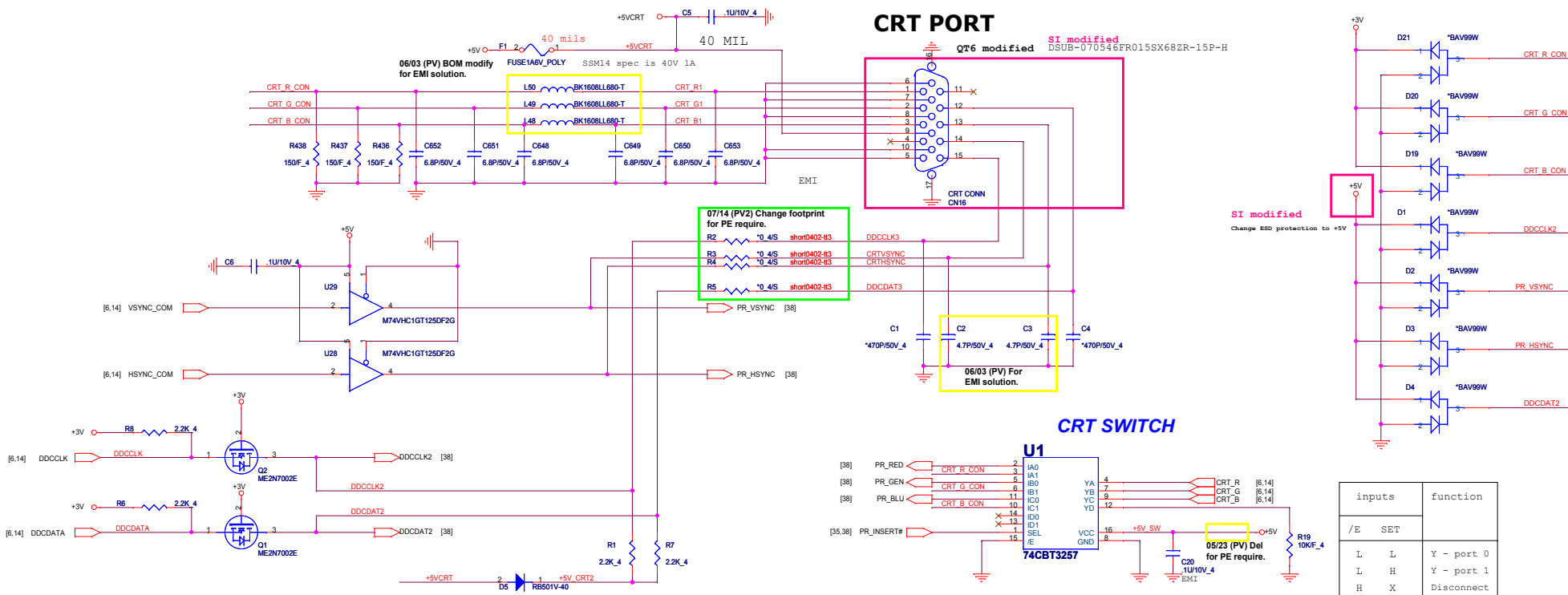


Nvidia suggest:
Pull low 10KR for R95, R7 & R525



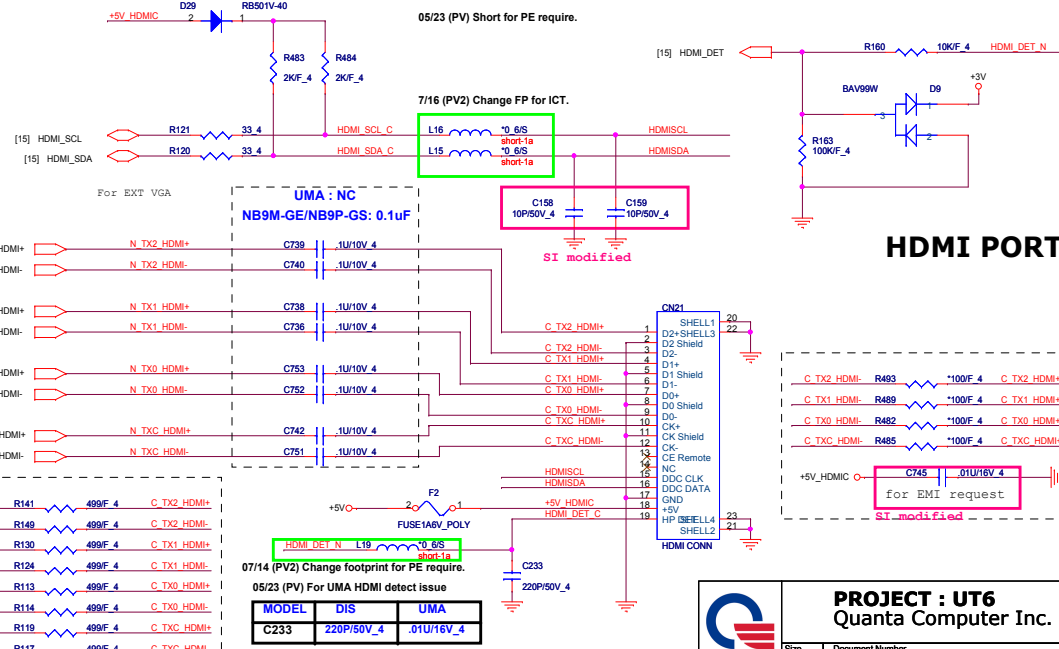
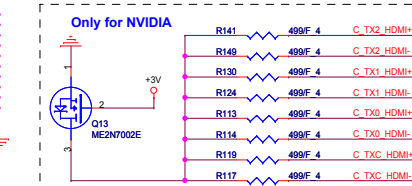
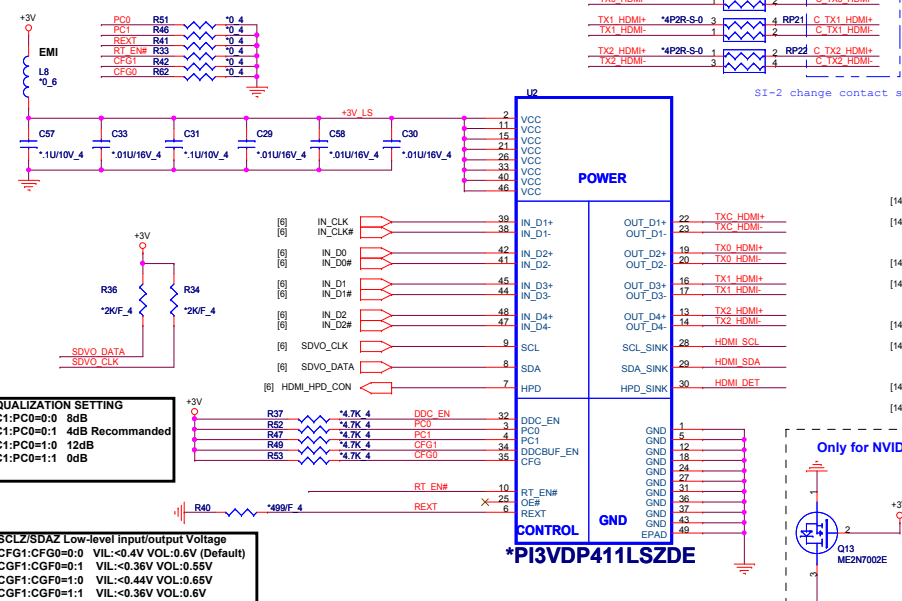
PROJECT : UT6
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Size B	Document Number LCD CONN/Lid function	Rev E3A
Date: Friday, July 18, 2008		Sheet 19 of 46

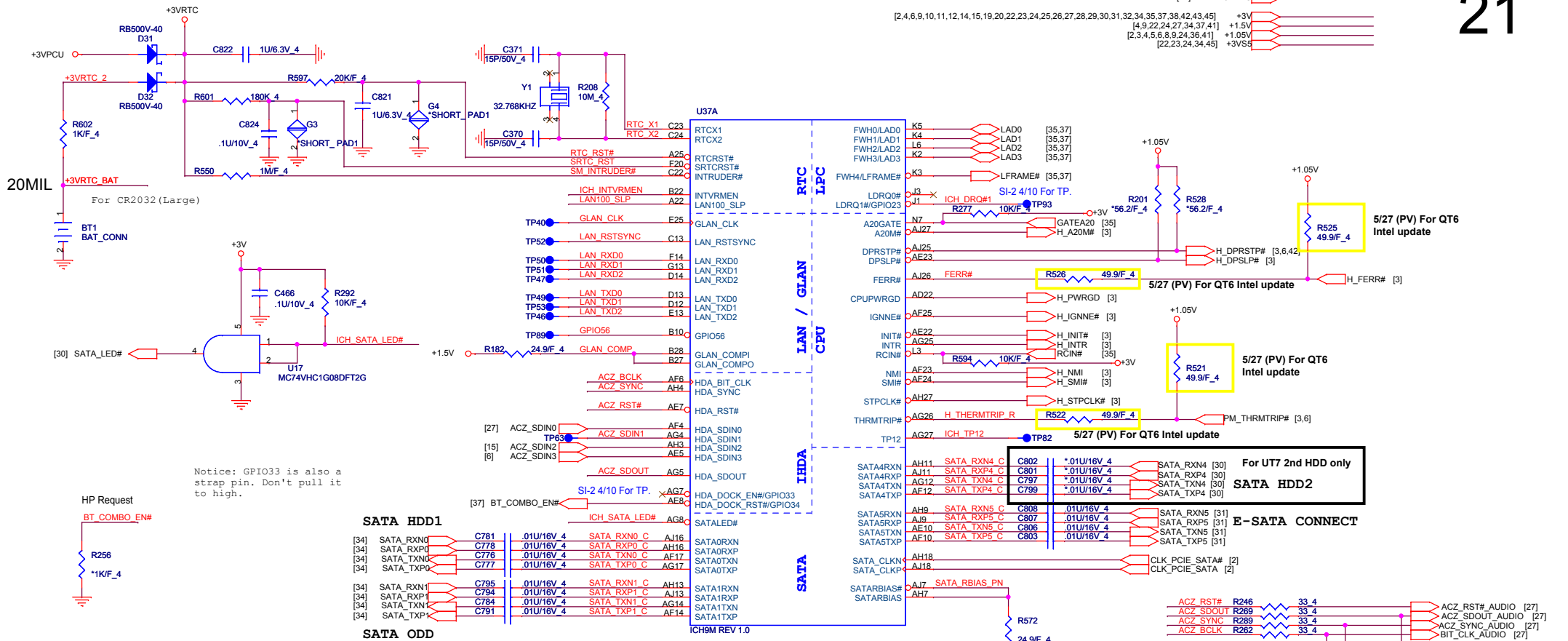


inputs		function
/E	SET	
L	L	Y - port
L	H	Y - port
H	X	Disconnect

For UMA HDMI function



HDMI PORT



SB Strap

ICH9-M Internal VR Enable strap (Internal VR for VccSus1_05, VccSus1_5 and VccCL1_5)	ICH9-M LAN100_SLP Strap (Internal VR for VccLAN1_05 and VccCL1_05)
INTVRMEN Low = Internal VR disable High = Internal VR enable(Default)	LAN100_SLP Low = Internal VR disable High = Internal VR enable(Default)

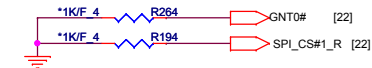
XOR Chain Entrance Strap

ICH_TP3	HDA_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal operation(Default)
1	1	Set PCIE port config bit 1

ICH9 Boot BIOS select

STRAP	PCI_GNT0#	SPI_CS#1
SPI	0	1
PCI	1	0
LPC	1	1

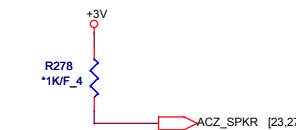
(default)



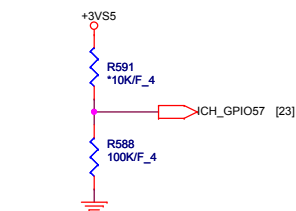
A16 swap override strap	
PCI_GNT#3	Low = A16 swap override enabled Hi = Default



No Reboot Strap	
ACZ_SPKR	Low: Default Hi: No reboot



TPM physical presence	
ICH_GPIO57	Low: Default



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Size	Document Number	Rev
Custom	ICH9-M Host 1/4	E3A
Date: Friday, July 18, 2008	Sheet 21 of 46	

SWAP PCIE PORT6 to PORT2 (Lan and New card swap) -->Rename the port name by function and port

[2,4,6,9,10,11,12,14,15,19,20,21,23,24,25,26,27,28,29,30,31,32,34,35,37,38,42,43,45] +1.5V
 [23,31,37,41,42,43,45] +3V
 [21,23,24,34,45] +3VSUS
 +3VS5

22

MINI CARD PCI-E (WLAN)

PCIE-LAN

MINI CARD PCI-E (ROBSAN)

TV CARD PCI-E

FireWire PCI-E

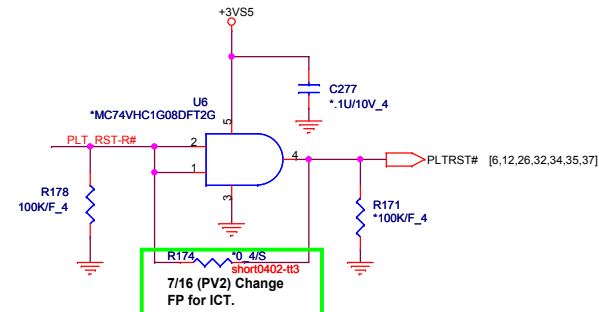
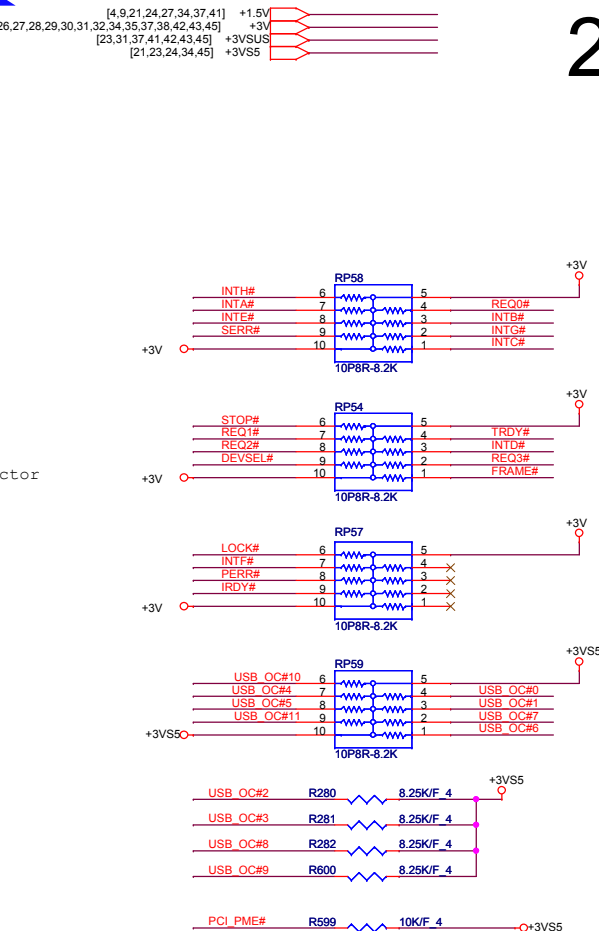
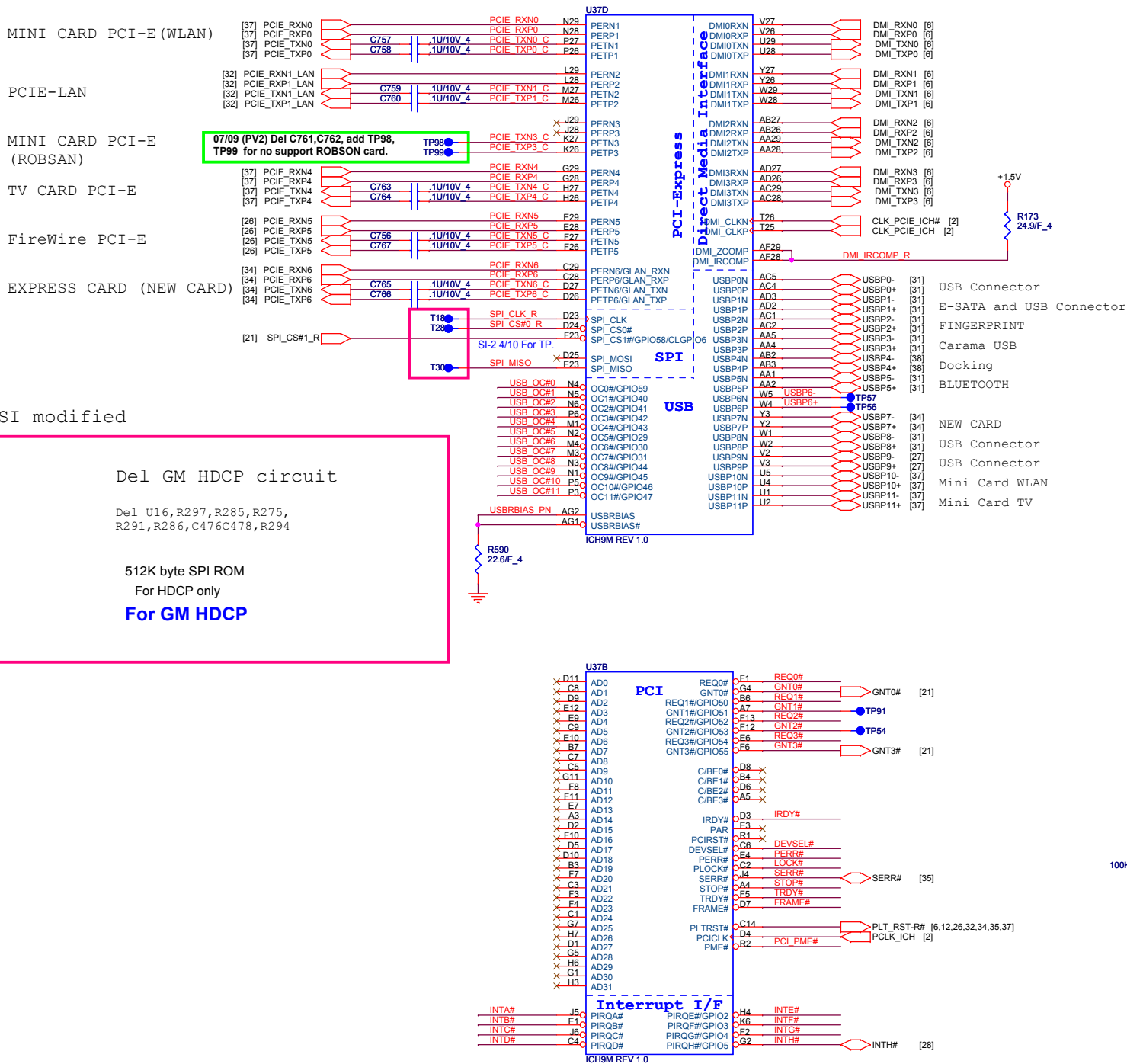
EXPRESS CARD (NEW CARD)

SI modified

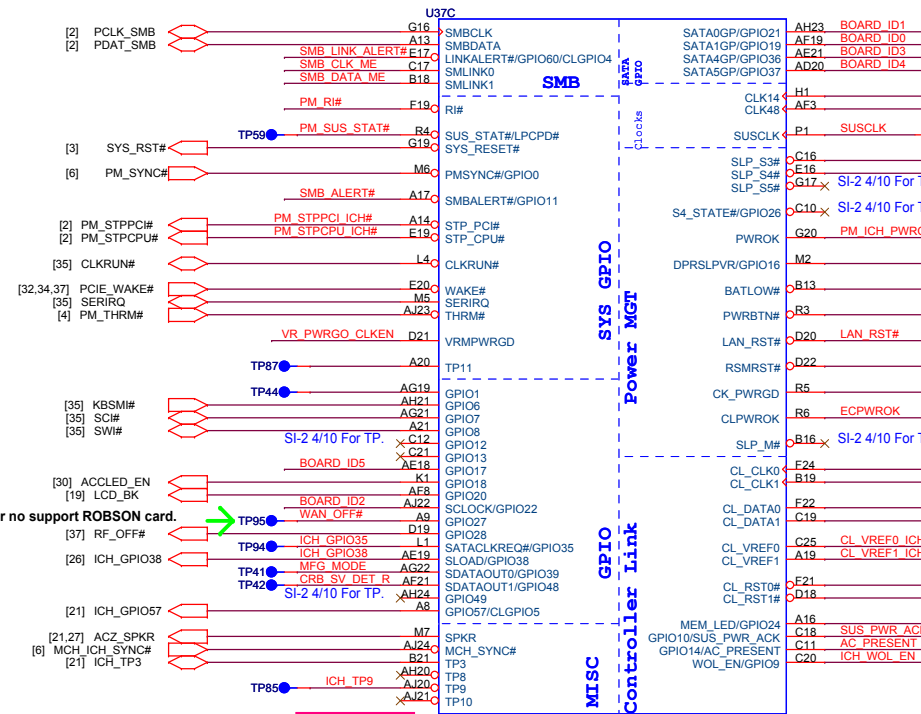
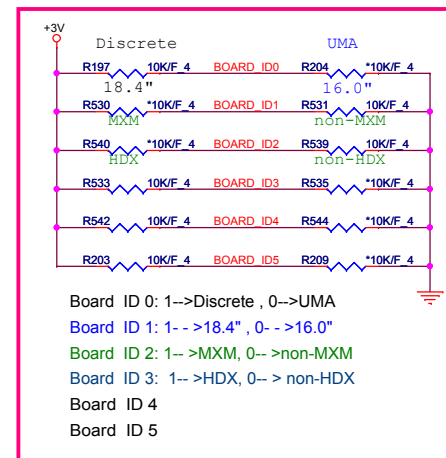
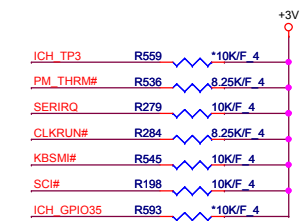
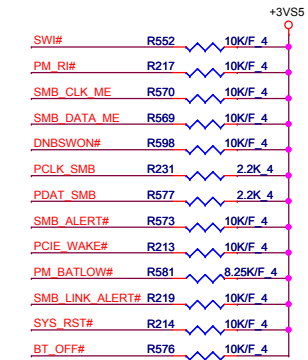
Del GM HDCP circuit

Del U16, R297, R285, R275,
 R291, R286, C476C478, R294

512K byte SPI ROM
 For HDCP only
For GM HDCP

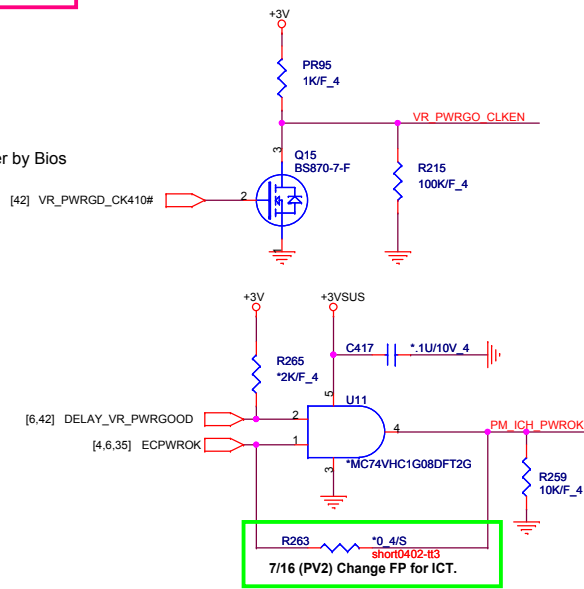
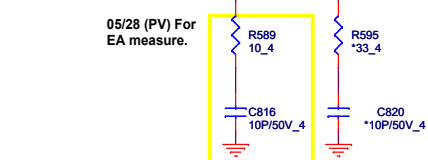


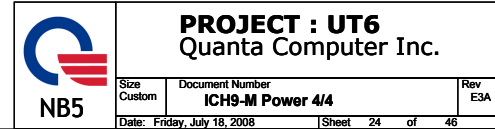
[2,4,6,9,10,11,12,14,15,19,20,21,22,24,25,26,27,28,29,30,31,32,34,35,37,38,42,43,45] +3V
[21,22,24,34,45] +3VS5
[31,37,41,42,43,45] +3VSUS

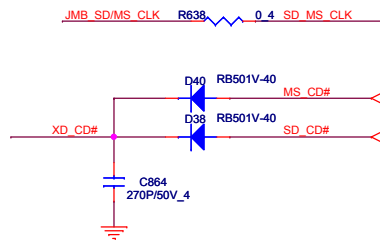


07/09 (PV2) Add TP95 for no support ROBSON card.

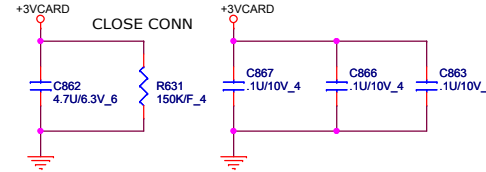
SI-2 Build
Delete R574,G2 as Bios_Rec can be cover by Bios





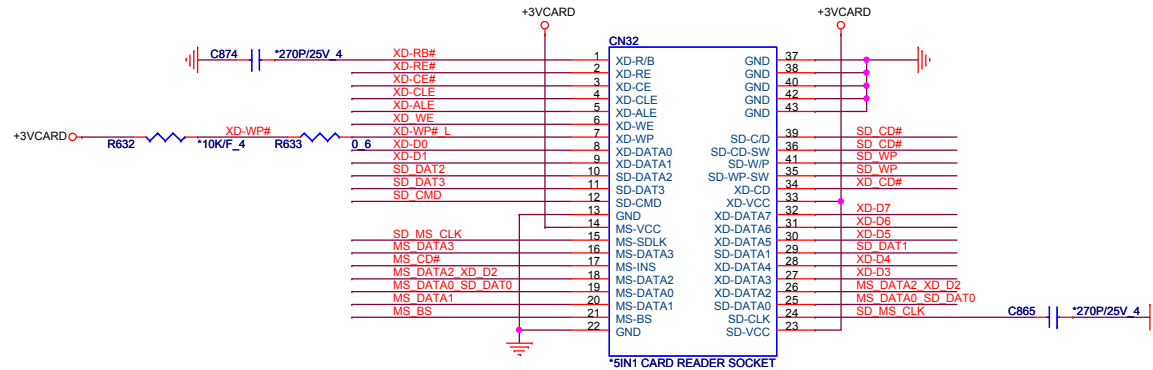


Close to CN34



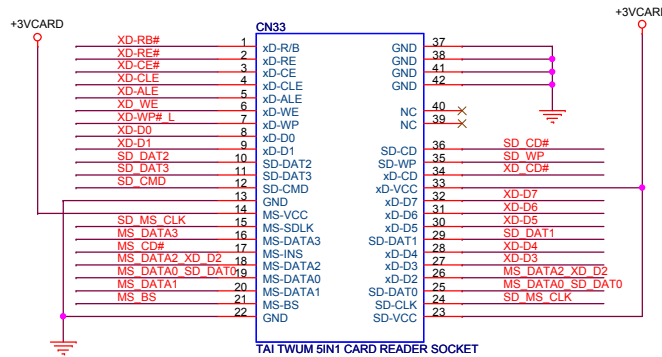
SD/MMC	MS	XD
MDIO0	SD DAT0	MS D0
MDIO1	SD DAT1	MS D1
MDIO2	SD DAT2	MS D2
MDIO3	SD DAT3	MS D3
MDIO4	SD CMD	MS BS
MDIO5	SD CLK	MS SCLK
MDIO6	SD WP	MS WP
MDIO7		XD CLE
MDIO8	SD DAT4	XD D4
MDIO9	SD DAT5	XD D5
MDIO10	SD DAT6	XD D6
MDIO11	SD DAT7	XD D7
MDIO12		XD RE#
MDIO13		XD RB#
MDIO14		XD ALE
CR1 LEDN	SD1 LED#	MS1 LED#
CR1 PCTLN	SD1 PCTL#	MS1 PCTL#
CR1 CD0	SD1 CD#	MS1 CD#
CR1 CD1		MS1 CD#

5 IN1 CARD READER XD , MMC/SD , MS/MSP



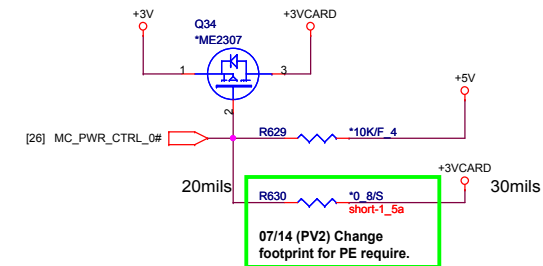
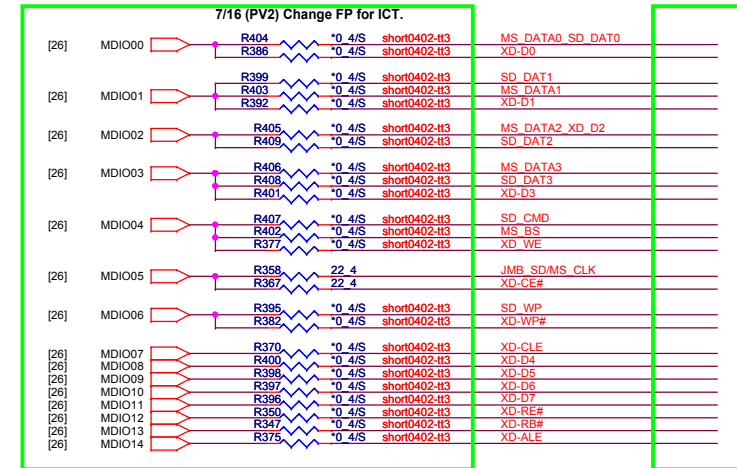
SI modified Footprint: "4in1-72700327123-43p-1"

2ND SOURCE

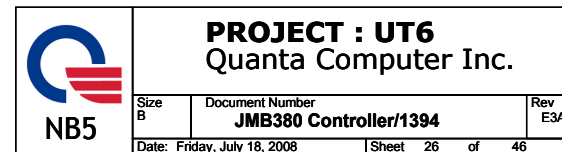


SI modified Footprint: "7IN1-R015-B11-1M-42P-L"

7/16 (PV2) Delete net for ICT.



PROJECT : UT6
Quanta Computer Inc.



05/23 (PV) For IDT Dolby functionality.
07/14 (PV2) R697 change footprint for PE require.

06/03 (PV) For EMI solution.
R343 BLM18AG221SN1D
R342 0.4/S short0402-tb3

QT6 Modified-0117
C762, C763 10P

07/14 (PV2) Change footprint for PE require.
+3V_DVDD

Close to Pin1
TP65
[21] ACZ_SDOUT_AUDIO
[21] BIT_CLK_AUDIO
[21] ACZ_SDIN0
[21] ACZ_SYNC_AUDIO
[21] ACZ_RST#_AUDIO
[21,23] ACZ_SPKR

Close to Pin9
+3V_DVDD
IDT modified

05/20 (PV) FOR Audio noise.
05/20 (PV) FOR Audio noise.

IDT
92HP61B7X5NLGXA1X

QT6 Modified-0117
R485, R499 Reserve

Close to Pin38
+4.75VAVDD
C556 100U/6.3V_8
C557 0.1U/10V_4

TO Headphone jack
AGND SHIELD
TO Audio Jack Sub Woofer

TO Internal Speakers
HP-R
HP-L
LINE_OUTL [28,29]

TO Audio Jack MIC
EXT_MIC_R
EXT_MIC_L

TO DOCKING MIC
DOCK_MIC_R
DOCK_MIC_L

TO DOCK Headphone
DOCK_RSPK+ [38]
DOCK_LSPK+ [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

TO DOCK MIC DETECT
DOCK_MIC_L [38]
DOCK_MIC_R [38]

07/14 (PV2) Add R713 for Audio power timing.
R713 1K/F_4
Vset=1.242V
SI modified

TO AUDIO/B CON.
C568 27P/50V_4
C565 27P/50V_4
FOR EMI

05/12 (PV) FOR EMI Solution
C930 0.1U/10V_4

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

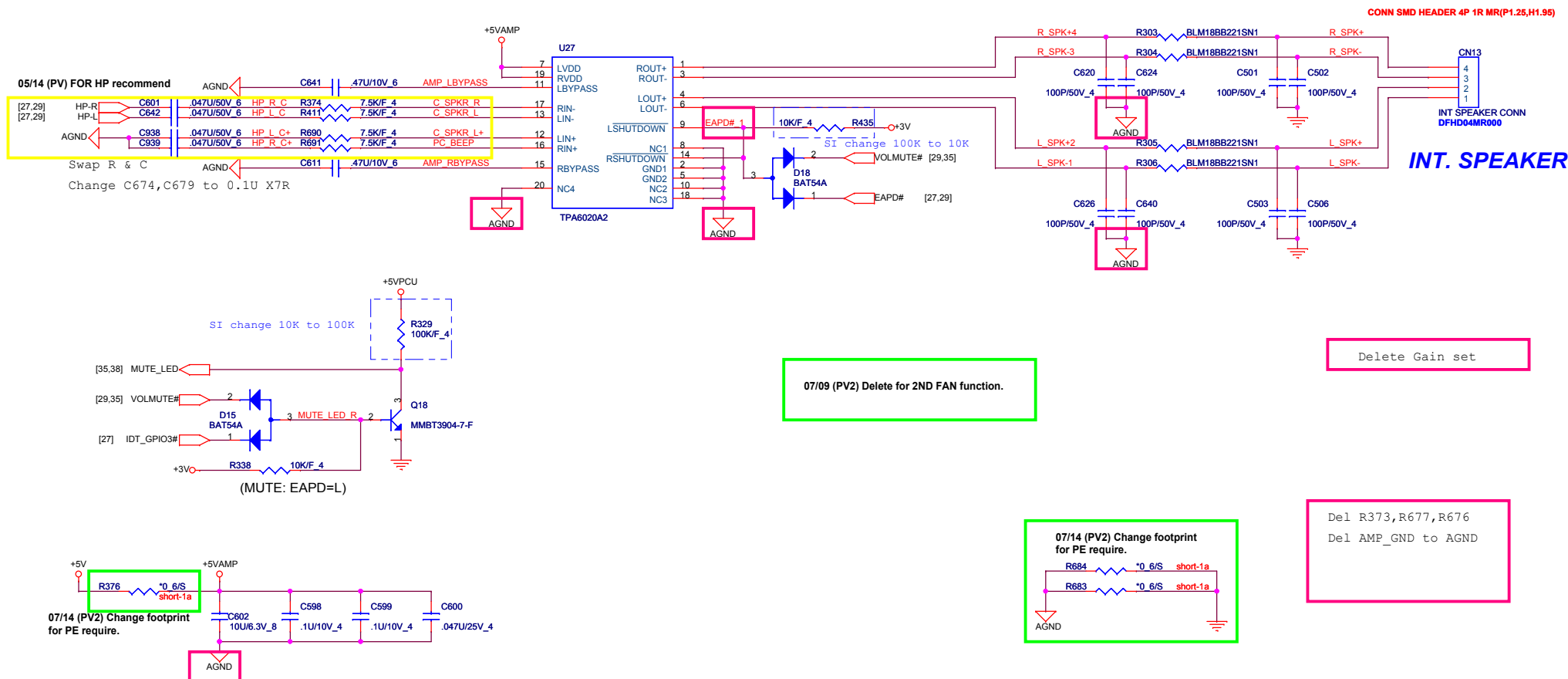
07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

07/14 (PV2) Change footprint for PE require.
R380 0.6/S short-1a
R675 0.6/S short-1a
R341 0.6/S short-1a
R336 0.6/S short-1a
R429 0.6/S short-1a
R554 0.6/S short-1a
R603 0.6/S short-1a
SI-2 EMI Requests Add

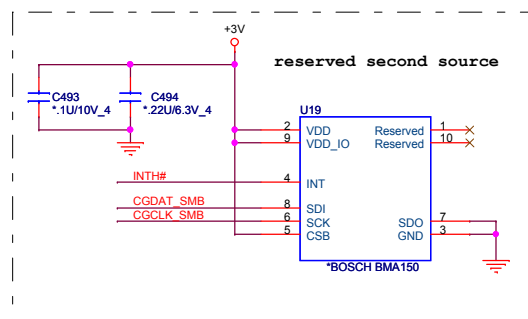
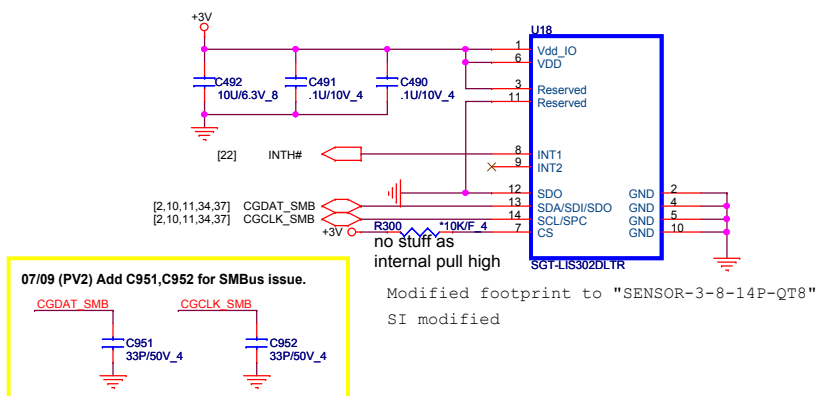
PORT	PLACE TO
MONO_OUT	X
PORT A	HP OUT
PORT B	M/B MIC
PORT C	X
PORT D	Internal Speckers
PORT E	Docking MIC
PORT F	X
DM	DIGITAL MIC

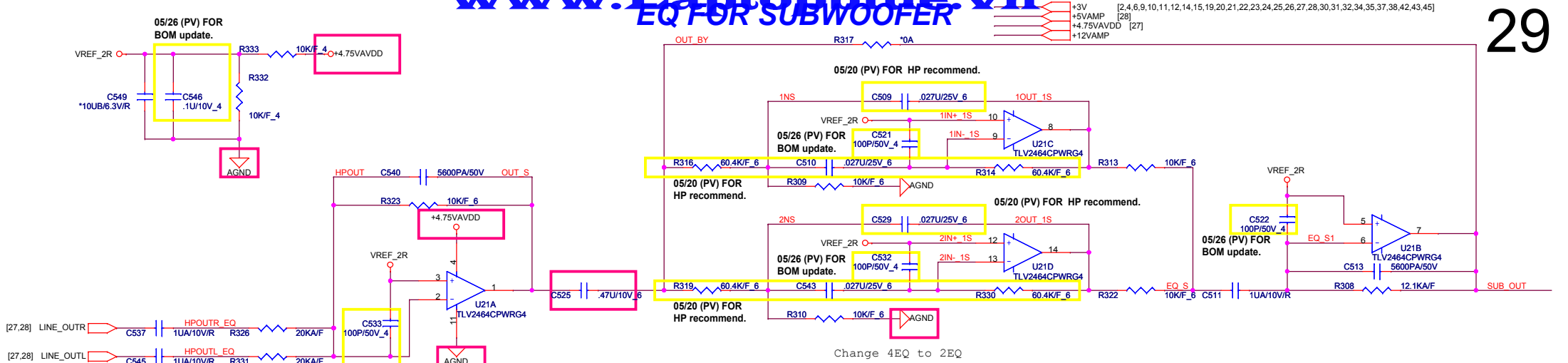
SA_A# -->EXT HP
SA_B# -->EXT MIC
SB_E#--> DOCK MIC
Audio JACK: Normal Open

AUDIO AMPLIFIER

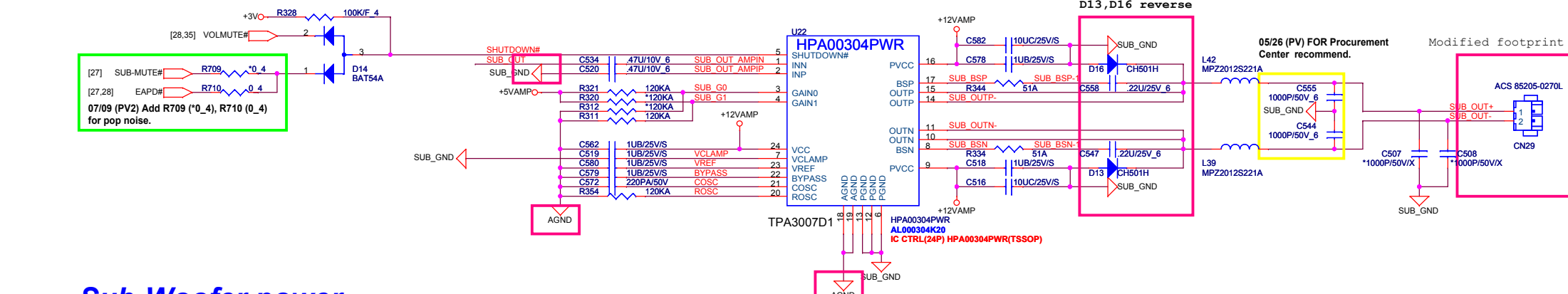


Accelerometer Sensor

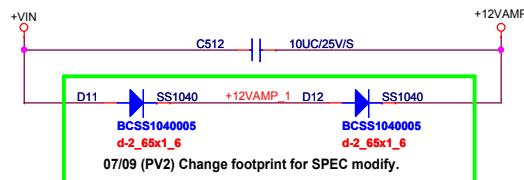




MODEL	UT6	UT7
R316	60.4K/F_6	40.2K/F_6
R319	60.4K/F_6	40.2K/F_6
R330	60.4K/F_6	80.6K/F_6
R314	60.4K/F_6	80.6K/F_6
C509	0.027U/25V_6	0.022U/50V_6
C510	0.027U/25V_6	0.022U/50V_6
C529	0.027U/25V_6	0.039U/16V_6
C543	0.027U/25V_6	0.039U/16V_6

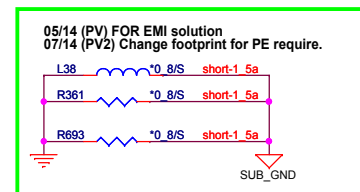


Sub-Woofer power

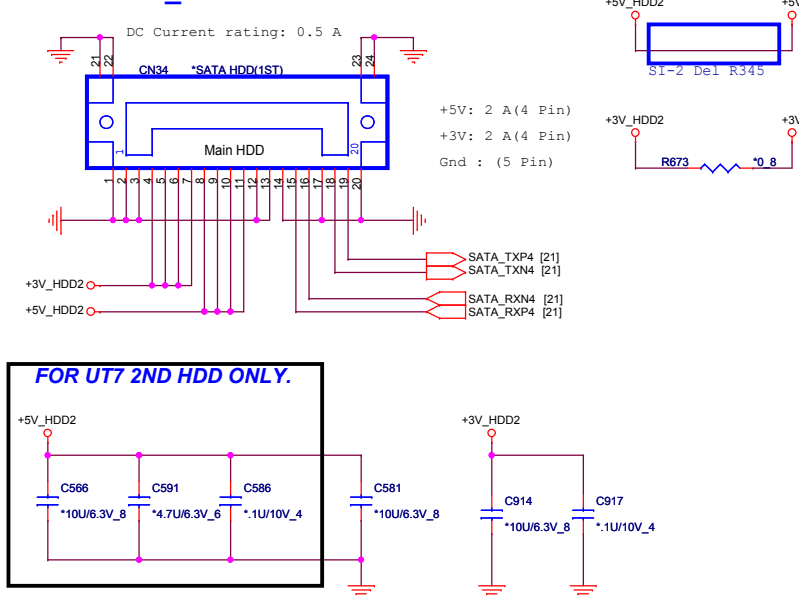


Del HP_GND to GND
Del R307,R315

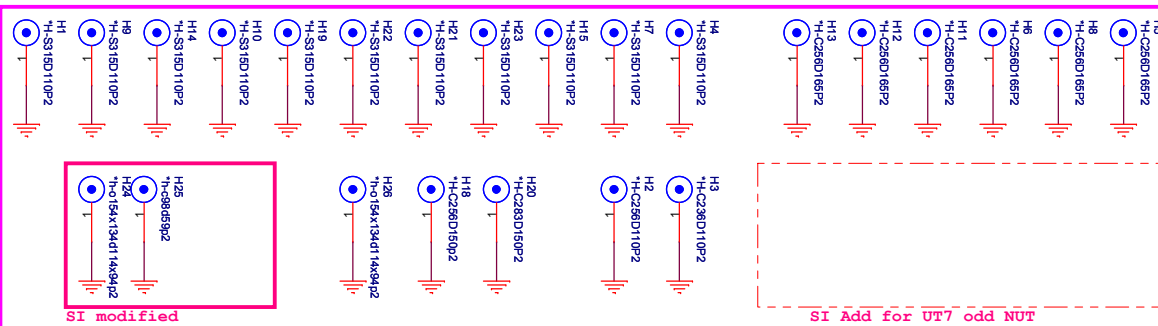
Delete L1003



SATA_2 CONNECTOR

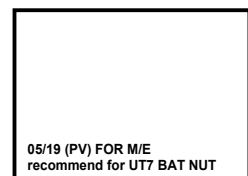
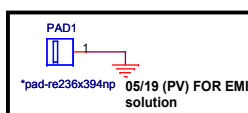


M/B Screw Hole



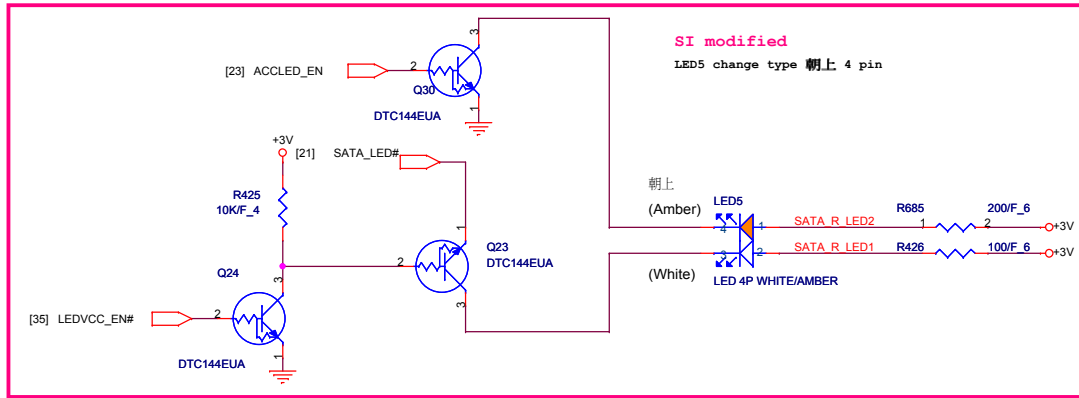
delete all PAD & change screw footprint

07/14 (PV2) Delete H16,H17 for no support ROBSON card.

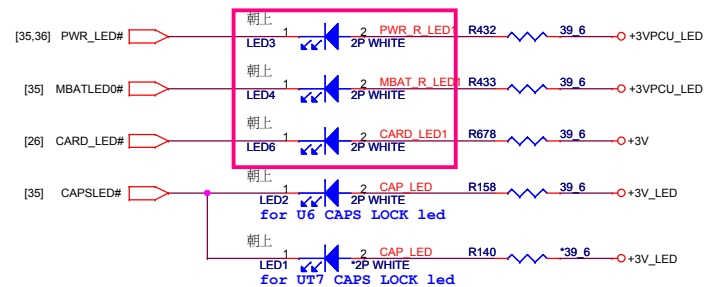


SI modified

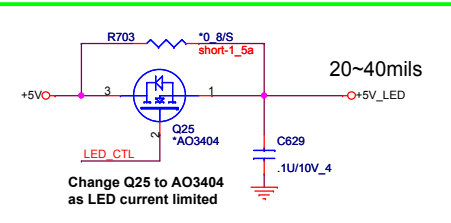
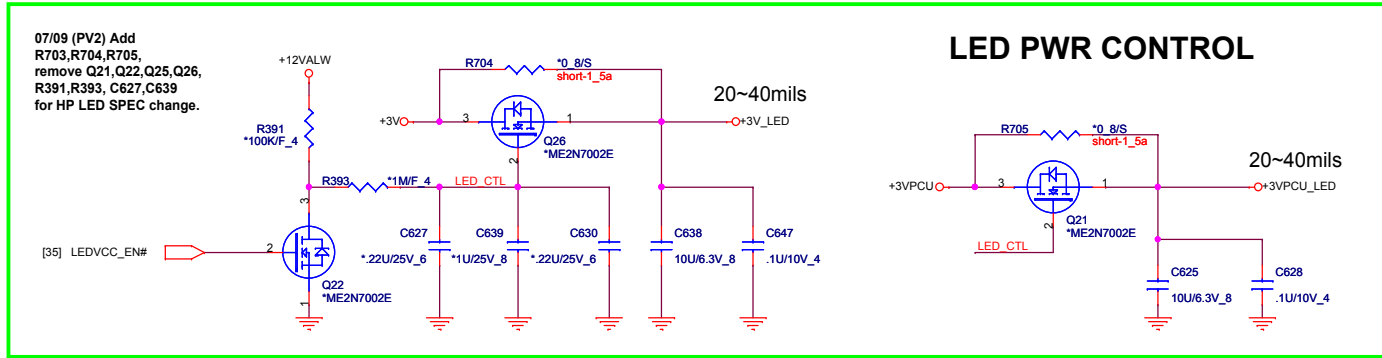
LED5 change type 朝上 4 pin

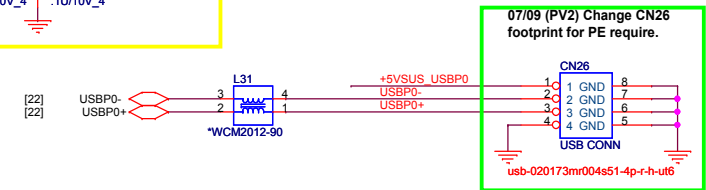
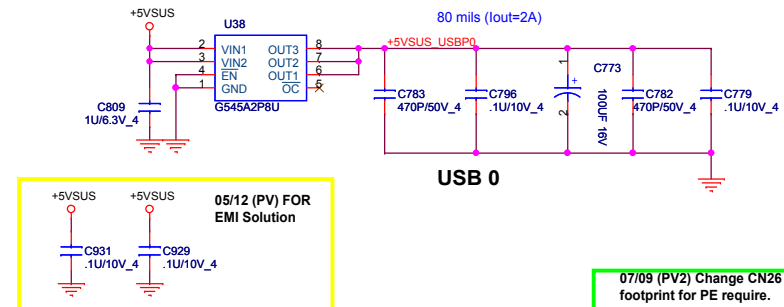
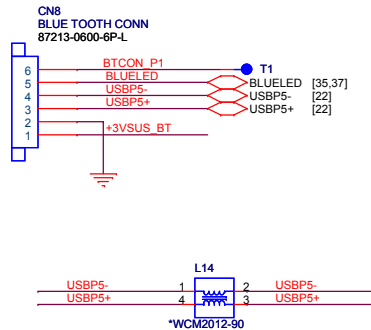
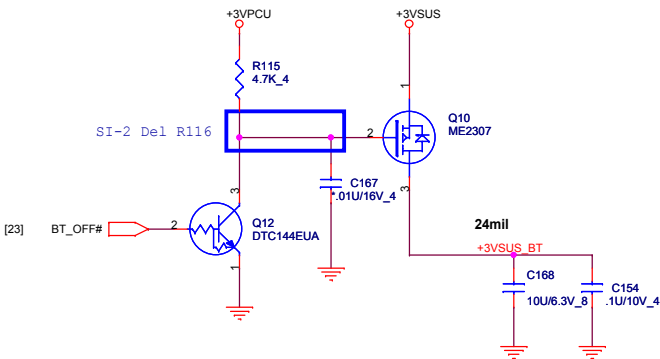


SI modified LED3,4,6 change type 朝上 2 pin

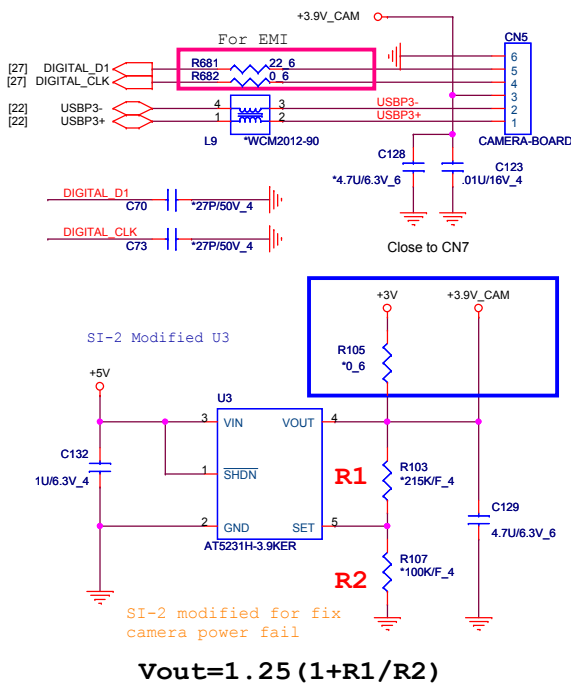


LED PWR CONTROL

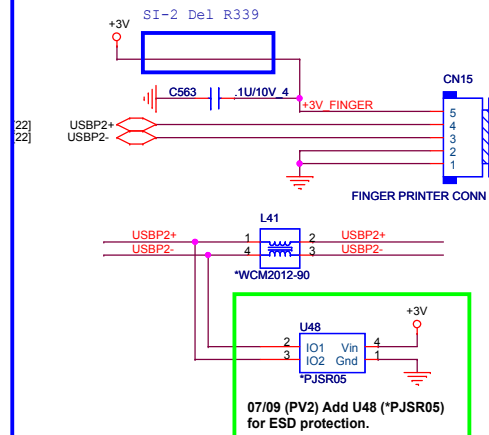




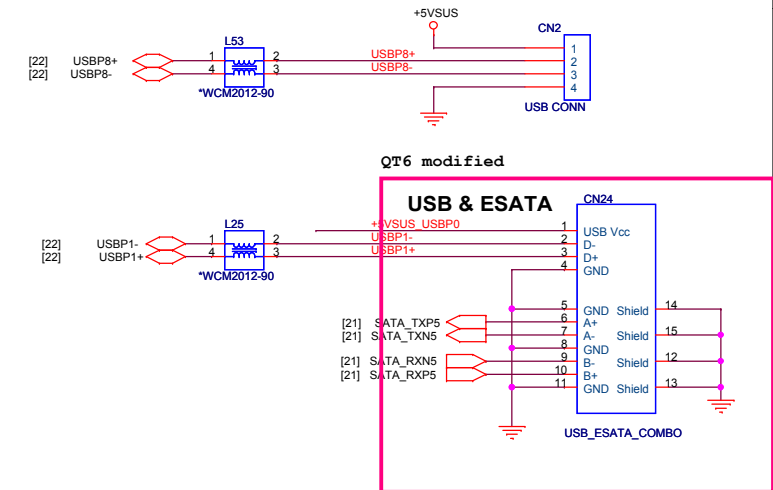
USB CAMERA /DIGITAL MIC CONNECT



USB fingerprint CON



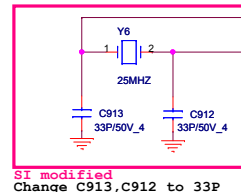
1. ESD GND
2. SYSTEM GND
3. USB-
4. USB+
5. USB PWR(+3V)



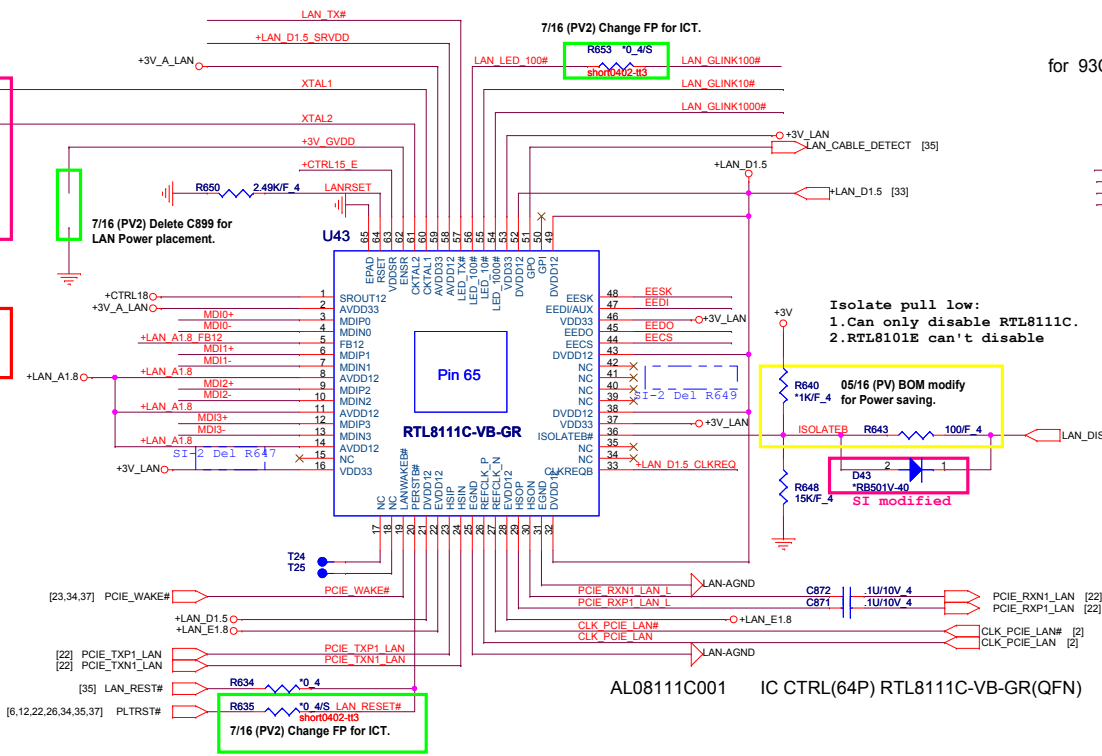
T : Stuffed for RTL8111C(10/100/1000)

+LAN_D1.5 +LAN_D1.5_SRVD0
05/23 (PV) Del for PE require.

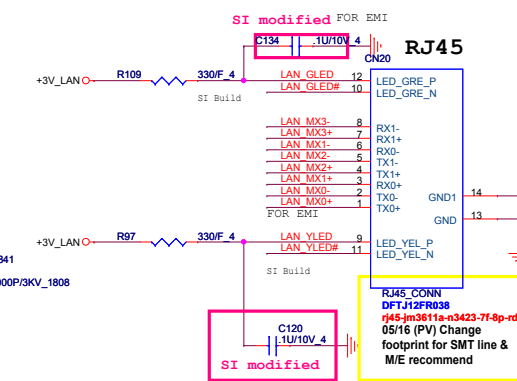
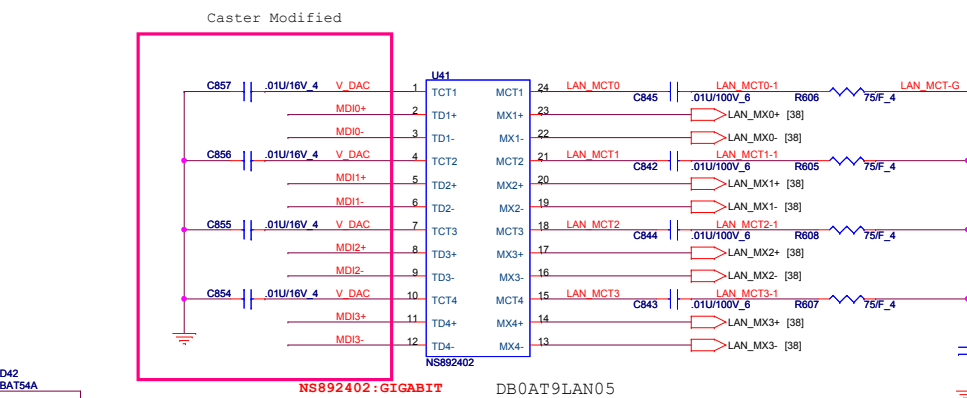
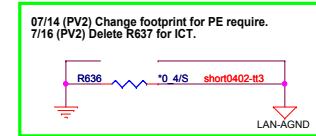
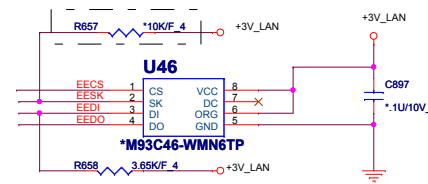
+LAN_D1.5 CLKREQ
T26 Stuffed for 8102E/RTL8111C



U18#63 wider than 40 mils
U18#1 wider than 60 mils

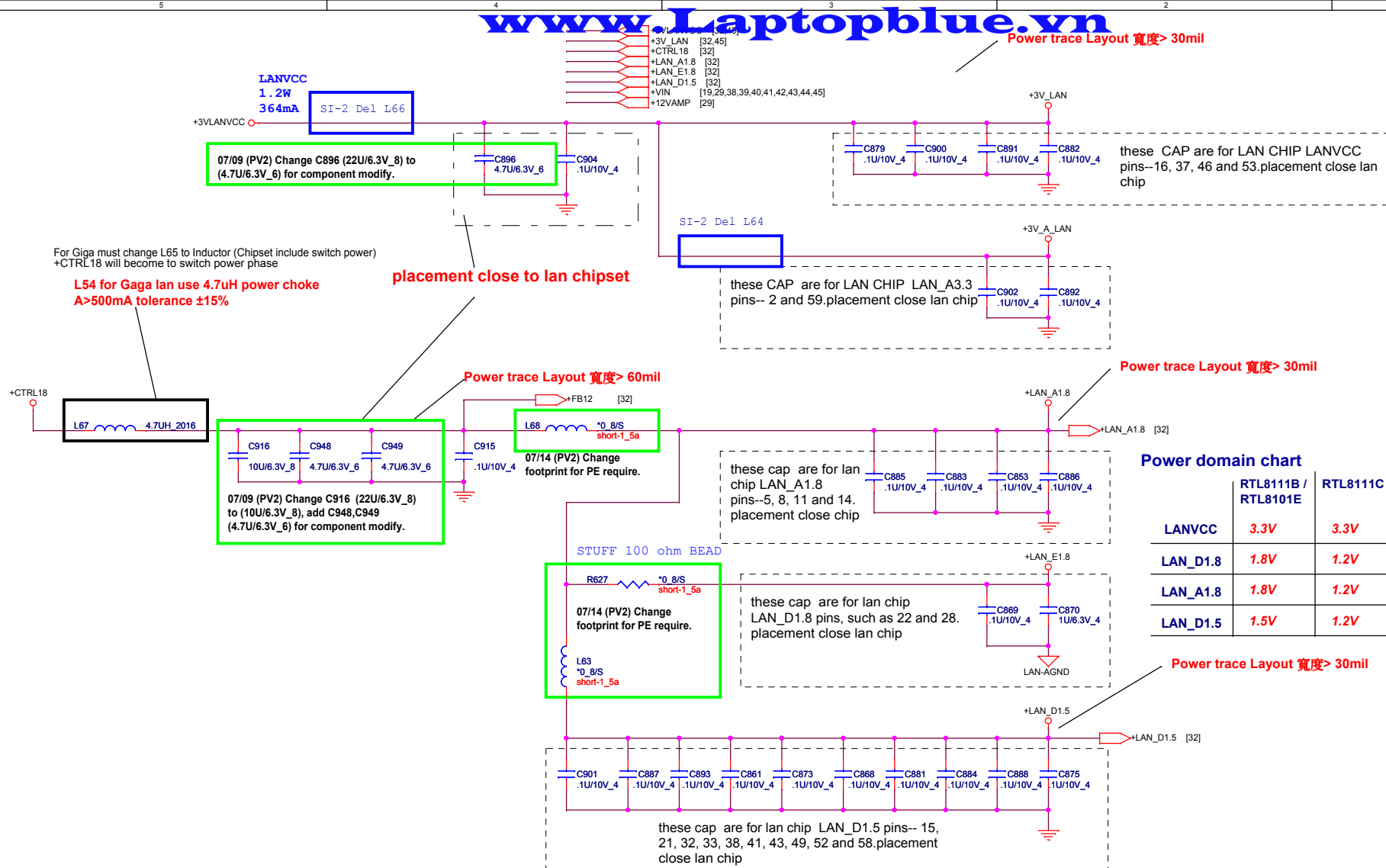


for 93C56 used. NC if 93C46 is used.



PROJECT : UT6
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Size Custom Document Number
RTL8111C & RJ45
Date: Friday, July 18, 2008 Sheet 32 of 46 Rev E3A



Power domain chart

	RTL8111B / RTL8101E	RTL8111C
LANVCC	3.3V	3.3V
LAN_D1.8	1.8V	1.2V
LAN_A1.8	1.8V	1.2V
LAN_D1.5	1.5V	1.2V



PROJECT : UT6
Quanta Computer Inc.

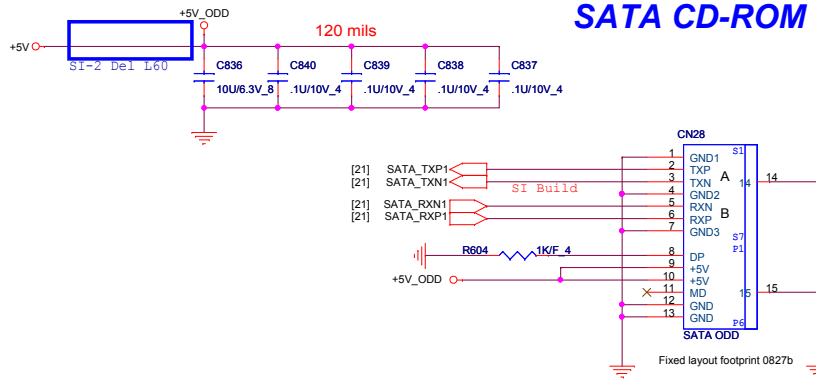
Size
A3

Document Number
LAN Power

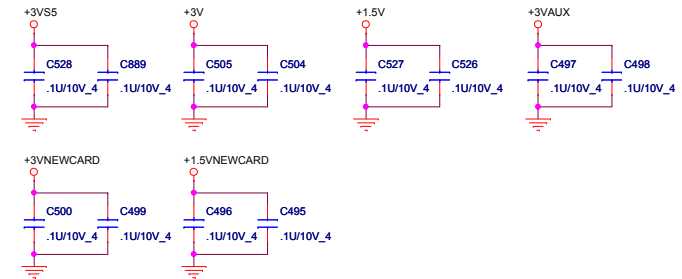
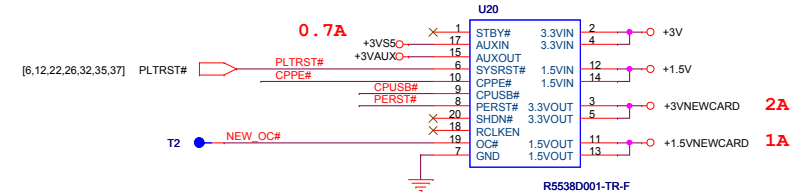
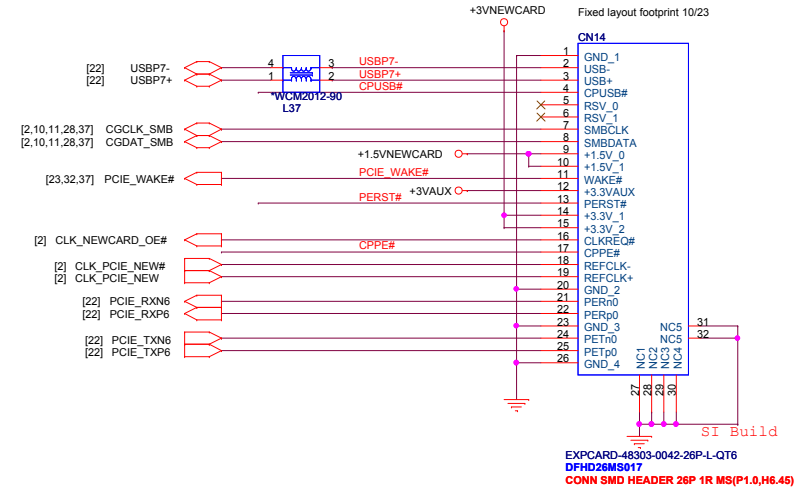
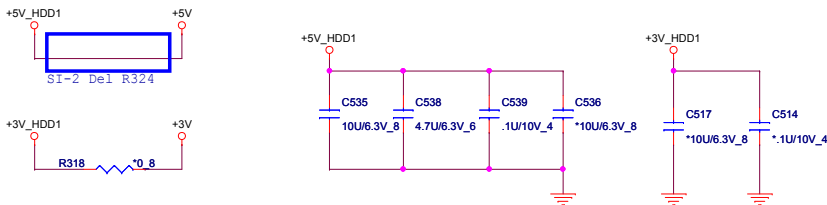
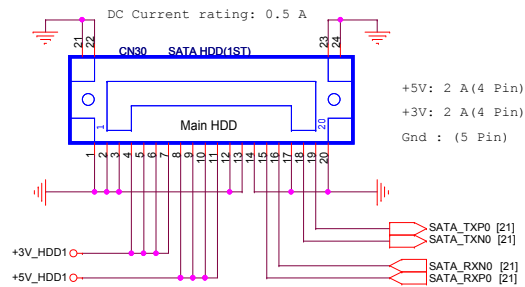
Rev
E3A

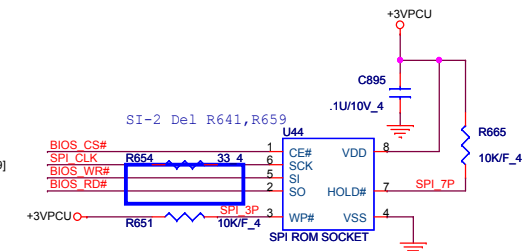
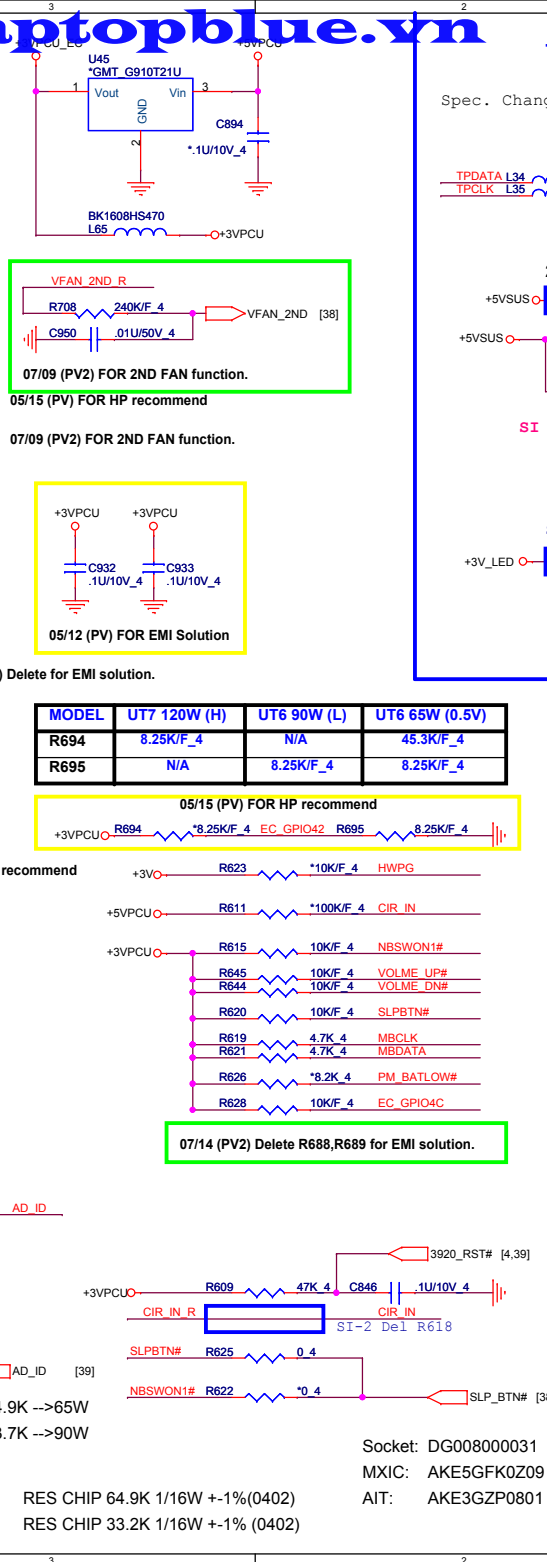
Date: Friday, July 18, 2008

Sheet 33 of 46



SATA_1 CONNECTOR

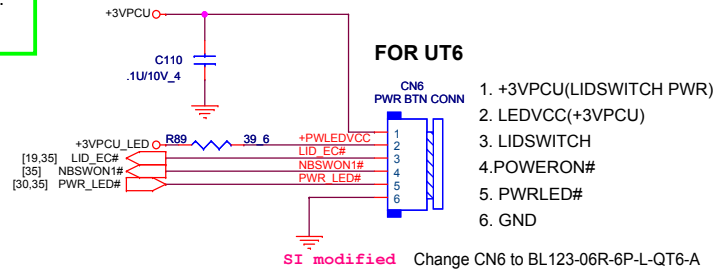
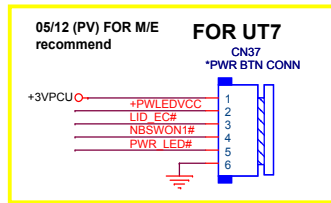
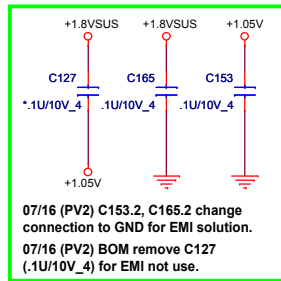




07/14 (PV2) Delete R688,R689 for EMI solution.

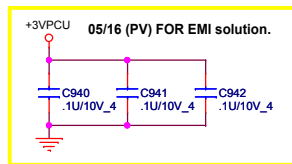
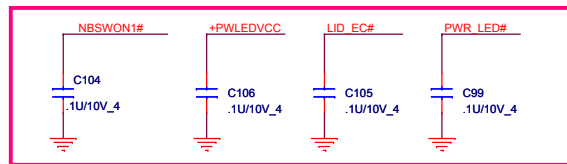
Size Custom	Document Number KB3926/ROM/TP	Rev E3A
Date: Friday, July 18, 2008	Sheet 35 of 46	

Socket: DG008000031
MXIC: AKE5GFK0Z09
AIT: AKE3GZP0801



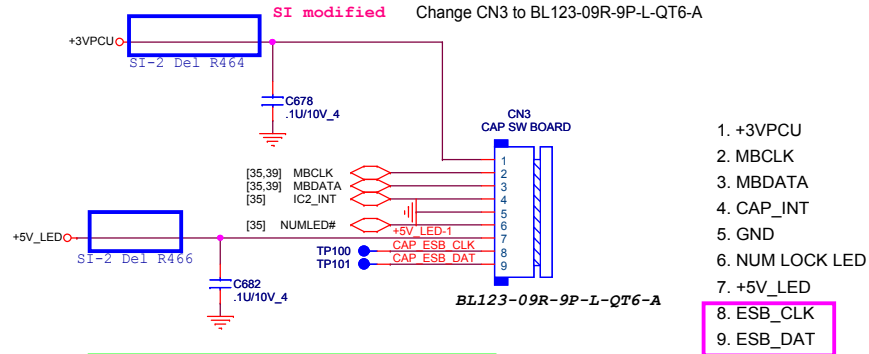
POWER BOTTOM CONNECT

SI modified For EMI

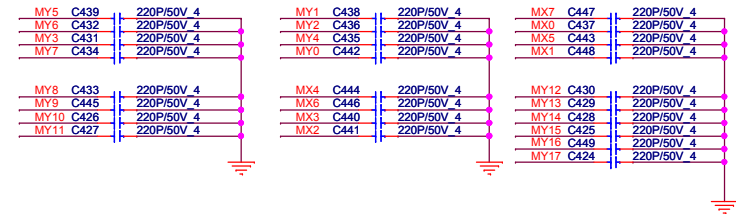
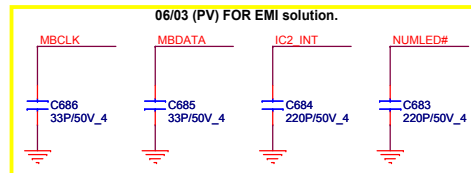


CAP SW CONNECT

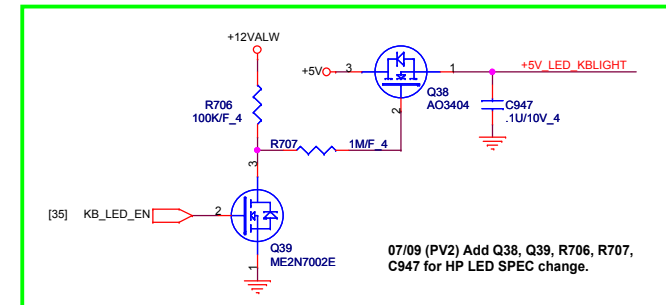
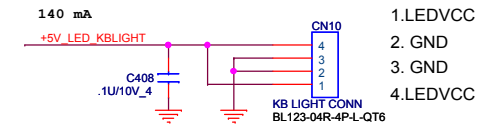
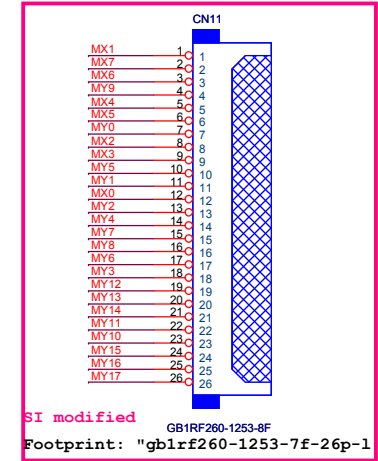
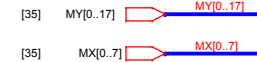
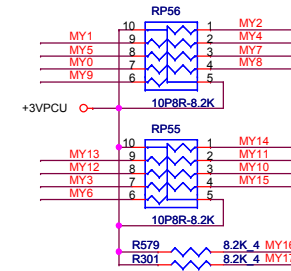
Change CN3 to BL123-09R-9P-L-QT6-A



07/14 (PV2) Delete L69,L70,C922,C923 for EMI solution.



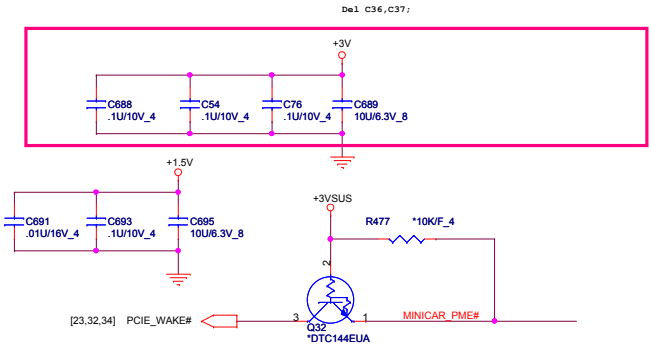
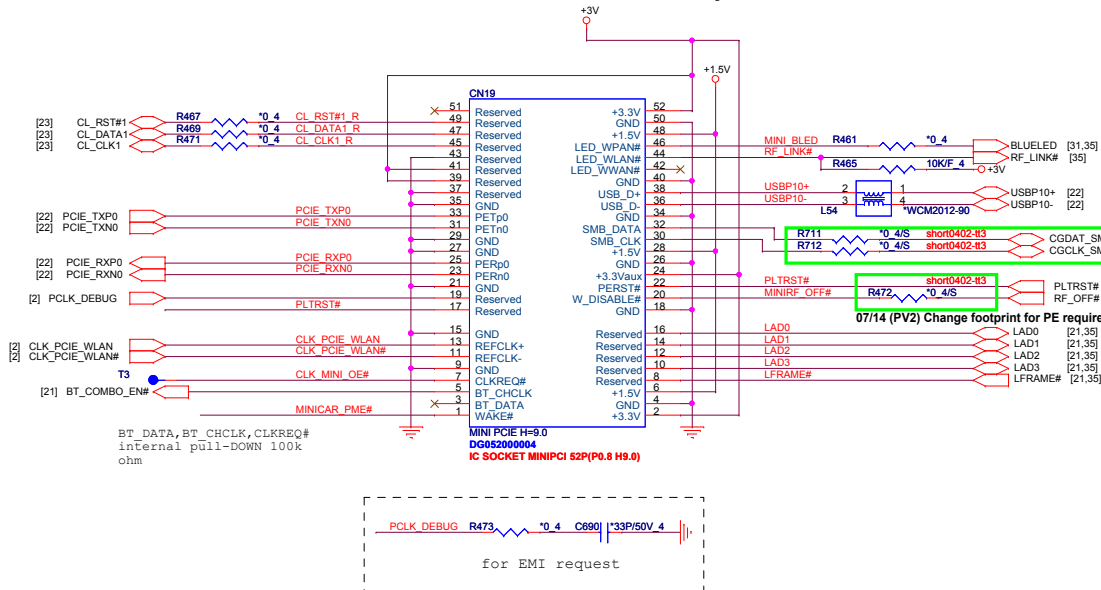
KEYBOARD PULL-UP



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Mini PCI-E Card 1 WLAN

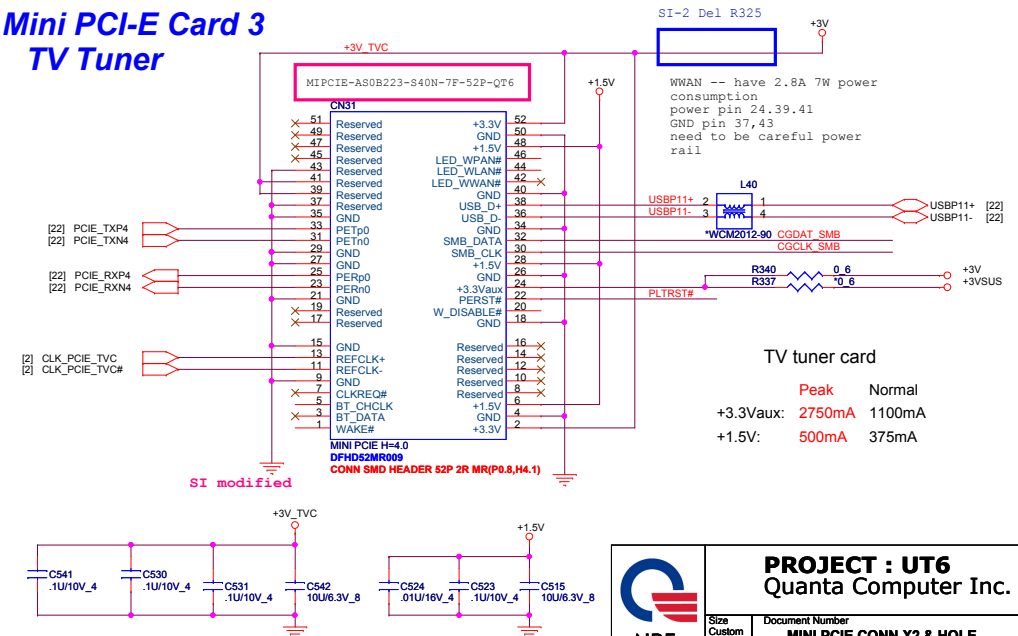
Delete R110,R78

+3V must have a 120mil plane
Each pin 25mil

Mini PCI-E Card 2 ROBSON

07/09 (PV2) Delete for no support ROBSON card.

Mini PCI-E Card 3 TV Tuner



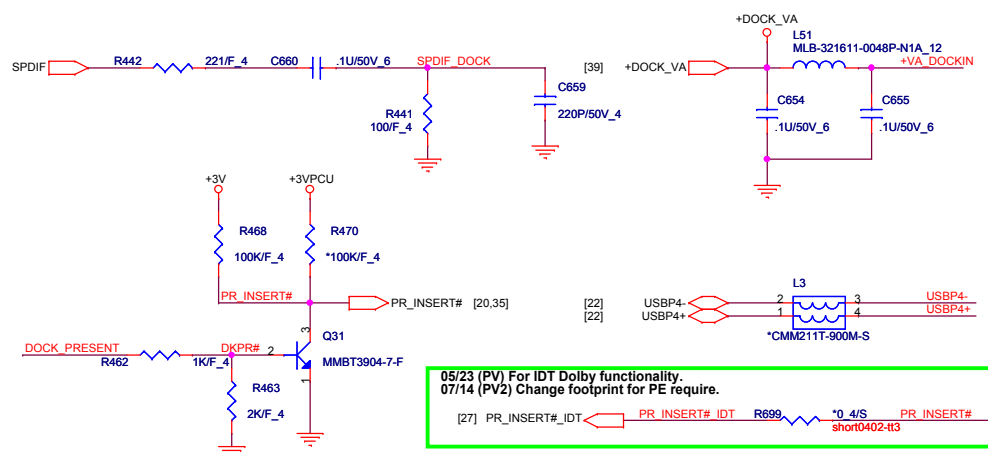
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Size	Document Number	Rev
Custom	MINI PCIE CONN X2 & HOLE	E3A

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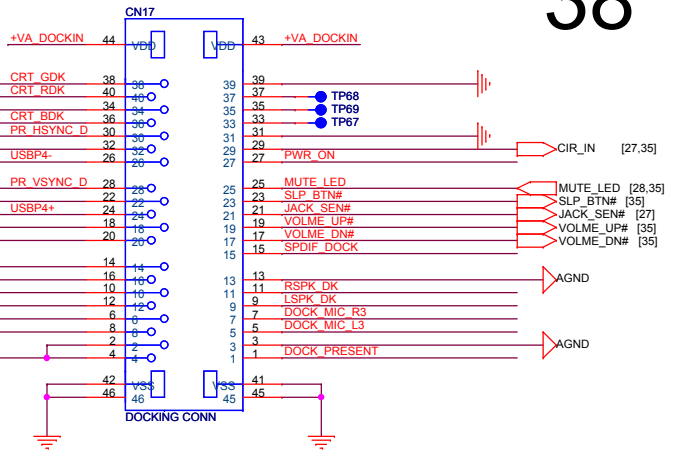
CABLE DOCK

support 6A 200mils
CX000480005



07/14 (PV2) Change footprint for PE require.

- [20] DDCDAT2
- [20] PR_HSYNC
- [20] DDCCLK2
- [20] PR_VSYNC

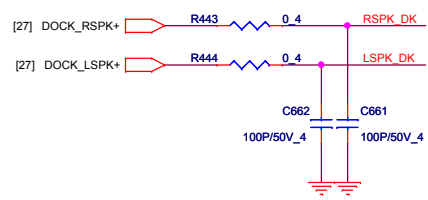
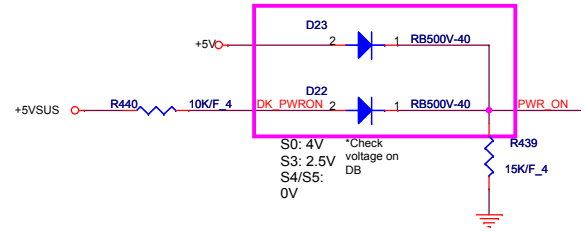


05/23 (PV) For IDT Dolby functionality.
07/14 (PV2) Change footprint for PE require.

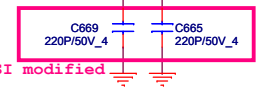
[27] PR_INSERT#_IDT

SI-2 Modified

Change to RB500 as Current loss

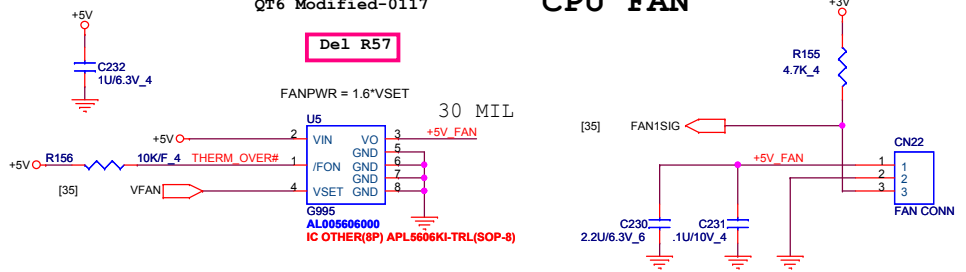


07/14 (PV2) Change footprint for PE require.

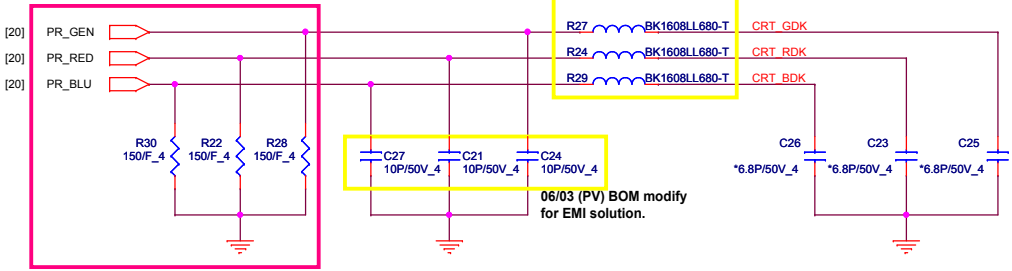


QT6 Modified-0117

CPU FAN

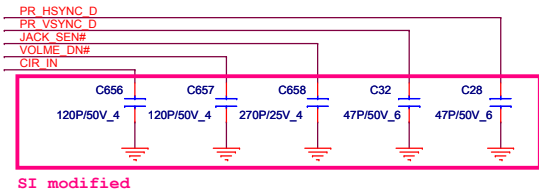
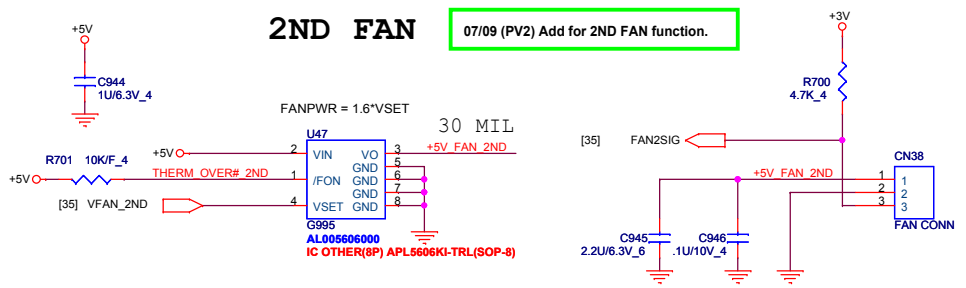


QT6 modified-0117



2ND FAN

07/09 (PV2) Add for 2ND FAN function.

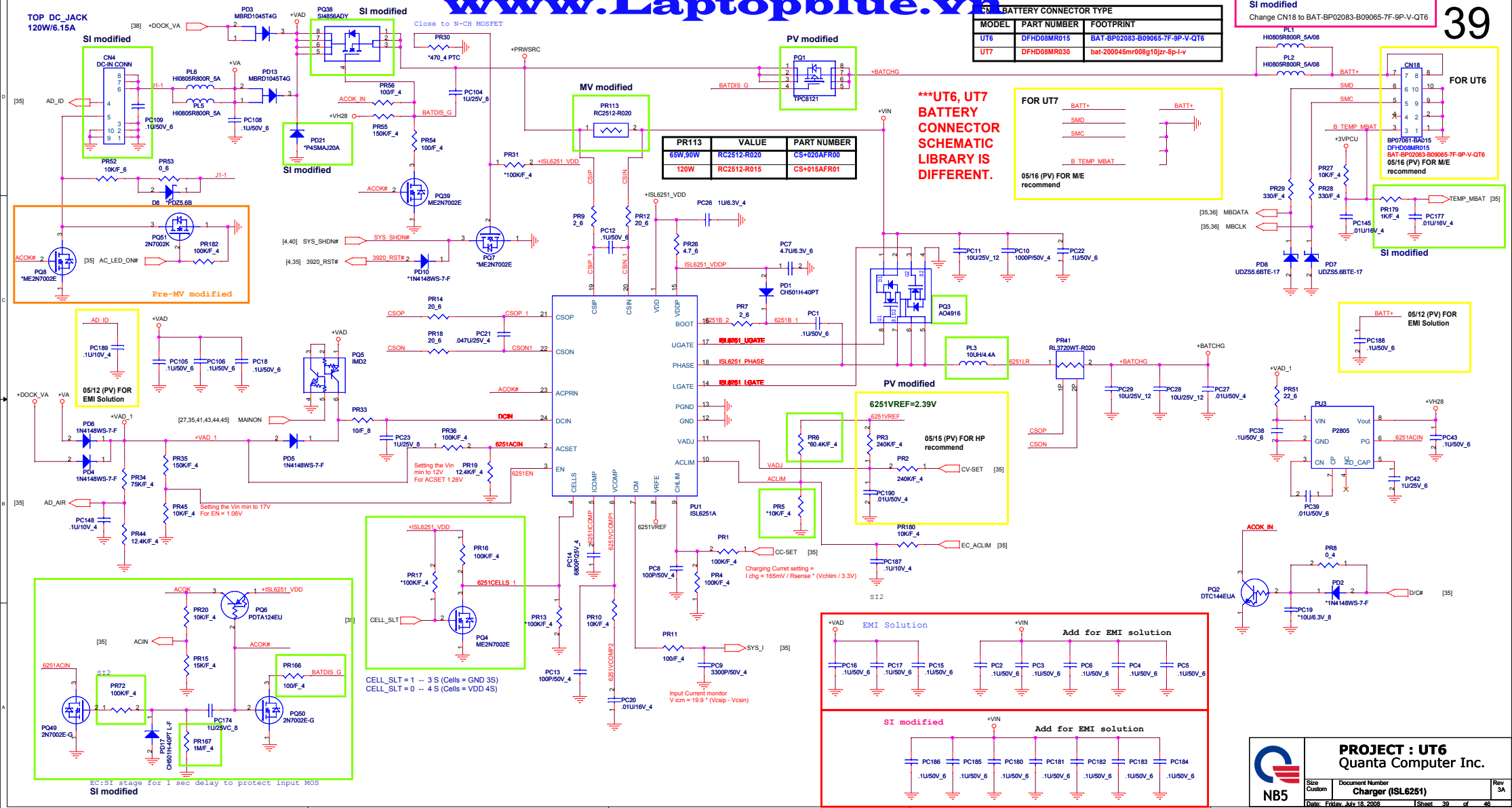


TOP DC_JACK
120W/6.15A

BATTERY CONNECTOR TYPE

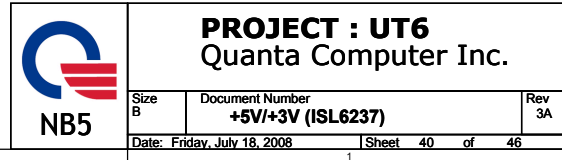
SI modified
Change CN18 to BAT-BP02083-B09065-7F-9P-V-QT6

MODEL	PART NUMBER	FOOTPRINT
UT6	DFHD08MR015	BAT-BP02083-B09065-7F-9P-V-QT6
UT7	DFHD08MR030	bat-200045mr008g10jzr-8p-l-v



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Size Custom	Document Number Charger (ISL6251)
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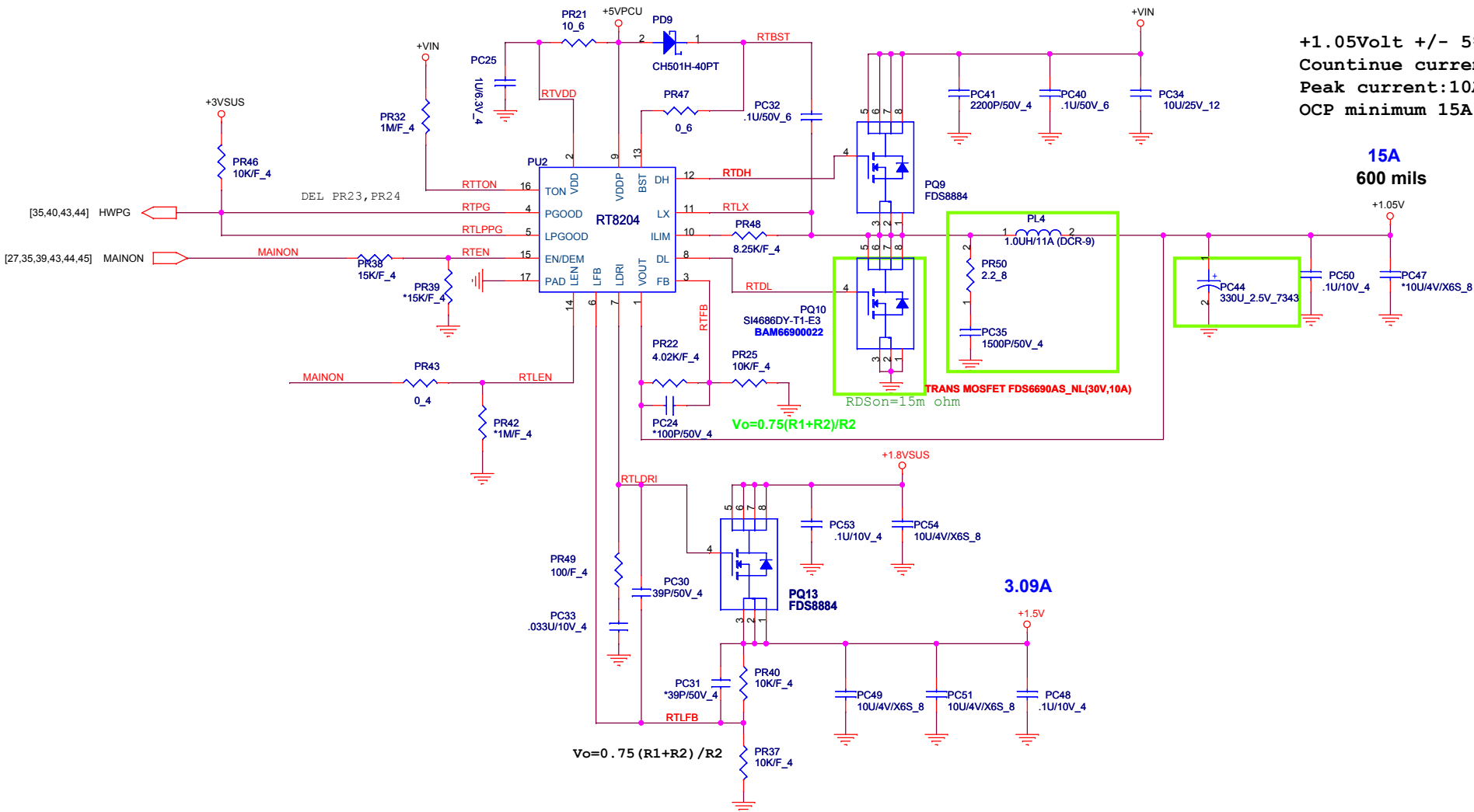


VCCP1.05V & +1.5V

+1.05Volt +/- 5%
 Countinue current:7.5A
 Peak current:10A
 OCP minimum 15A

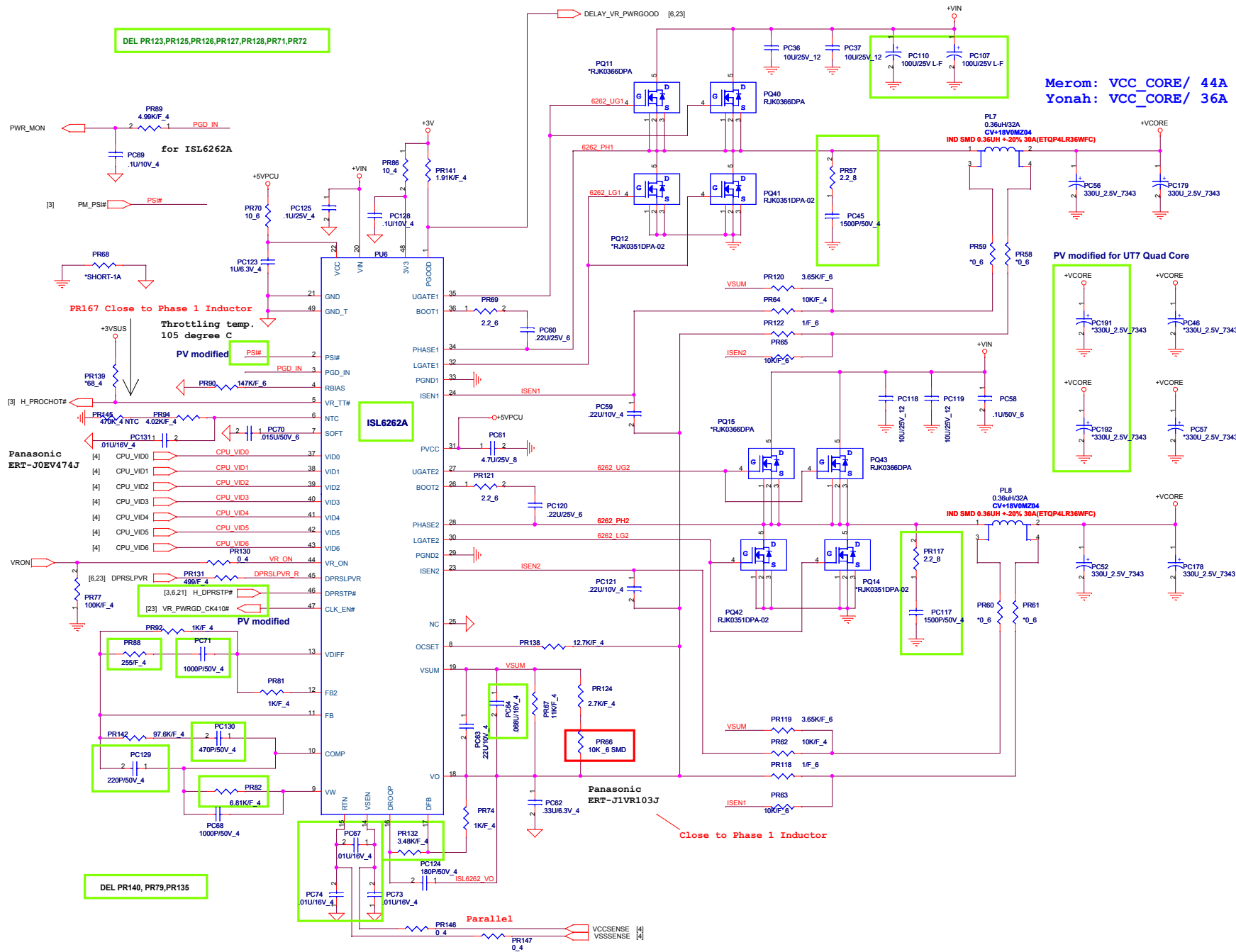
15A
 600 mils

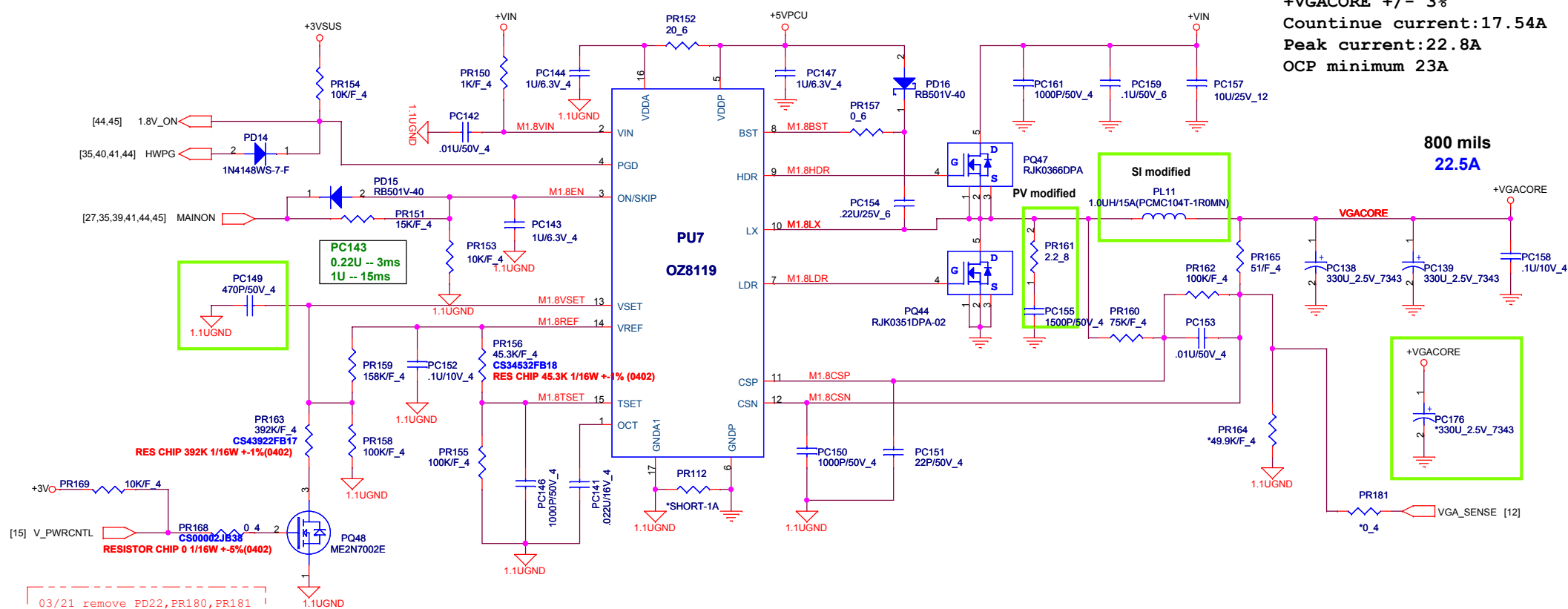
3.09A



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Size B	Document Number	Rev
	+1.05V/+1.5V (RT8204)	3A
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+VGACORE +/- 3%
Countinue current:17.54A
Peak current:22.8A
OCP minimum 23A

800 mils
22.5A

VREF=2.75V +/-1.5%

NB9P-GS: PR163=392Kohm
Output = 0.9V

NB9M-GE: PR203=590Kohm
NB9P-GS: PR203=768Kohm

CS45902FB10 RES CHIP 590K 1/16W +-1%(0402)
CS47682FB10 RES CHIP 768K 1/16W +-1%(0402)

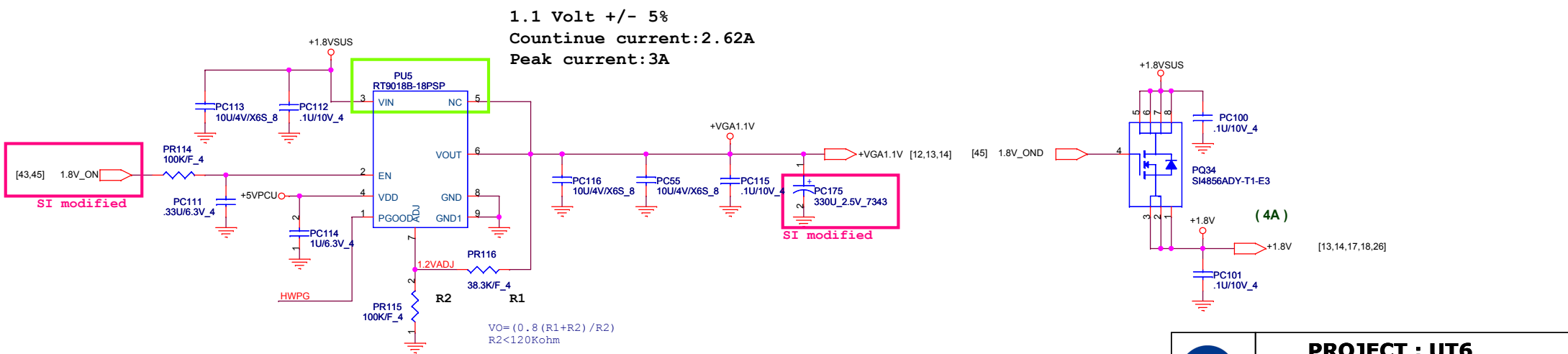
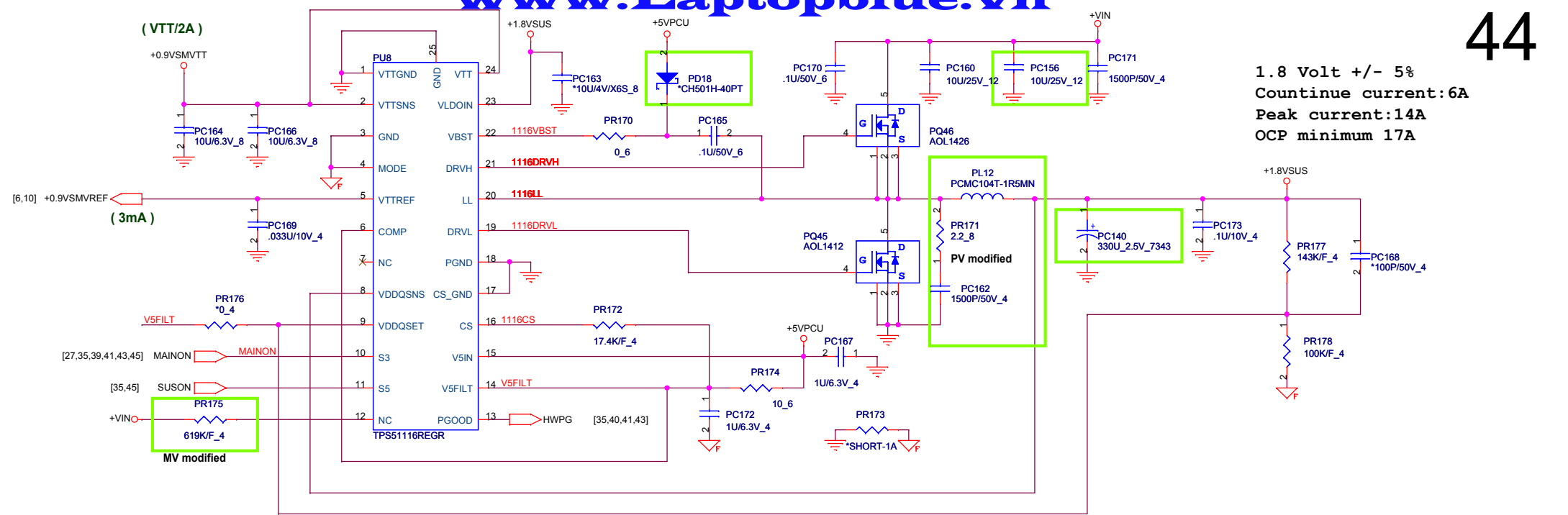
V_PWRCNTL	NB9P-GS
GPI05	
Low	1.05V
High	0.9V

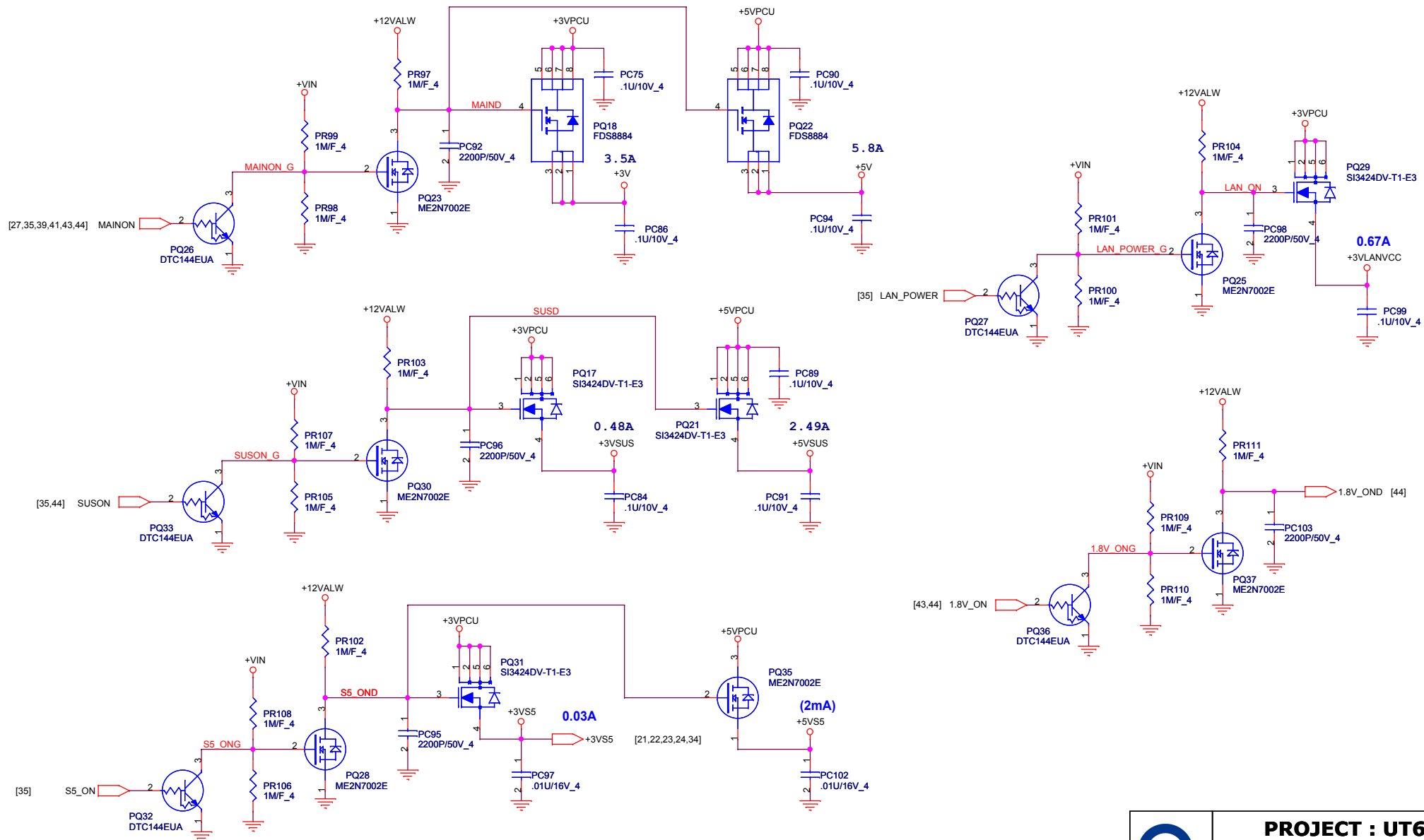
VGA_GPIO6	V_PWCNTL		NB9P-GS	NB9M-GS
GPIO6	GPIO5			
Low	Low	MAX BAT	0.9V	0.9V
Low	High	SD DVD	0.9V	0.9V
High	Low	HD DVD	0.9V	0.9V
High	High	MAX PERF	1.05V	1.09V



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Size B	Document Number	Rev
	DISCHARGE/3VS5/5VS5/LAN	3A

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	Voltage level	AC MODE				DC MODE			
		S0	S3	S4	S5	S0	S3	S4	S5
+3VPCU	3.3V +/- 5%	V	V	V	V	V	V	V	V
+5VPCU	5V +/- 5%	V	V	V	V	V	V	V	V
+3VRTC	3.3V +/- 5%	V	V	V	V	V	V	V	V
+3VS5	3.3V +/- 5%	V	V	V	V	V	V		
+5VS5	5V +/- 5%	V	V	V	V	V	V		
+3VSUS	3.3V +/- 5%	V	V			V	V		
+5VSUS	5V +/- 5%	V	V			V	V		
+1.8VSUS	1.8V +/- 5%	V	V			V	V		
+0.9VSMVTT	0.9V +/- 5%	V	V			V	V		
+1.5V	1.5V +/- 5%	V				V			
+1.05V	1.05V +/- 5%	V				V			
+VCORE	0.9~1.15V	V				V			
+VGA_CORE	0.9~1.2V	V				V			
+VGA1.1V	1.1V +/- 5%	V				V			
+1.8V	1.8V +/- 5%	V				V			
+3VLAVCC	3.3V +/- 5%	V				V			



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