

Appendix B:Schematic Diagrams

This appendix has circuit diagrams of the *D400S/D410S* notebook's PCB's. The following table indicates where to find the appropriate schematic diagram.

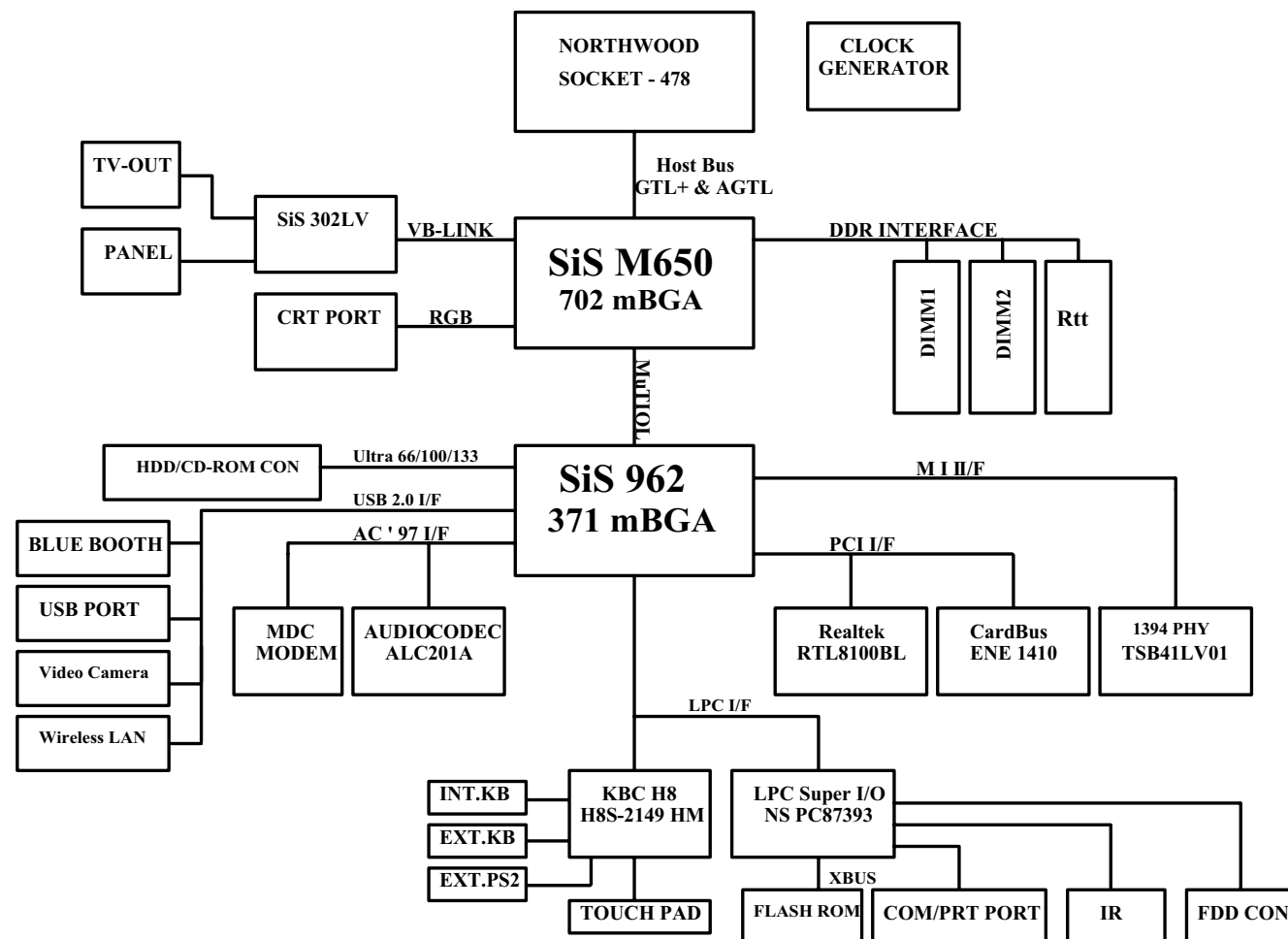
Diagram - Page	Diagram - Page
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Table 1
**Schematic
Diagrams**

Schematic Diagrams

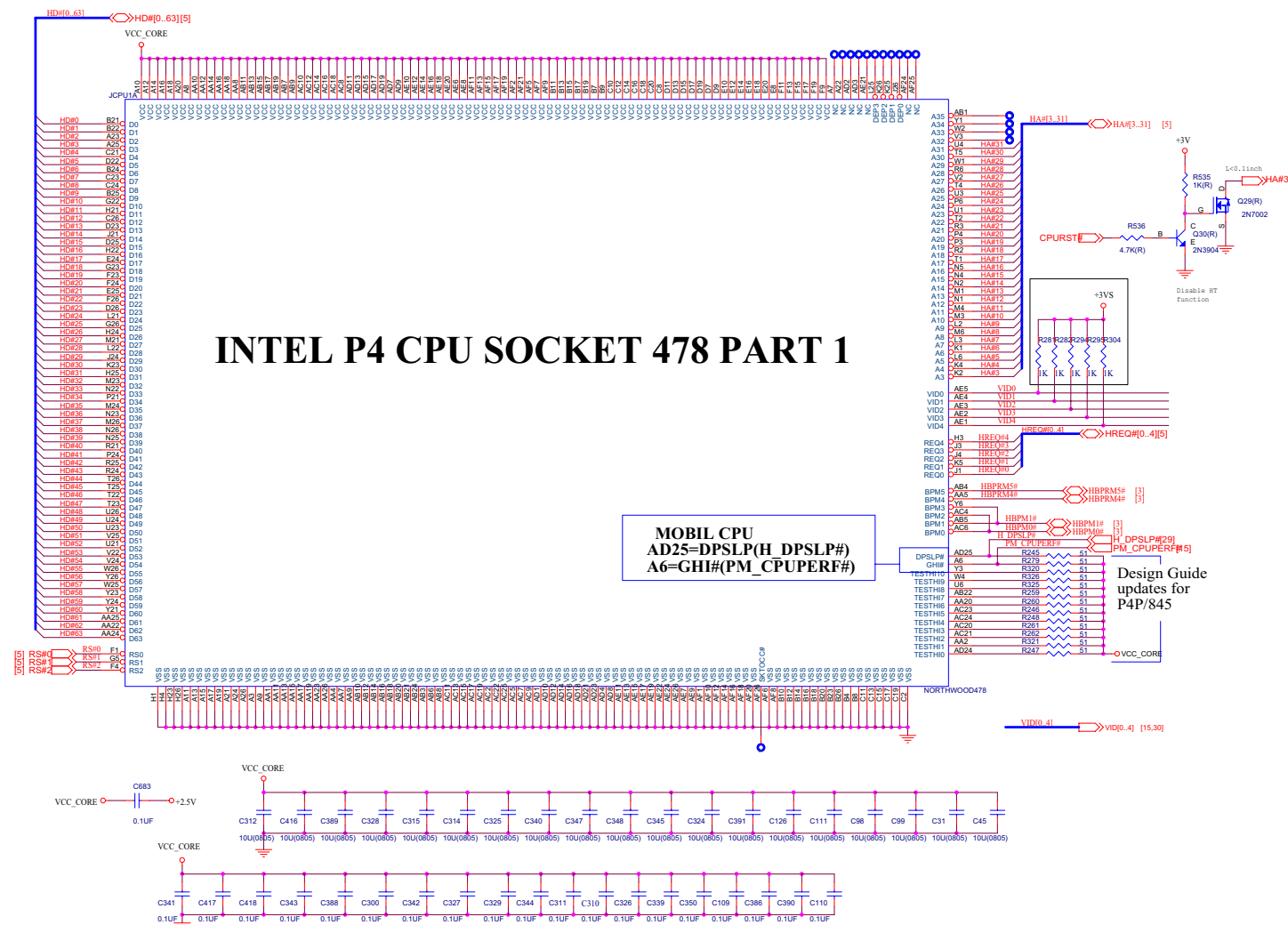
System Block Diagram

D400 System Block Diagram



Sheet 1 of 35
System Block
Diagram

Socket 478 1 of 2

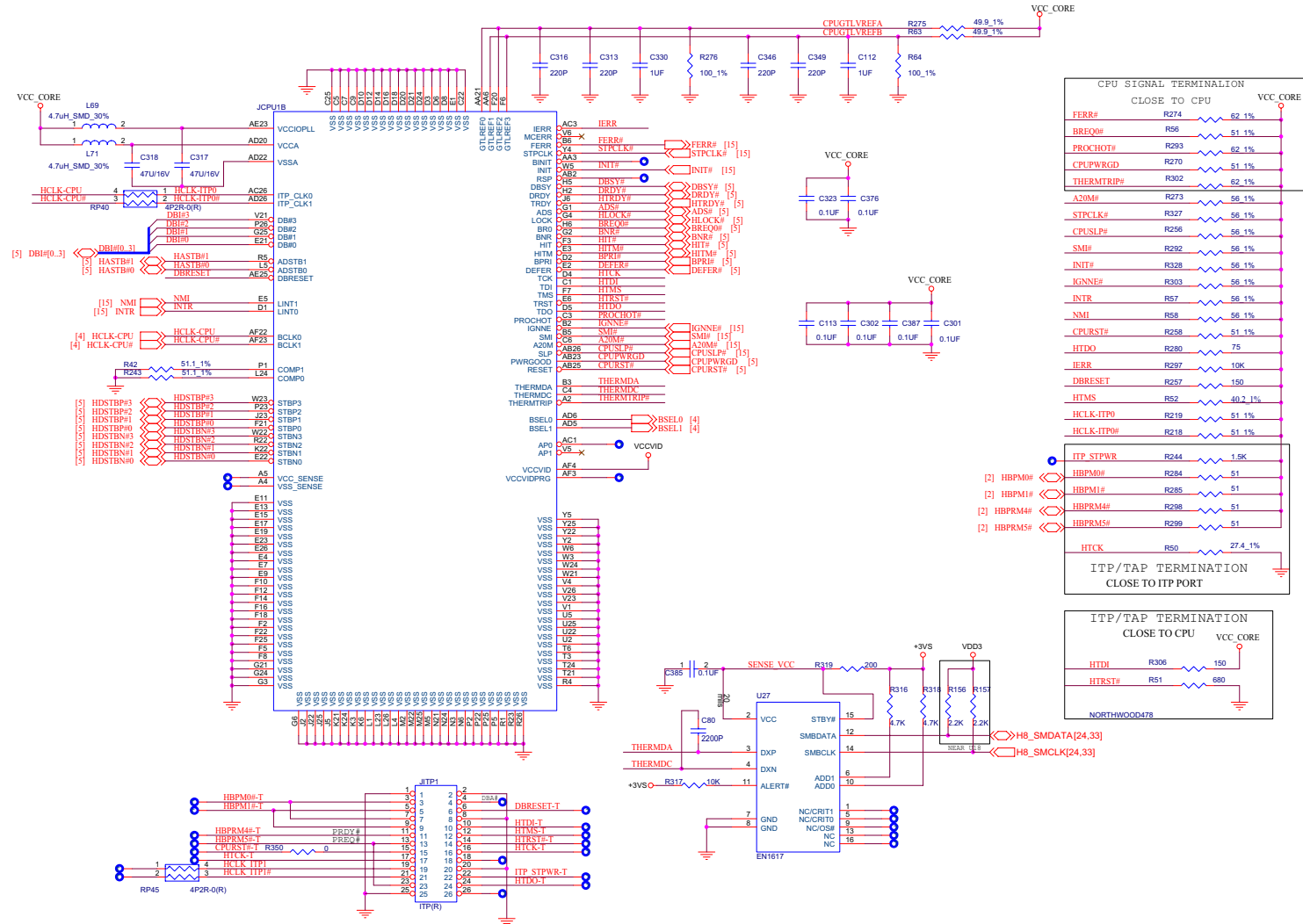


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Socket 478 1 of 2

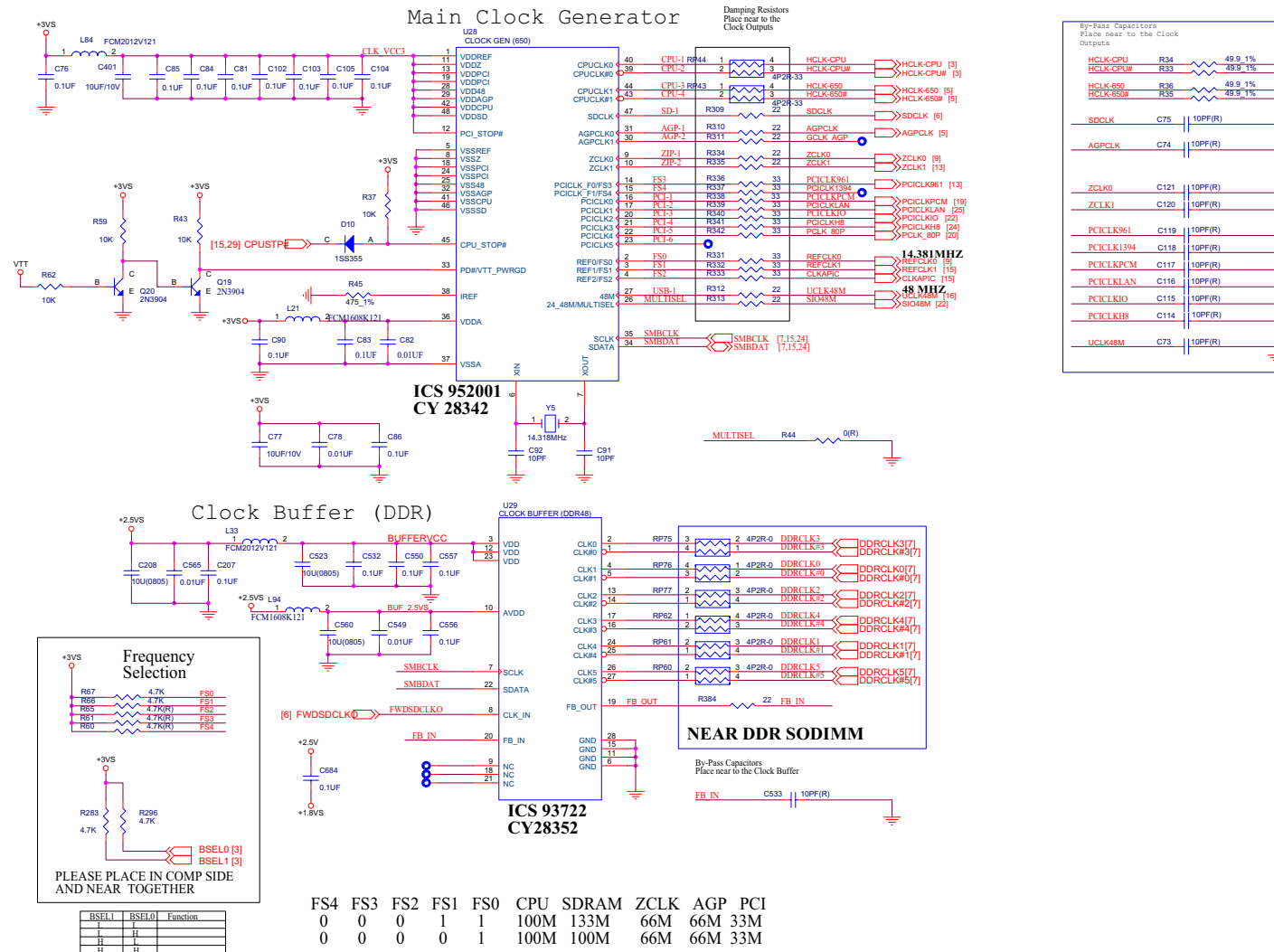
Schematic Diagrams

Socket 478 & ITP 2 of 2

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Socket 478 & ITP
2 of 2



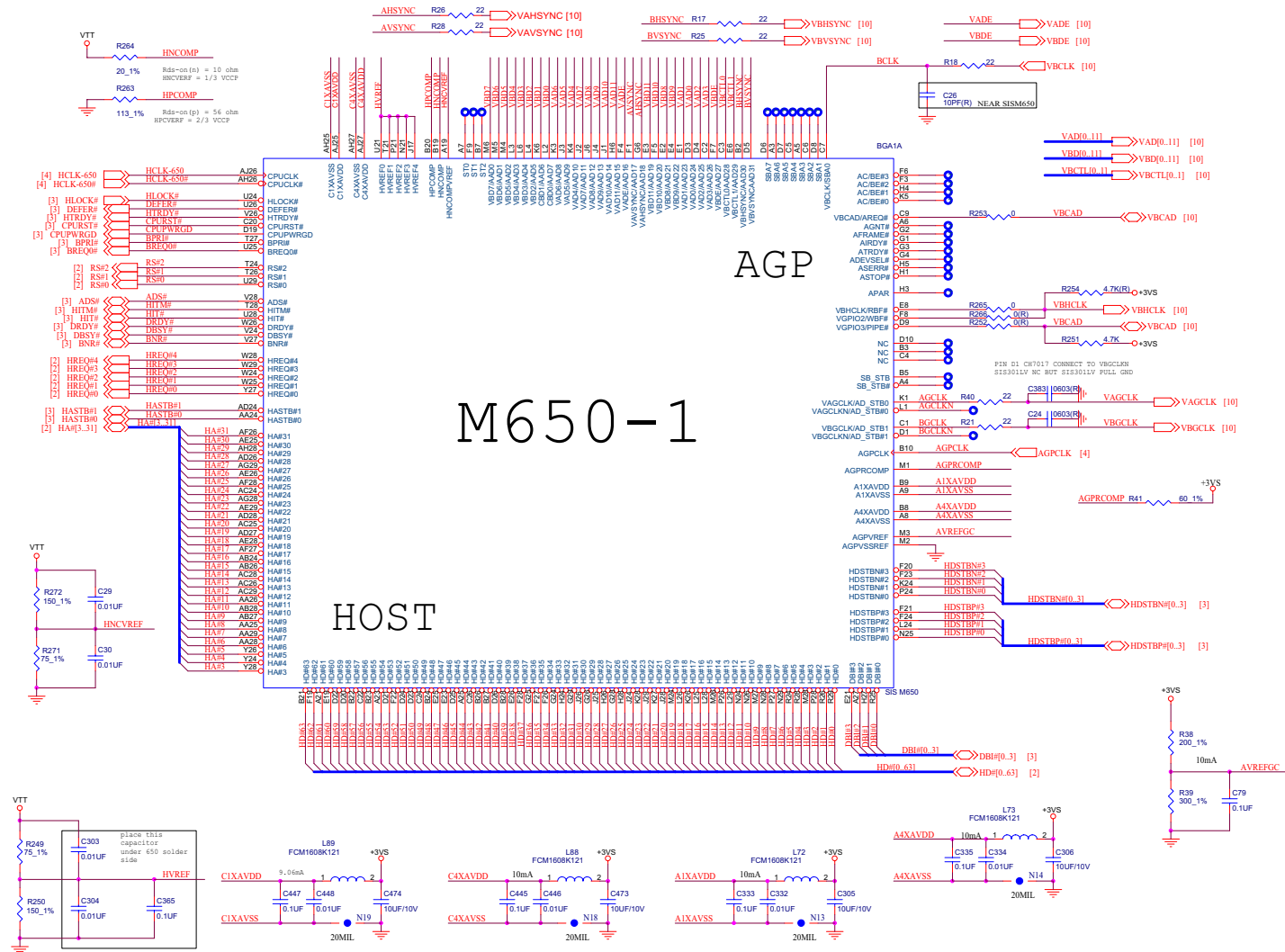
Clock Generator



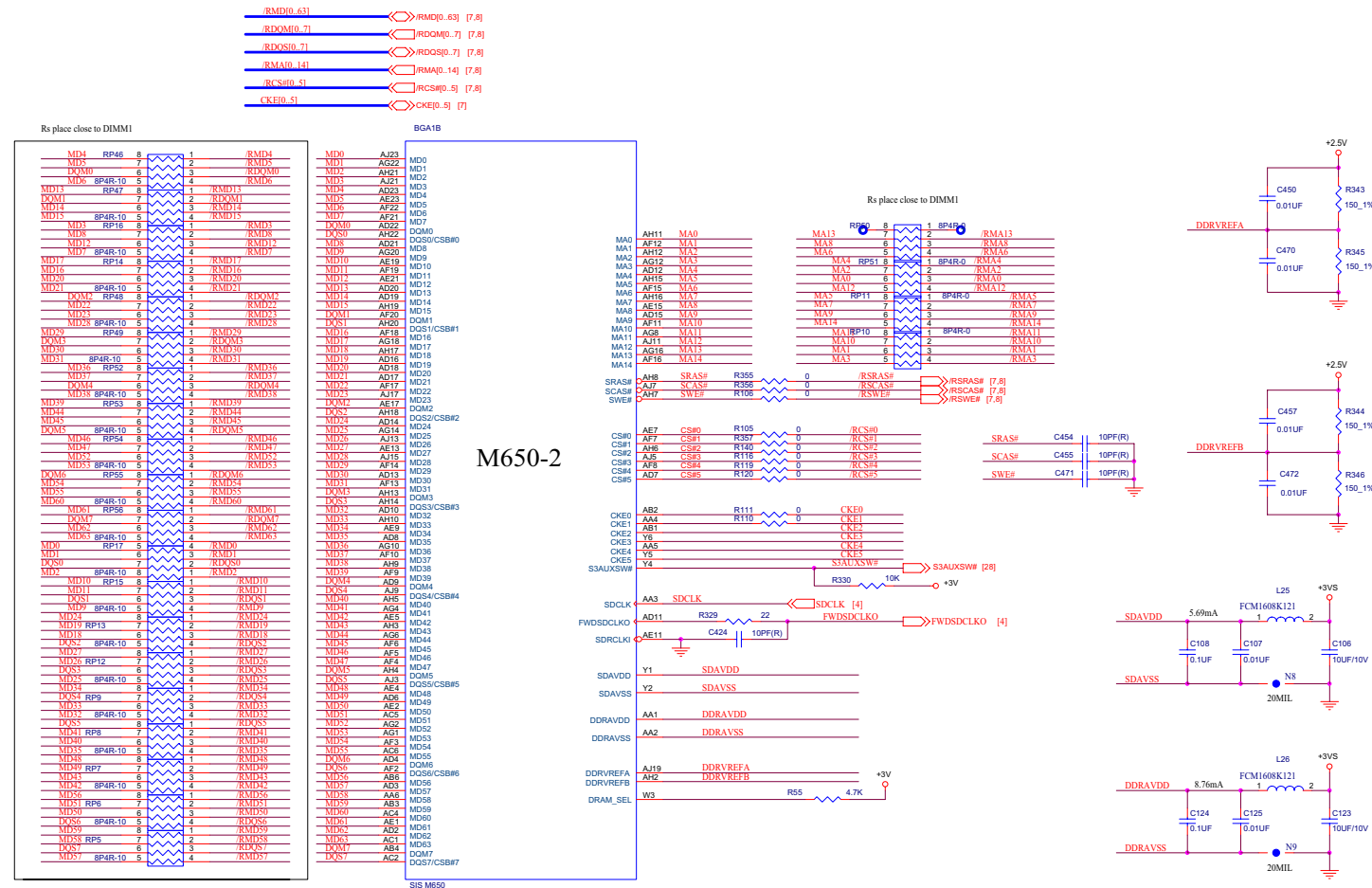
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Clock Generator

M650 (Host/AGP) 1 of 4

M650-1



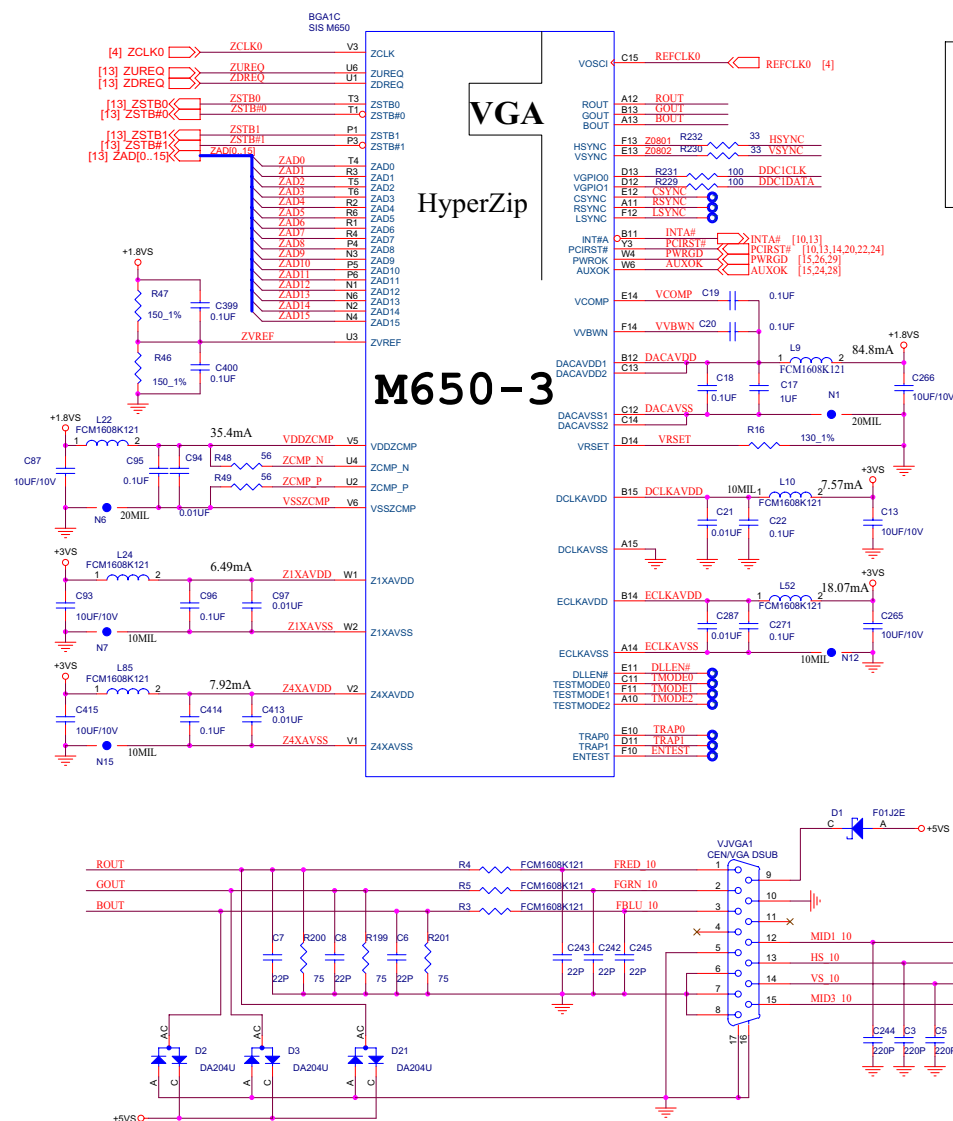
M650 (Memory for DDR) 2 of 4



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M650
(Memory for DDR)
2 of 4

M650 (& CRT Out) 3 of 4

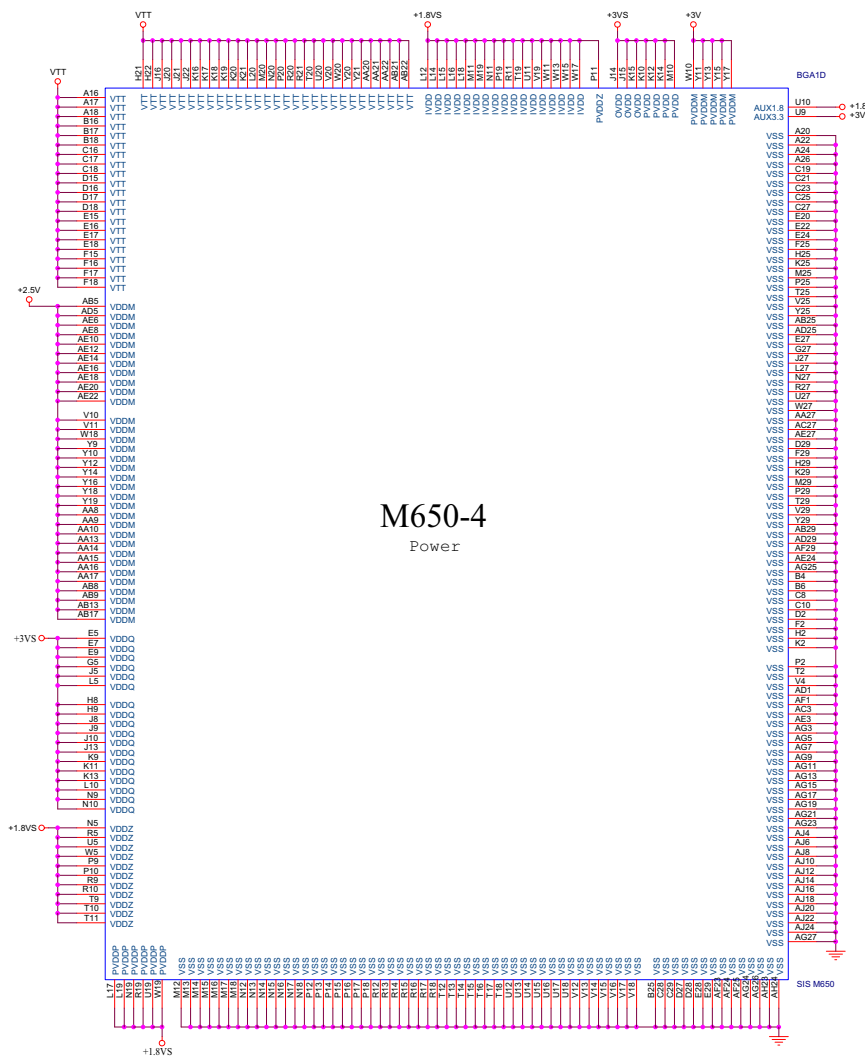
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M650 & CRT Out
3 of 4



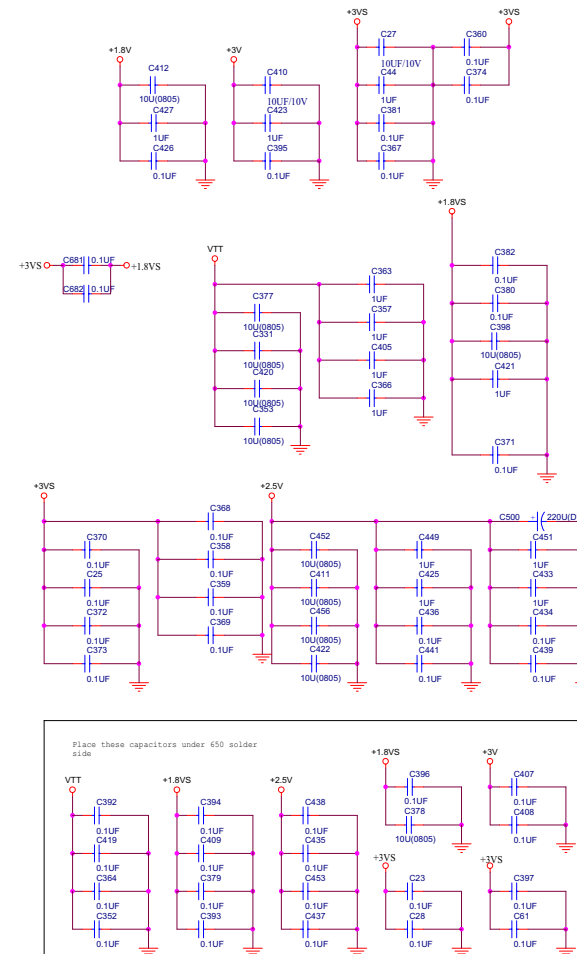
NOTE: This page is for universal PCB design(suitable for both 645 or 650)

	0	1	Default	embedded pull-up (30–50K Ohm)
DLENG _i	enable PLT	disable PLT	0	yes
DRAM_SEI	SDR	DDR	1 (DDR)	yes
TRAP0	NB debug mode	0	0	yes
TRAP1	TV selection, NTSC/PAL(0/1)	0	0	
CSYNC	enable VB	0	0	
RSYNC	enable VCA interface	1	1	
LSYNC	enable panel link	0	0	

M650 (Power) 4 of 4



M650-4
Power

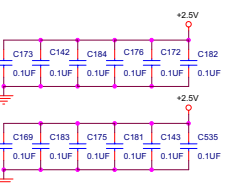
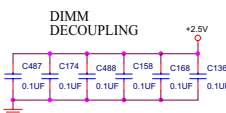
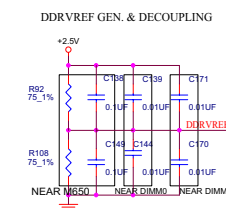
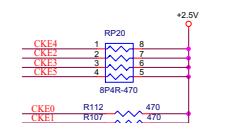
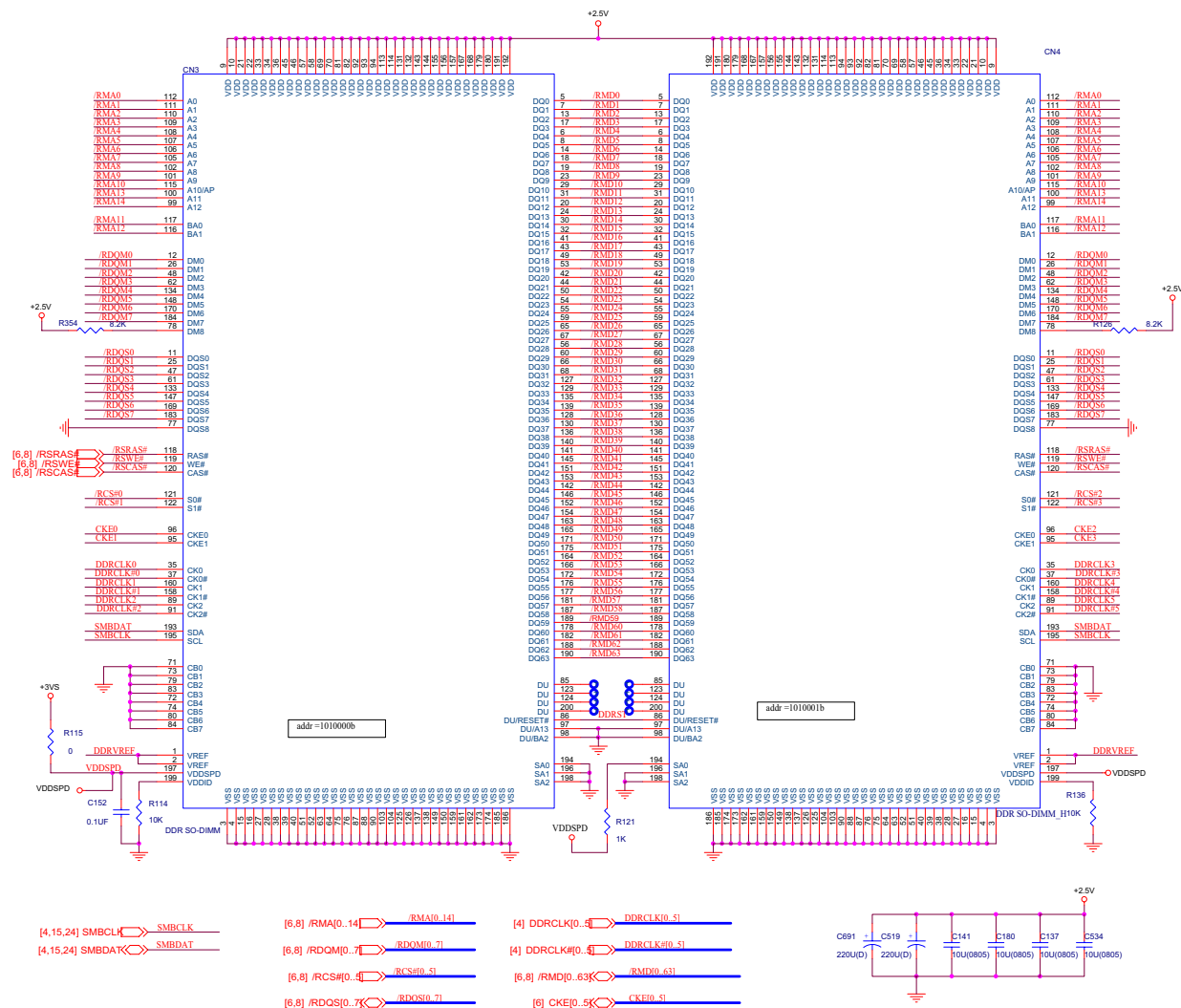


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M650 (Power)
4 of 4

DDR Memory DIMM

Schematic Diagrams

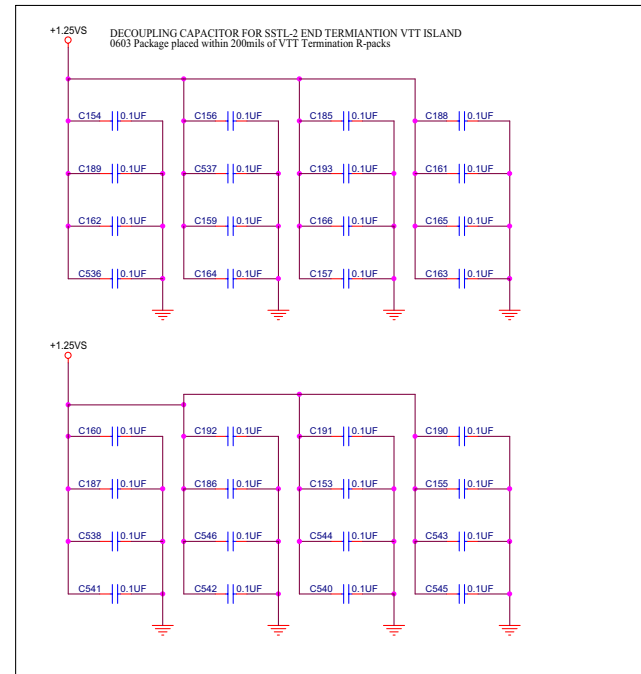
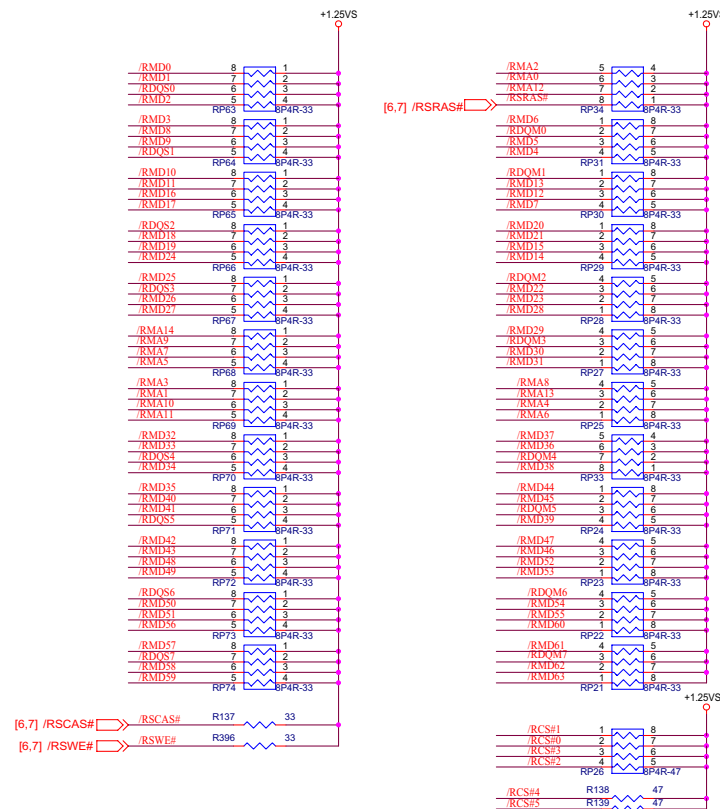
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DDR Memory DIMM



DDR SSTL-2 Termination

SSTL-2 Termination Resistors

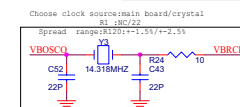
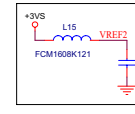
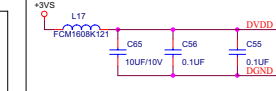
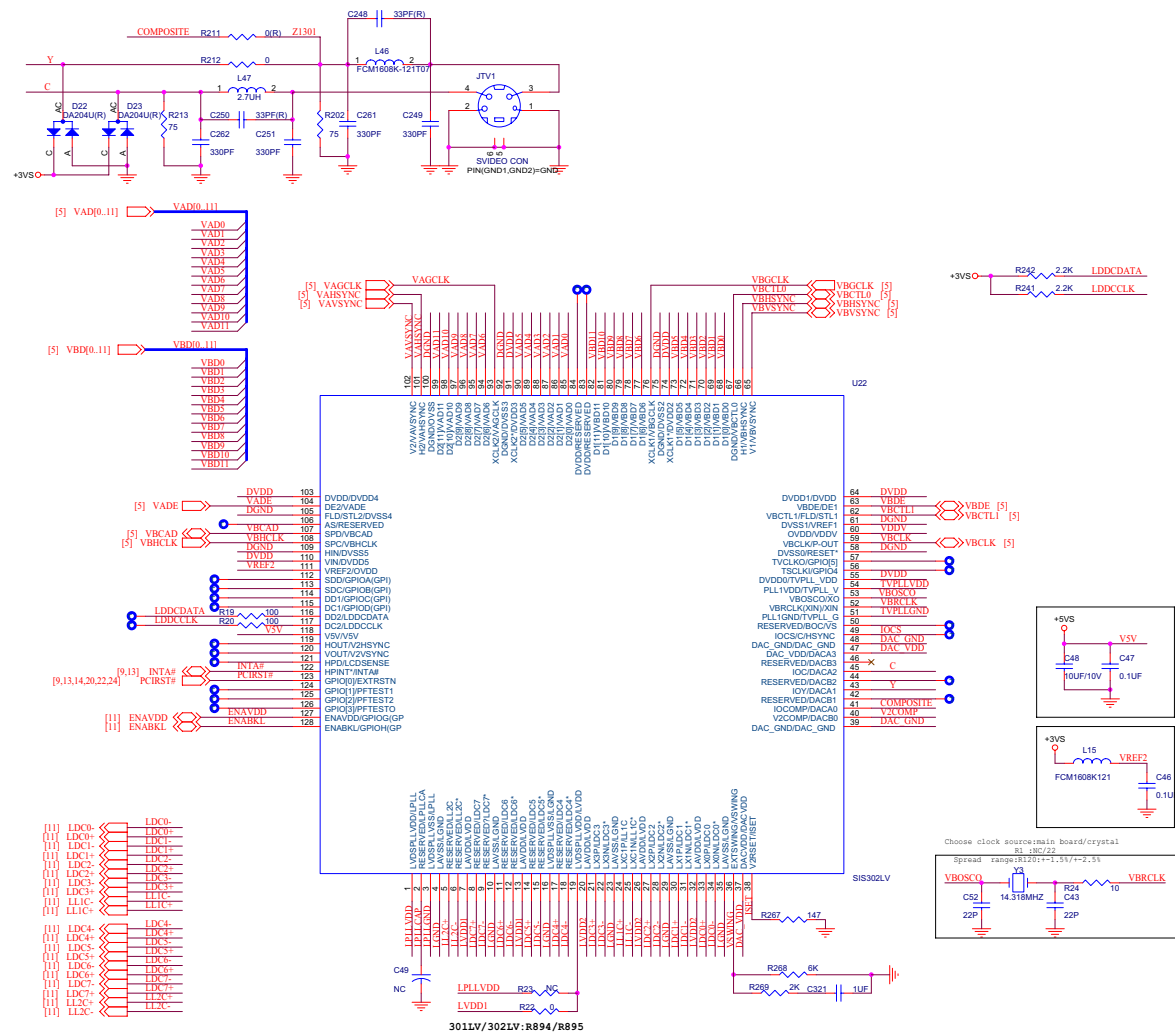
	DDR	R _s	R _{tt}
MD/DQM/(DQS)	SSTL-2	10	33
MA/Control	SSTL-2	0	33
CS	SSTL-2	0	47
CKE	OD 2.5V		47



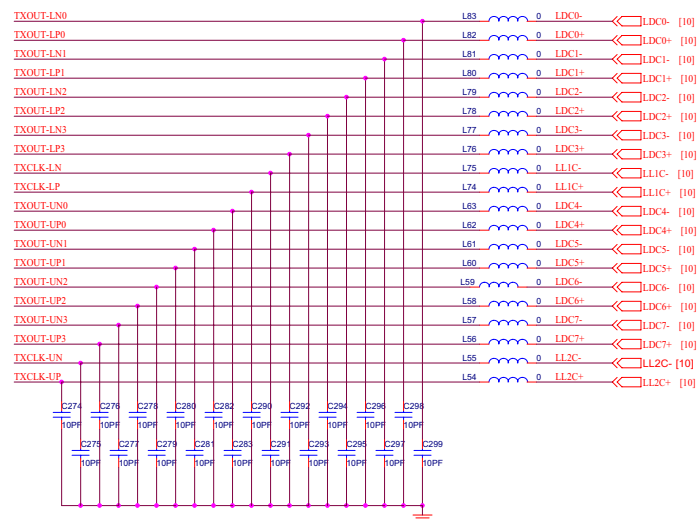
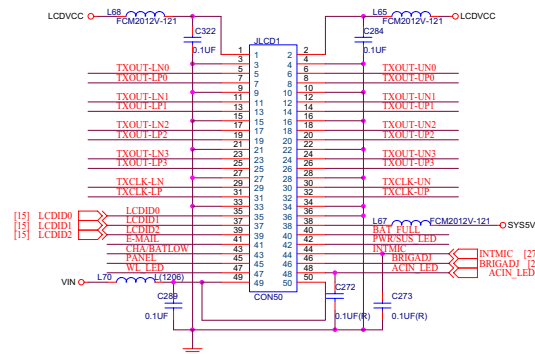
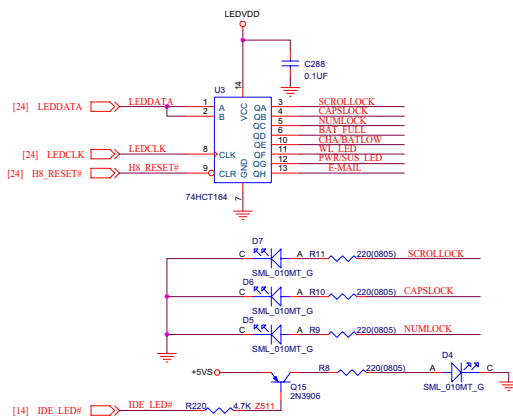
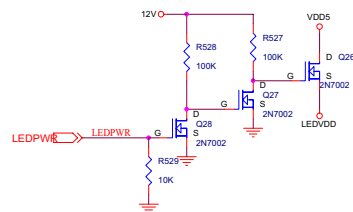
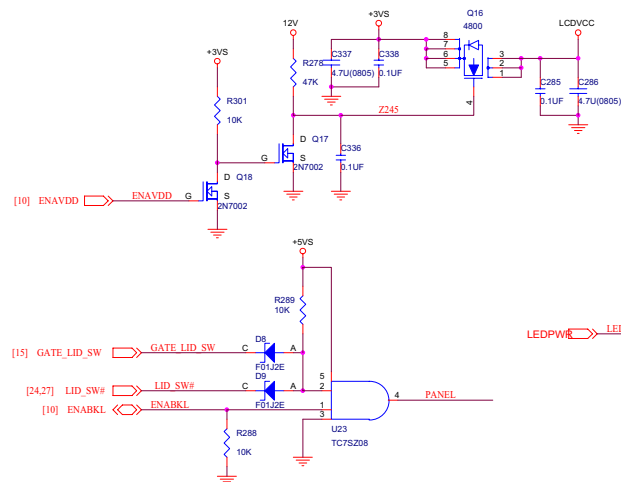
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DDR SSTL-2
Termination

LVDS Interface (SiS302LV)

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LVDS interface
(Sis302LV)



Panel Con & LED Indicator



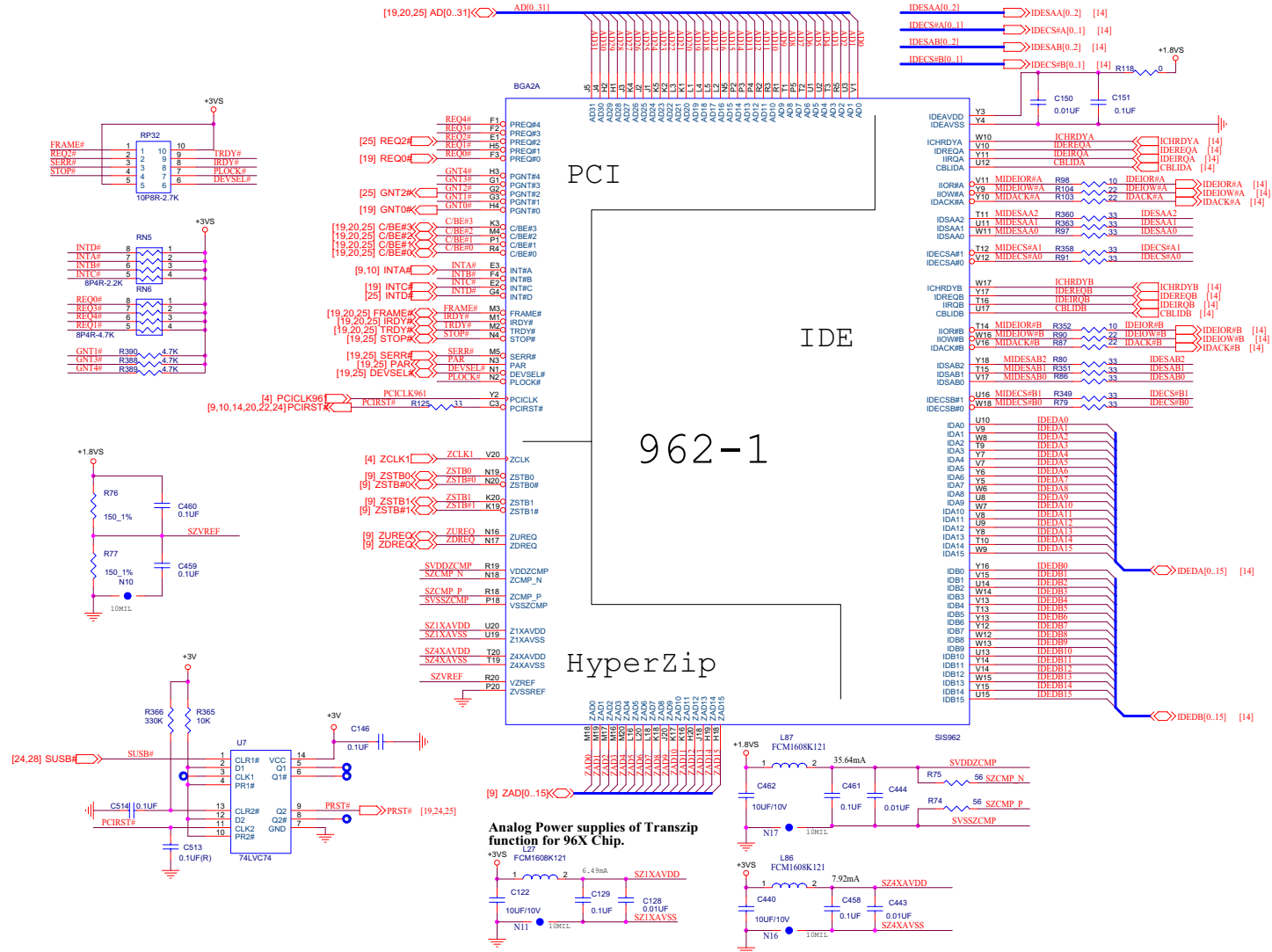
Sheet 12 of 35
Panel Con & LED
Indicator

B. Schematic Diagrams

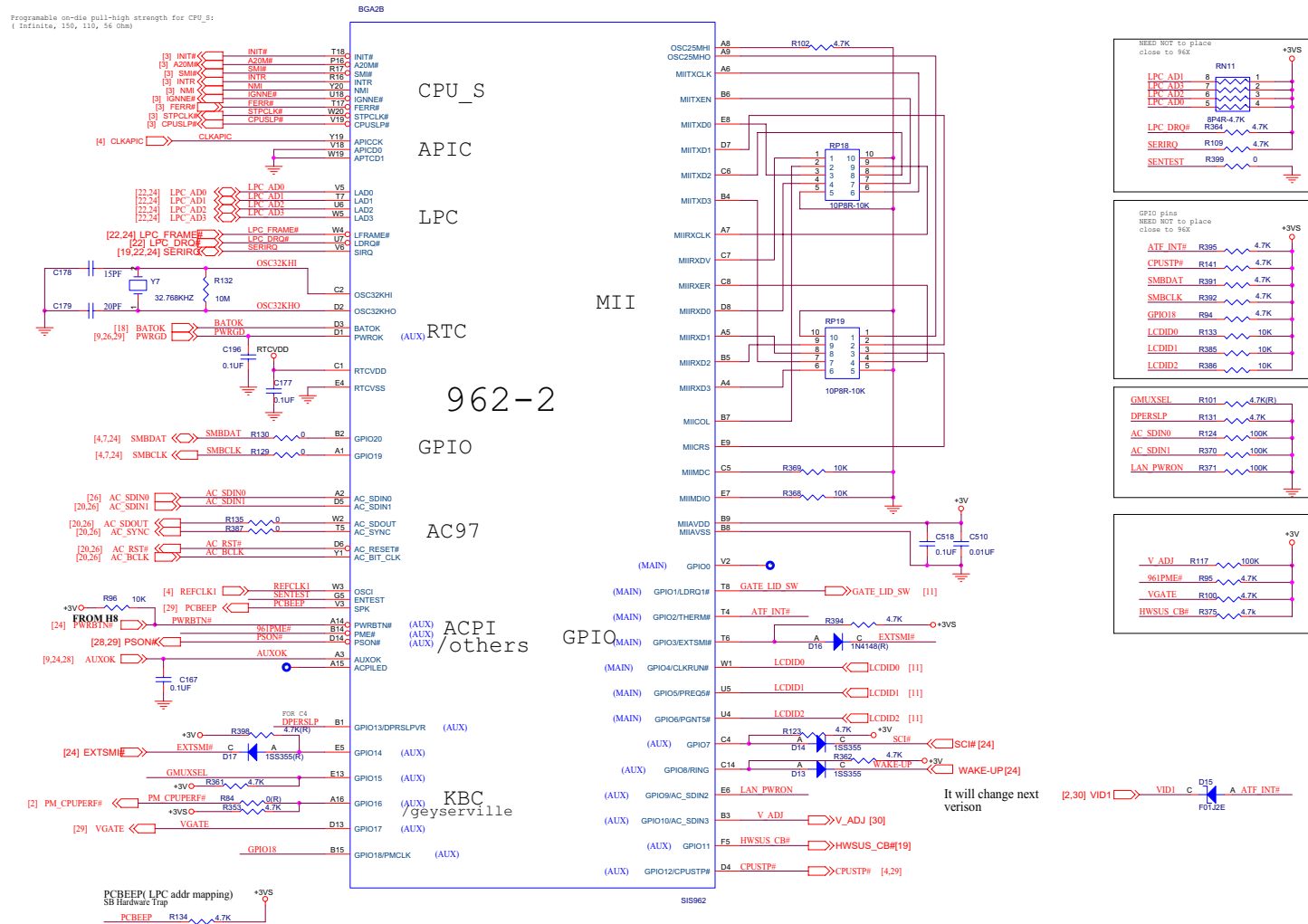
Schematic Diagrams

962 (PCI/IDE/HyperZip) 1 of 4

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962 (PCI/IDE/
HyperZip)
1 of 4



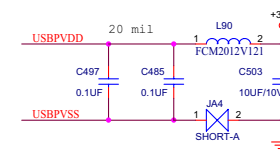
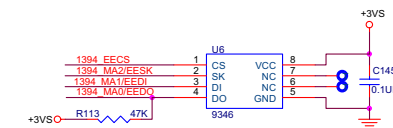
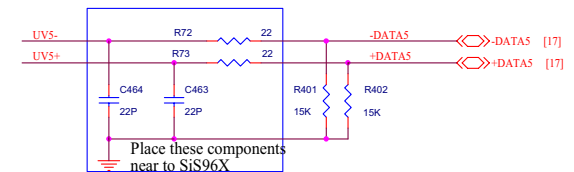
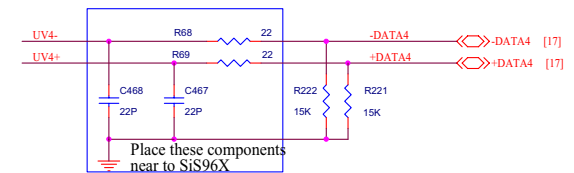
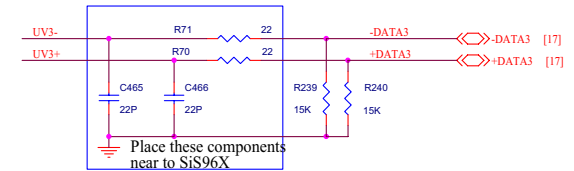
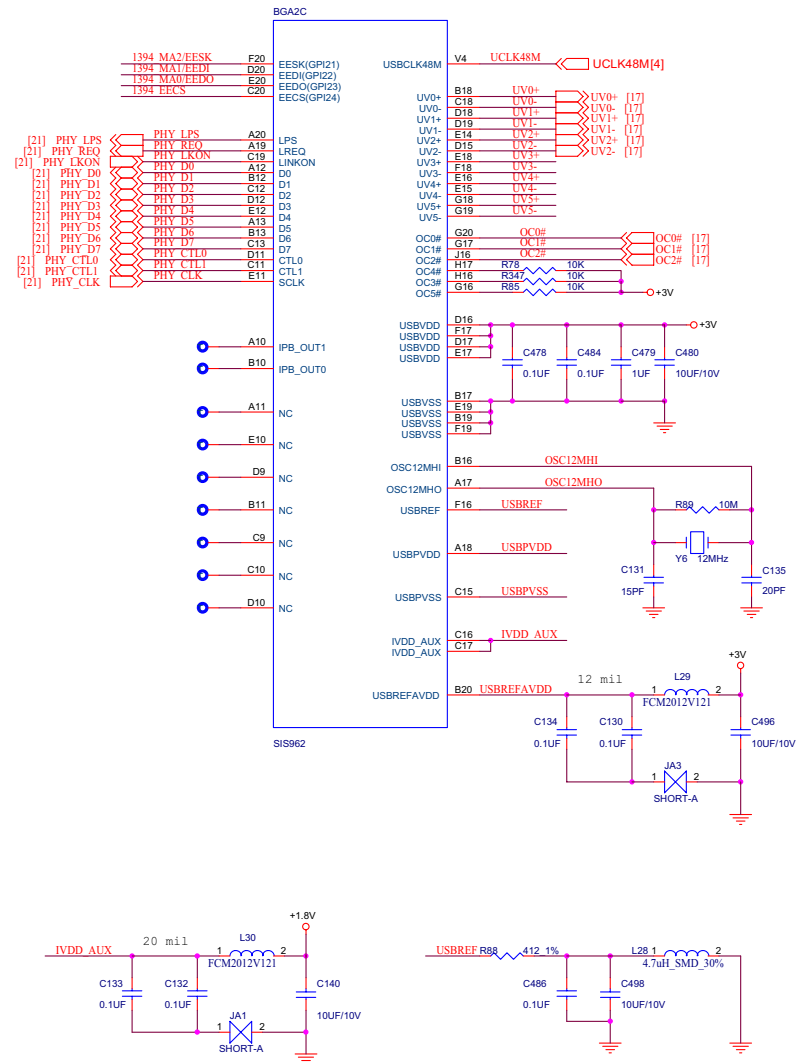
962 (Misc Signals) 2 of 4



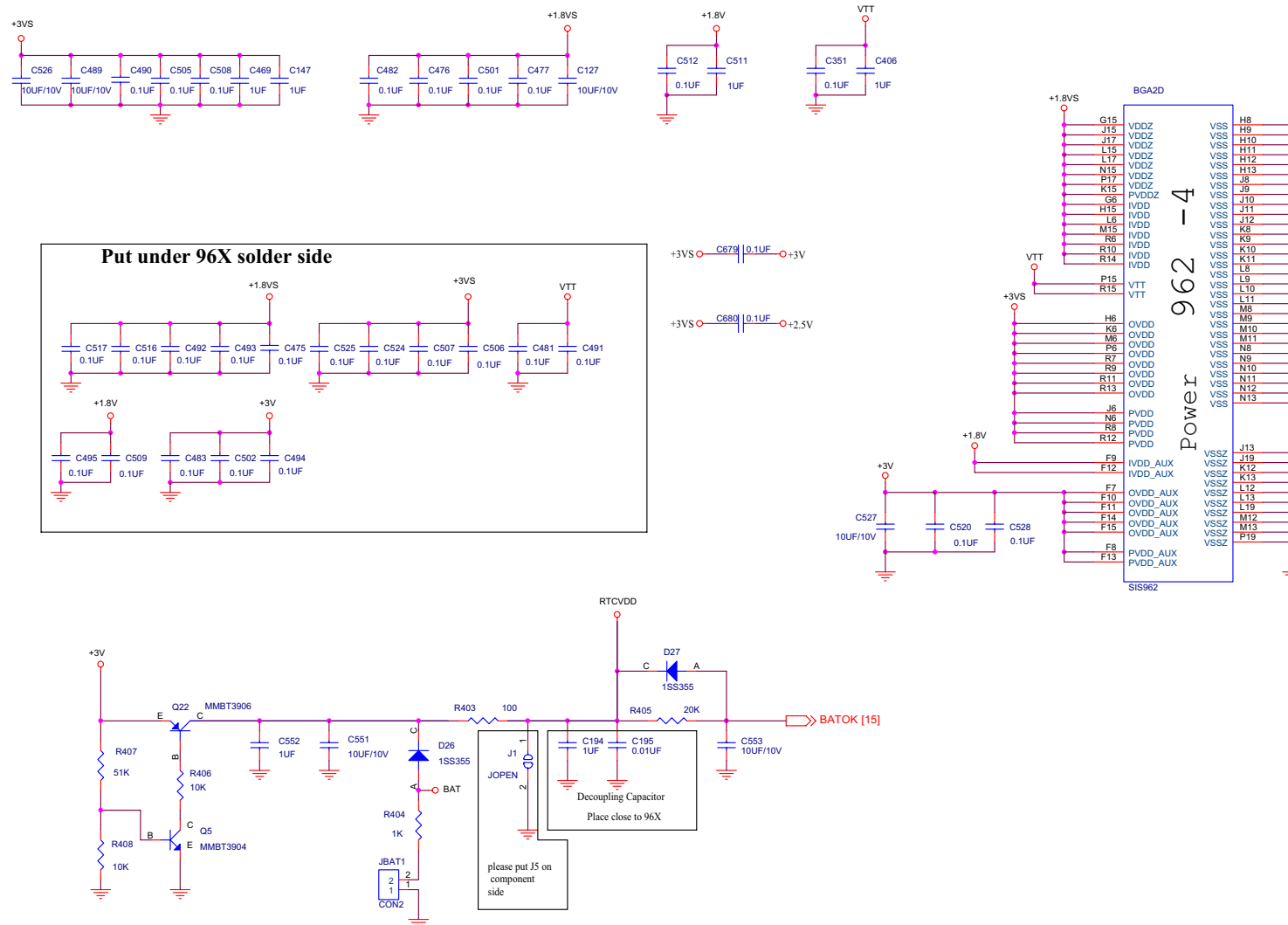
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962 (Misc Signals)
2 of 4

962 (USB I/F) 3 of 4

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962 (USB I/F)
3 of 4



962 (Power & RTC) 4 of 4



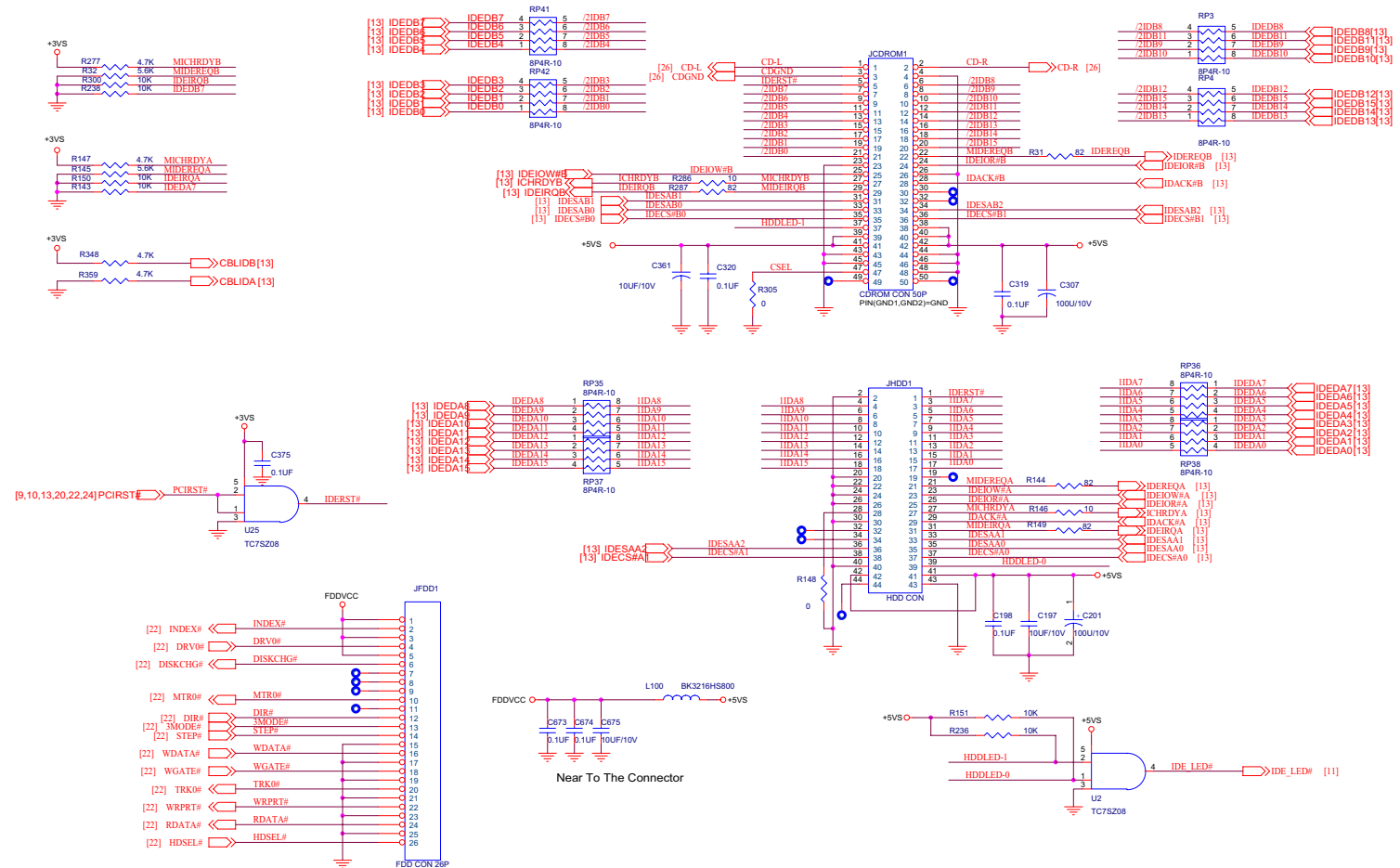
Sheet 16 of 35
962 (Power & RTC)
4 of 4

B.Schematic Diagrams

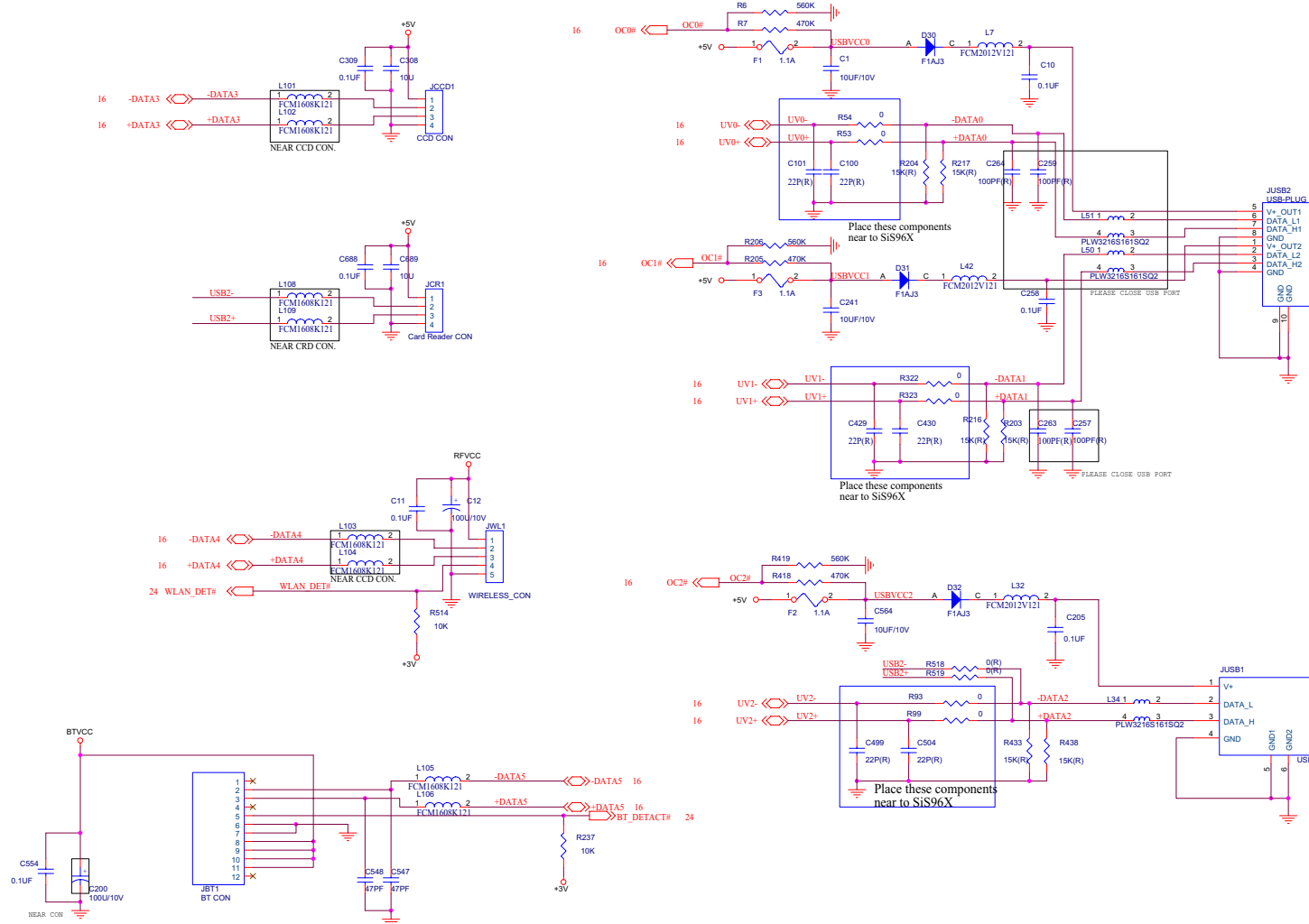
Schematic Diagrams

HDD/Combo Connector

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HDD/Combo
Connector



USB Port

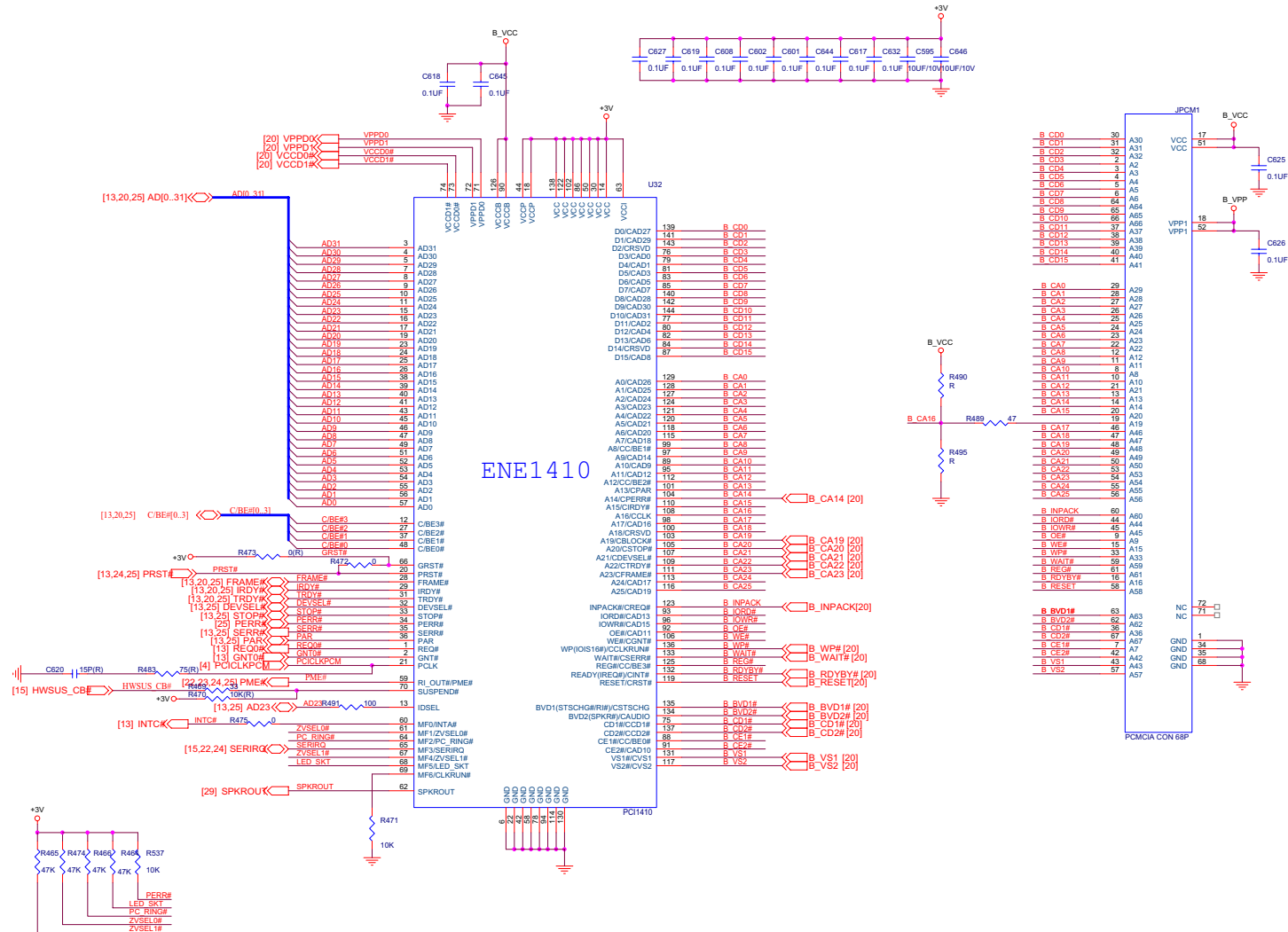


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USB Port

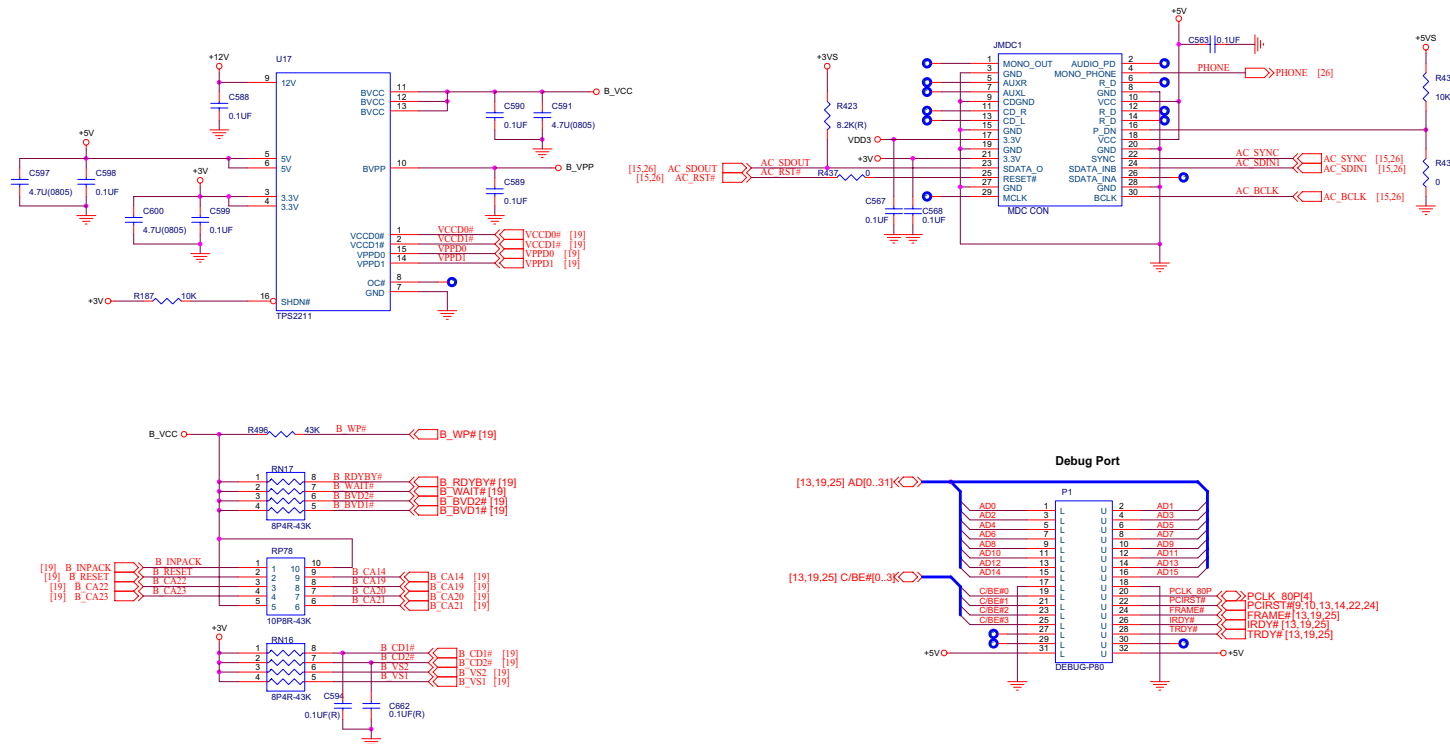
B.Schematic Diagrams

PCMCIA TI1410

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PCMCIA TI1410



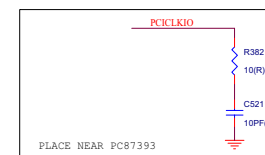
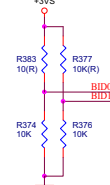
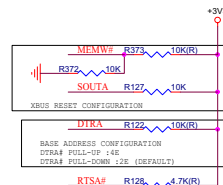
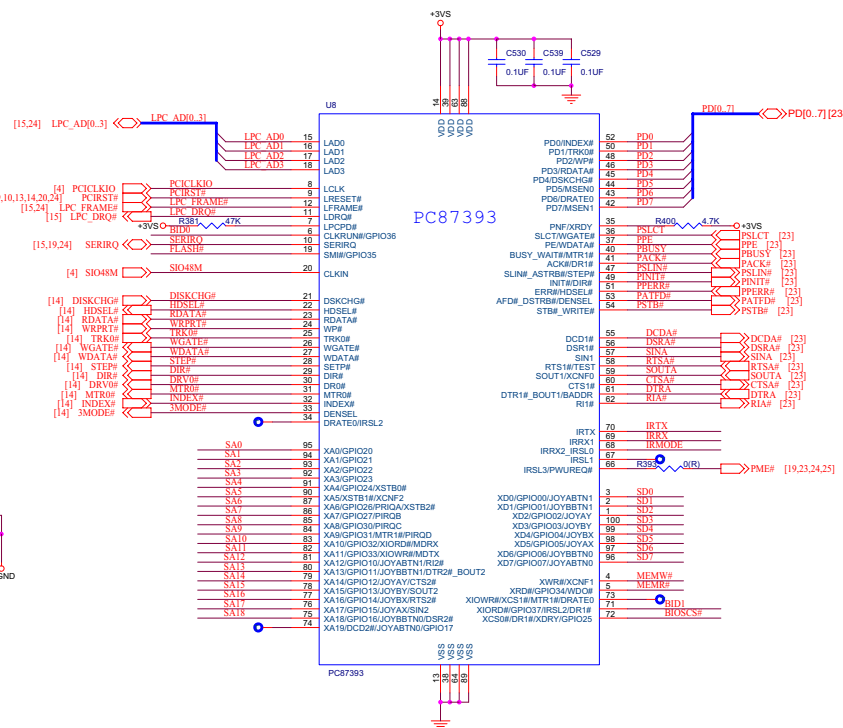
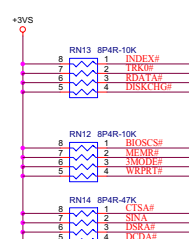
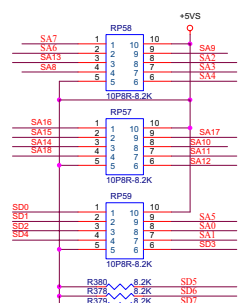
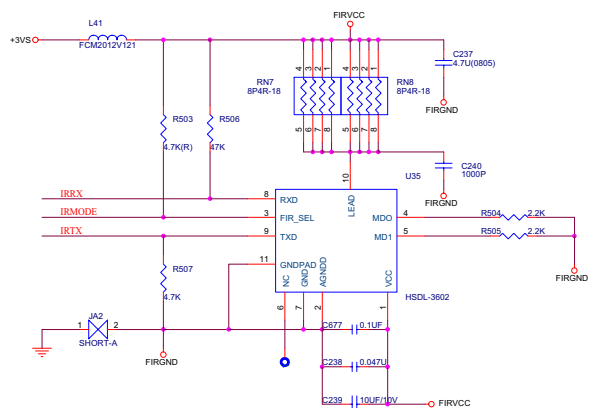
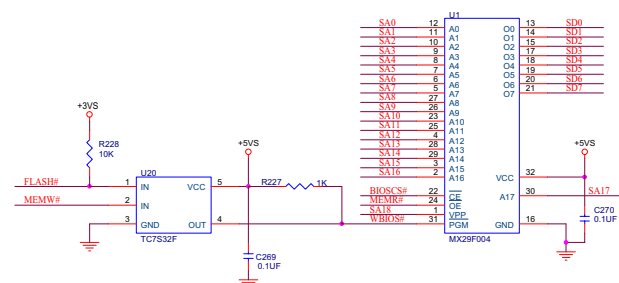
PCMCIA Power



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PCMCIA Power

1394 PHY TSB41LV01

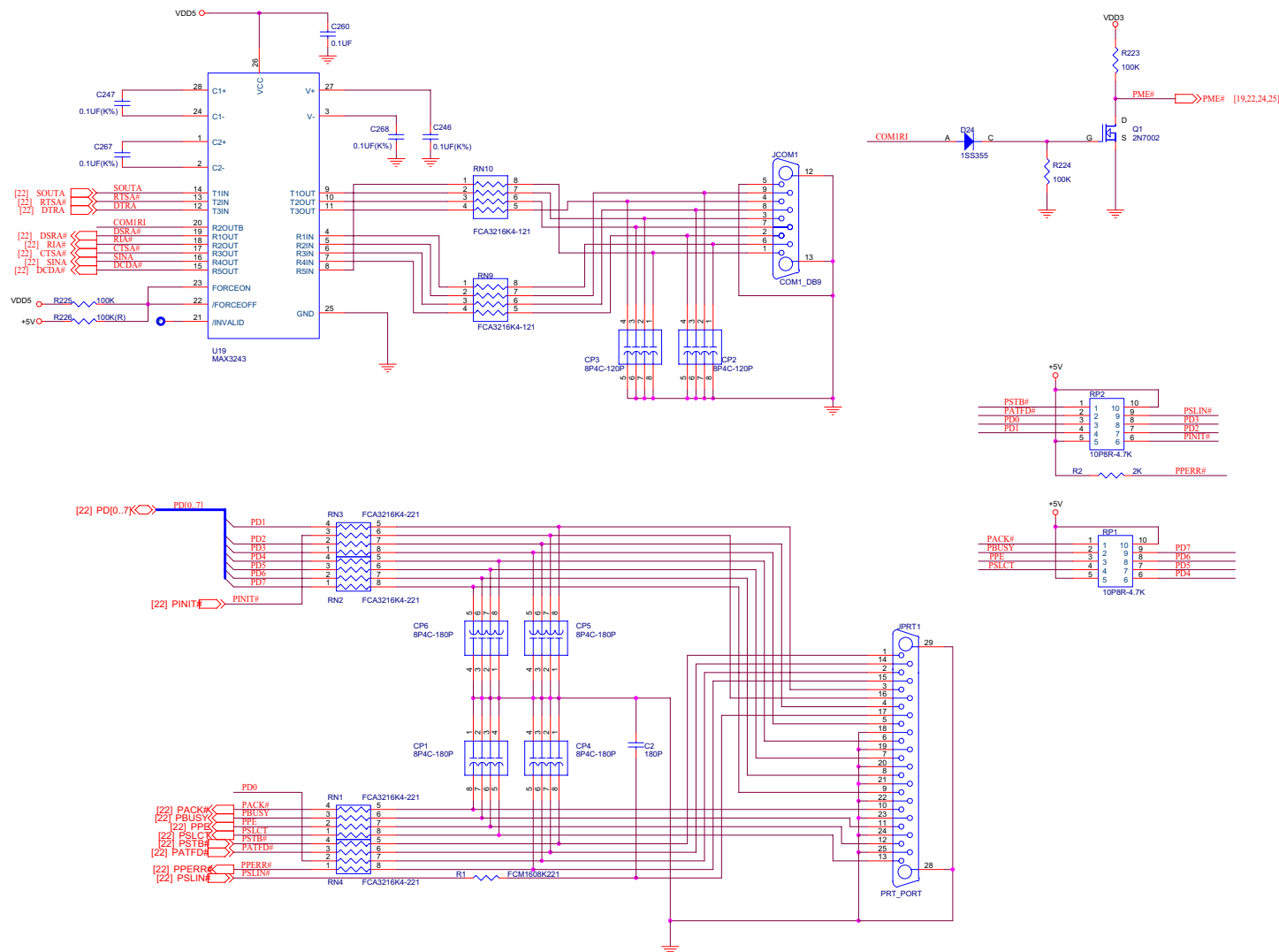
LPC SI/O



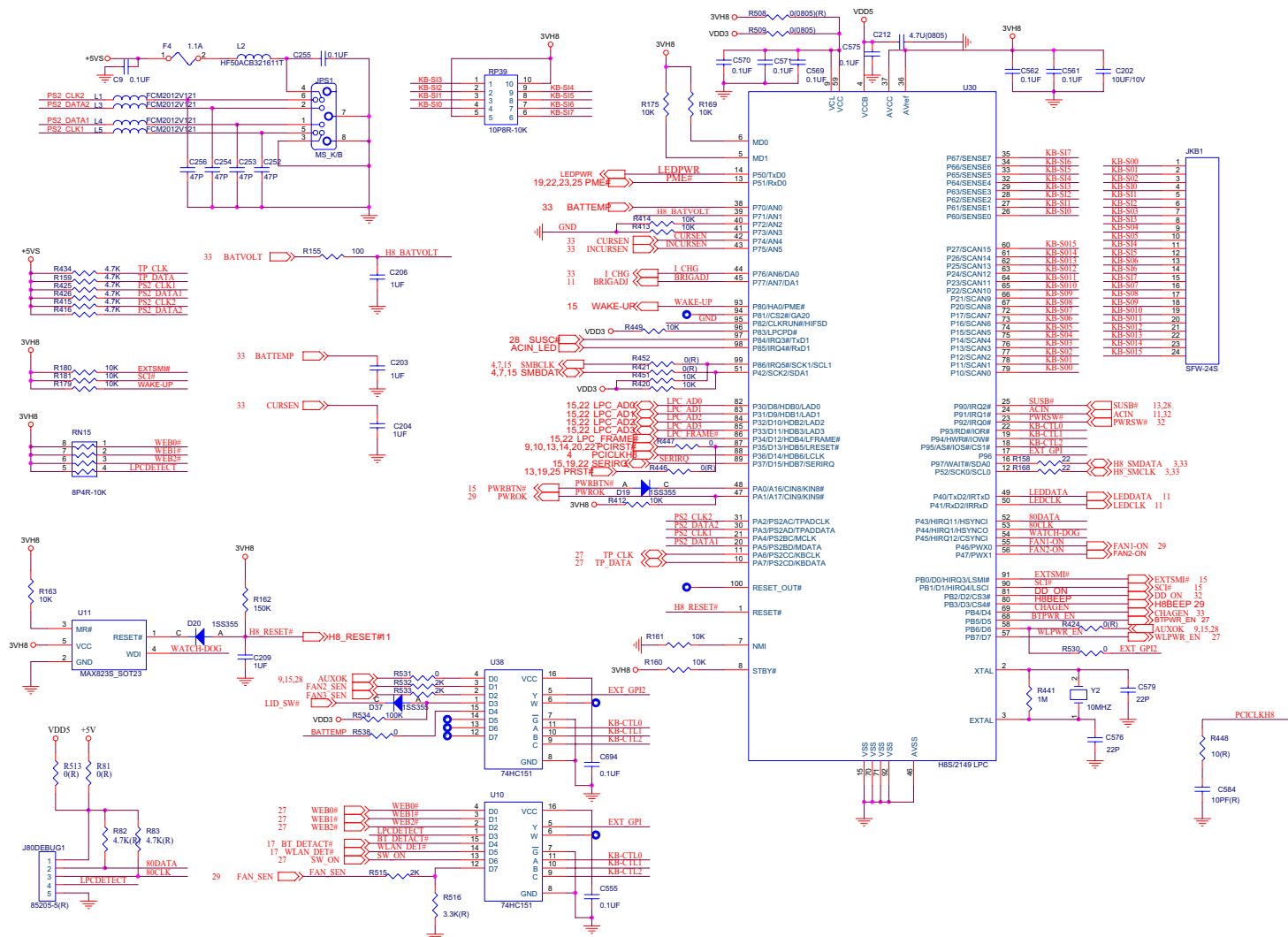
Sheet 22 of 35
LPC SI/O

LPT/COM Port

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LPT/COM Port

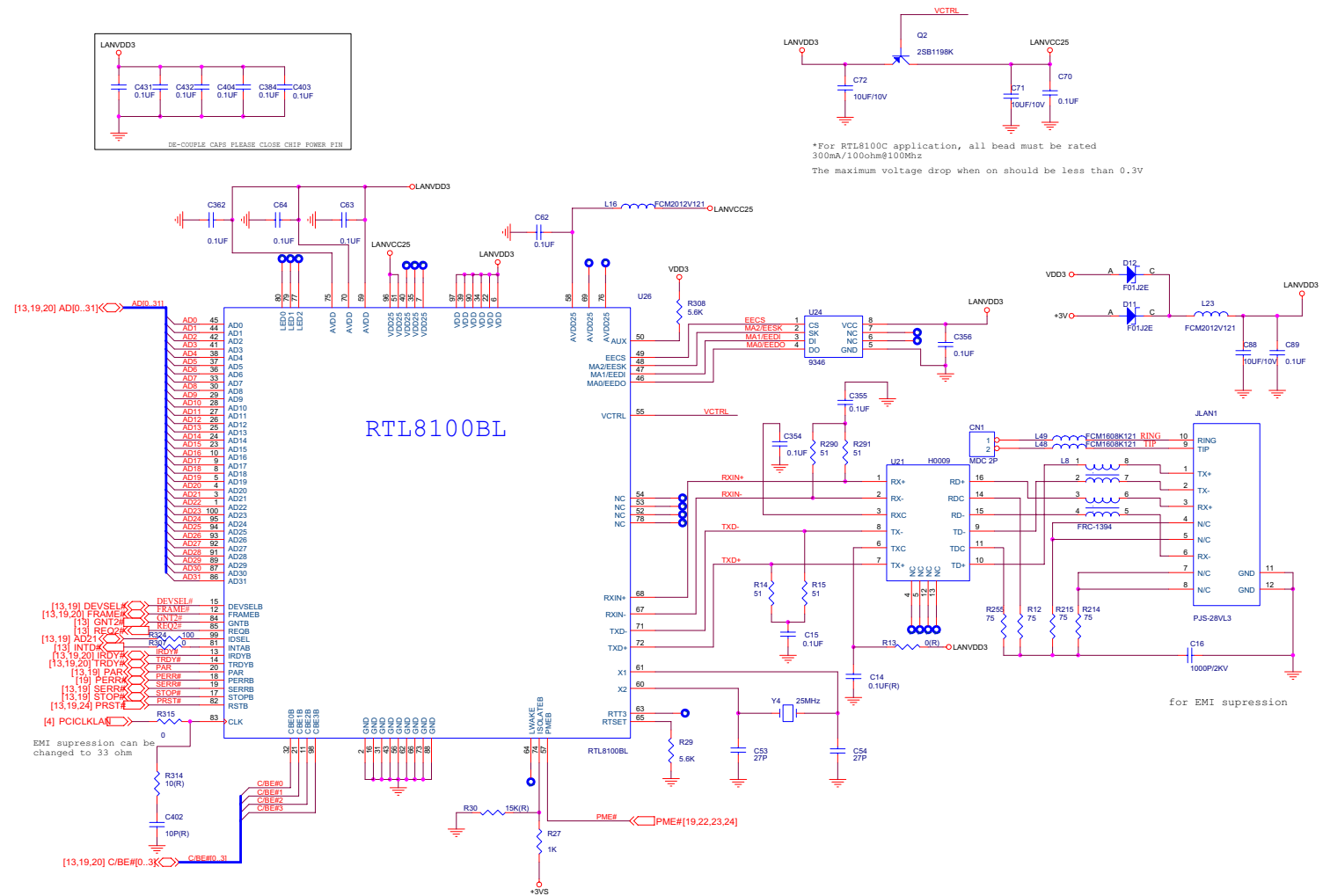


LPC H8

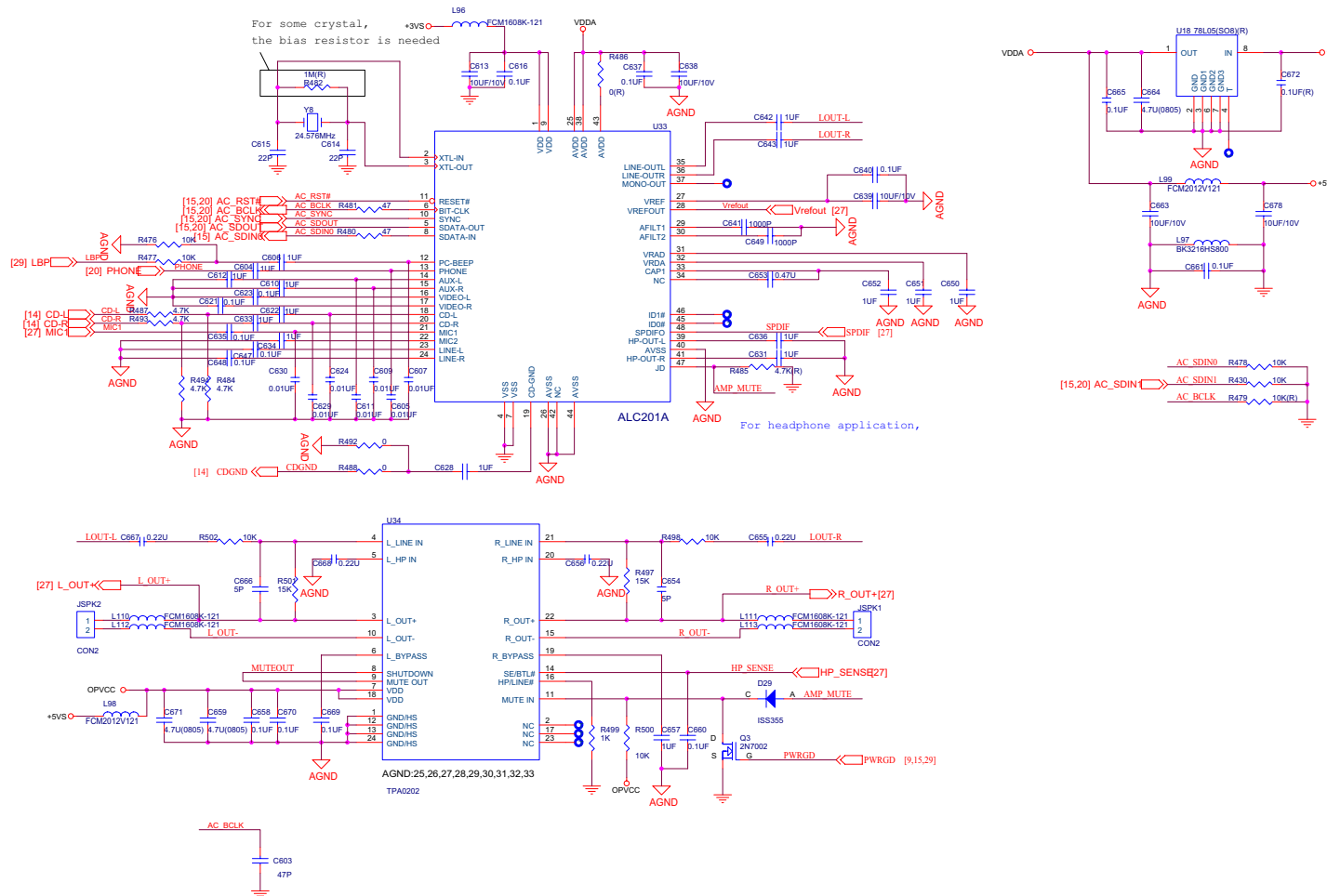


LAN RTL8100BL

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LAN RTL8100BL



Audio Codec ALC201A

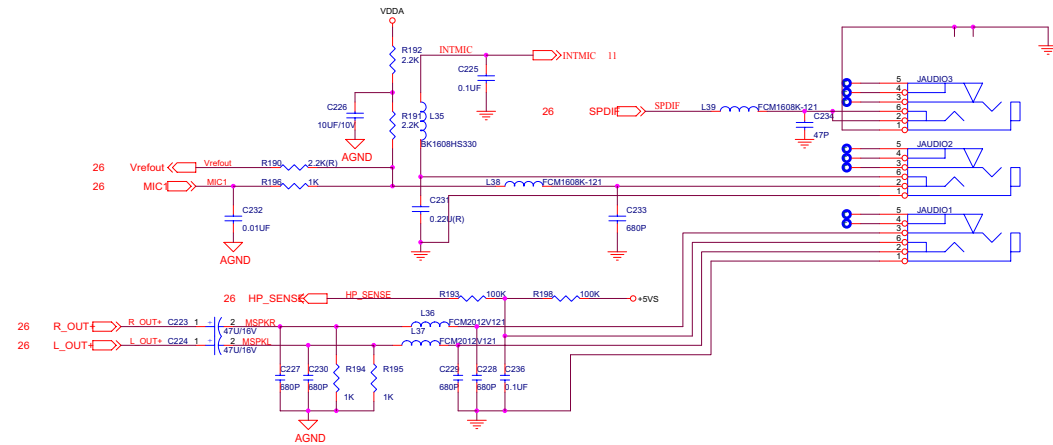


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Audio Codec
ALC201A

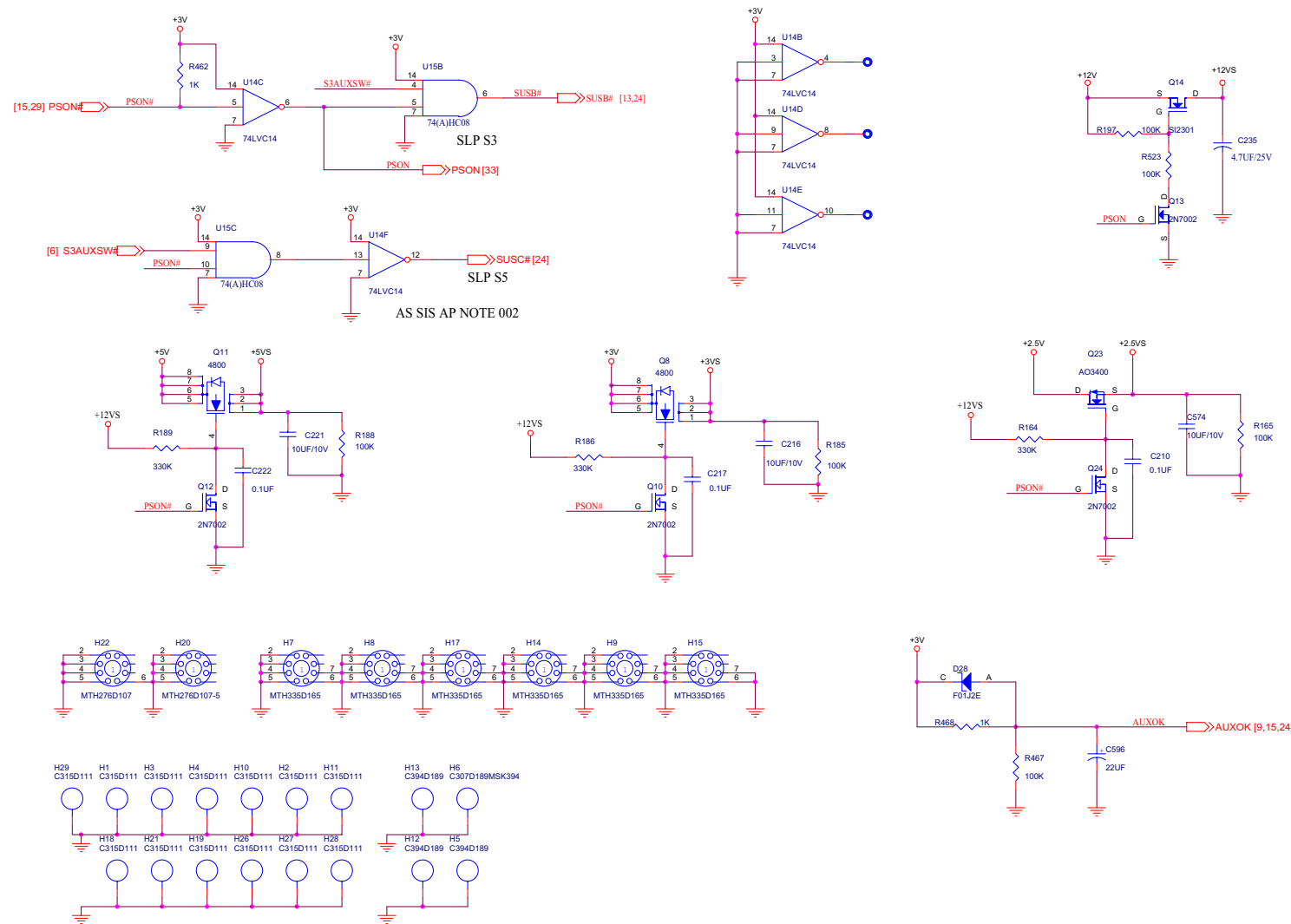
Schematic Diagrams

Audio Out & Off Board Connectors

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Audio Out & Off
Board Connectors



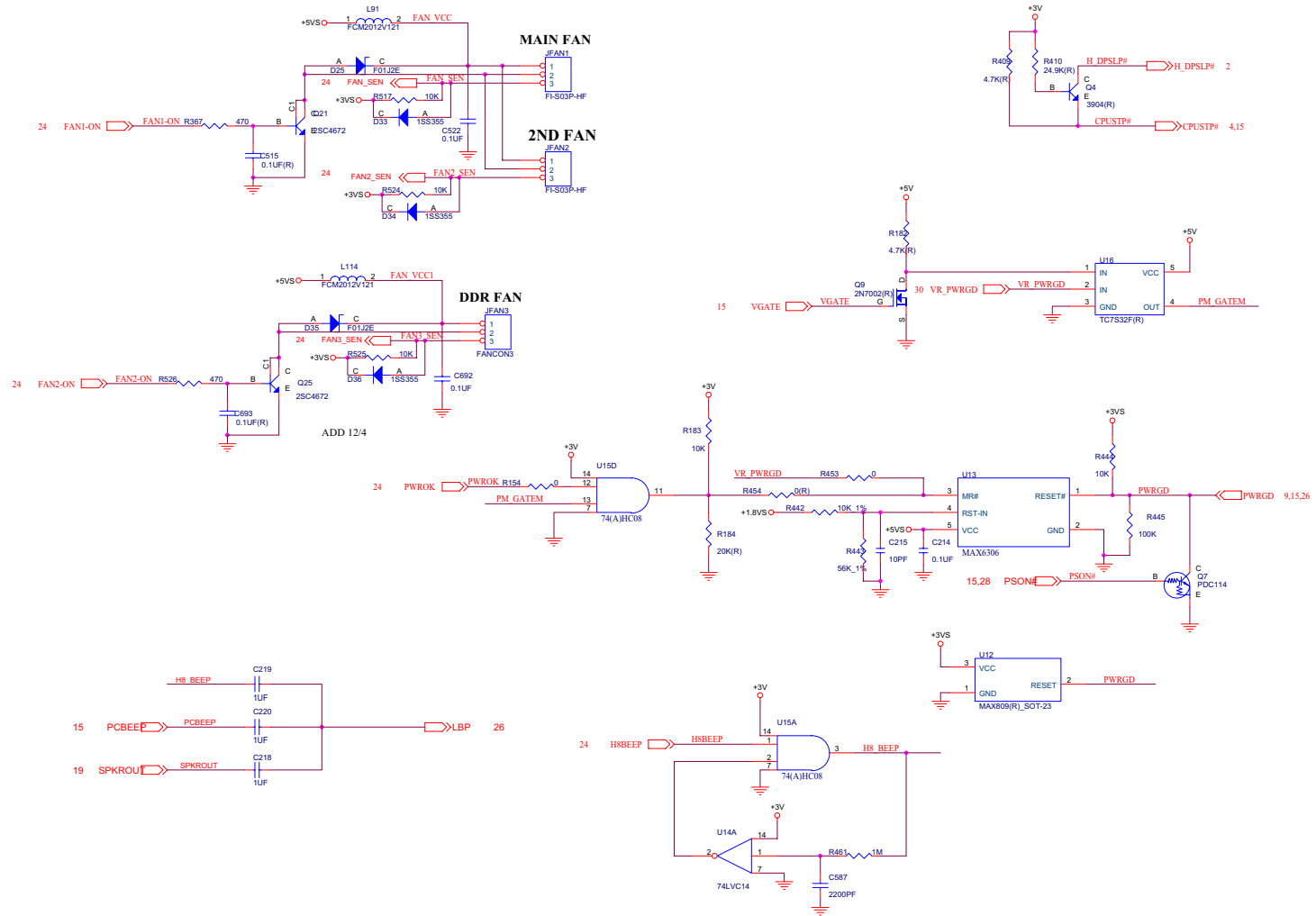
System Power Control



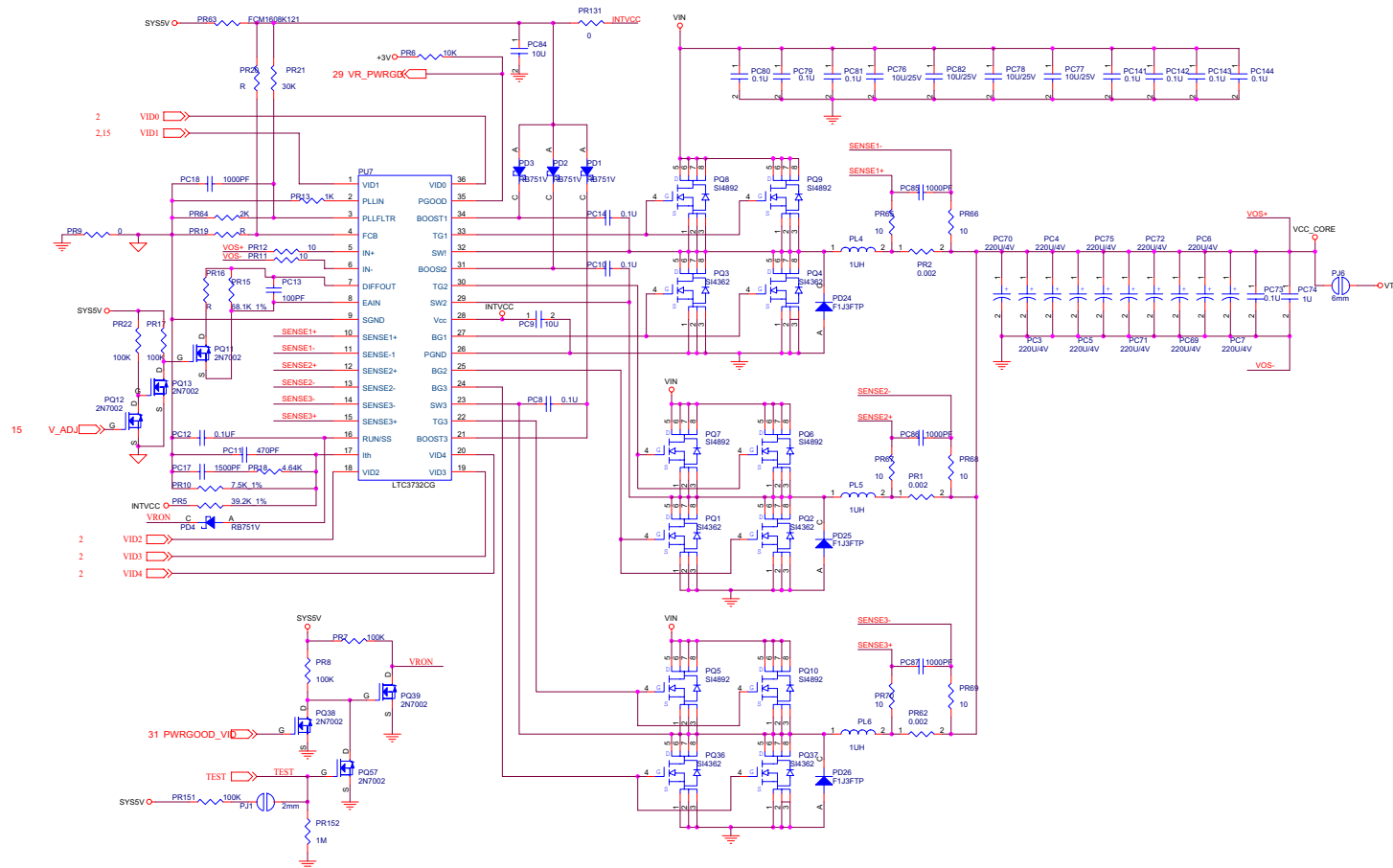
Sheet 28 of 35
System Power
Control

Fan Control and SpeedStep

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Fan Control &
SpeedStep



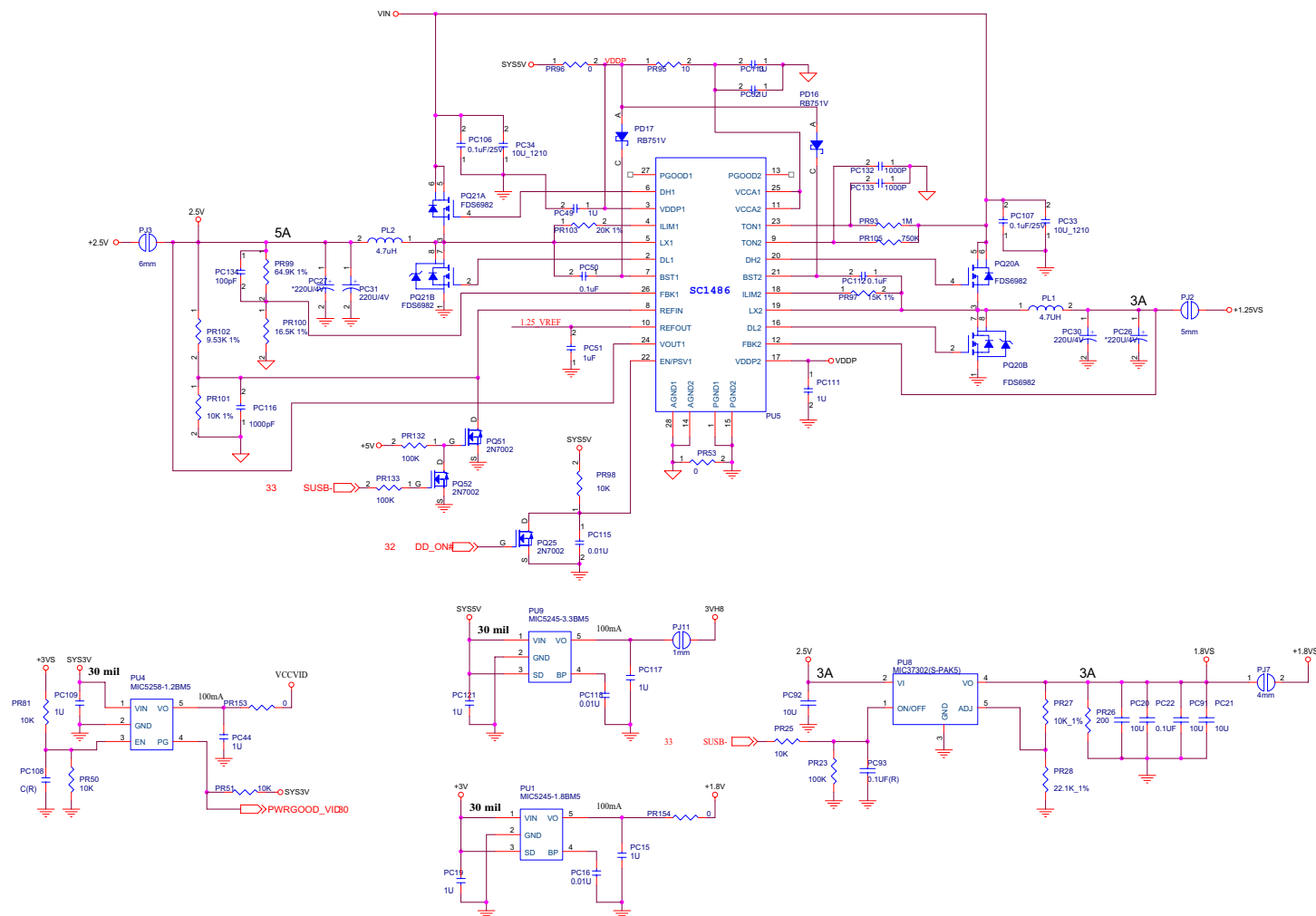
VCORE



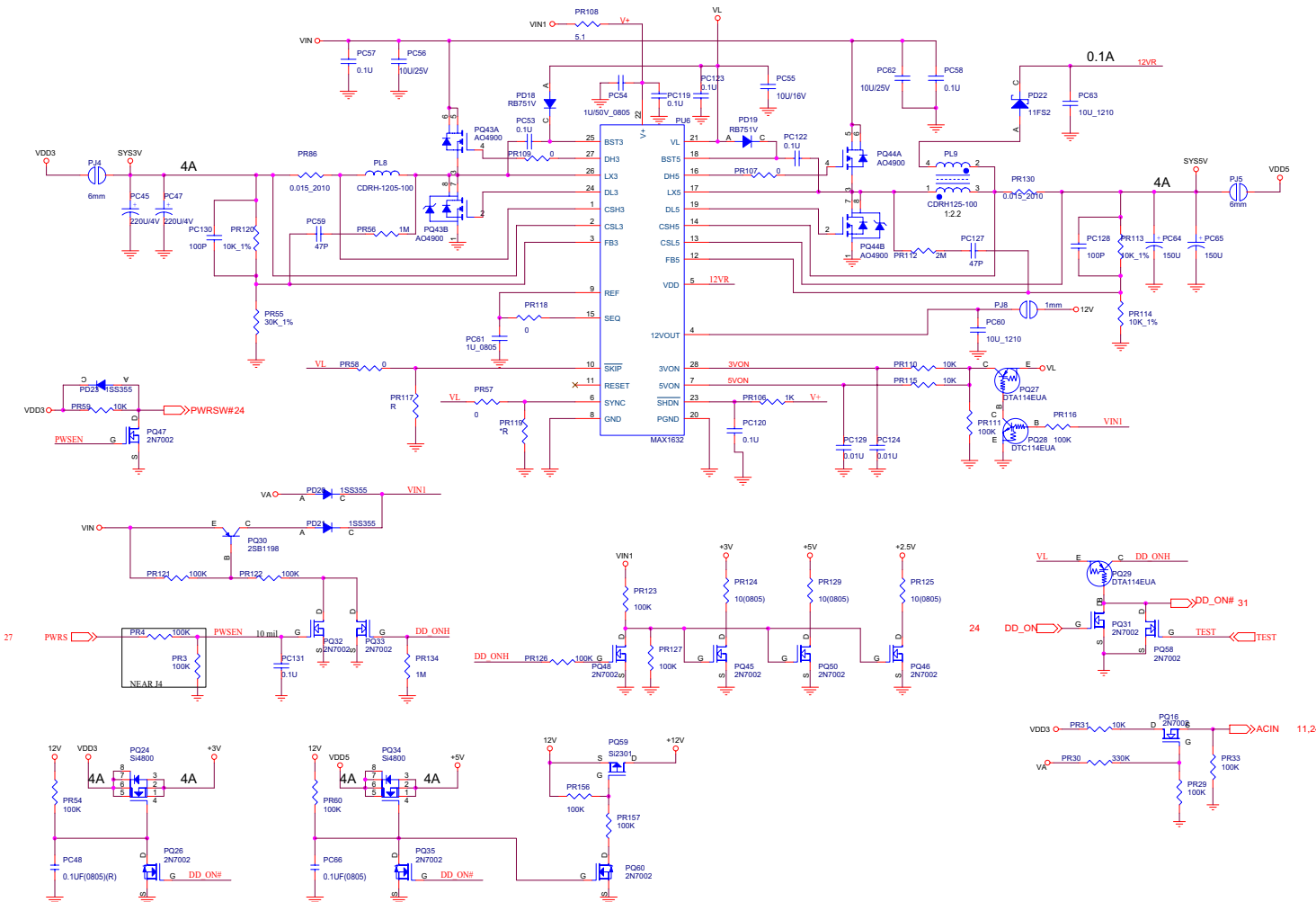
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VCORE

DDR Power

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DDR Power



System

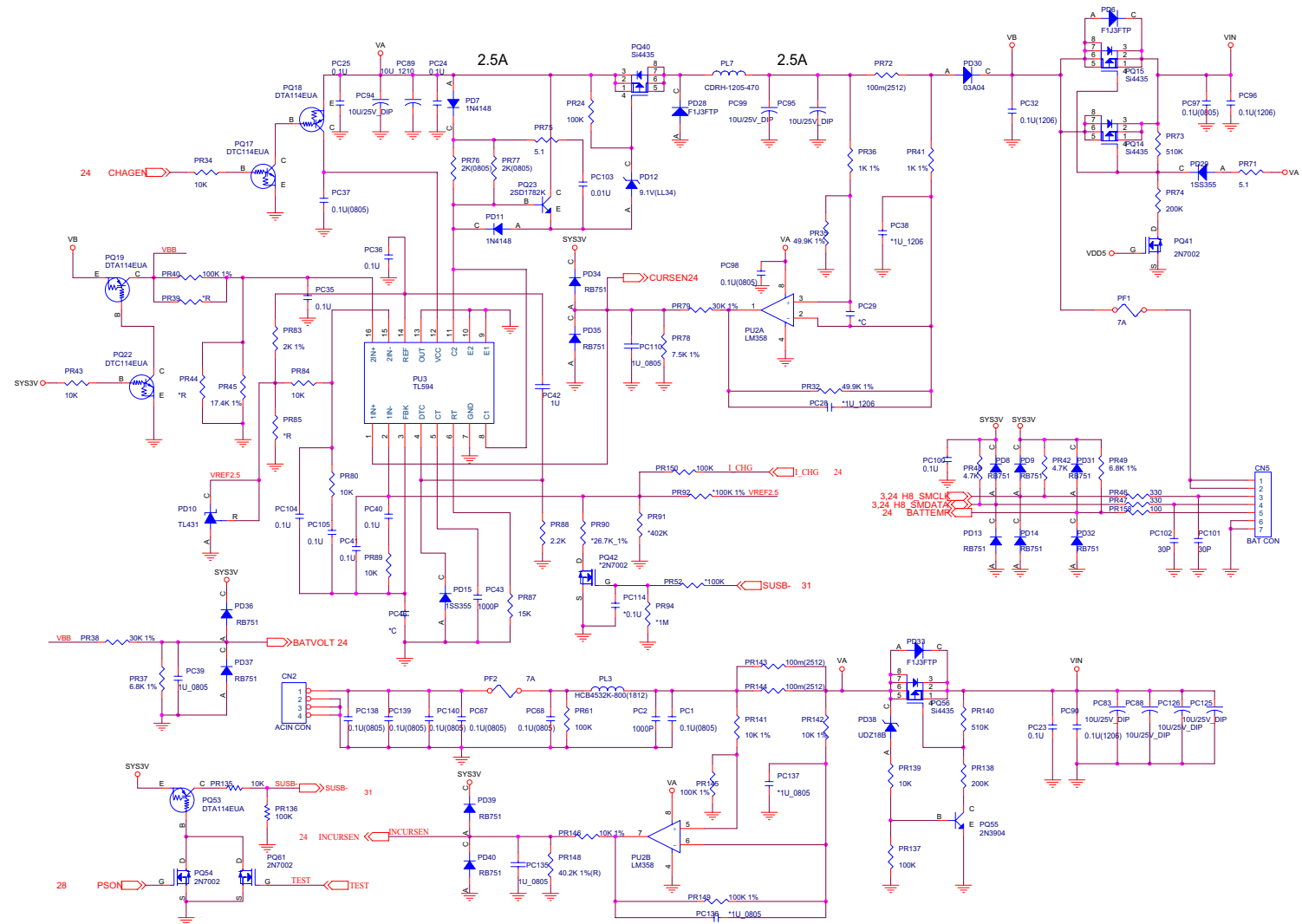


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System

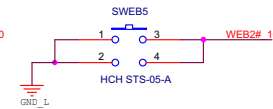
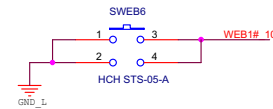
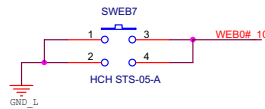
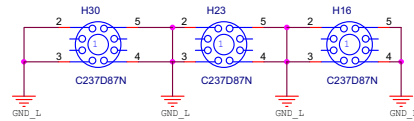
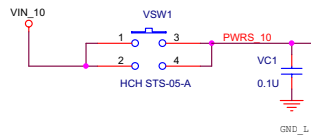
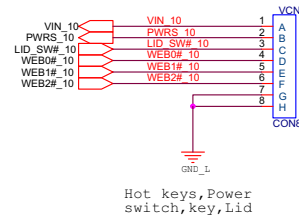
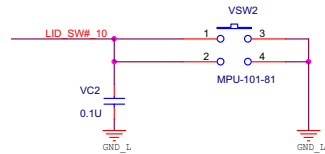
Schematic Diagrams

Charger

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Charger



SW Board and HotKey



Sheet 34 of 35
SW Board and
HotKey

Schematic Diagrams

TouchPad and Switch Board

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TouchPad and
Switch Board

