

## Appendix B:Schematic Diagrams

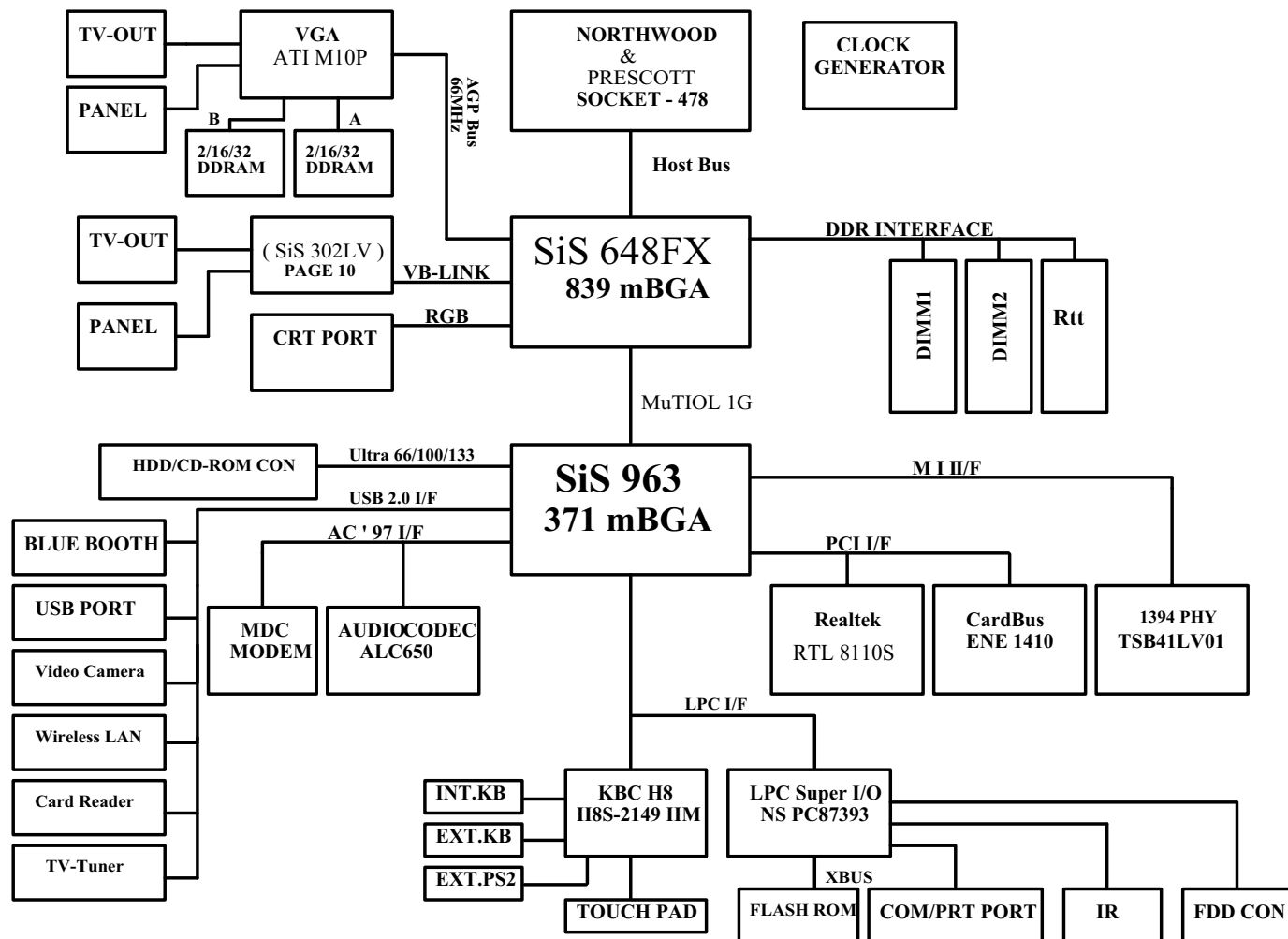
This appendix has circuit diagrams of the *D400V/D410V* notebook's PCB's. The following table indicates where to find the appropriate schematic diagram.

*Table 1*  
**Schematic  
Diagrams**

Diagram - Page	Diagram - Page	Diagram - Page
<i>System Block Diagram - Page B - 2</i>	<i>963-3 (USB I/F) 3 of 4 - Page B - 16</i>	<i>Fan Control and SpeedStep - Page B - 30</i>
<i>Socket 478 1 of 2 - Page B - 3</i>	<i>963-4 (Power &amp; RTC) 4 of 4 - Page B - 17</i>	<i>Mobility M10-P - Page B - 31</i>
<i>Socket 478 &amp; ITP 2 of 2 - Page B - 4</i>	<i>HDD/Combo Connector - Page B - 18</i>	<i>Mobility M10-P MEM A/B - Page B - 32</i>
<i>Clock Generator - Page B - 5</i>	<i>USB Port - Page B - 19</i>	<i>VGA DDR DRAM Channel A - Page B - 33</i>
<i>M648FX-1 (Host/AGP) 1 of 4 - Page B - 6</i>	<i>PCMCIA ENE1410 - Page B - 20</i>	<i>VGA DDR DRAM Channel B - Page B - 34</i>
<i>M648FX-2 (Memory for DDR) 2 of 4 - Page B - 7</i>	<i>PCMCIA Power &amp; MDC Interface - Page B - 21</i>	<i>VGA DDR DRAM Termination - Page B - 35</i>
<i>M648FX-3 &amp; CRT Out 3 of 4 - Page B - 8</i>	<i>1394 PHY TSB41LV01 - Page B - 22</i>	<i>Mobility M10-P POW - Page B - 36</i>
<i>M648FX-4 (Power) 4 of 4 - Page B - 9</i>	<i>LPC Super I/O - Page B - 23</i>	<i>VCORE - Page B - 37</i>
<i>DDR Memory DIMM - Page B - 10</i>	<i>LPT/COM Port - Page B - 24</i>	<i>+1.2V, +1.5V - Page B - 38</i>
<i>DDR SSTL-2 Termination - Page B - 11</i>	<i>LPC H8 - Page B - 25</i>	<i>+2.5V, +1.25V, +1.8V - Page B - 39</i>
<i>LVDS Interface (SiS302LV) - Page B - 12</i>	<i>LAN RTL8110S(B)-32/RTL8100C - Page B - 26</i>	<i>+3V, +5V, +12V - Page B - 40</i>
<i>Panel Con &amp; LED Indicator - Page B - 13</i>	<i>Audio Codec ALC650 - Page B - 27</i>	<i>Charger - Page B - 41</i>
<i>963-1 (PCI/IDE/HyperZip) 1 of 4 - Page B - 14</i>	<i>Audio Out &amp; Off Board Connectors - Page B - 28</i>	<i>SW Board and HotKey - Page B - 42</i>
<i>963-2 (Misc Signals) 2 of 4 - Page B - 15</i>	<i>System Power Control - Page B - 29</i>	<i>TouchPad and Switch Board - Page B - 43</i>

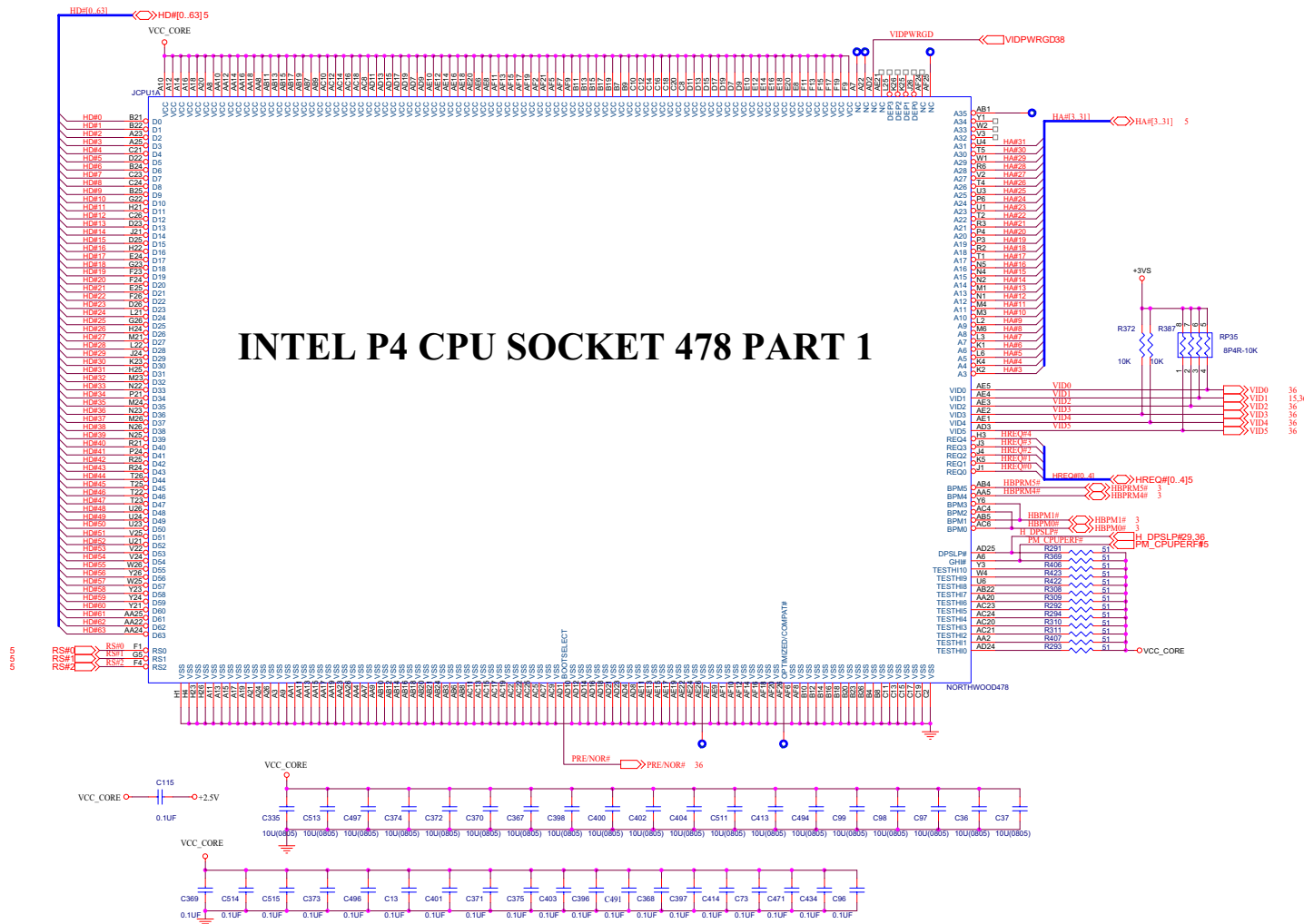
Schematic Diagrams

# System Block Diagram



Sheet 1 of 42  
System Block  
Diagram

## Socket 478 1 of 2

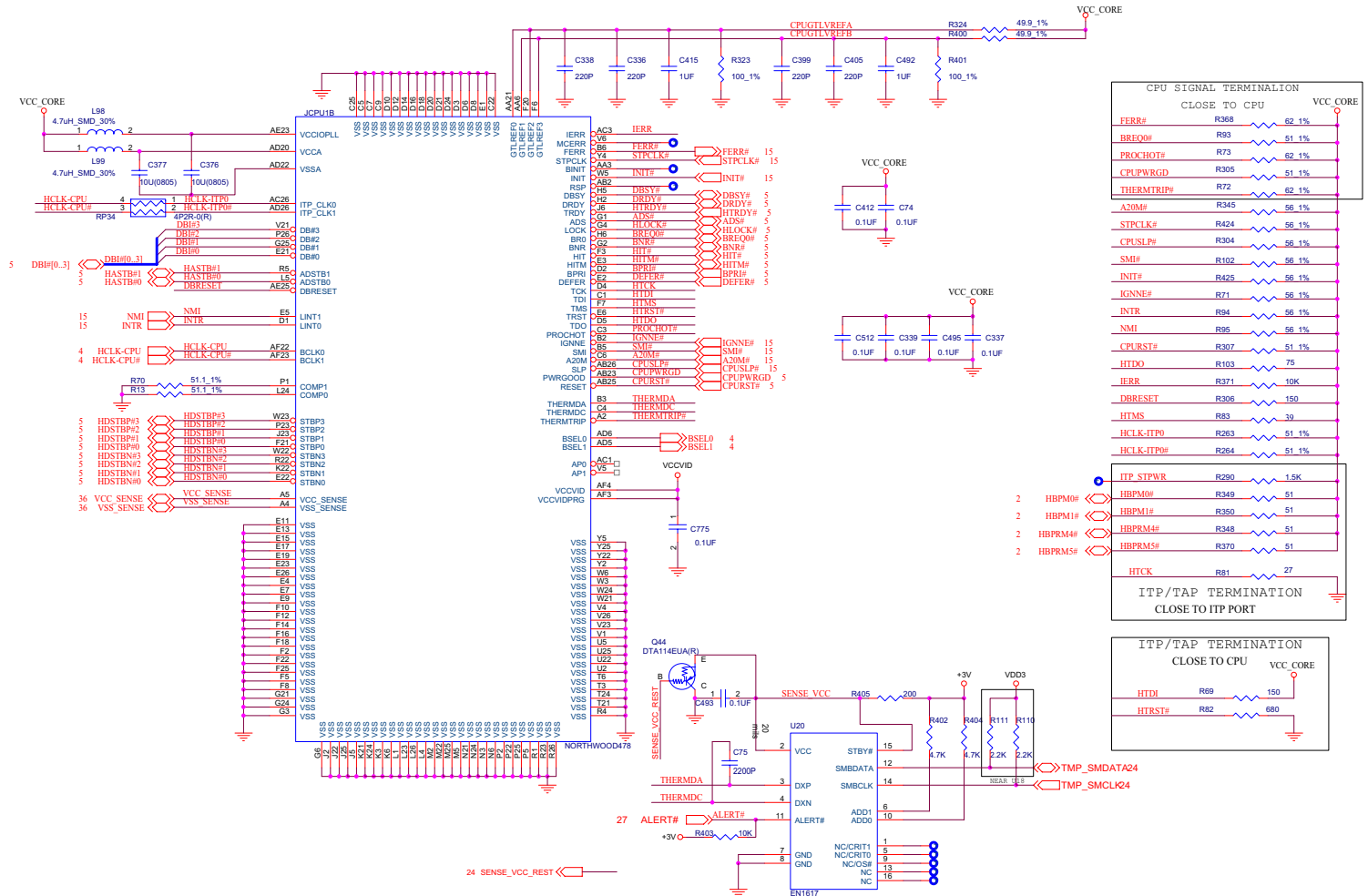


Sheet 2 of 42  
Socket 478 1 of 2

## Schematic Diagrams

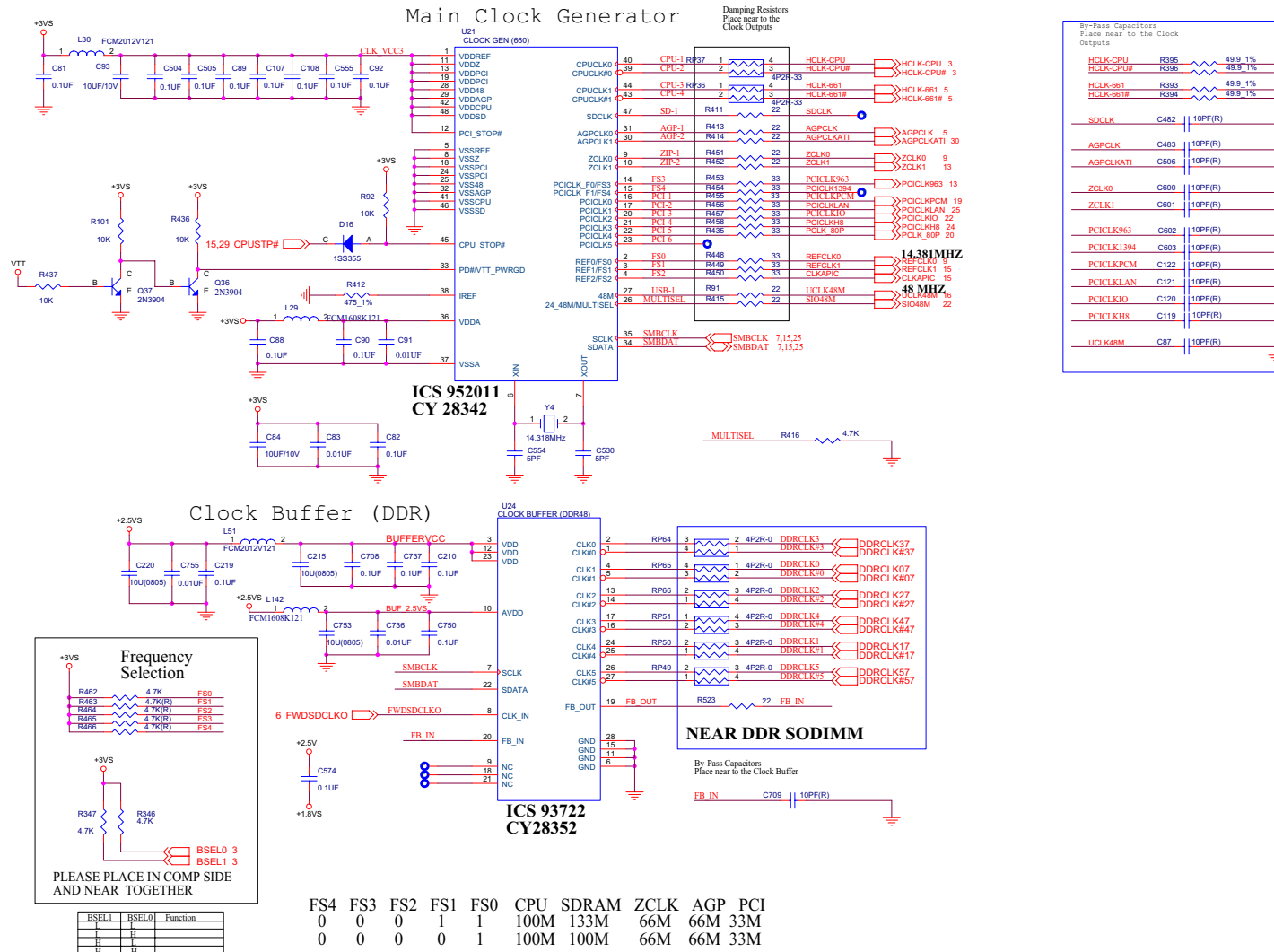
### Socket 478 & ITP 2 of 2

Sheet 3 of 42  
Socket 478 & ITP  
2 of 2



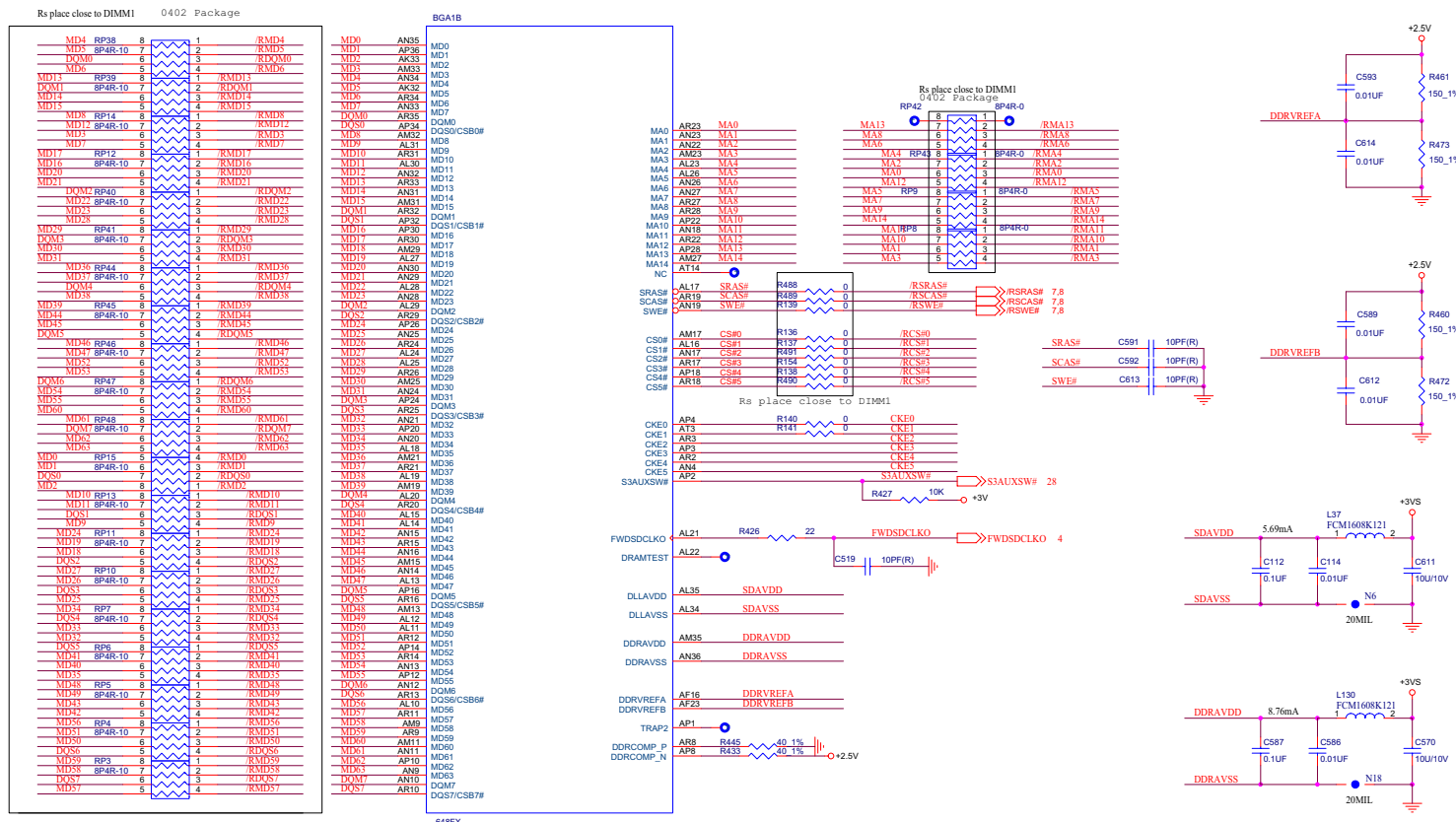
# Clock Generator

Sheet 4 of 42  
Clock Generator





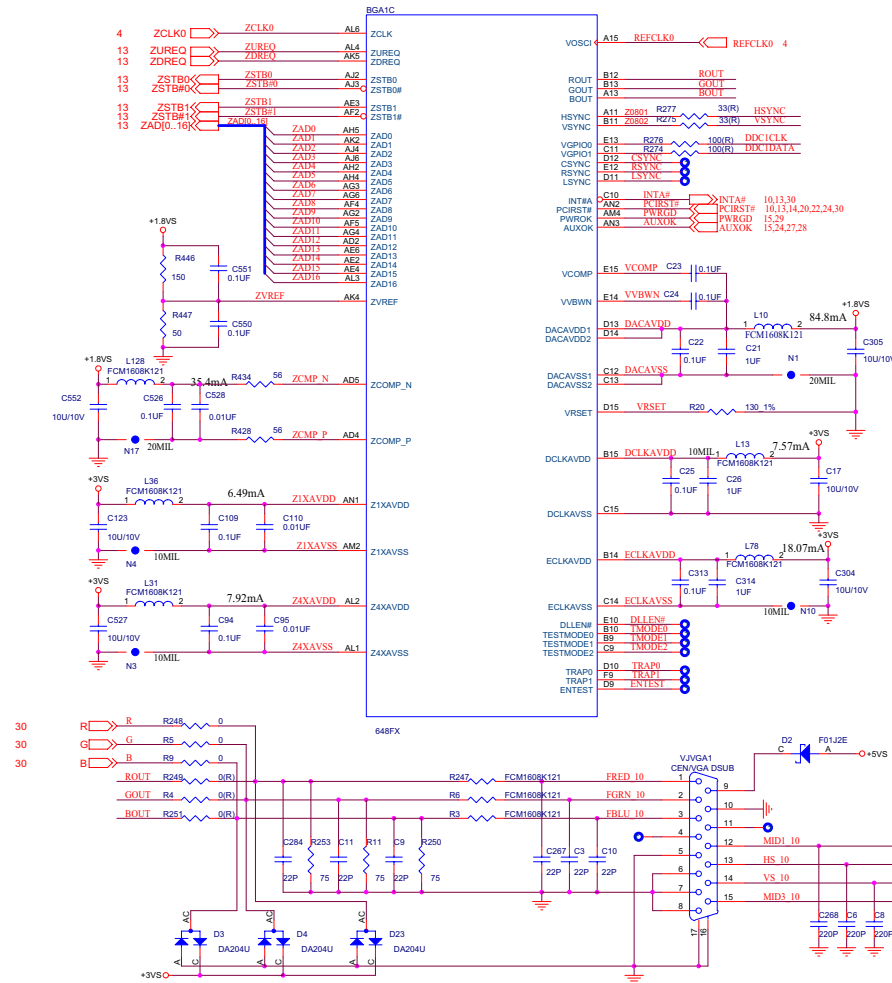
# M648FX-2 (Memory for DDR) 2 of 4



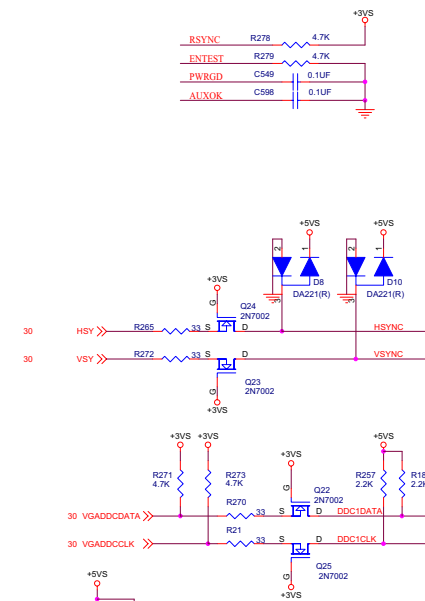
Sheet 6 of 42  
M648FX-2  
(Memory for DDR)  
2 of 4

Sheet 7 of 42  
M648FX-3  
& CRT Out  
3 of 4

## B.Schematic Diagrams

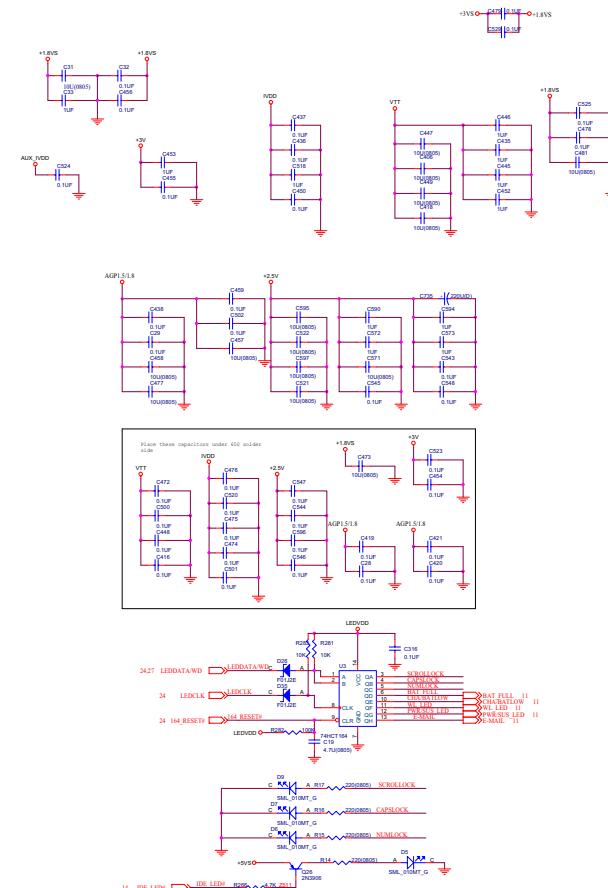
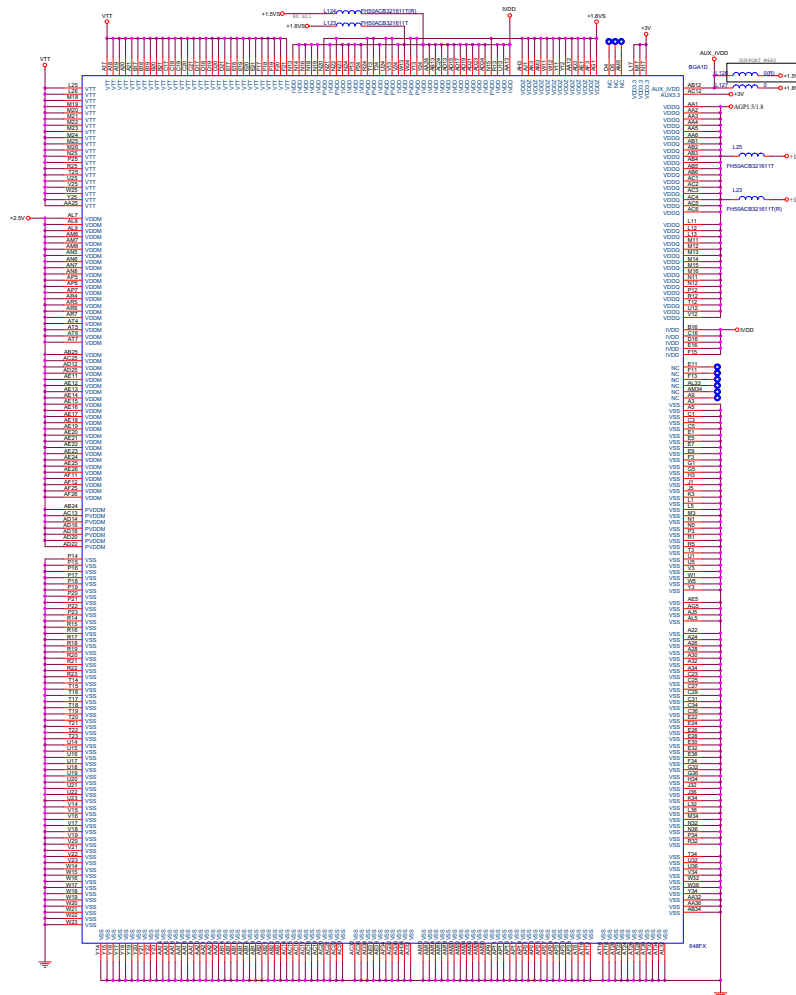


	0	1	Default	embedded pull-down (0=50K Ohm)
DTLENF	enable Pt 1	disable Pt 1	0	yes
DRAMSEL	store	load	(1)/0	yes
TRAP0	normal	NB debug mode	0	yes
TRAP1	TV selection, NTSC/PAL(0/1)		0	
CSYNC	enable VB		0	
VSYN	enable VGA interface		1	
LSYN	enable panel link		0	





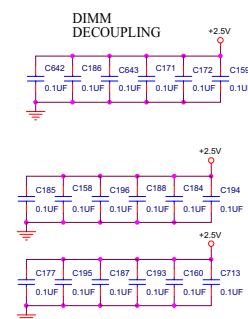
## M648FX-4 (Power) 4 of 4



Sheet 8 of 42  
M648FX-4 (Power)  
4 of 4

## B.Schematic Diagrams

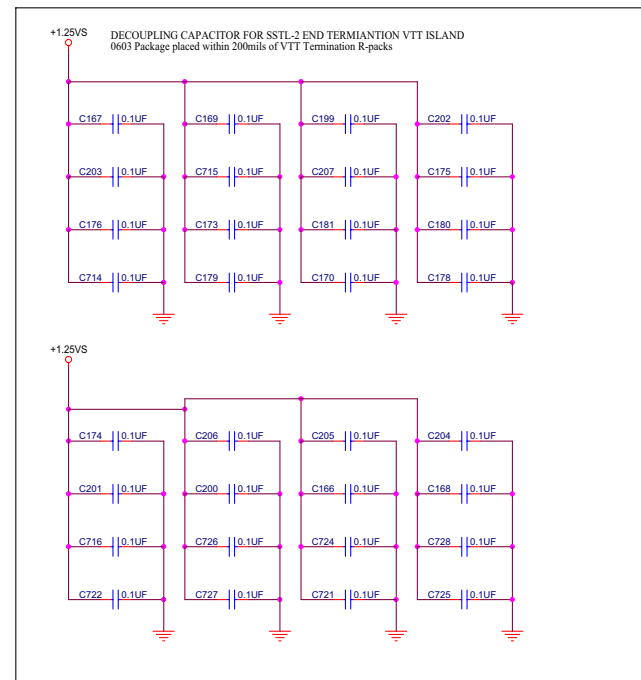
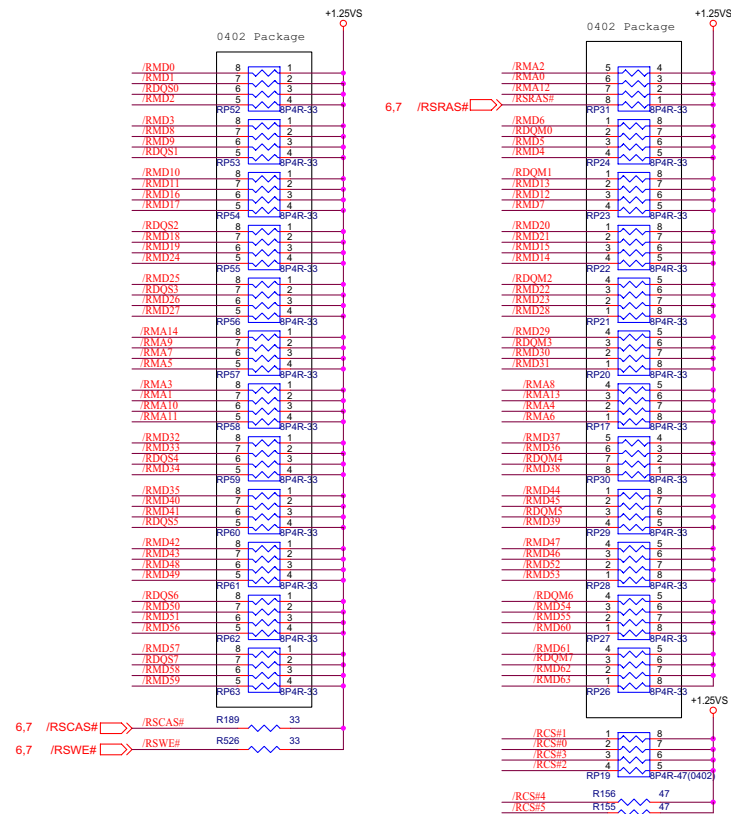
## B. Schematic Diagrams



# DDR SSTL-2 Termination

SSTL-2 Termination Resistors

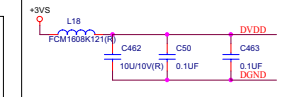
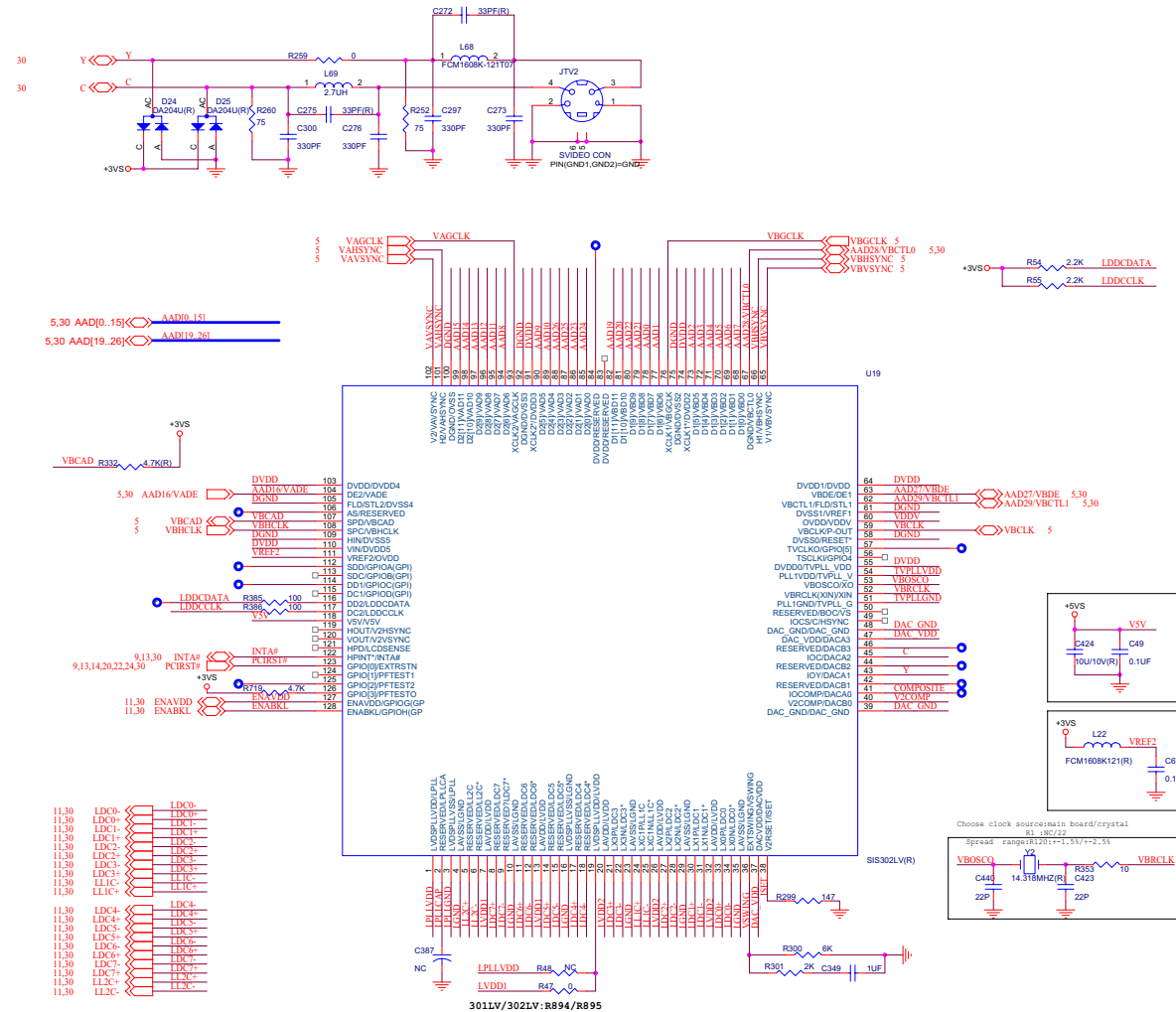
	DDR	Rs	Rtt
MD/DQM/(DQS)	SSTL-2	10	33
MA/Control	SSTL-2	0	33
CS	SSTL-2	0	47
CKE	OD 2.5V		



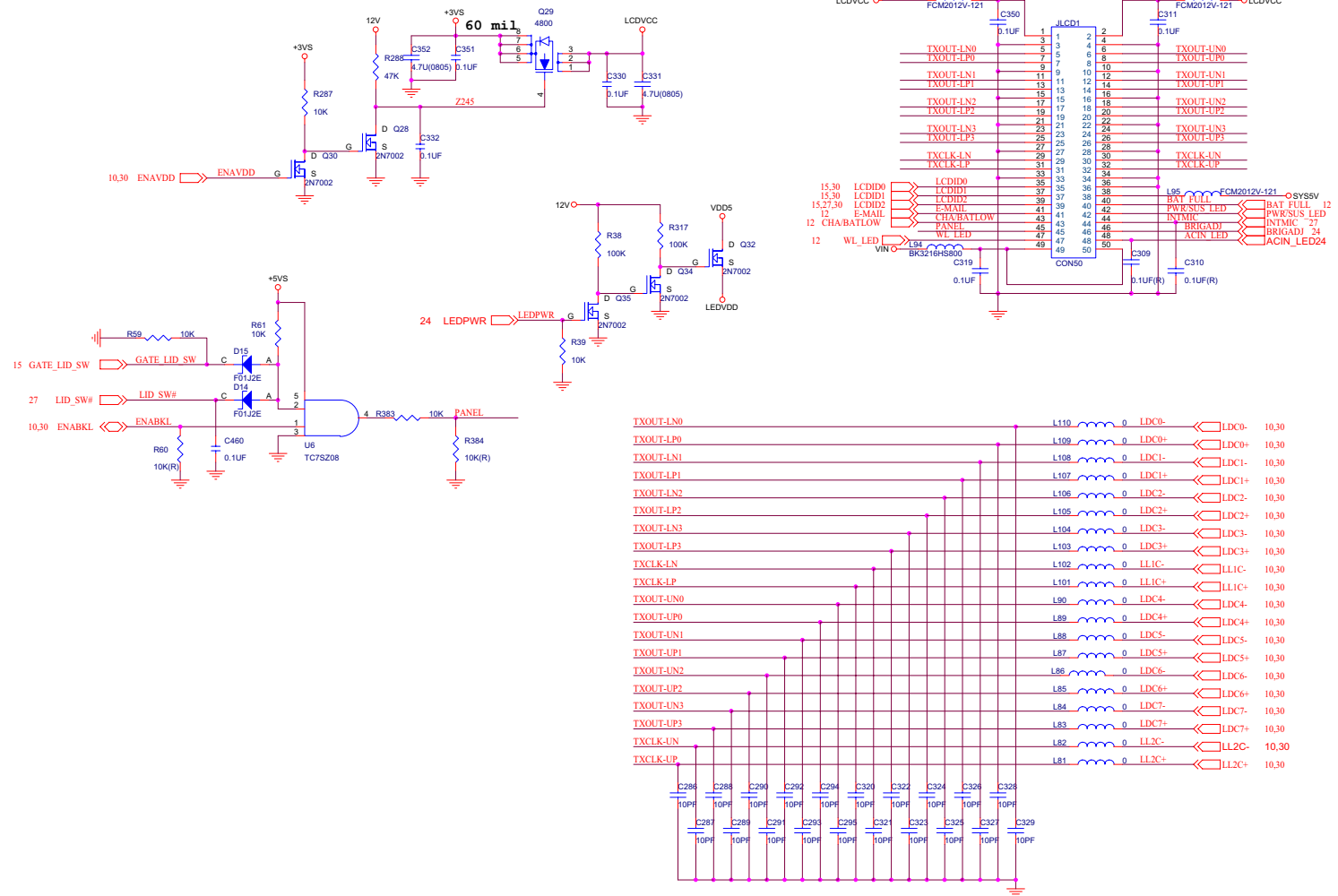
Sheet 10 of 42  
DDR SSTL-2  
Termination

## LVDS Interface (SiS302LV)

**Sheet 11 of 42**  
**LVDS interface**  
**(SiS302LV)**



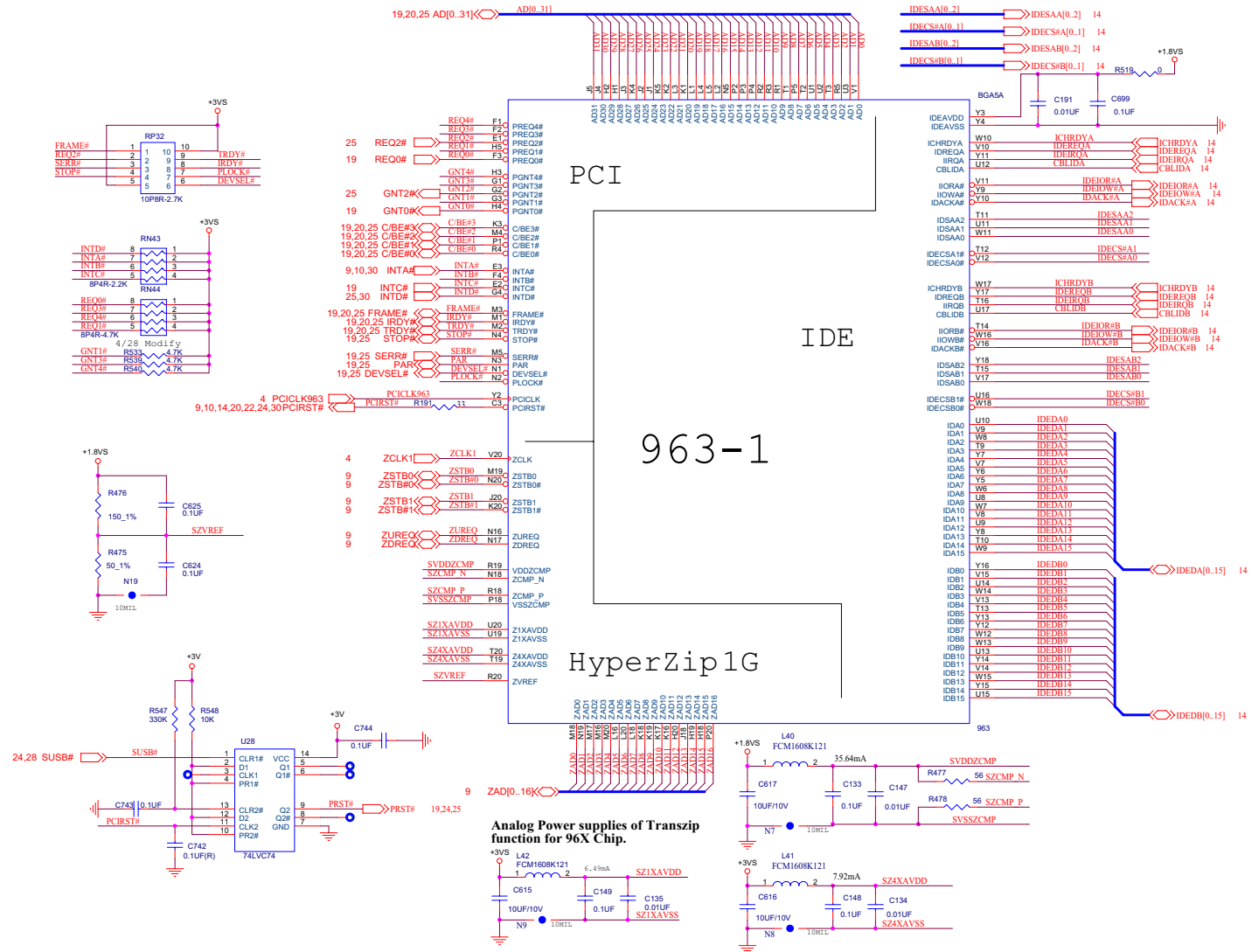
# Panel Con & LED Indicator



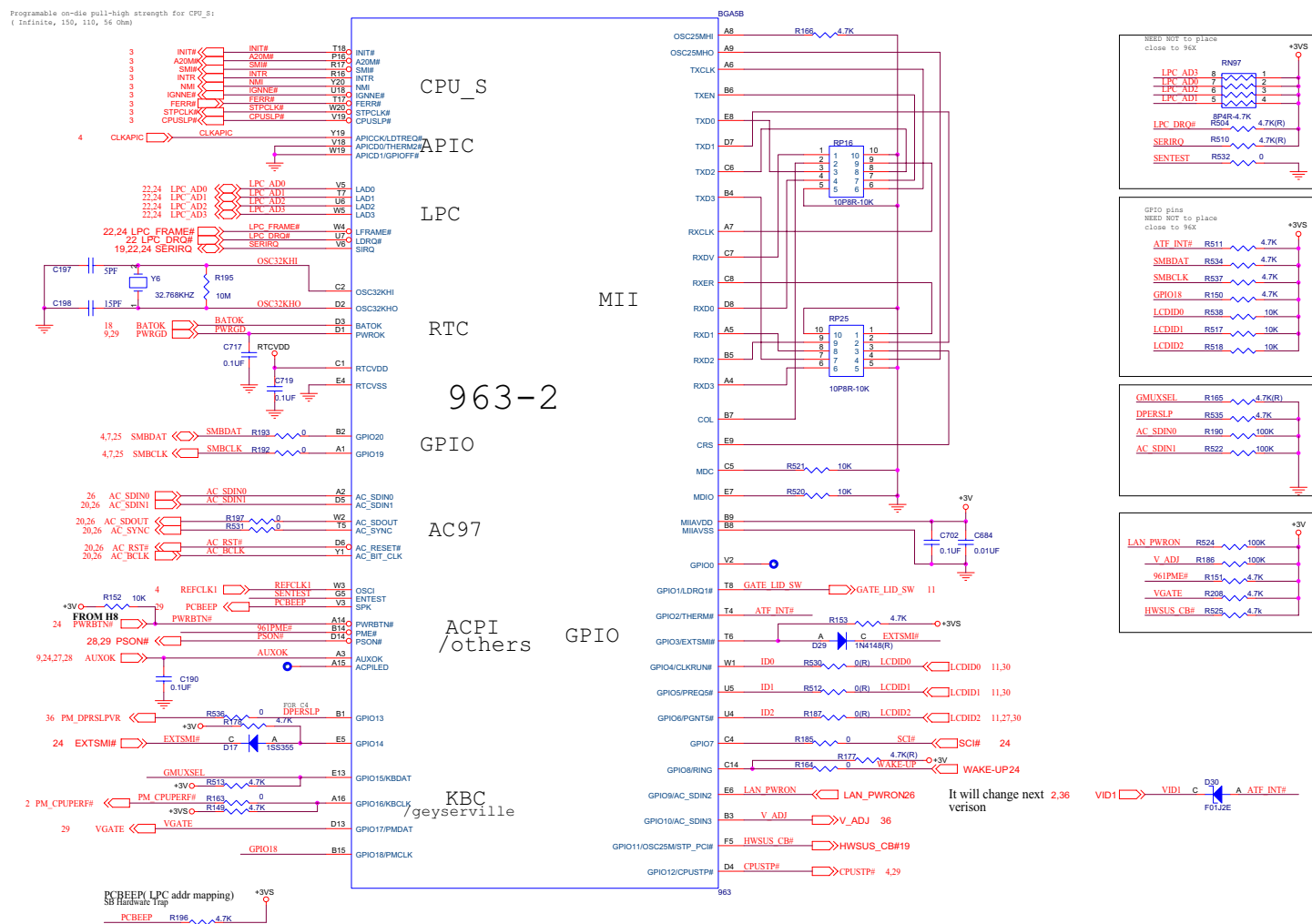
Sheet 12 of 42  
Panel Con & LED  
Indicator

## 963-1 (PCI/IDE/HyperZip) 1 of 4

**B - 14 963-1 (PCI/IDE/HyperZip) 1 of 4 (71-D40U0-D03)**



## 963-2 (Misc Signals) 2 of 4



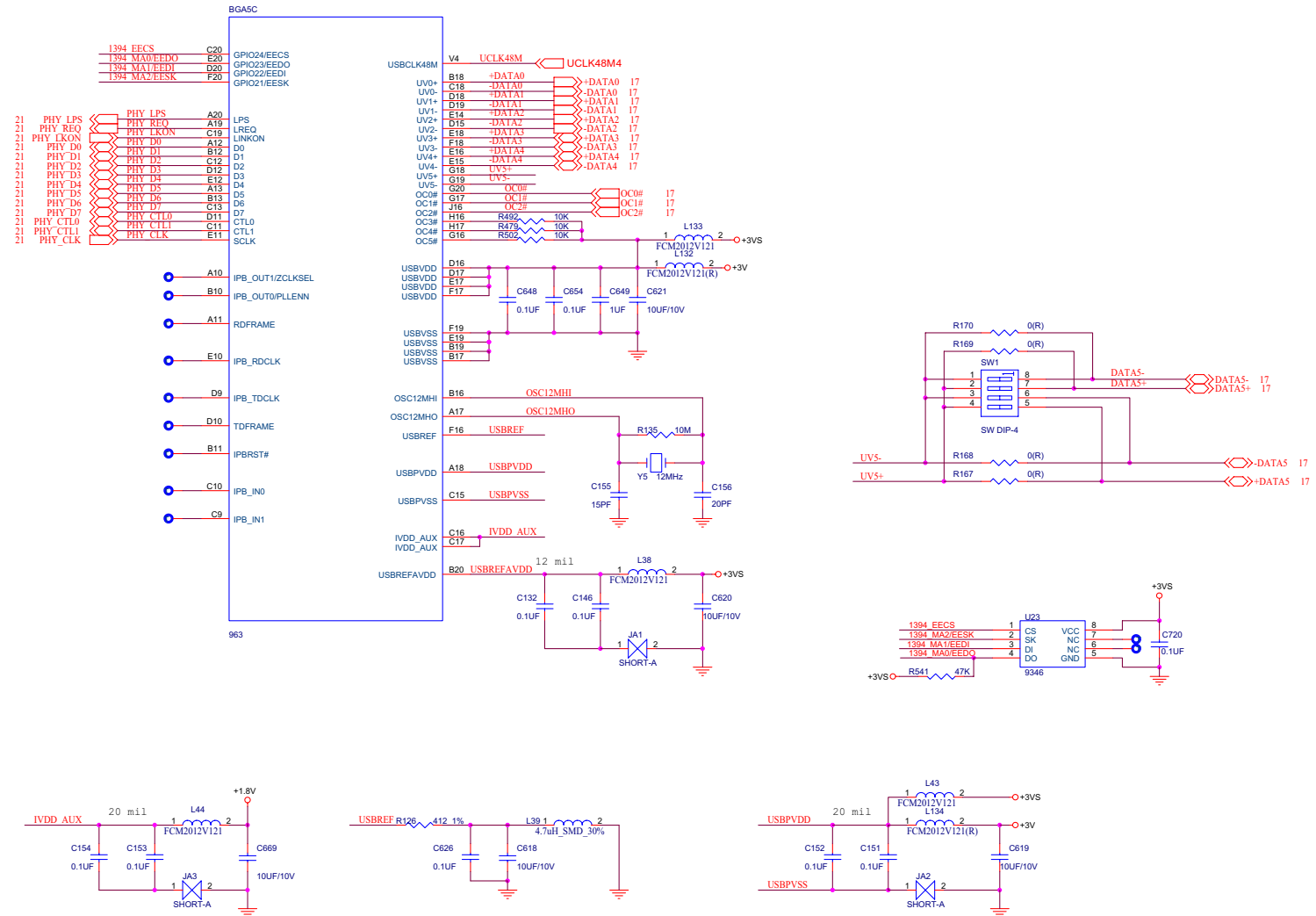
Sheet 14 of 42  
963-2  
(Misc Signals)  
2 of 4

## B.Schematic Diagrams

## Schematic Diagrams

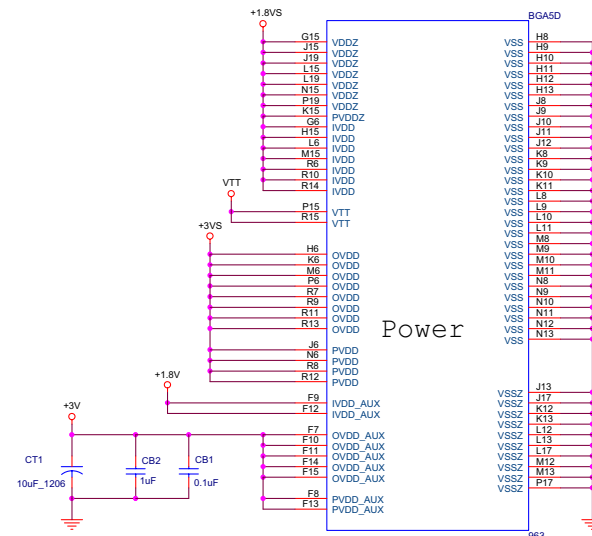
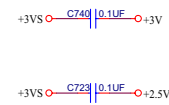
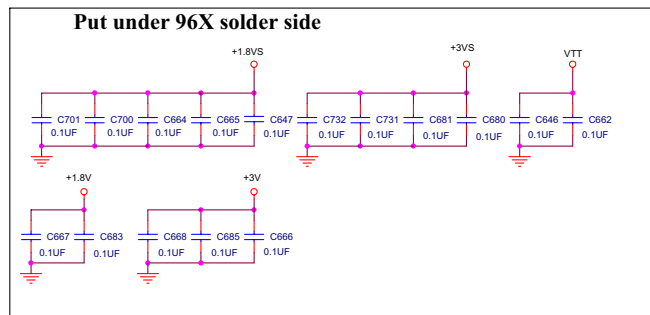
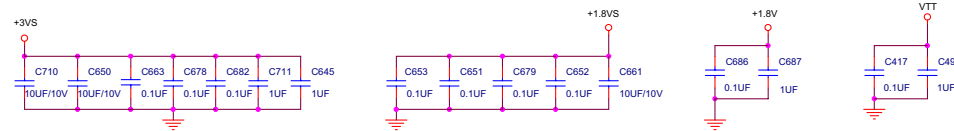
### 963-3 (USB I/F) 3 of 4

Sheet 15 of 42  
963-3 (USB I/F)  
3 of 4

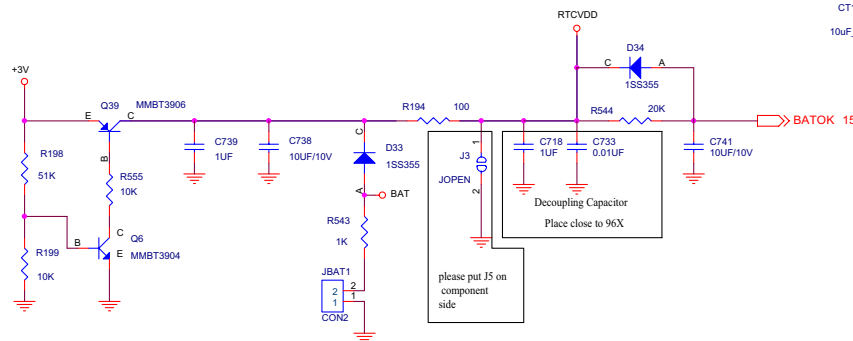




# 963-4 (Power & RTC) 4 of 4

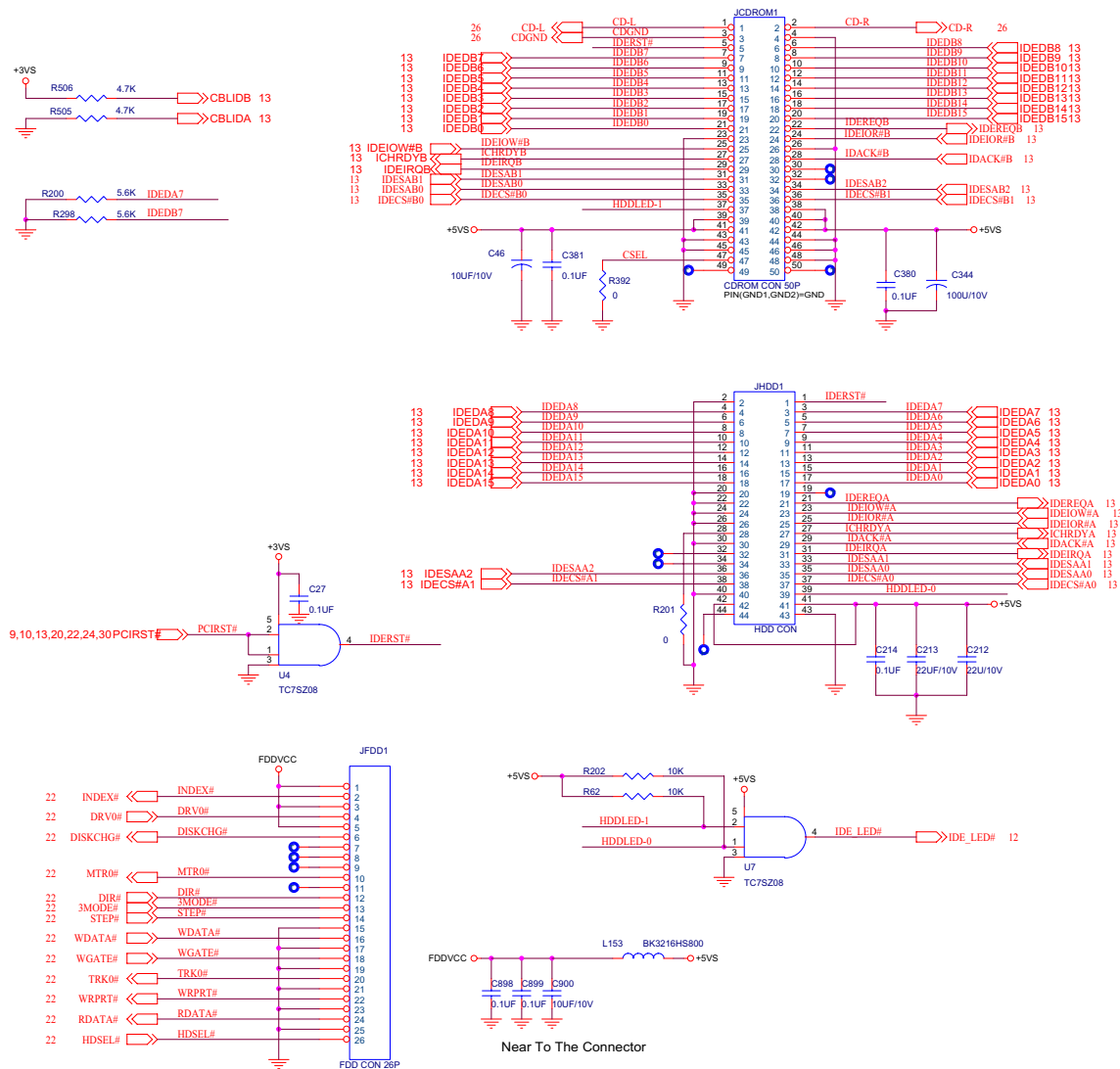


Sheet 16 of 42  
963-4  
(Power & RTC)  
4 of 4

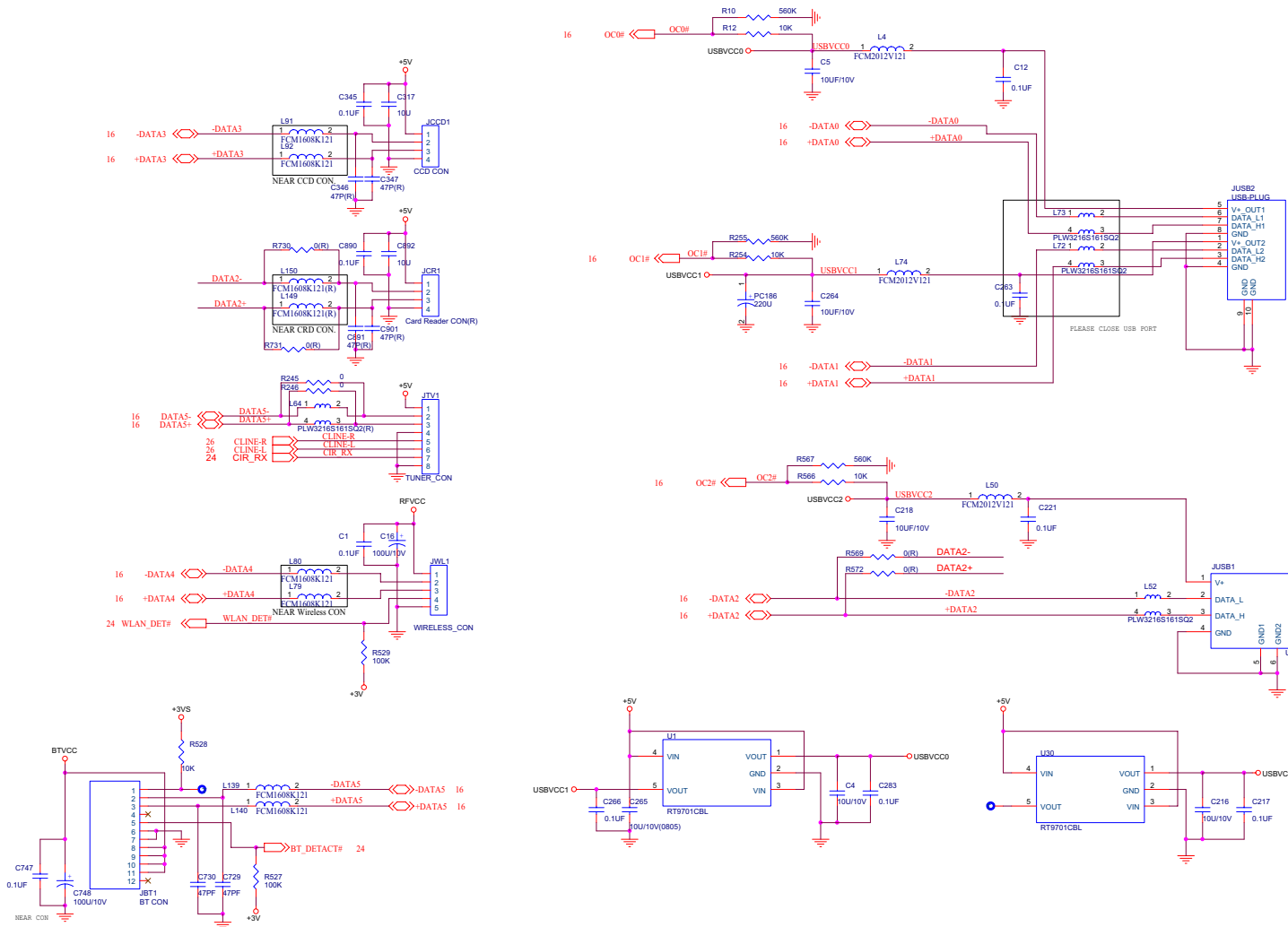


## HDD/Combo Connector

**Sheet 17 of 42**  
**HDD/Combo**  
**Connector**



## USB Port



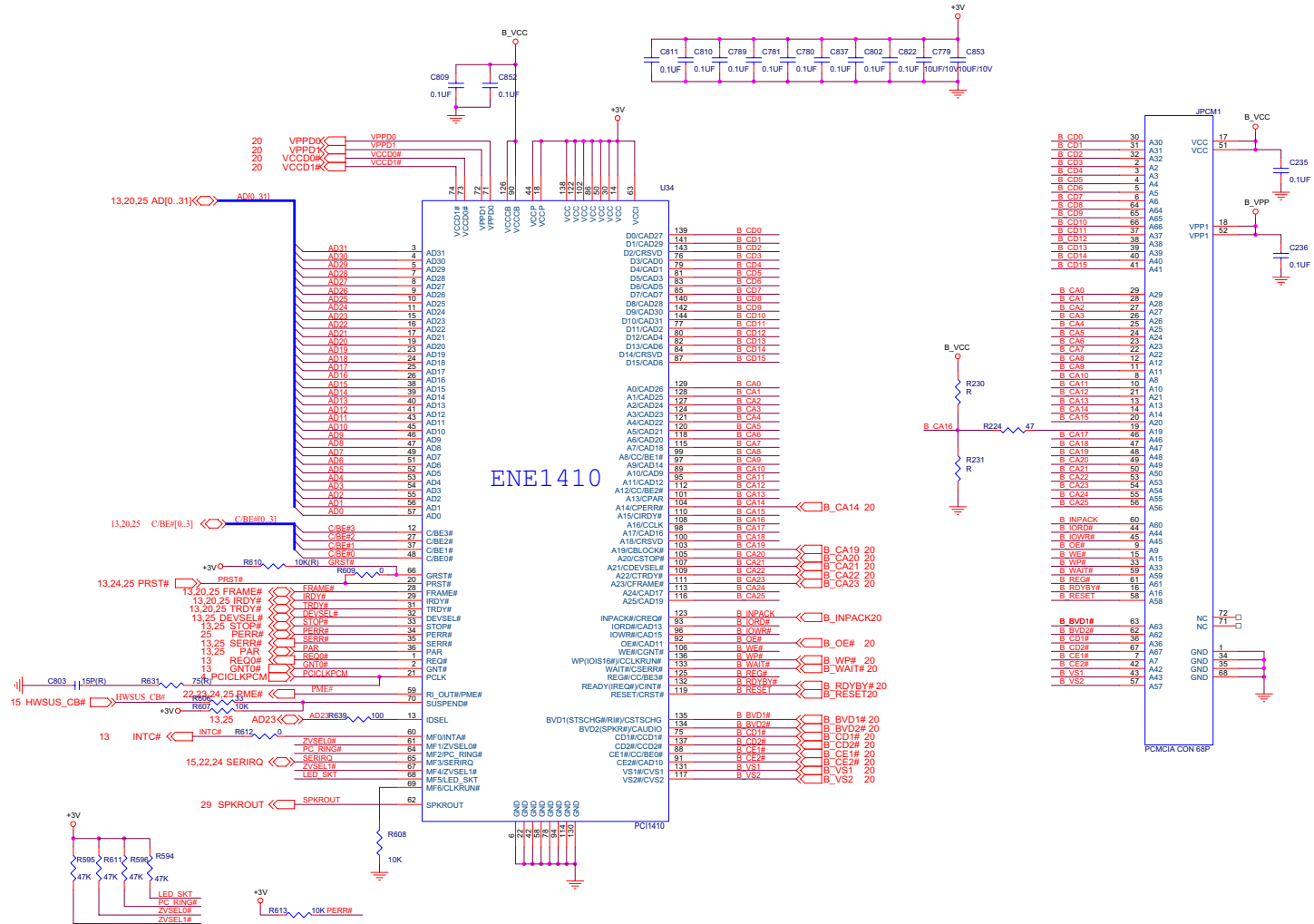
Sheet 18 of 42  
USB Port

## B.Schematic Diagrams

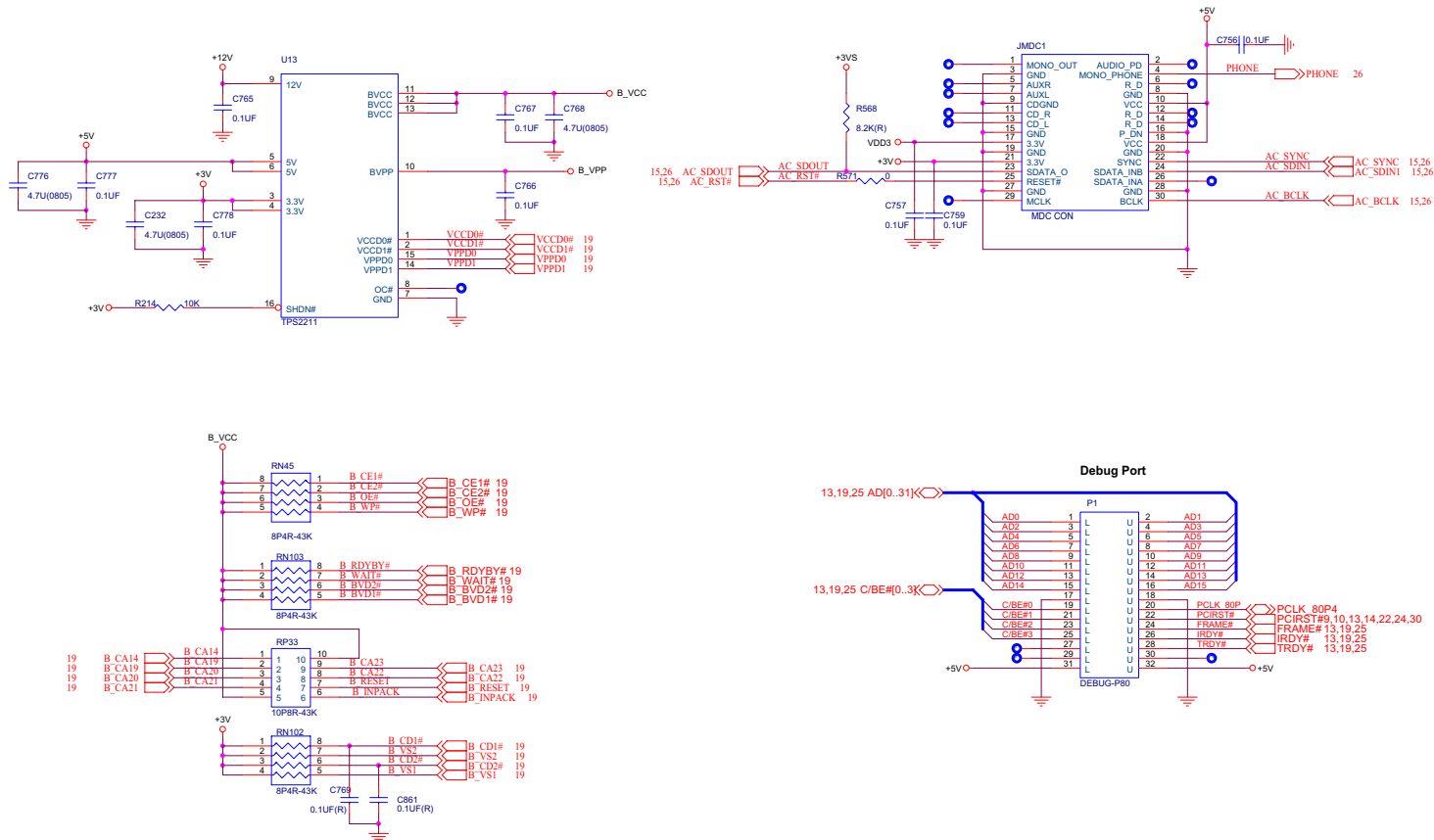
## Schematic Diagrams

# PCMCIA ENE1410

Sheet 19 of 42  
PCMCIA ENE1410



# PCMCIA Power & MDC Interface

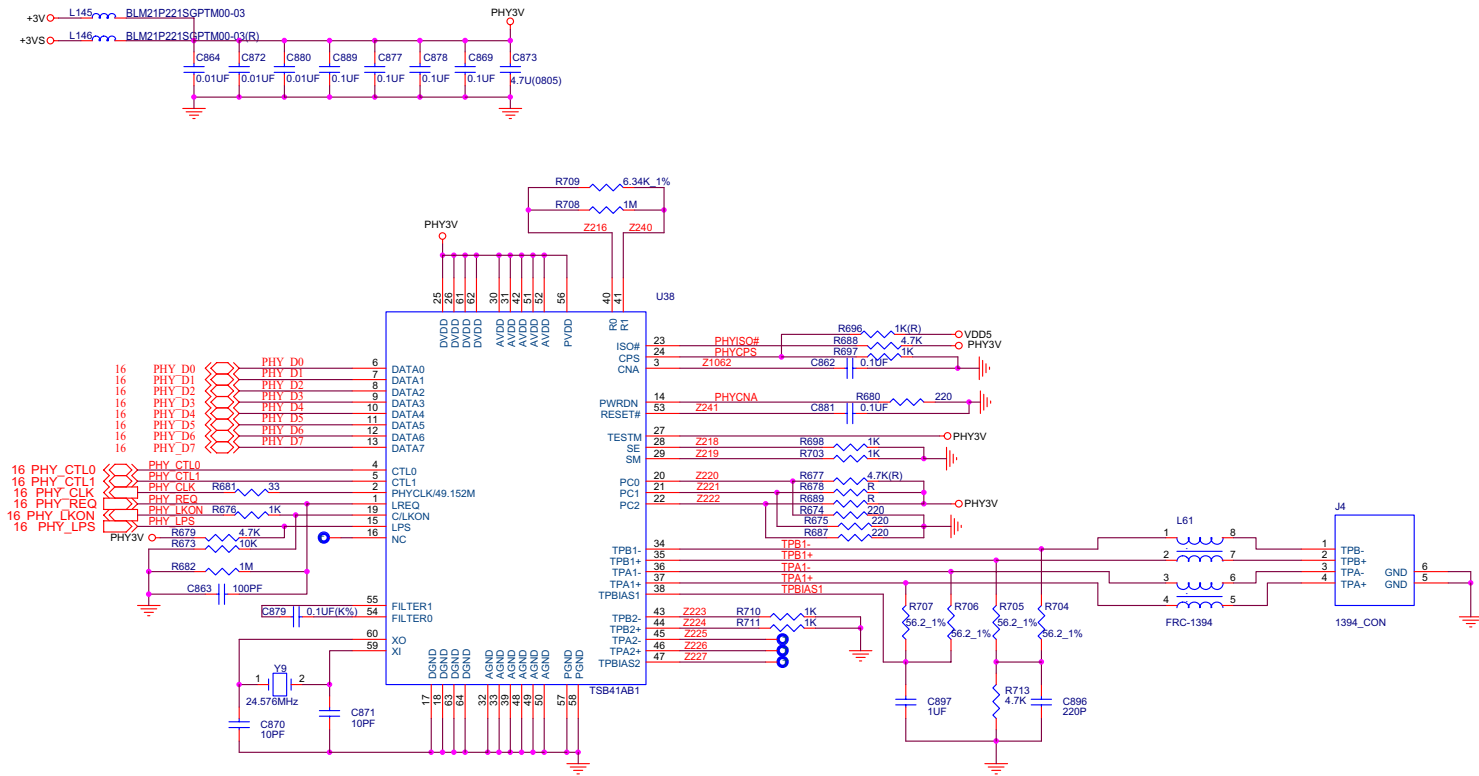


Sheet 20 of 42  
PCMCIA Power &  
MDC Interface

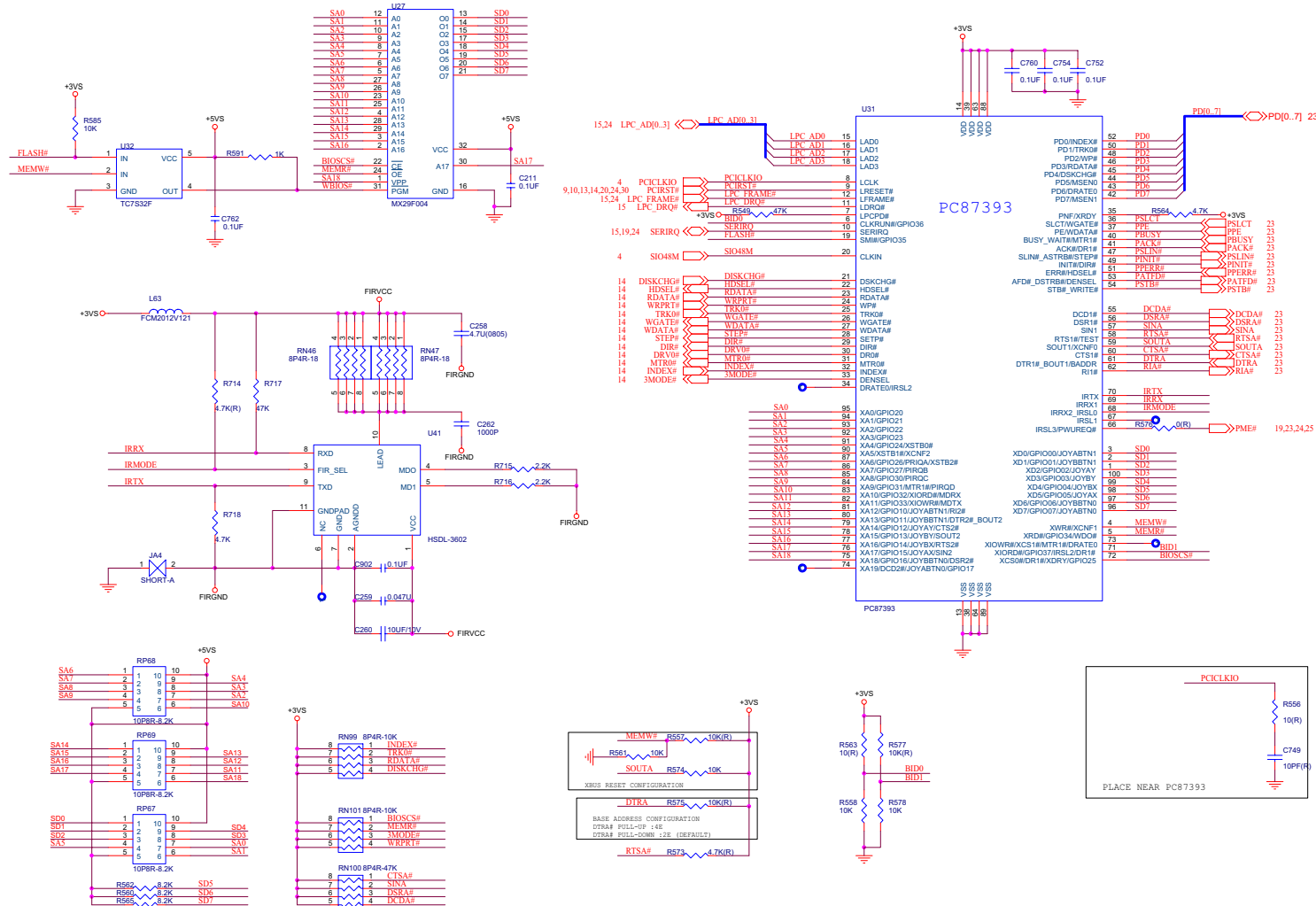
## Schematic Diagrams

# 1394 PHY TSB41LV01

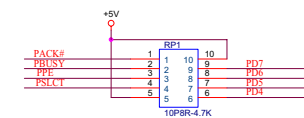
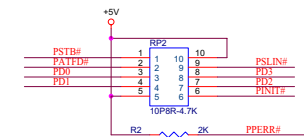
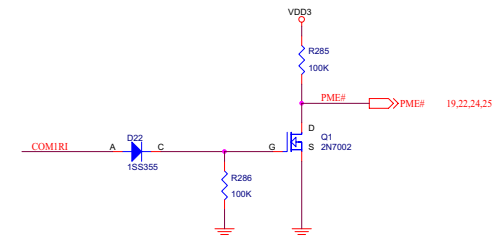
Sheet 21 of 42  
1394 PHY  
TSB41LV01



## LPC Super I/O

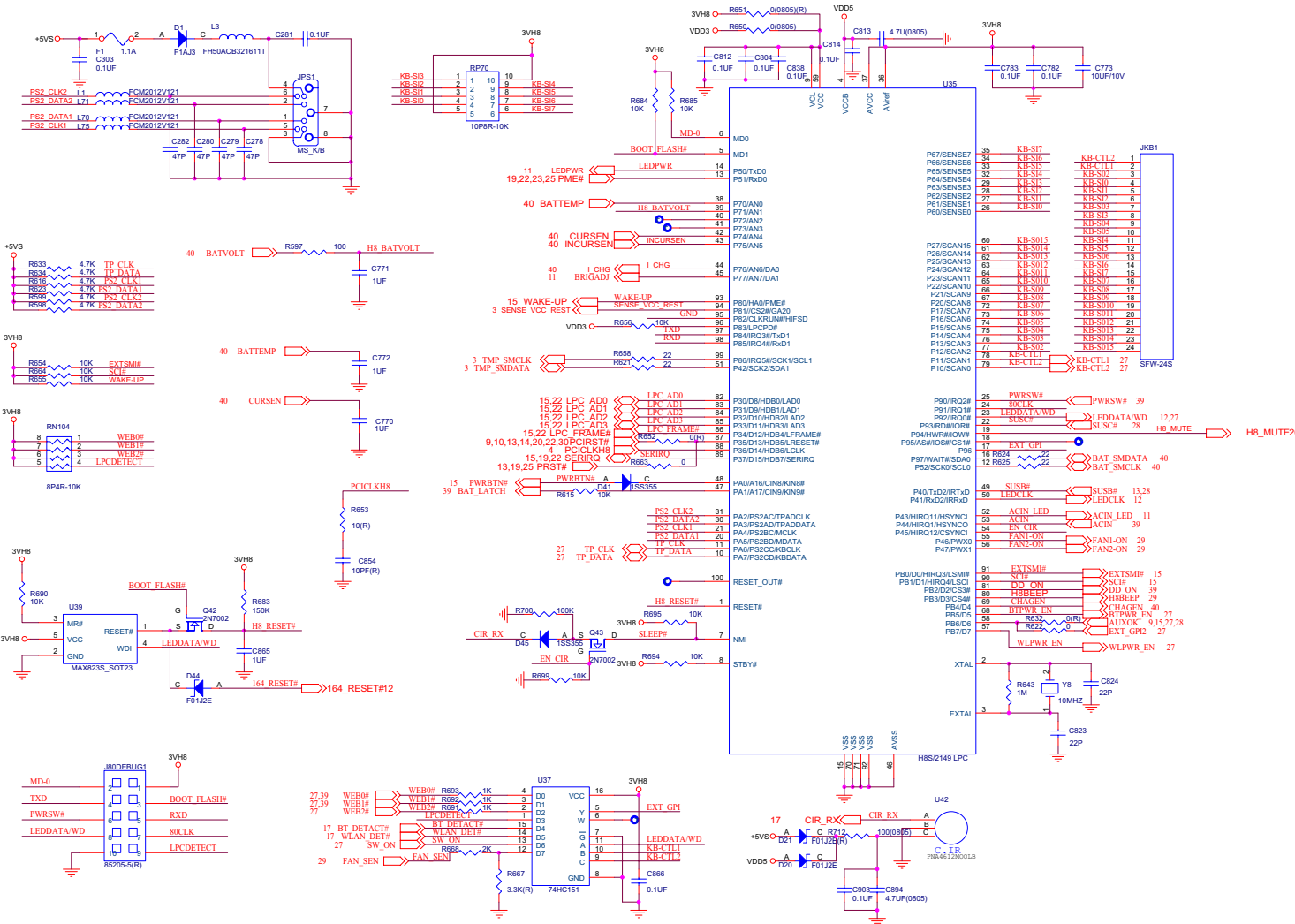


## B.Schematic Diagrams

[illegible]



# LPC H8

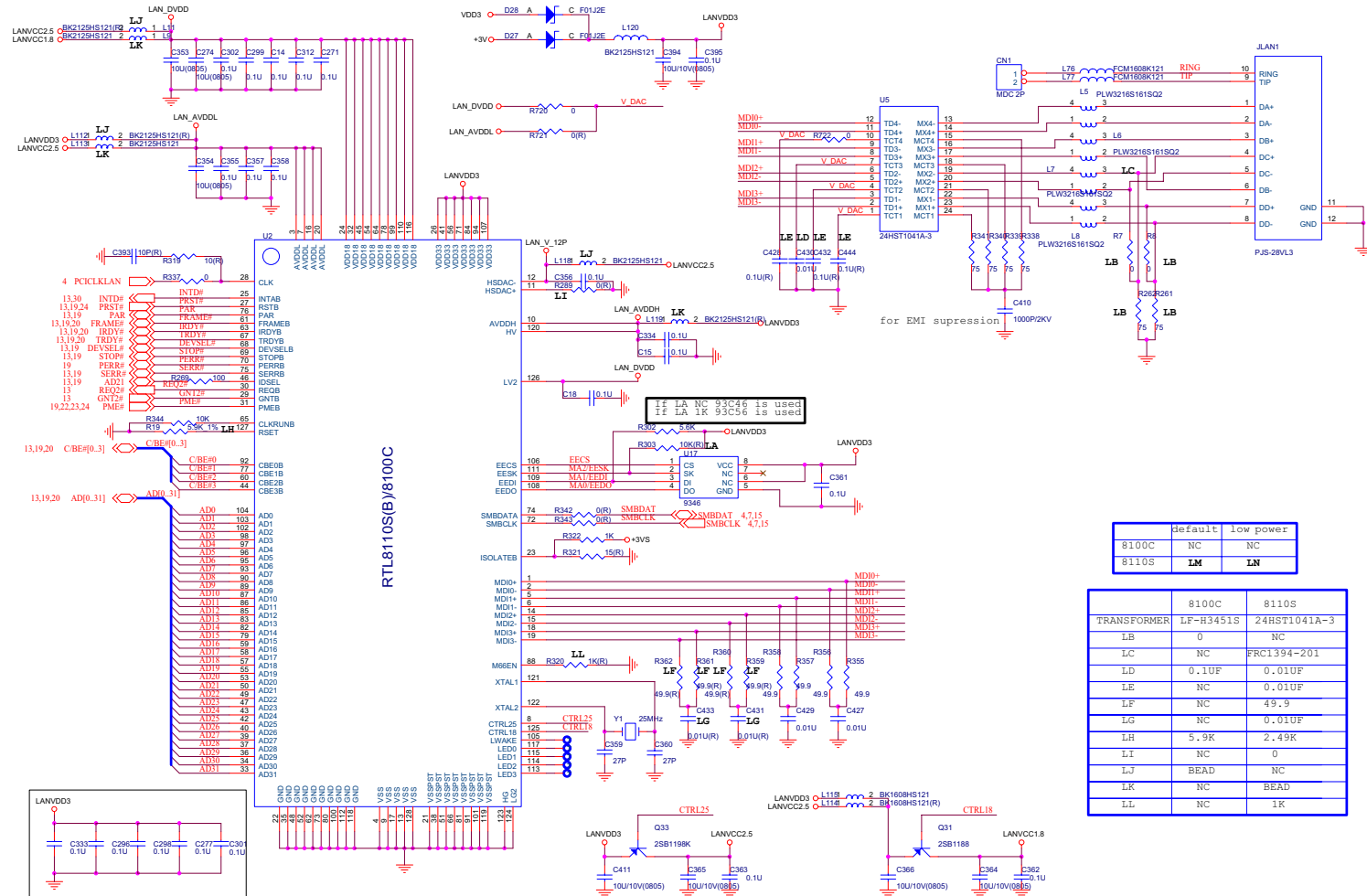


Sheet 24 of 42  
LPC H8

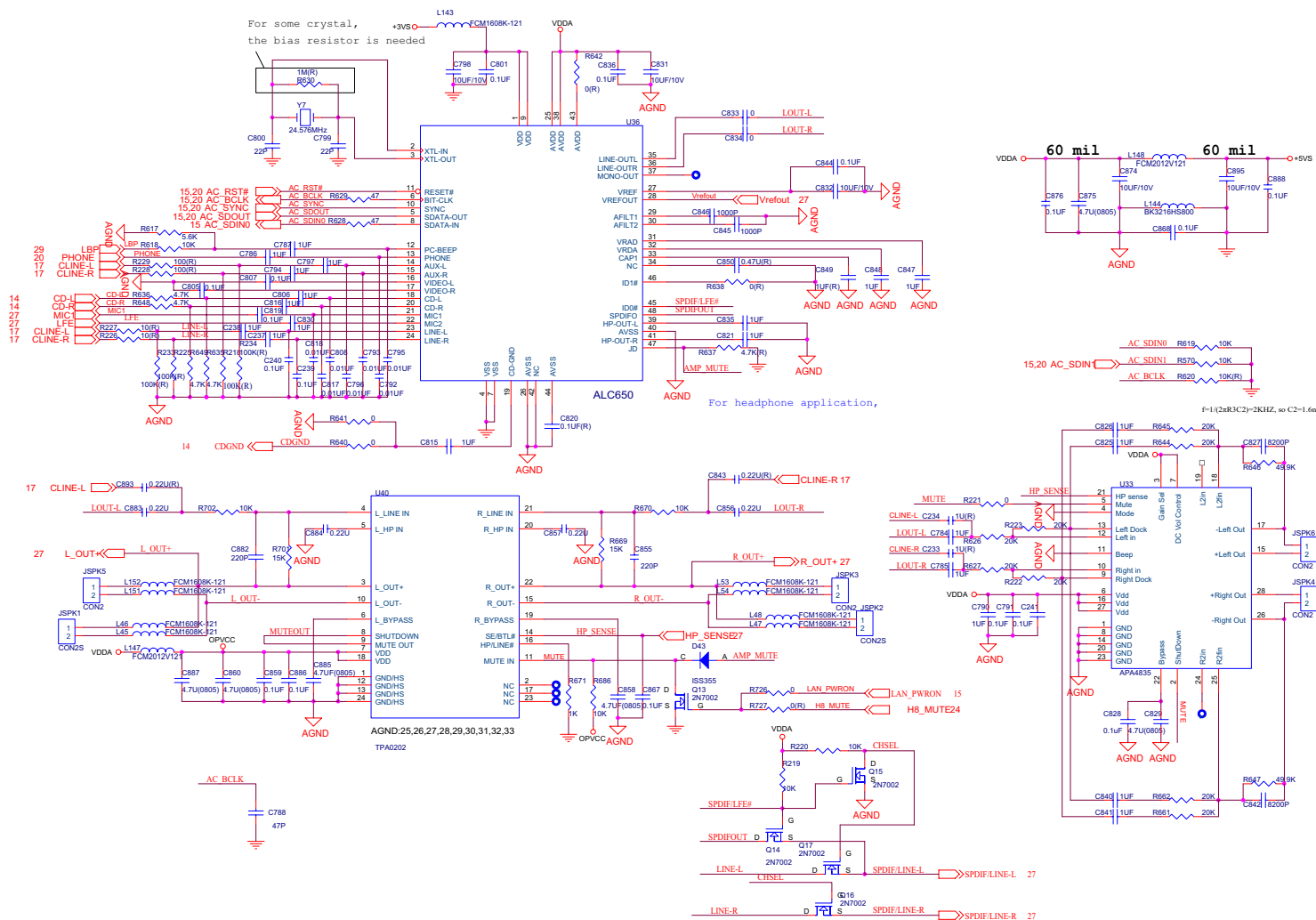
## Schematic Diagrams

# LAN RTL8110S(B)-32/RTL8100C

Sheet 25 of 42  
LAN RTL8110S(B)-  
32/RTL8100C



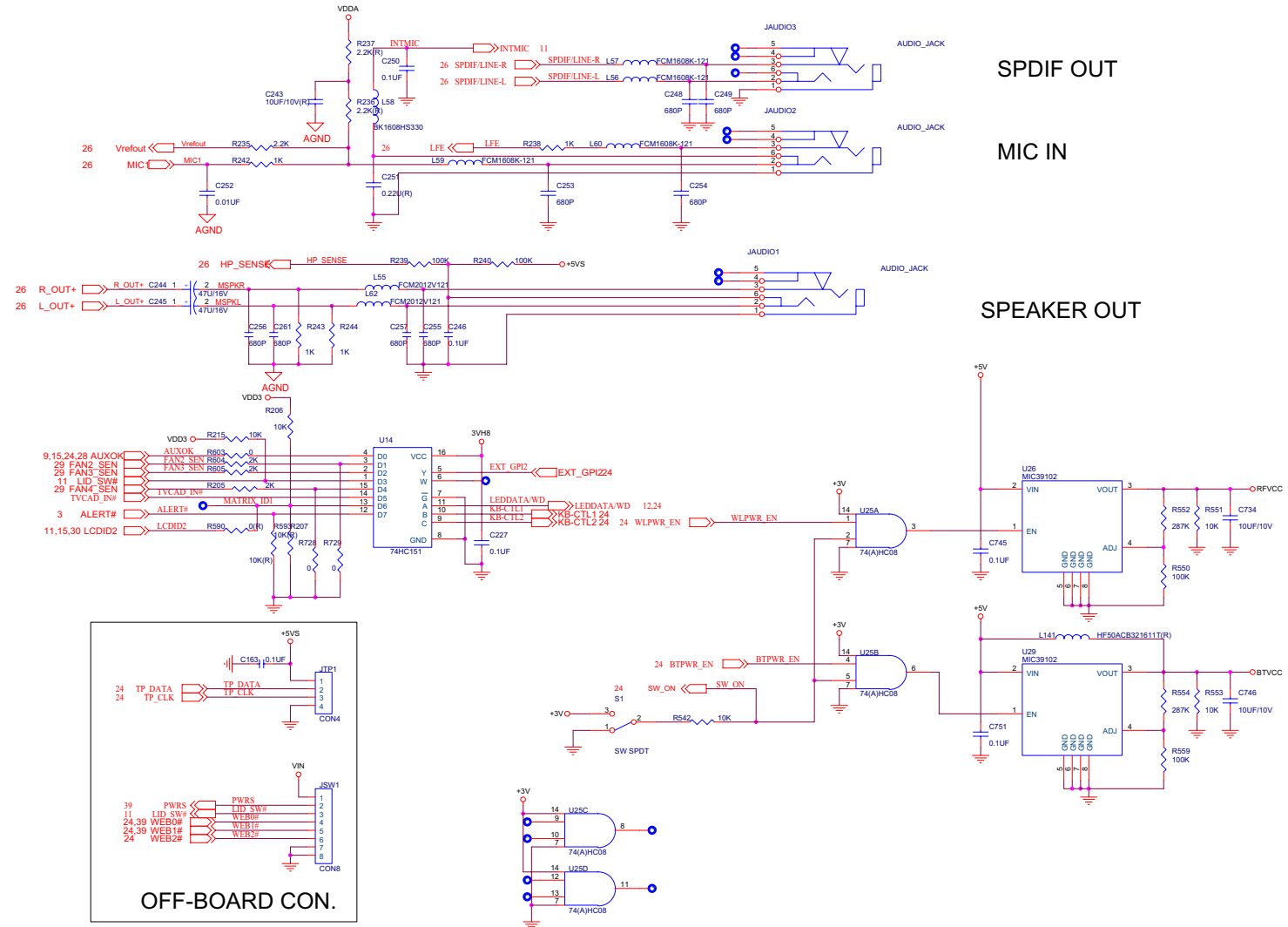
## Audio Codec ALC650



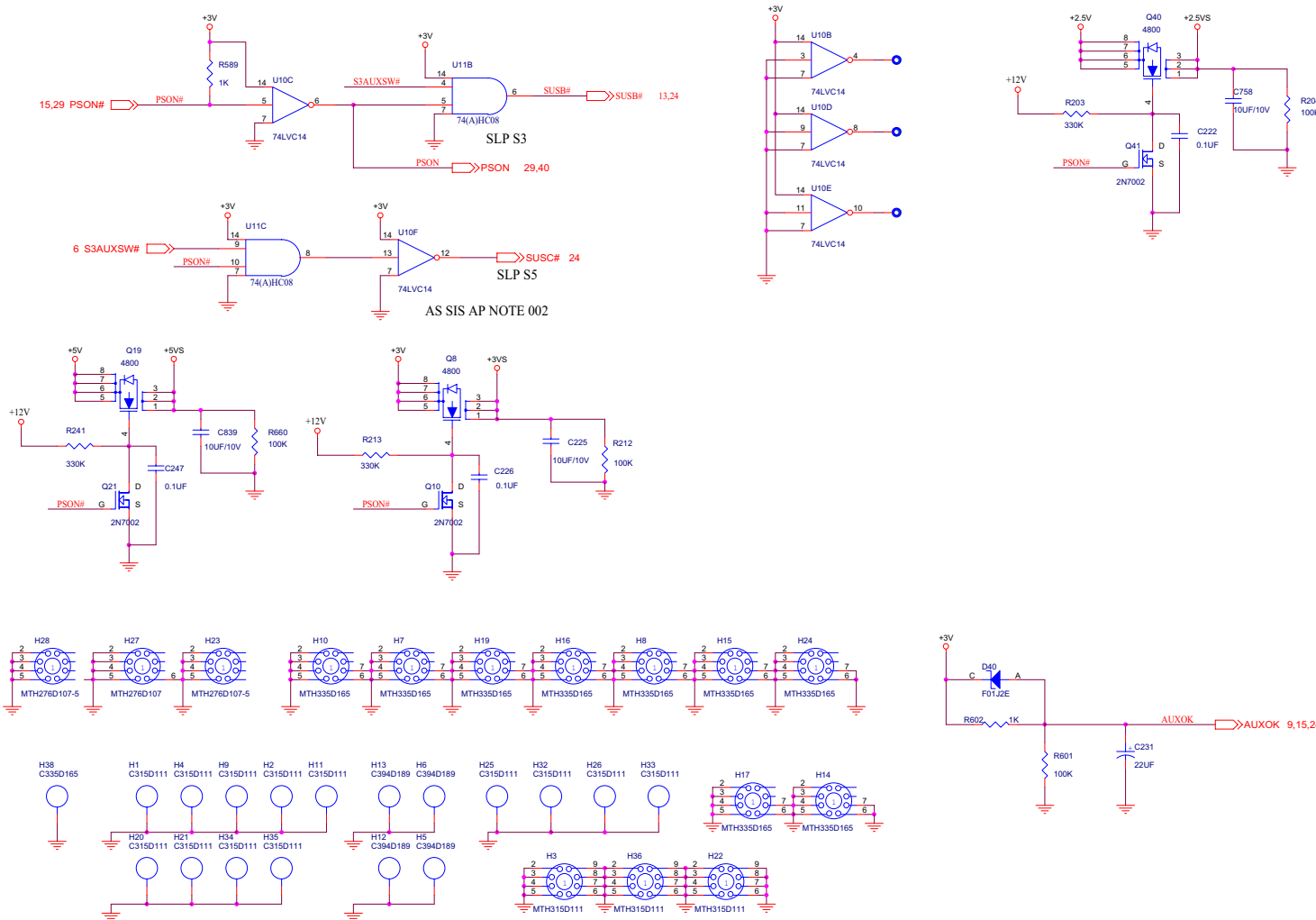
Sheet 26 of 42  
Audio Codec  
ALC650

## Audio Out & Off Board Connectors

**Sheet 27 of 42**  
**Audio Out & Off**  
**Board Connectors**



# System Power Control

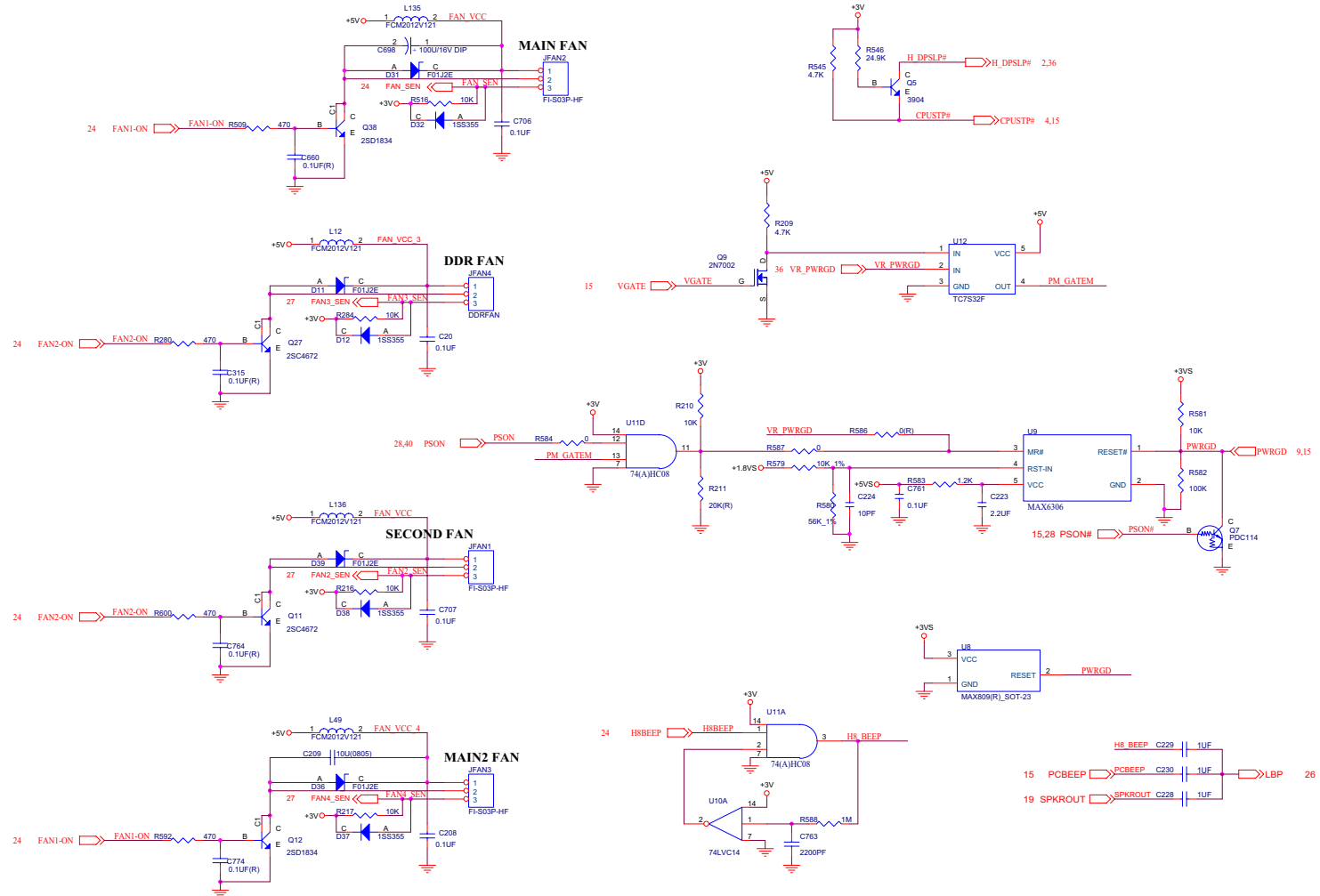


Sheet 28 of 42  
System Power  
Control

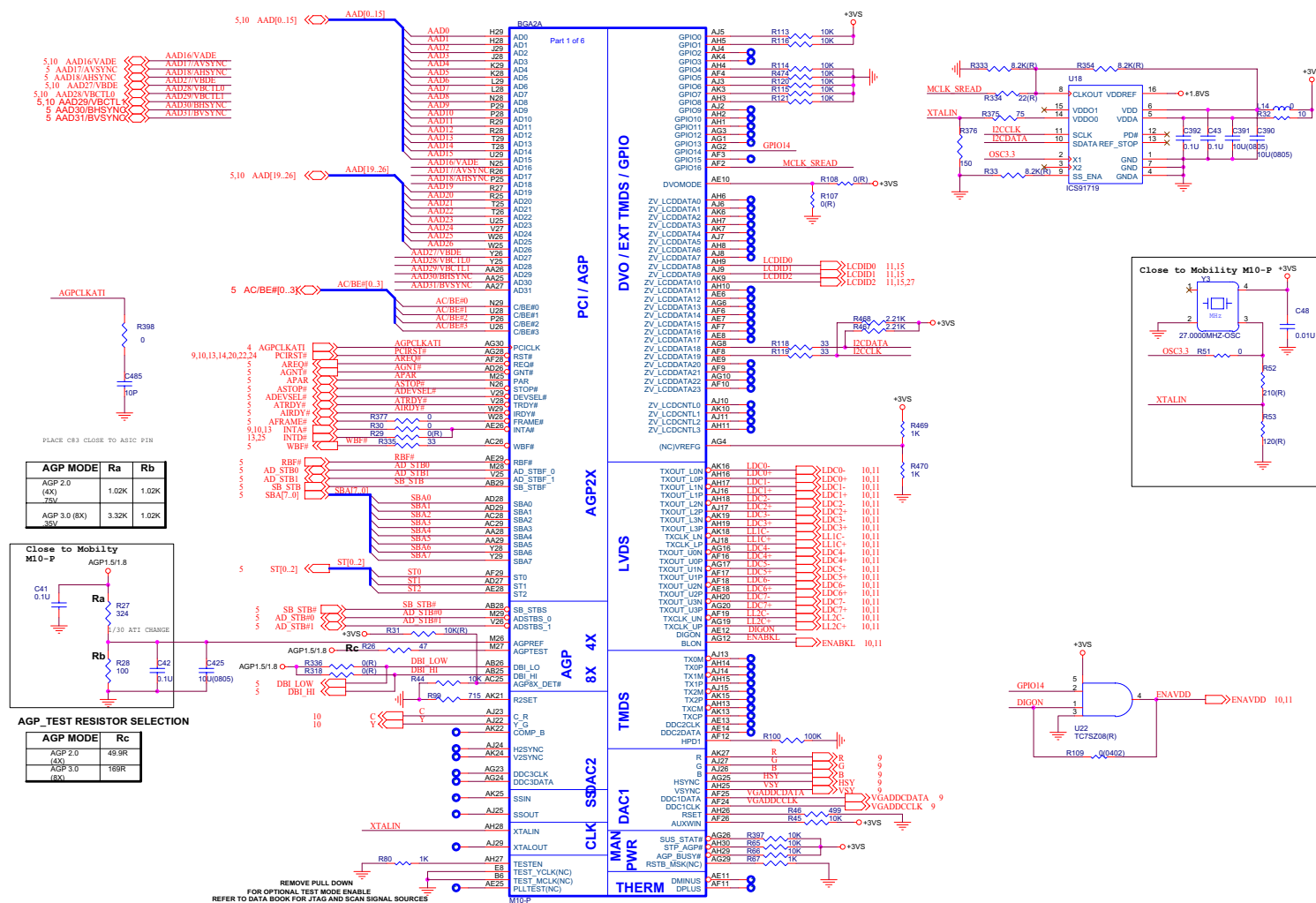
## Schematic Diagrams

# Fan Control and SpeedStep

Sheet 29 of 42  
Fan Control &  
SpeedStep



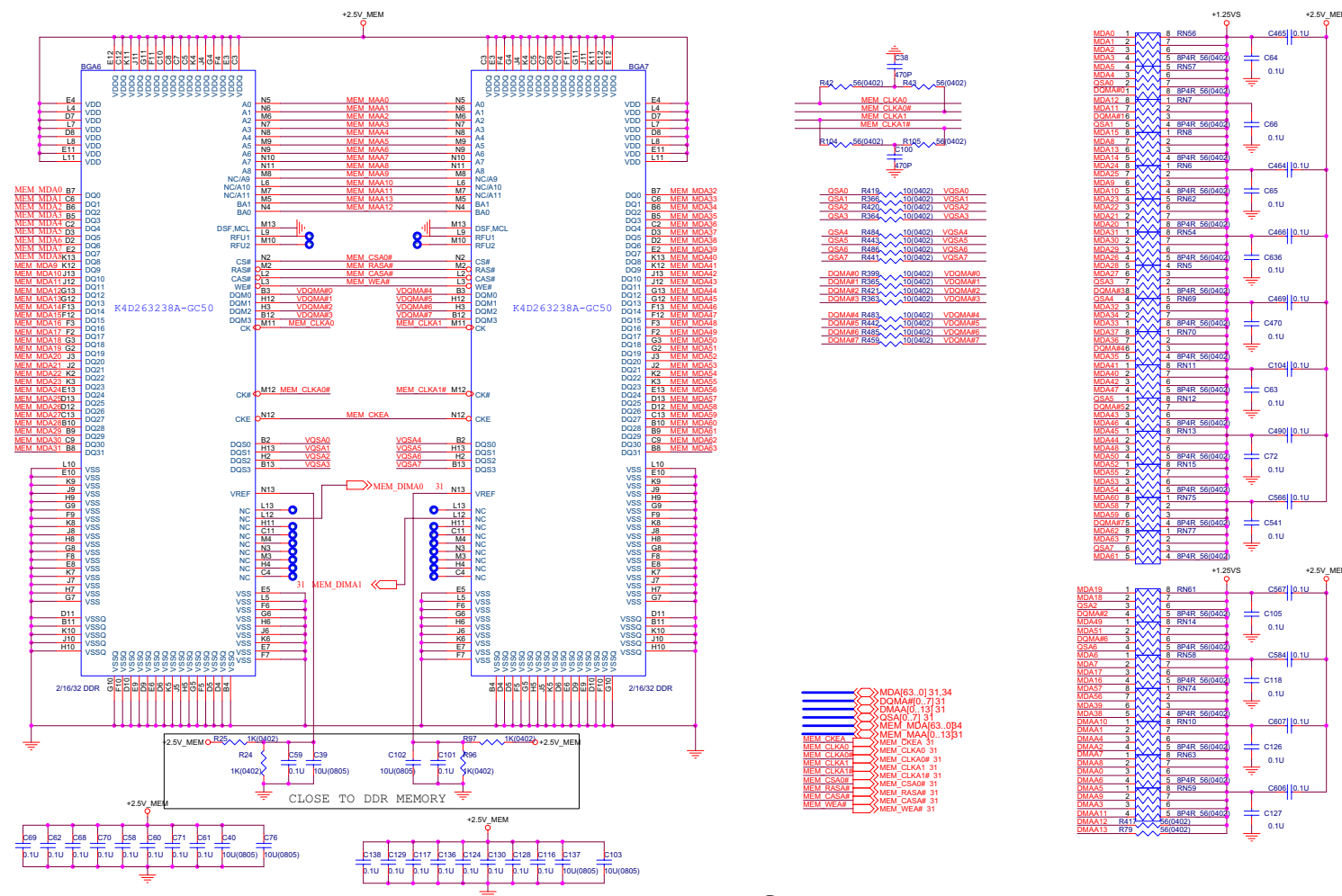
## Mobility M10-P







## VGA DDR DRAM Channel A

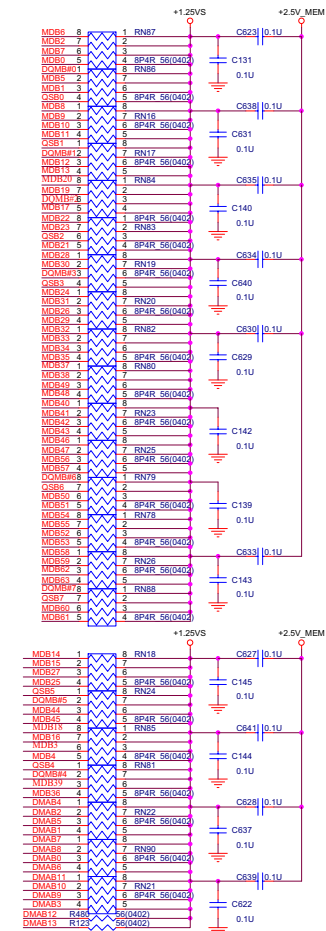
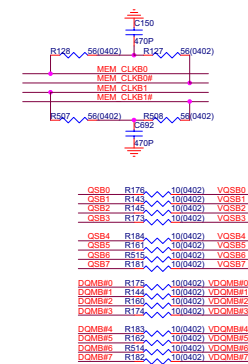


## CHANNEL A

VGA DDR DRAM Channel A (71-D40U0-D03) B - 33

## VGA DDR DRAM Channel B

Sheet 33 of 42  
VGA DDR DRAM  
Channel B



## CHANNEL B

# VGA DDR DRAM Termination



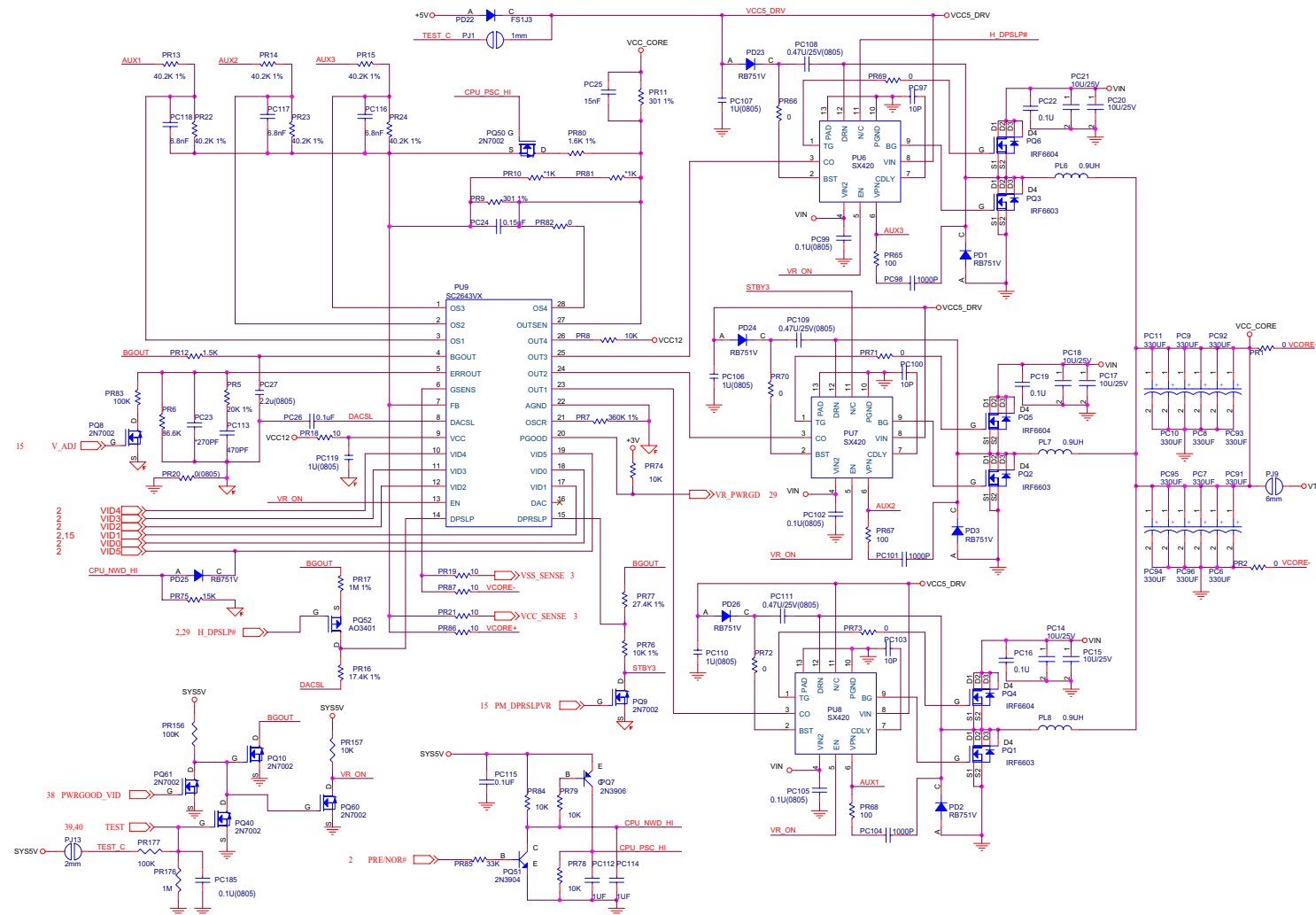
Sheet 34 of 42  
VGA DDR DRAM  
Termination

## Mobility M10-P POW

## B.Schematic Diagrams



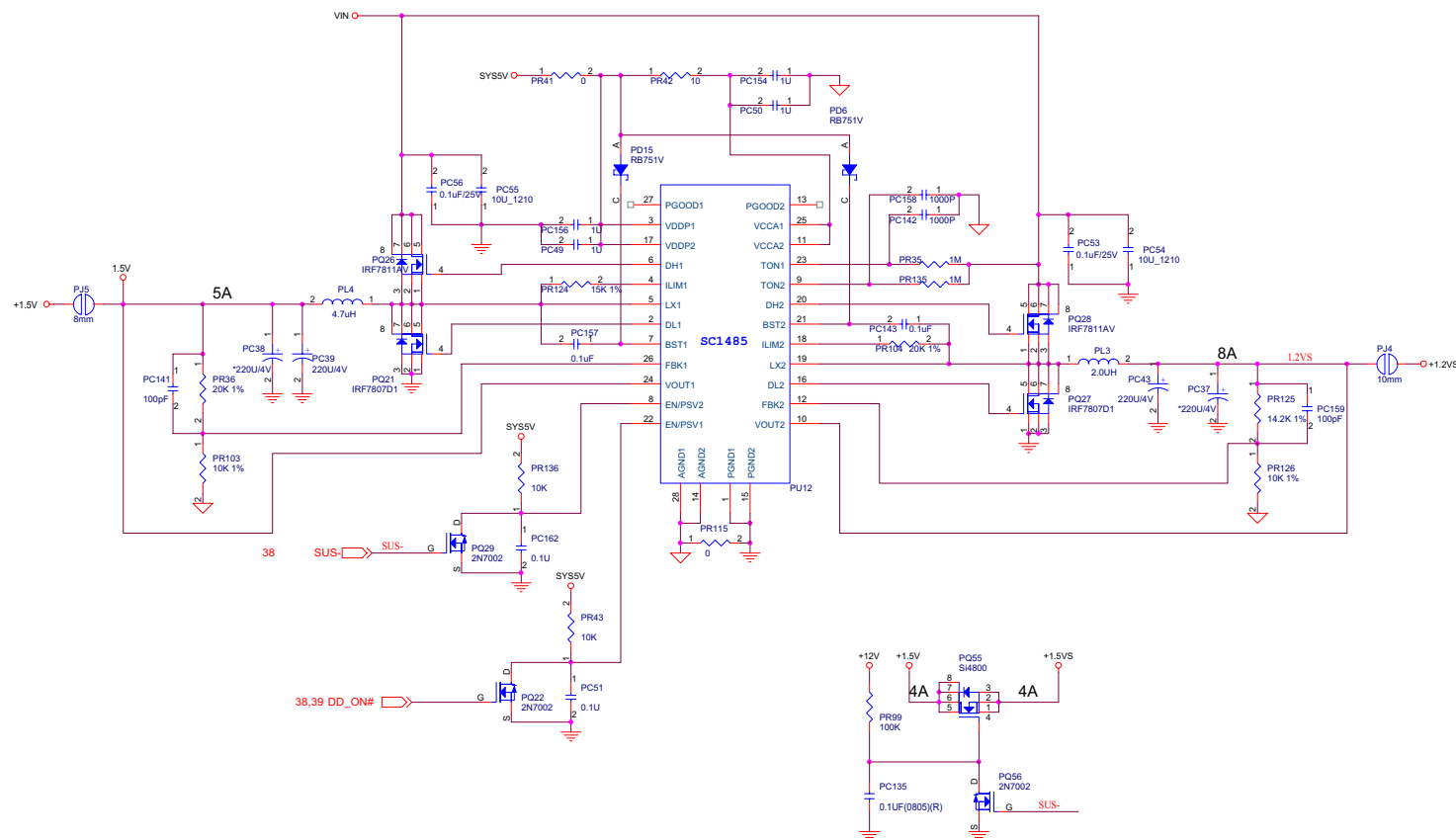
# VCORE



**Sheet 36 of 42**  
**VCORE**

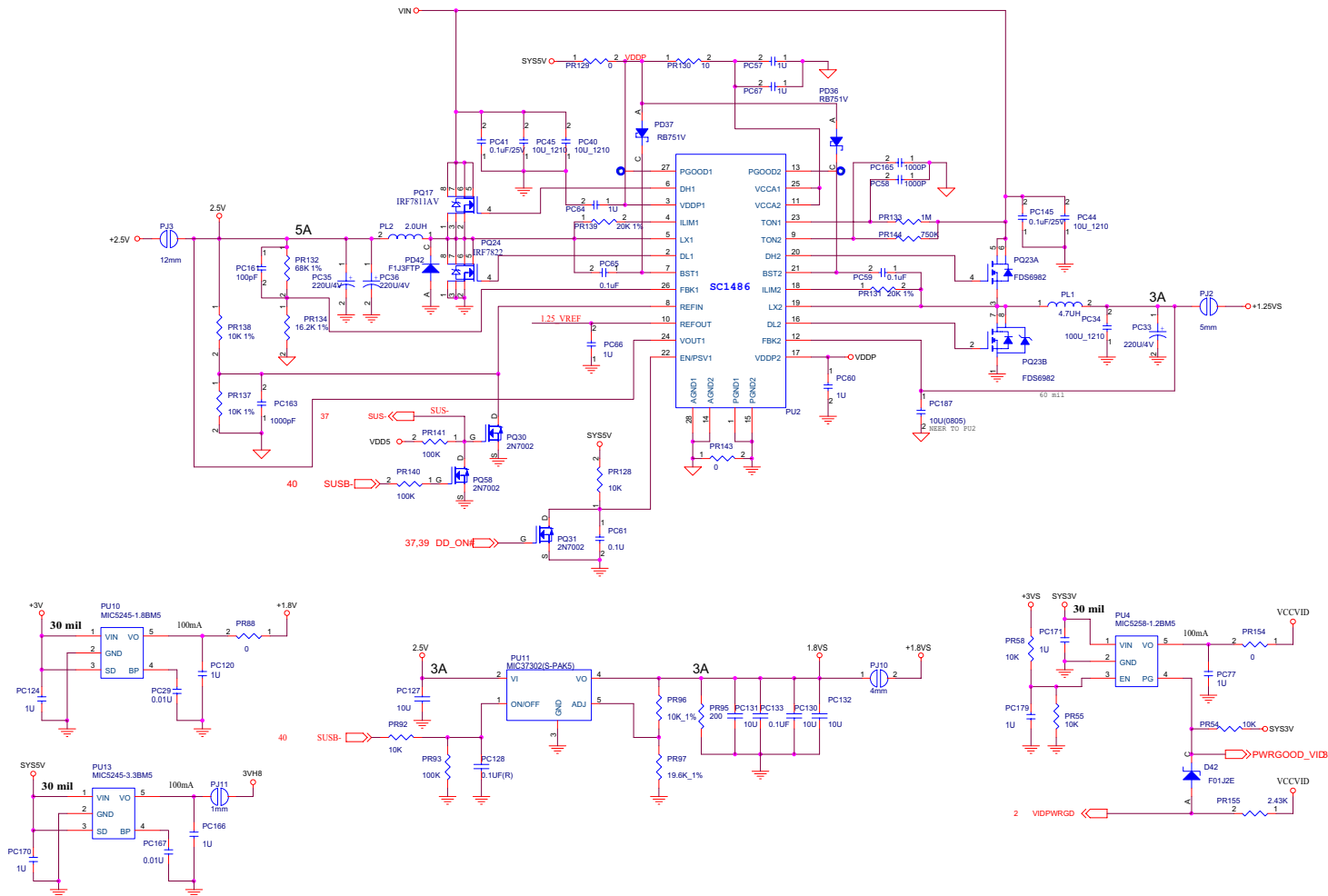
Sheet 37 of 42  
+1.2V, +1.5V

## B.Schematic Diagrams



+2.5V, +1.25V, +1.8V

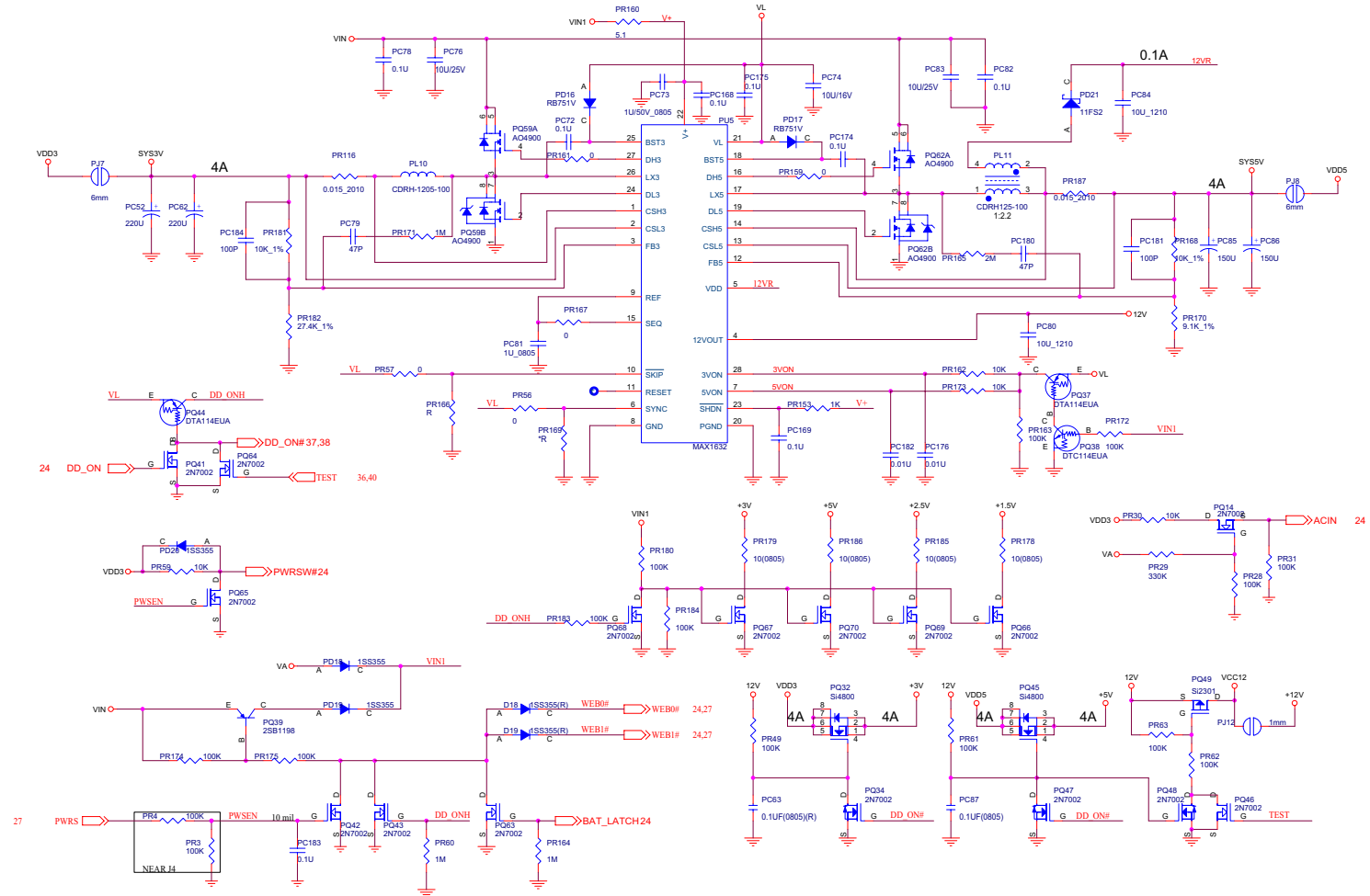
Sheet 38 of 42  
+2.5V, +1.25V,  
+1.8V



## Schematic Diagrams

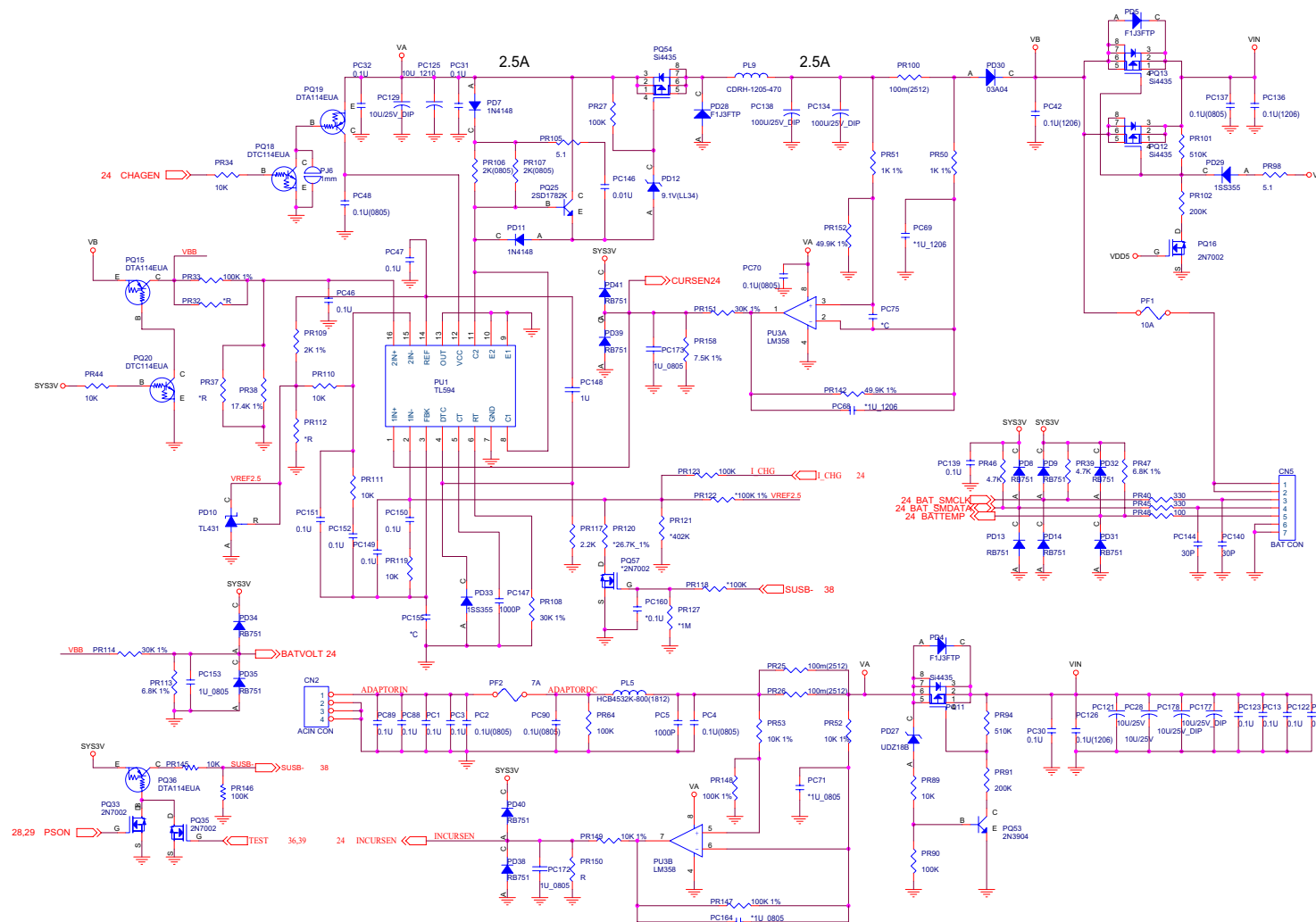
**+3V, +5V, +12V**

Sheet 39 of 42  
+3V, +5V, +12V





# Charger



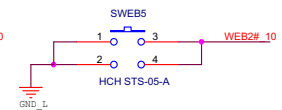
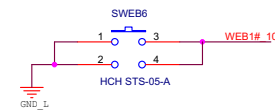
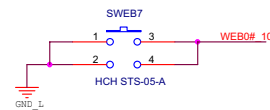
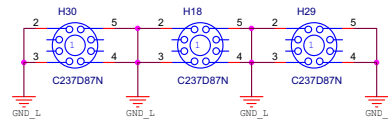
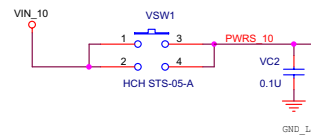
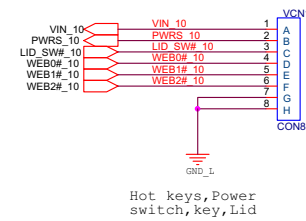
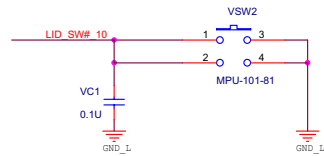
Sheet 40 of 42  
Charger

## B.Schematic Diagrams

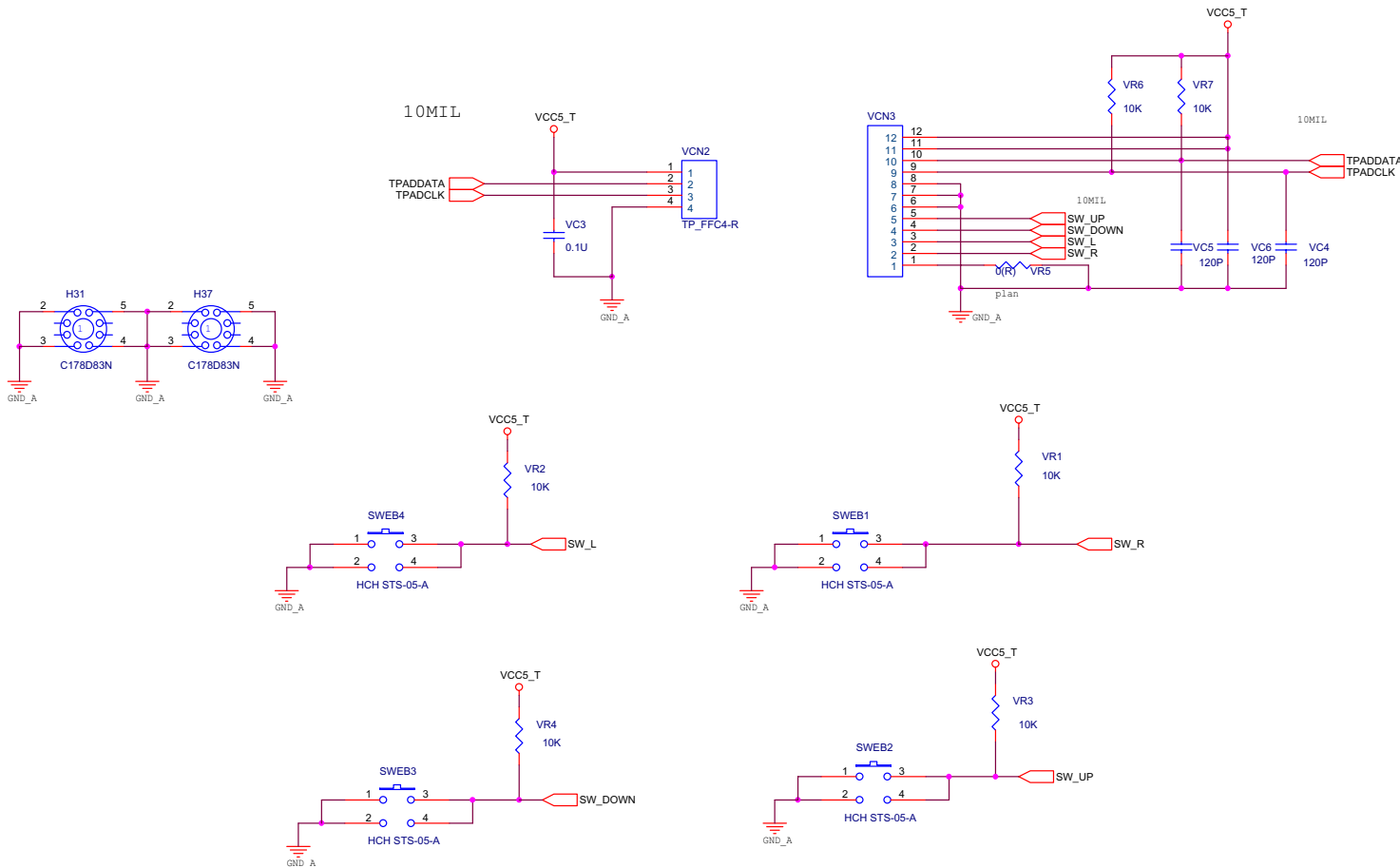
## Schematic Diagrams

### SW Board and HotKey

Sheet 41 of 42  
SW Board and  
HotKey



# TouchPad and Switch Board



Sheet 42 of 42  
TouchPad and  
Switch Board

## Schematic Diagrams