

Annika Block Diagram

01

PCB STACK UP

6L

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

10.1" LCD Panel
Page 16

CRT
Page 12

Intel Pineview-M
Micro-FCBGA8
22 x 22 mm
TDP~5.5W
Page 3~5

DDR2 667MT/s
Single Channel

DDR2 SO-DIMM
2GB Max.
Page 11

XDP
Page 31

CLOCK GEN
9LRS3165
Page 2

SATA

2.5"HDD/SSD
Page 19

Intel
Tigerpoint
17 x 17 mm
MMAP 360 Balls
TDP~1.5W
Page 6~10

USB 2.0

0,1,2
USB2.0
Port x3
Page 21

4
Card
Reader
Page 13
Card Reader
Socket
Page 13

5
Bluetooth
/WLAN
Page 20

6
Touch Screen
Page 22

7
WWAN
Page 20
SIM Card Socket
Page 20

8
Webcam
Page 16

PCI-Express

X1
WWAN
Page 20
SIM Card
Page 20

X1
LAN
Realtek
RTL8103EL-VB
10/100
Page 15
RJ 45
Page 15

X1
WLAN
Page 20

X1
HD Decoder
Page 18
DDR II
64MByte
Page 18

SYSTEM POWER
+3VPCU/+5VPCU(RT8206)
PAGE 24

DDR II SMDDR_VTERM
+0.9VSMVREF/+1.8VSUS(RT8207)
PAGE 25

CPU CORE RT8152D
PAGE 26

SYSTEM CHARGER ISL6251AHAZ-T
PAGE 27

GFX CORE(RT9025)
+1.2V(RT9025)
+1.5V(RT9025)
PAGE 28
PAGE 29
PAGE 30

VCCP 1.05V(RT8209A)
PAGE 29

Touch Pad
Keyboard
Page 22

Power SW
Page 14

LPC

ENC KBC
KB3926D2
Page 23

HDA

AUDIO CODEC
92HD80BX
Page 17


BIOS
SPI Flash
Page 23

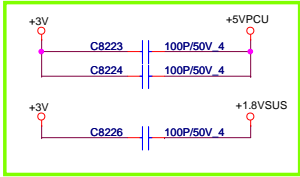
FAN
G991
Page 22

Int SPK
Page 17


Digital MIC
Page 17

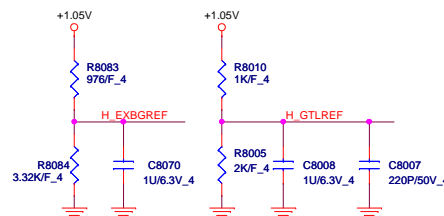
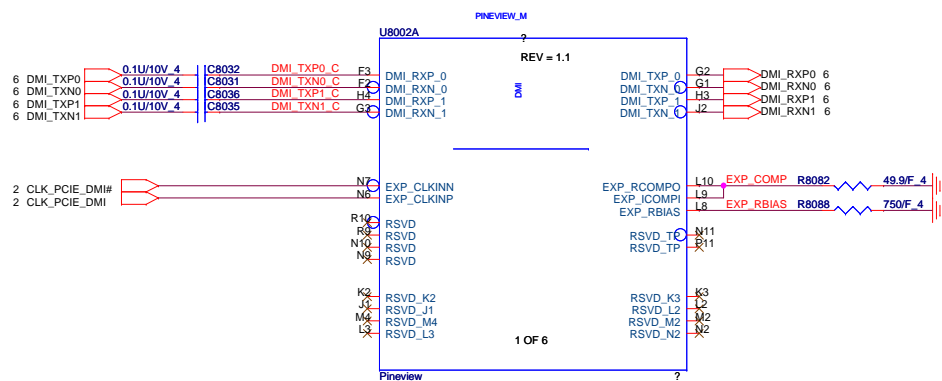
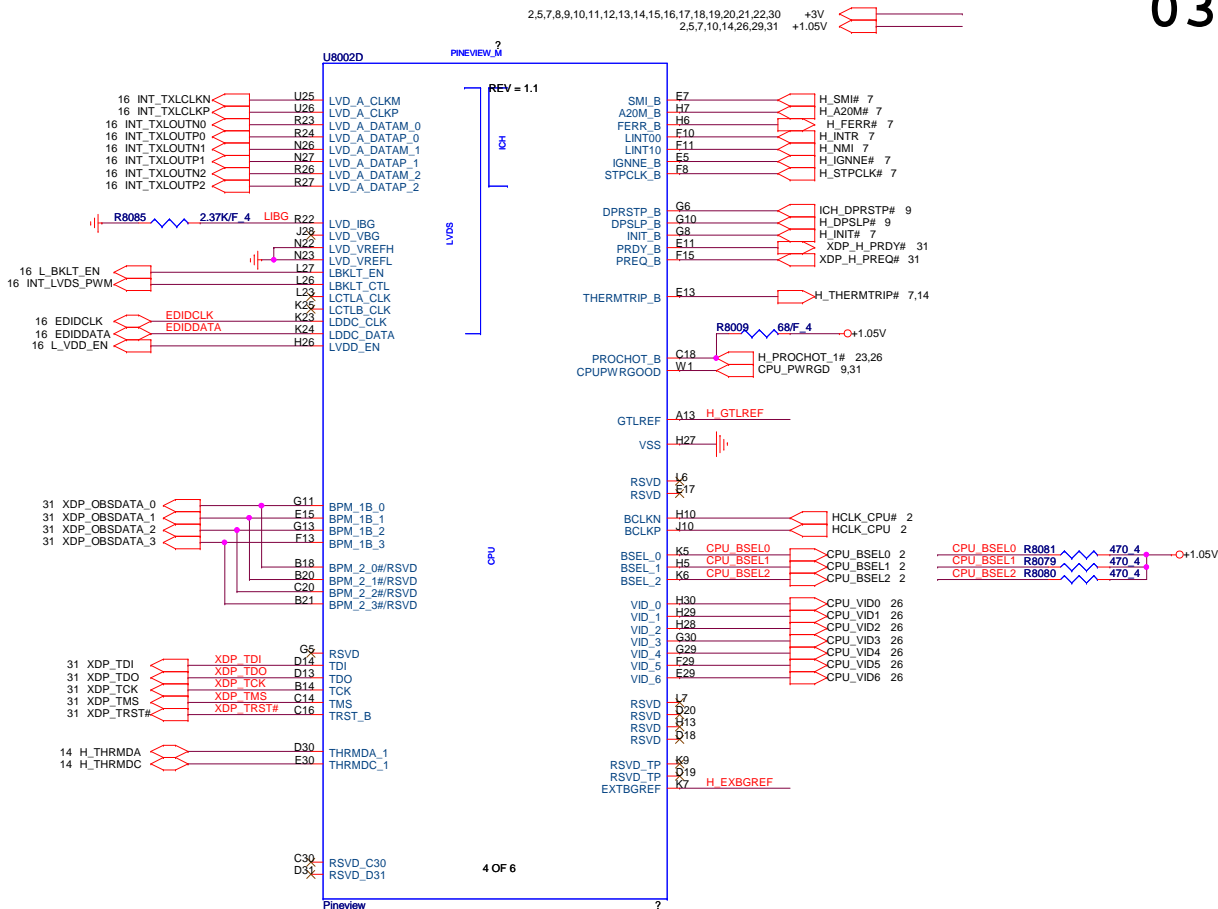
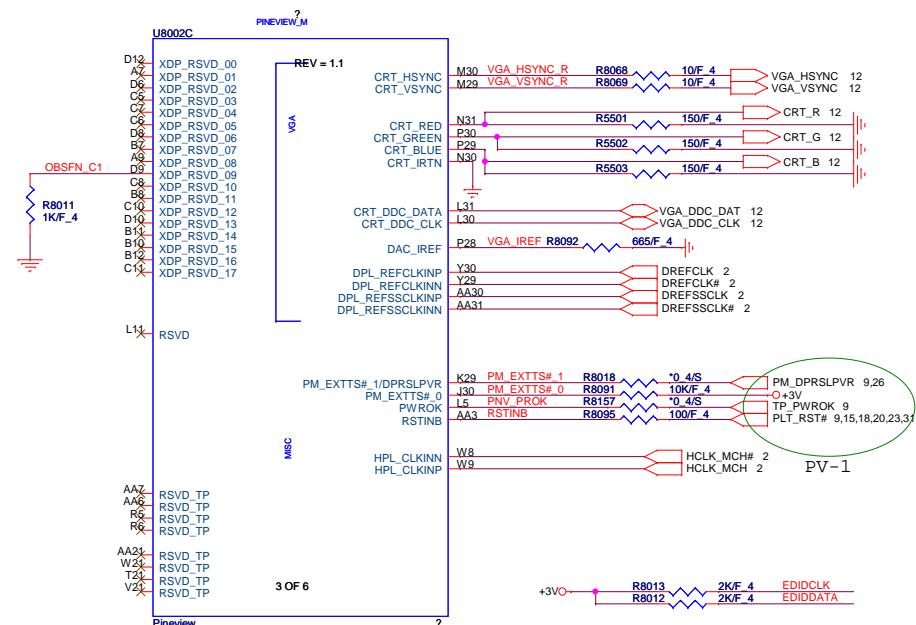
HP/MIC
COMBO JACK
Page 17

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BLOCK DIAGRAM	
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Rev. 1A	



ITP_EN(PIN14)	PIN53/54
0	SRC8#/SRC8
* 1	ITP/ITP#

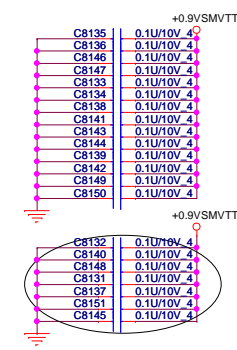
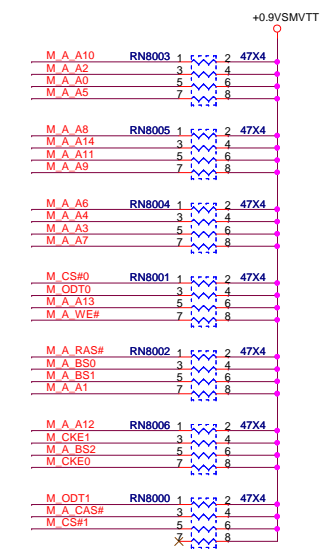
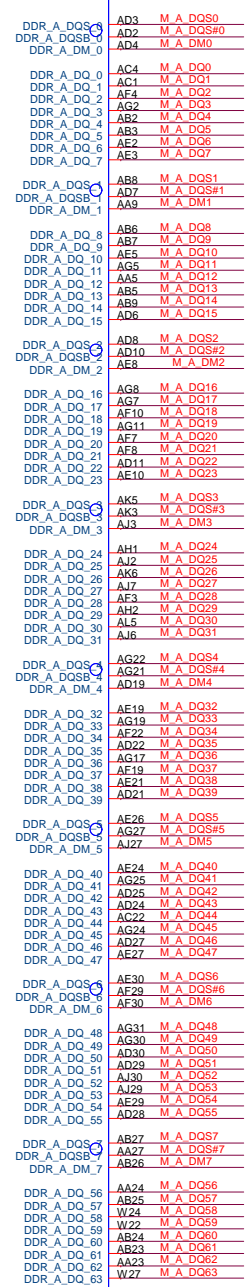
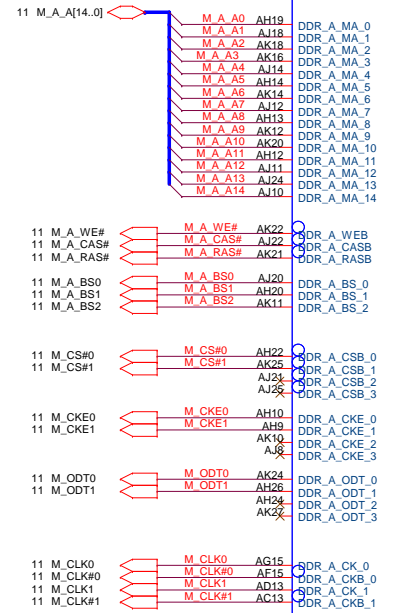
 Quanta Computer Inc. PROJECT : Annika	
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+1.8VSUS 2.5,11,25,28,29,30

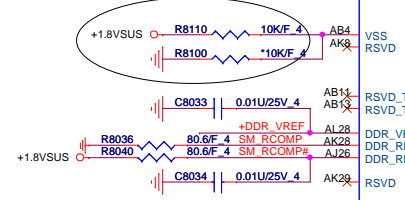
PINEVIEW_M

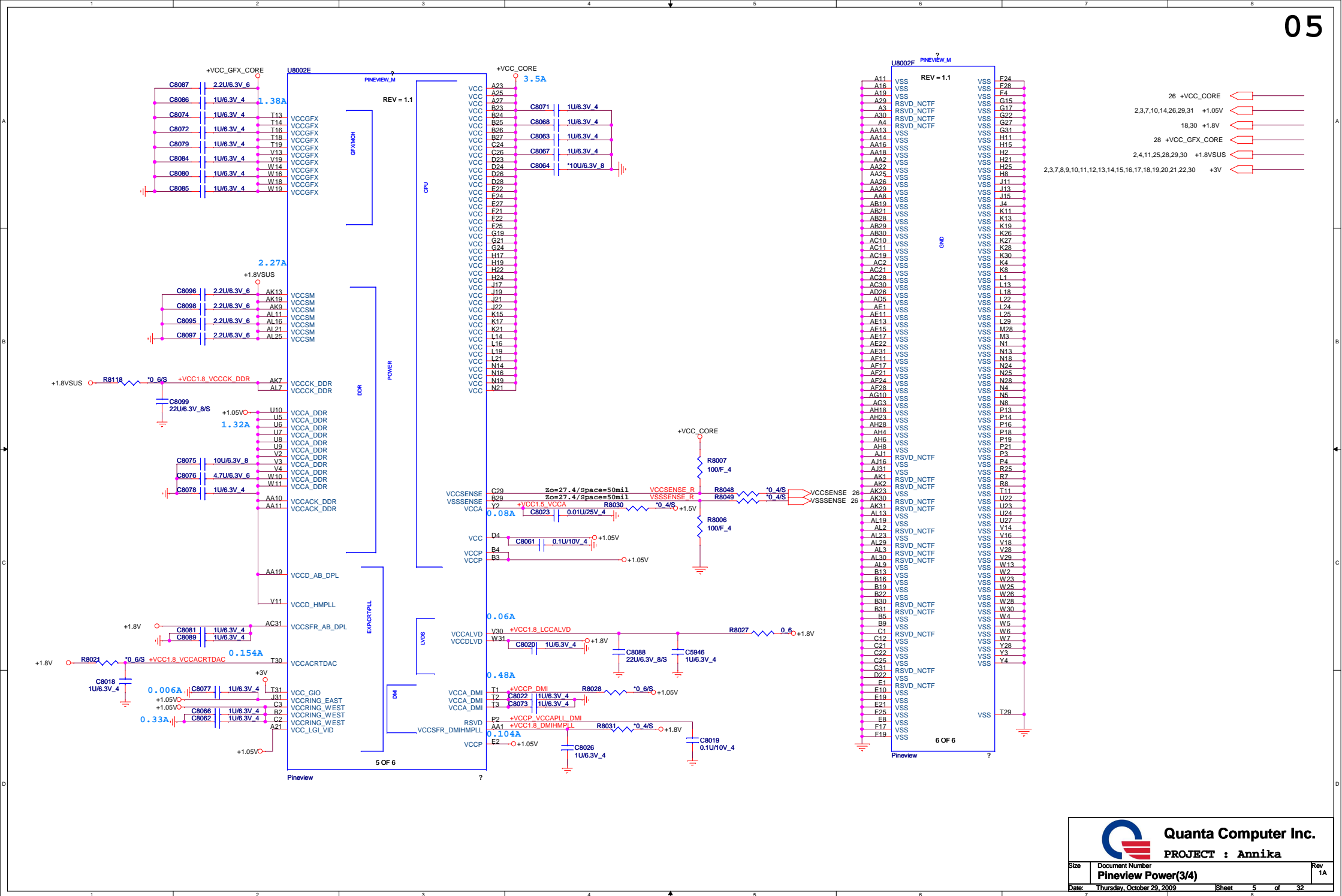
U8002B REV=1.1



1D: add 7 0.1u for VTT_MEM(Intel)

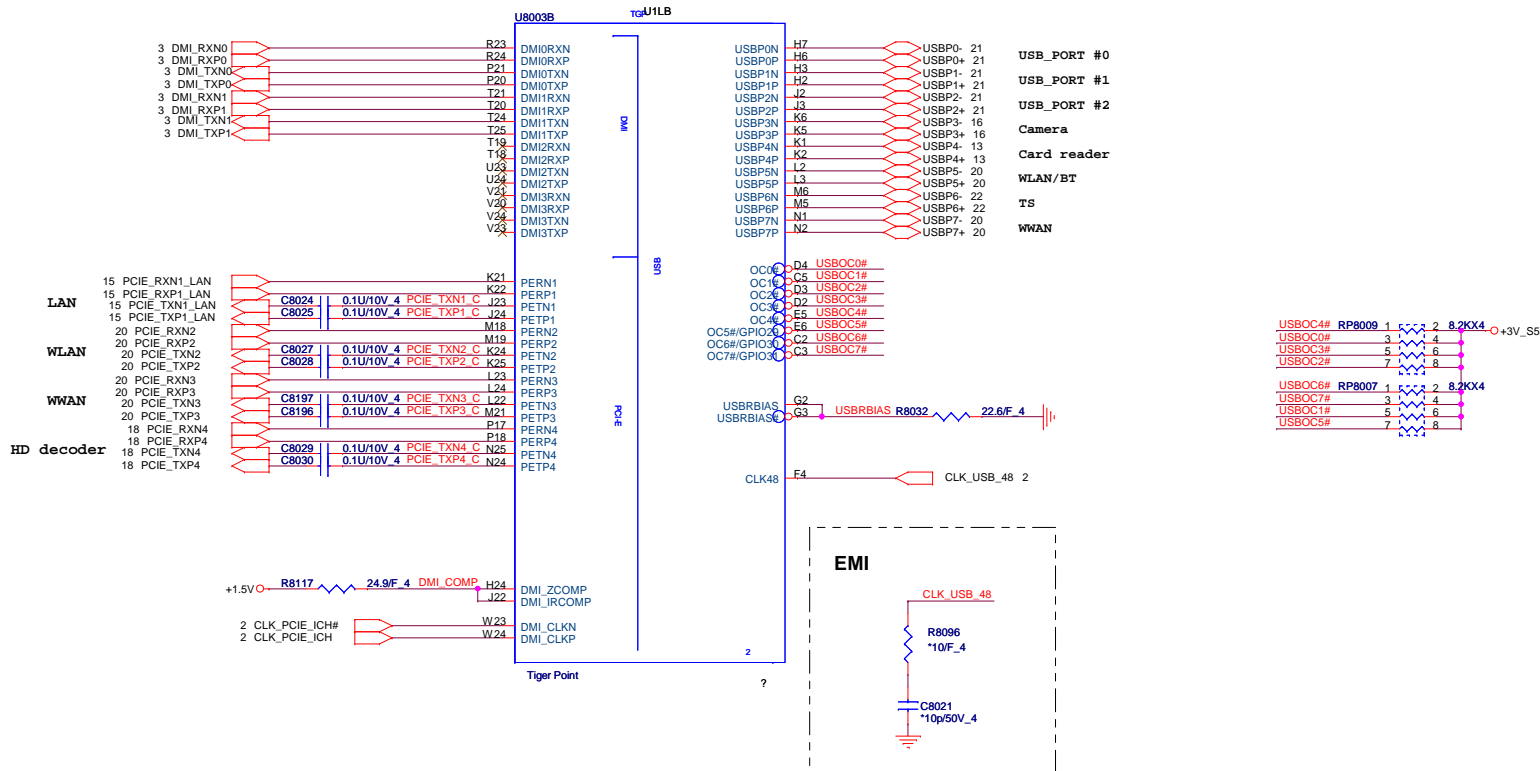
1C: reserve for ES1(WW10 MOW)

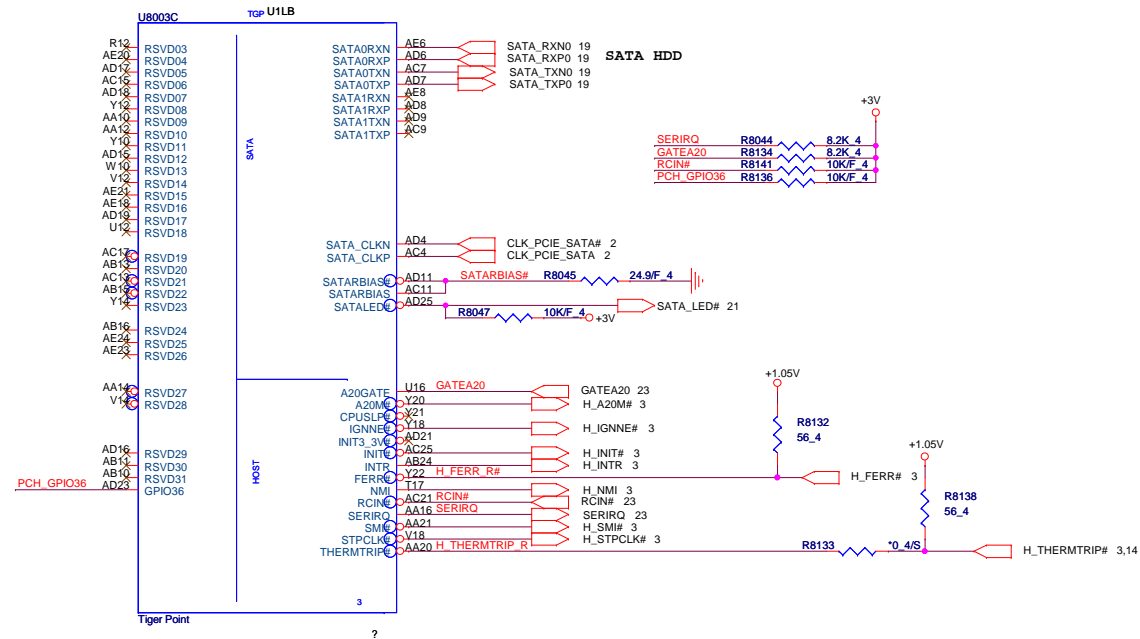




5,10,20,30 +1.5V

9,10,30 +3V_S5



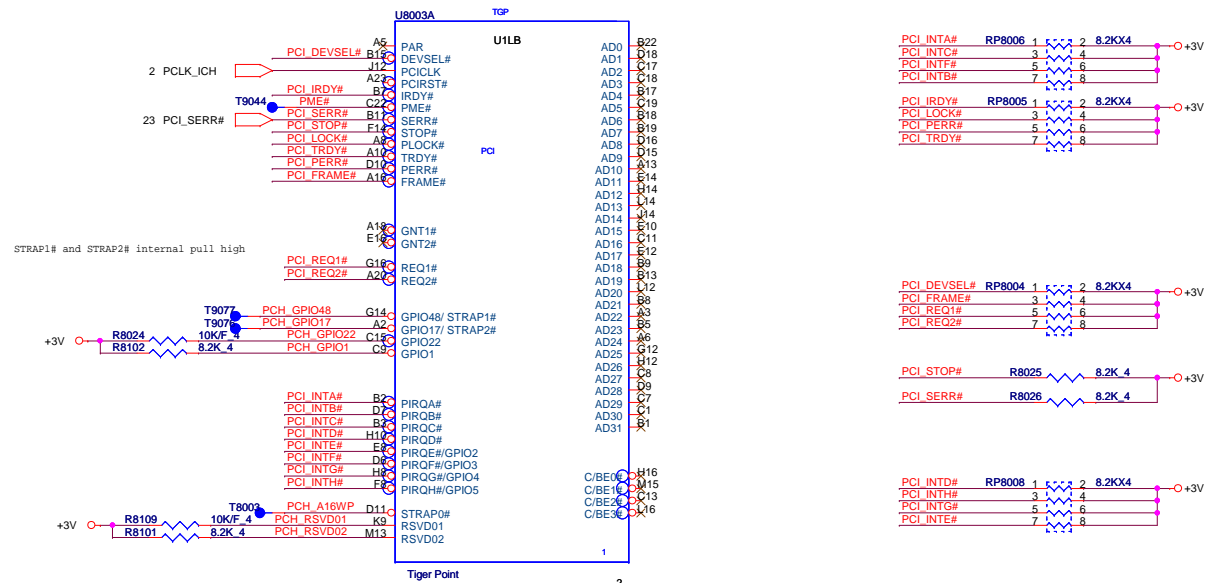


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PROJECT : Annika

Size	Document Number	Rev
	TigerPoint Sata/Host(2/6)	1A

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ICH Boot BIOS select

PCH_GPIO17 (INT PU)	PCH_GPIO48 (INT PU)	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	IPC (Default)

A16 SWAP Override strap

PCH_A16WP (INT PU)	Low = A16 swap override enabled High = Default
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IRQ	Description
PIRQA	USB UHCI Controller #1, #4
PIRQB	AC'97 Codec; option for SMBUS
PIRQC	USB UHCI Controller #3; SATA/IDE Native Mode
PIRQD	USB UHCI Controller #2
PIRQE	Internal LAN; Option for SCI, TCO, HPET#0,1,2
PIRQF	Option for SCI, TCO, HPET#0,1,2
PIRQG	Option for SCI, TCO, HPET#0,1,2
PIRQH	USB EHCI Controller; Option for SCI, TCO, HPET#0,1,2

PCI_GNT#2	Internal PU Should not be PD
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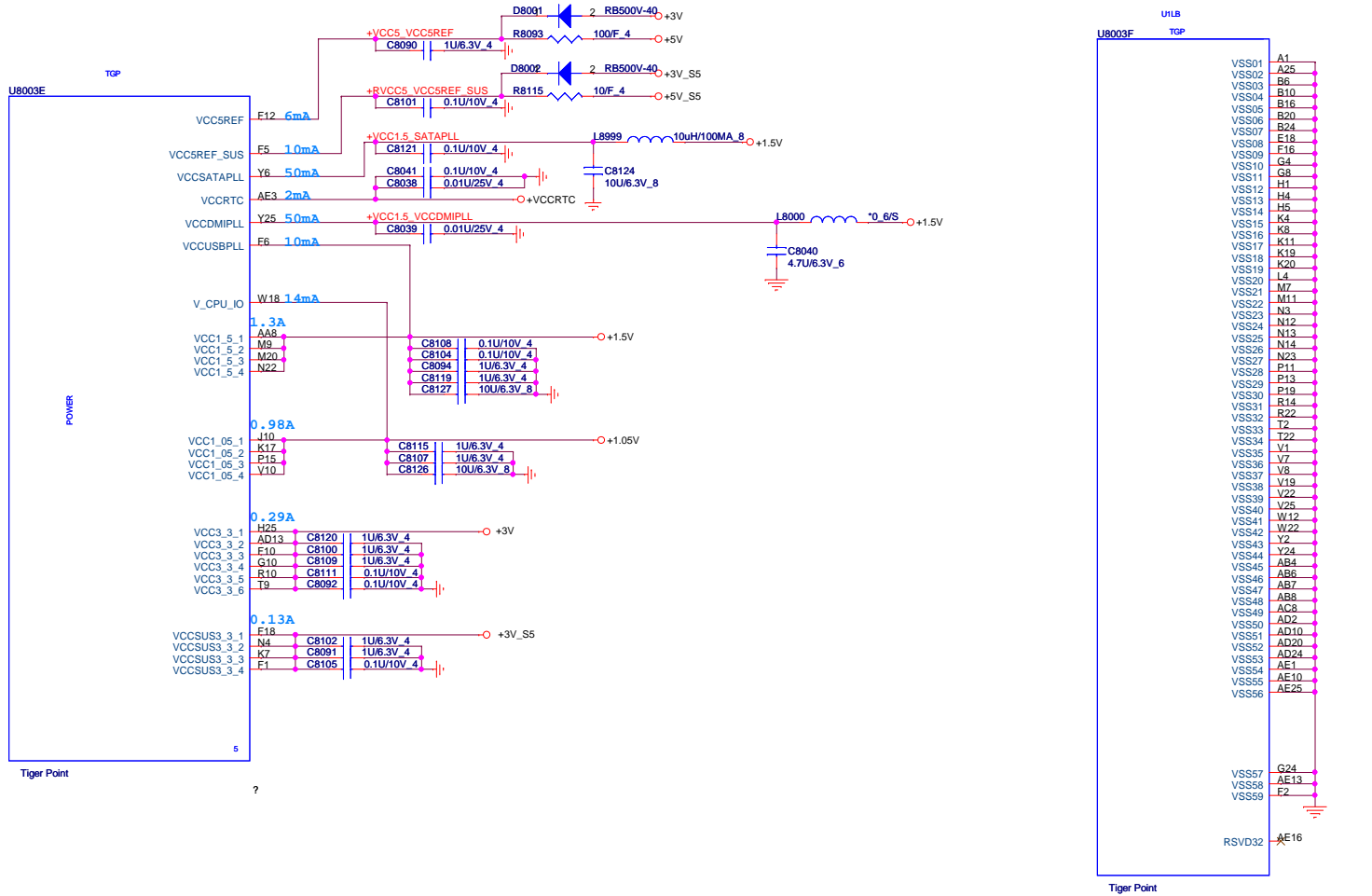
Quanta Computer Inc.

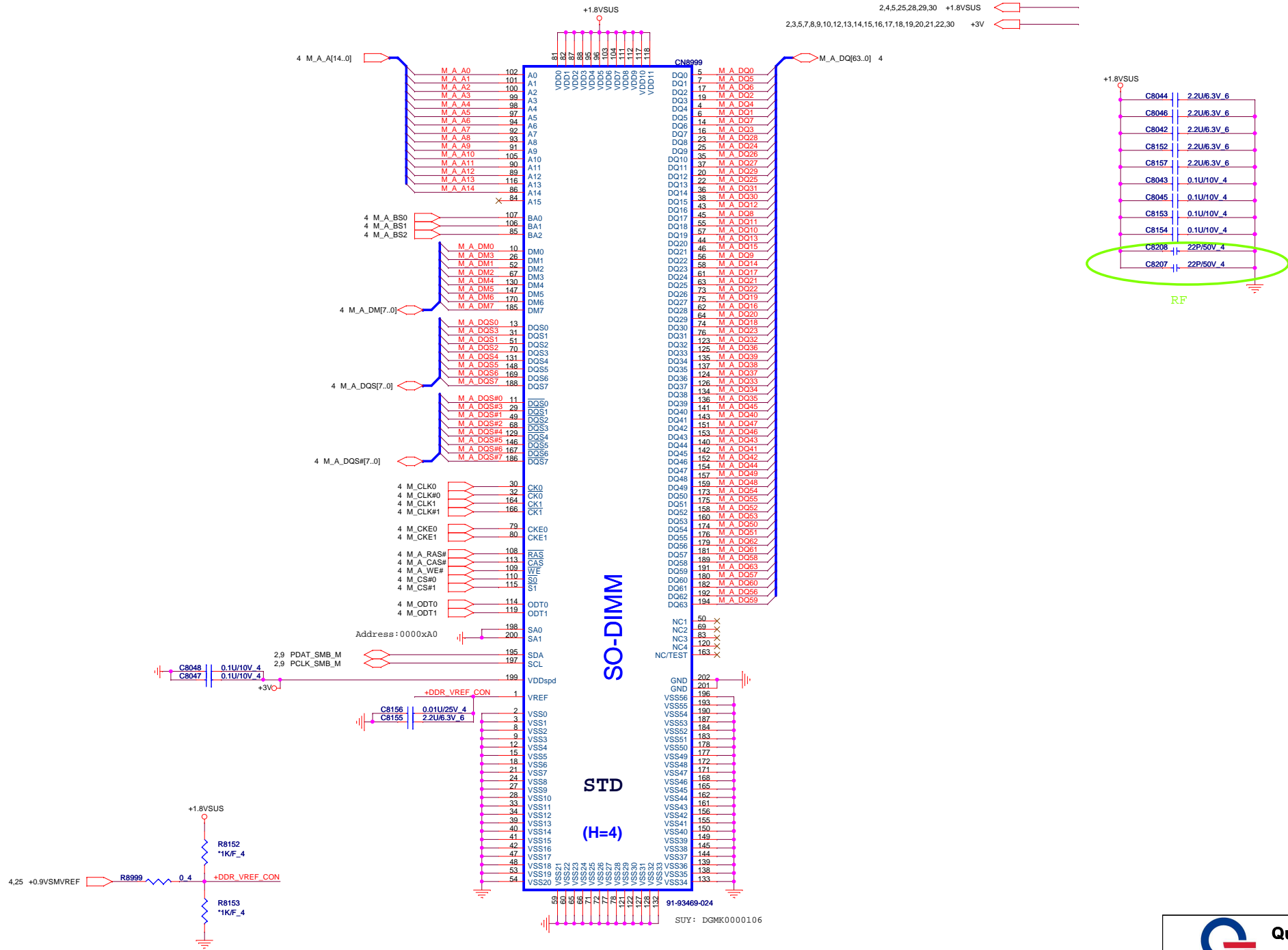
PROJECT : Annika

Size	Document Number	Rev
	TigerPoint PCI(3/6)	1A

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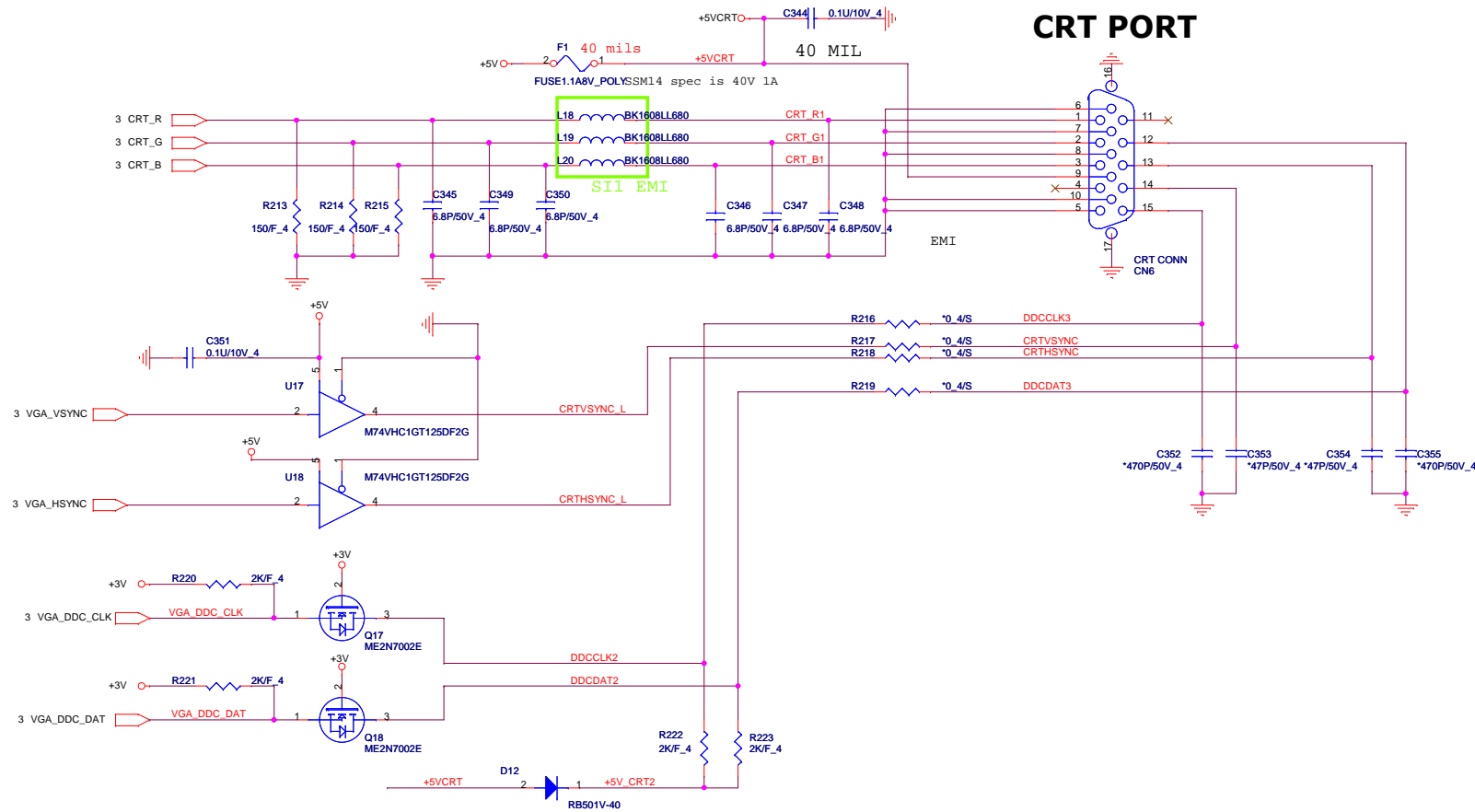




Quanta Computer Inc.

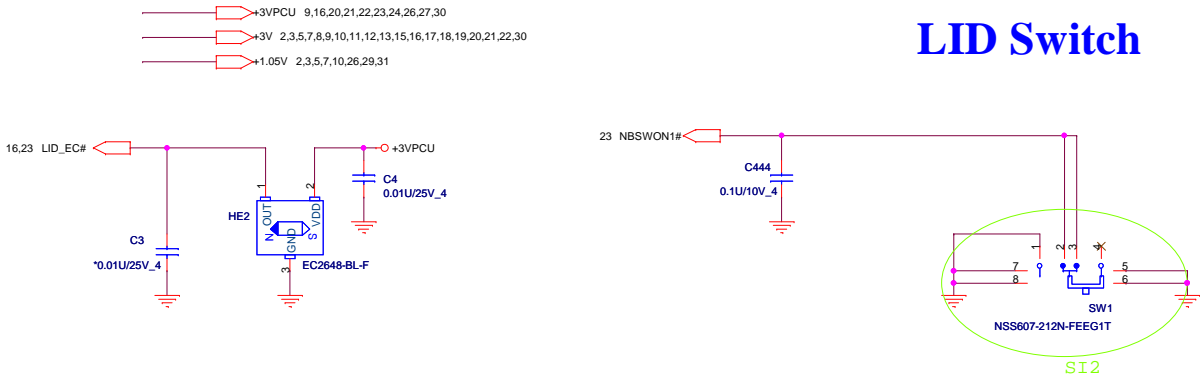
PROJECT : Annika

Size	Document Number	Rev
	DDRII SODIMM	1A
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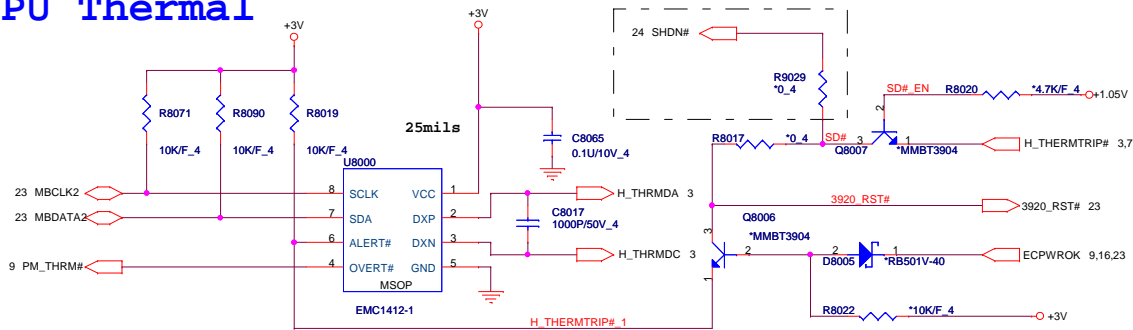




LID Switch



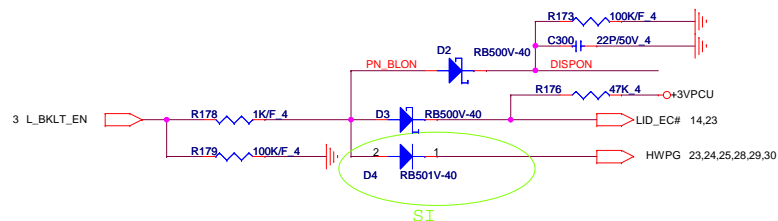
CPU Thermal



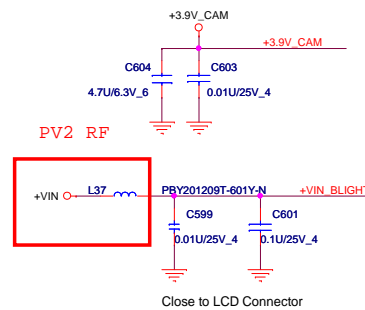
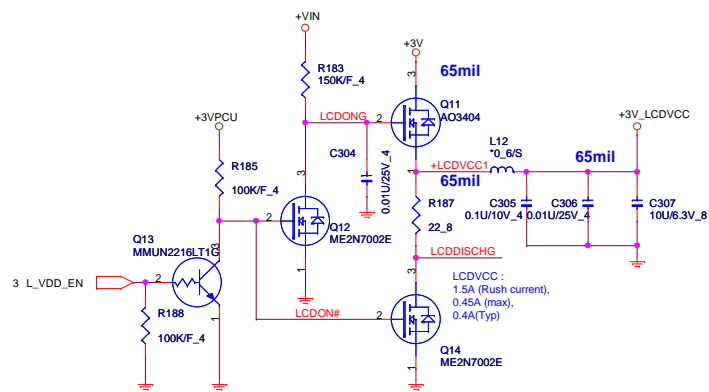
1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.



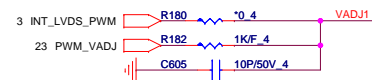
Backlight Control(LDS)



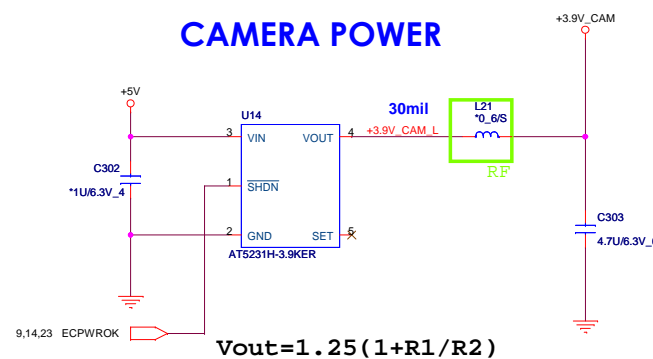
LCD POWER SWITCH



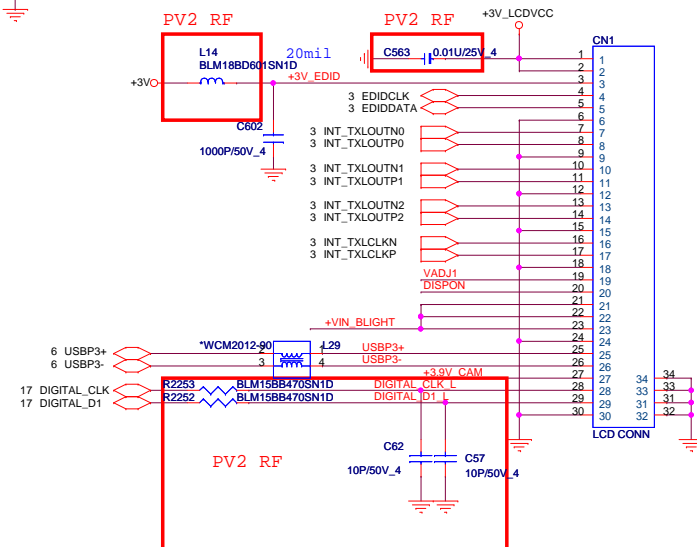
Close to LCD Connector



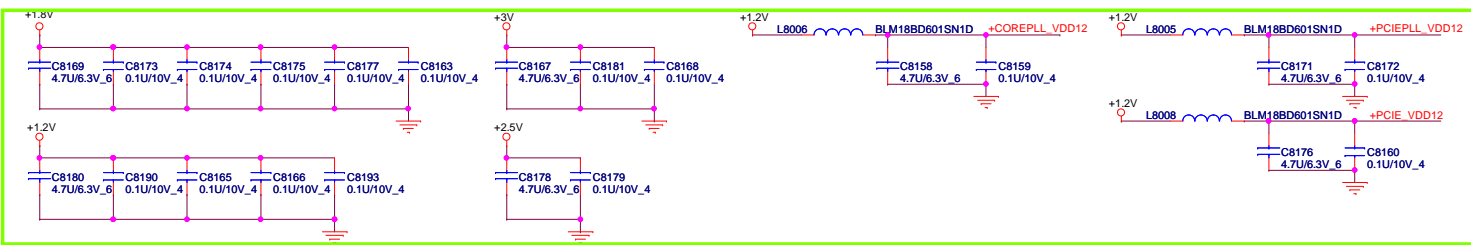
CAMERA POWER



$$V_{out} = 1.25(1 + R_1/R_2)$$





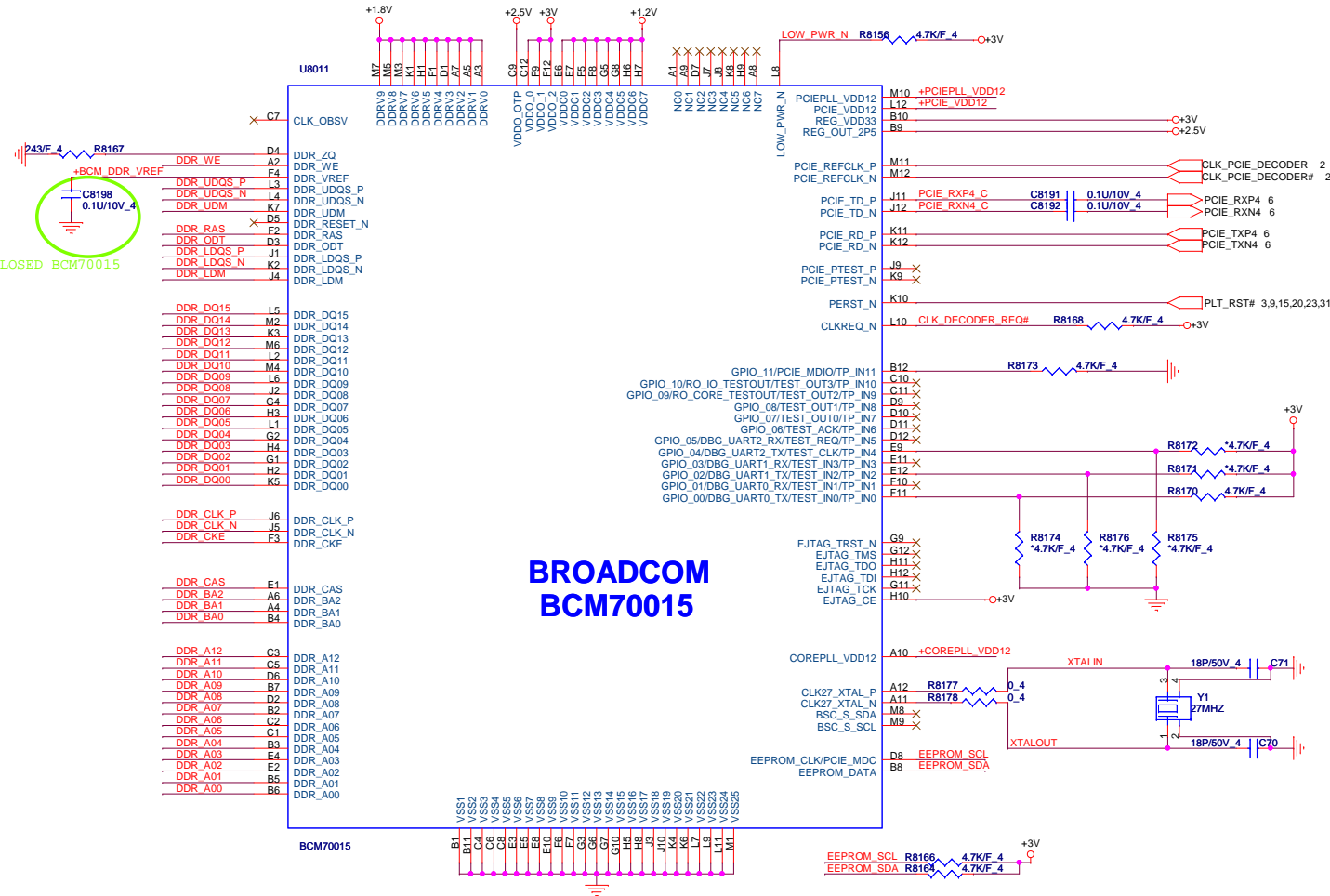


CLOSED BCM70015



CLOSED DDRII

RF



**BROADCOM
BCM70015**

DDR2 RAM Configuration Table

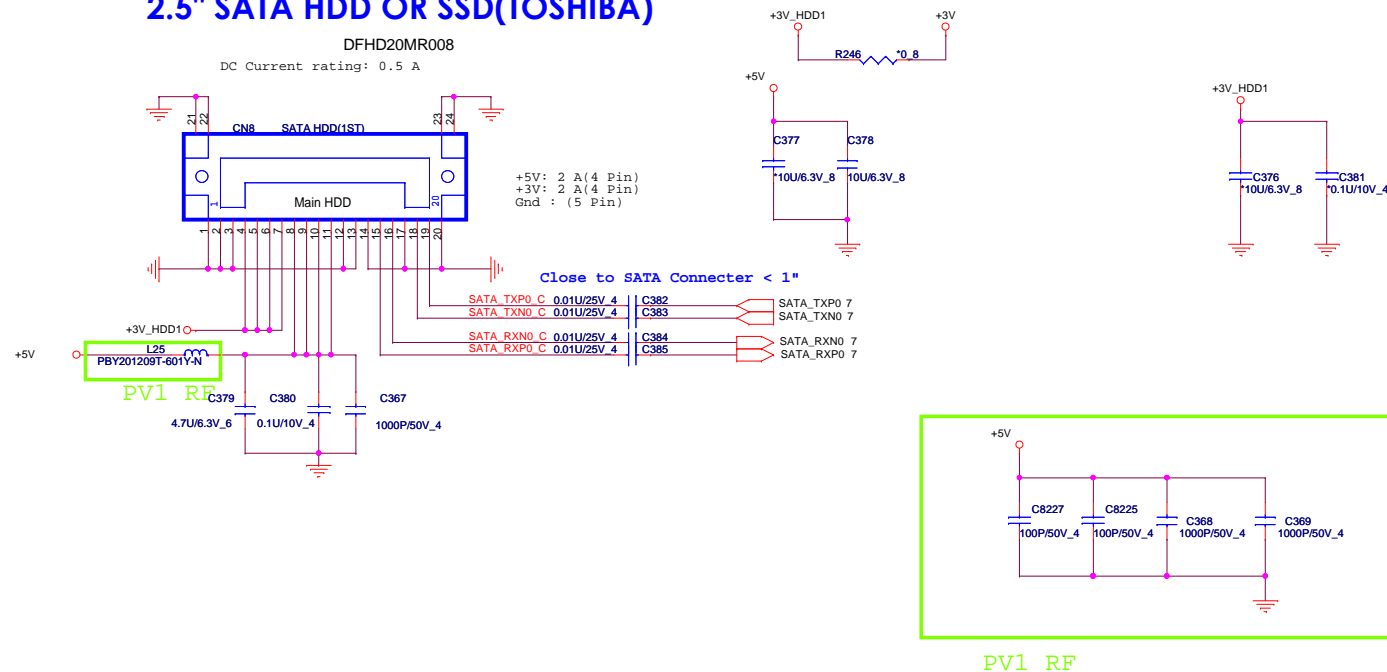
DESCRIPTION	Vendor	Vendor P/N	QCI P/N
DDR2 32Mx16, 128bit 400MHZ (-25)	HYNIX	H5PS5162FFR-25C	AKD5FG-TW03
DDR2 32Mx16, 128bit 400MHZ (-25)	SAMSUNG	K4N51163QG-HC25	AKD5FG-T501

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Size: Document Number
BROADCOM BCM70015

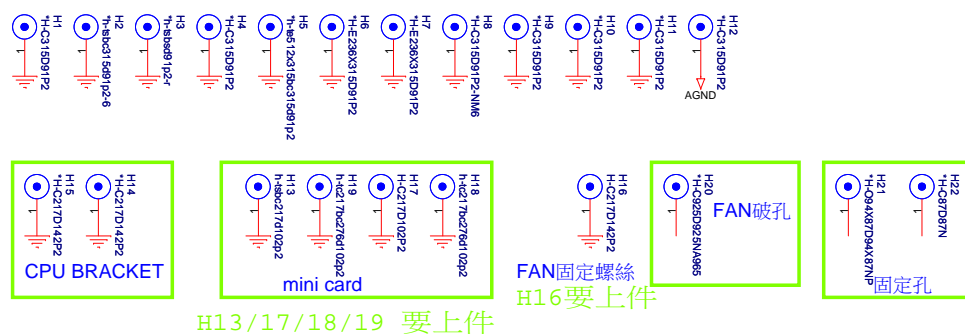
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2.5" SATA HDD OR SSD(TOSHIBA)

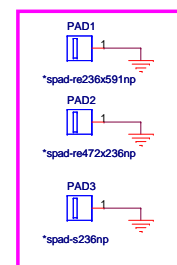


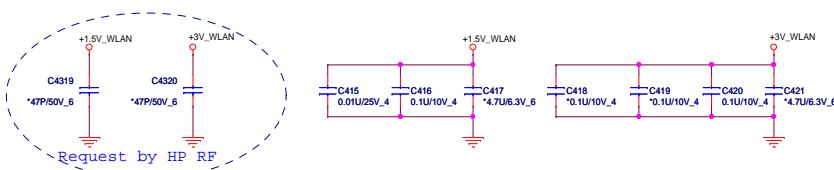
PV1 RF

M/B Screw Hole



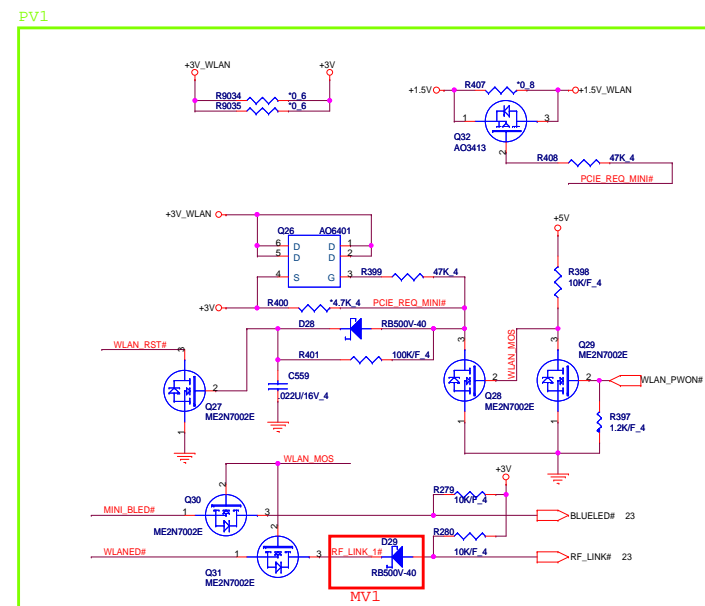
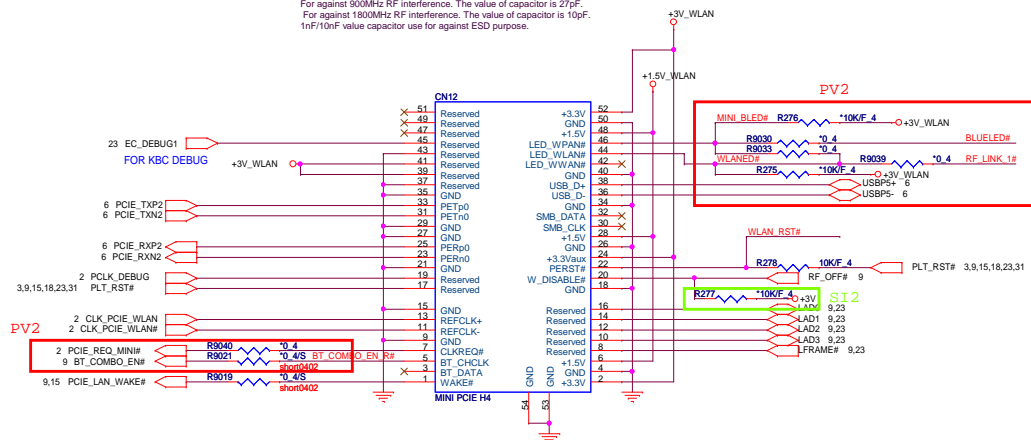
EMI spring



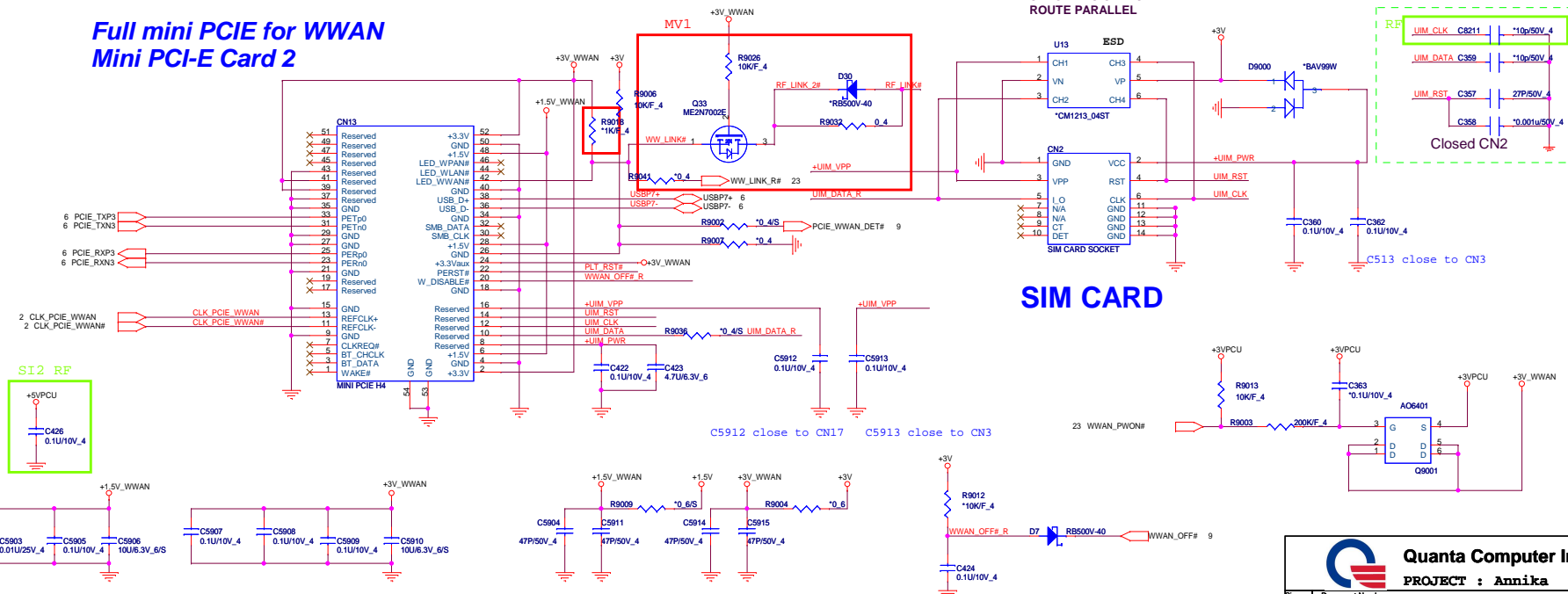


Mini PCI-E Card 1
Half Mini PCI-E WLAN

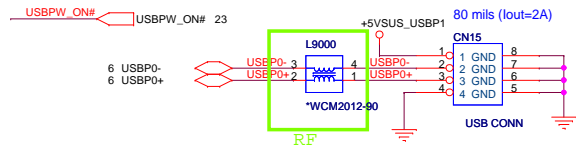
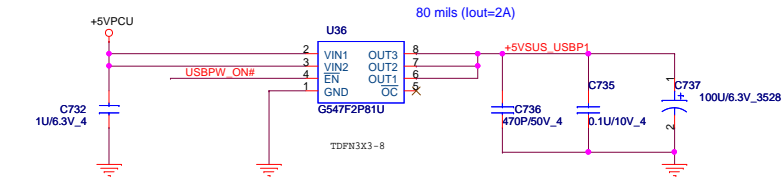
The value of the capacitor is suggest by Siemens HQ expert.
For against 900MHz RF interference. The value of capacitor is 27pF.
For against 1800MHz RF interference. The value of capacitor is 10pF
1nF/10nF value capacitor use for against ESD purpose.



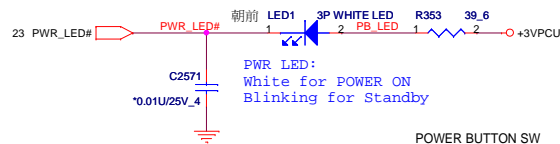
Full mini PCIE for WWAN
Mini PCI-E Card 2



1x Left side USB port supports Keyed USB.



PWR Button/LED

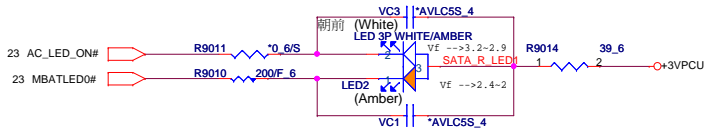


POWER BUTTON SW

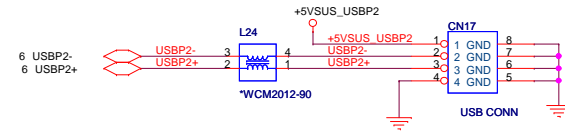
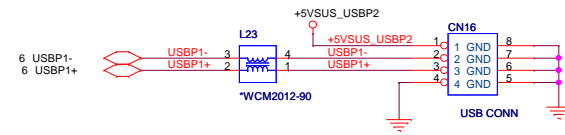
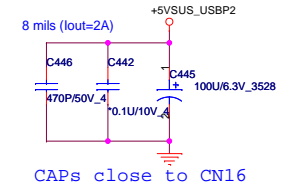
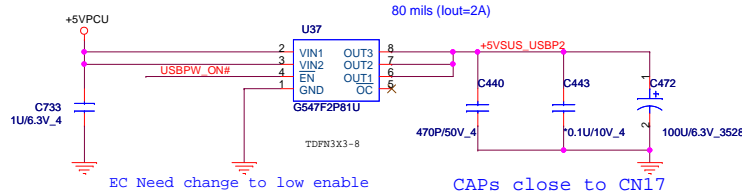
SATA/LED



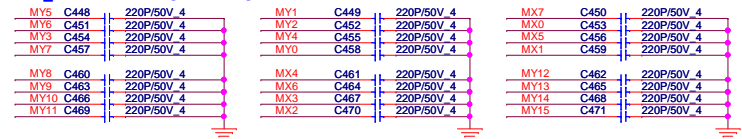
Charging & Discharging/LED



For Right 2xUSB Ports PWR

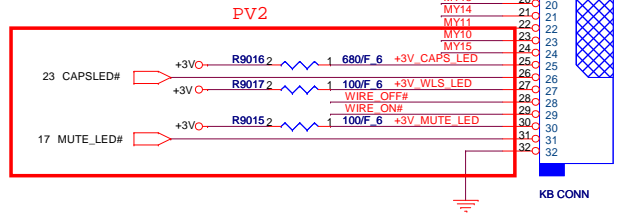
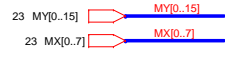
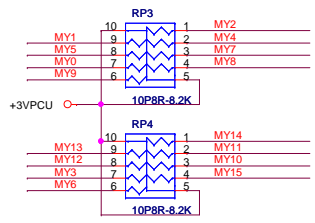


Keyboard (KBC)

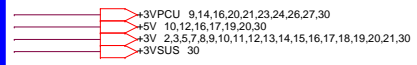
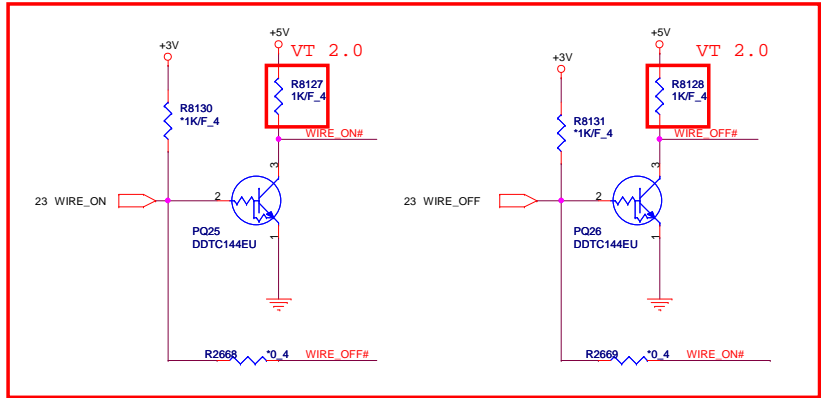


Modify CN2 footprint from bl137-32r1-tand-32p-1 to 196033-32041-32p-1

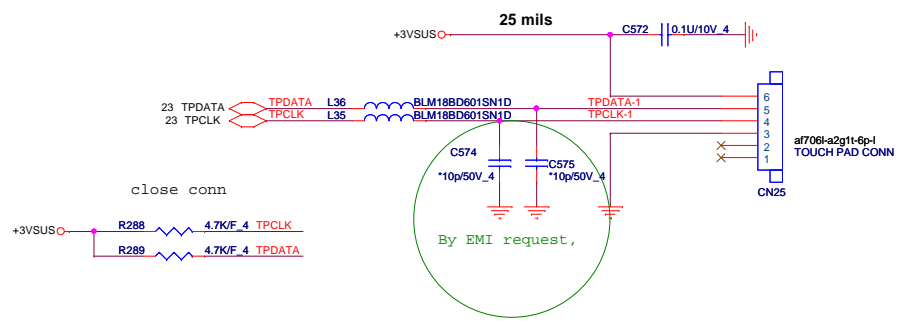
KEYBOARD PULL-UP



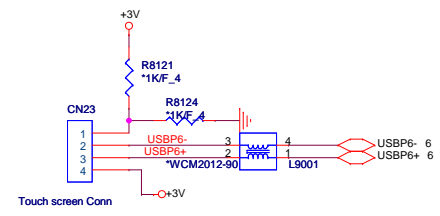
PV2



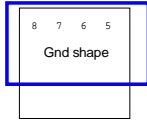
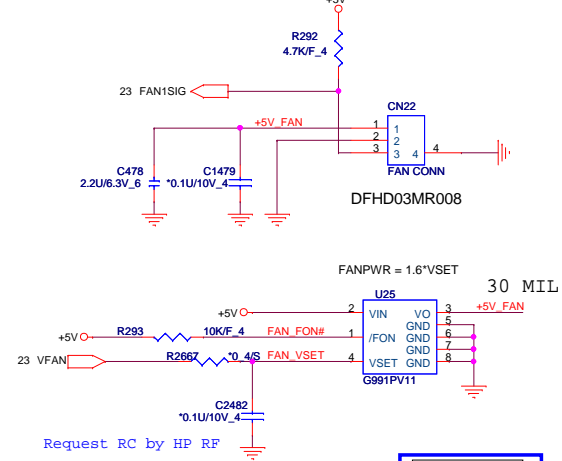
TOUCH PAD CONNECTOR



TOUCH SCREEN

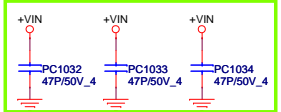


CPU FAN

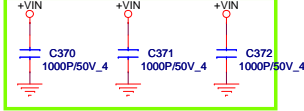


DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+15V_ALW

SI2_RF

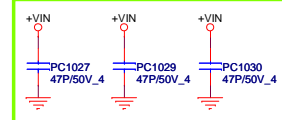


PV1_RF

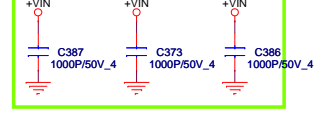


+5VPCU Volt +/- 5%
Countinue current:4A
Peak current:5.5A
OCP minimum 6A

SI2_RF

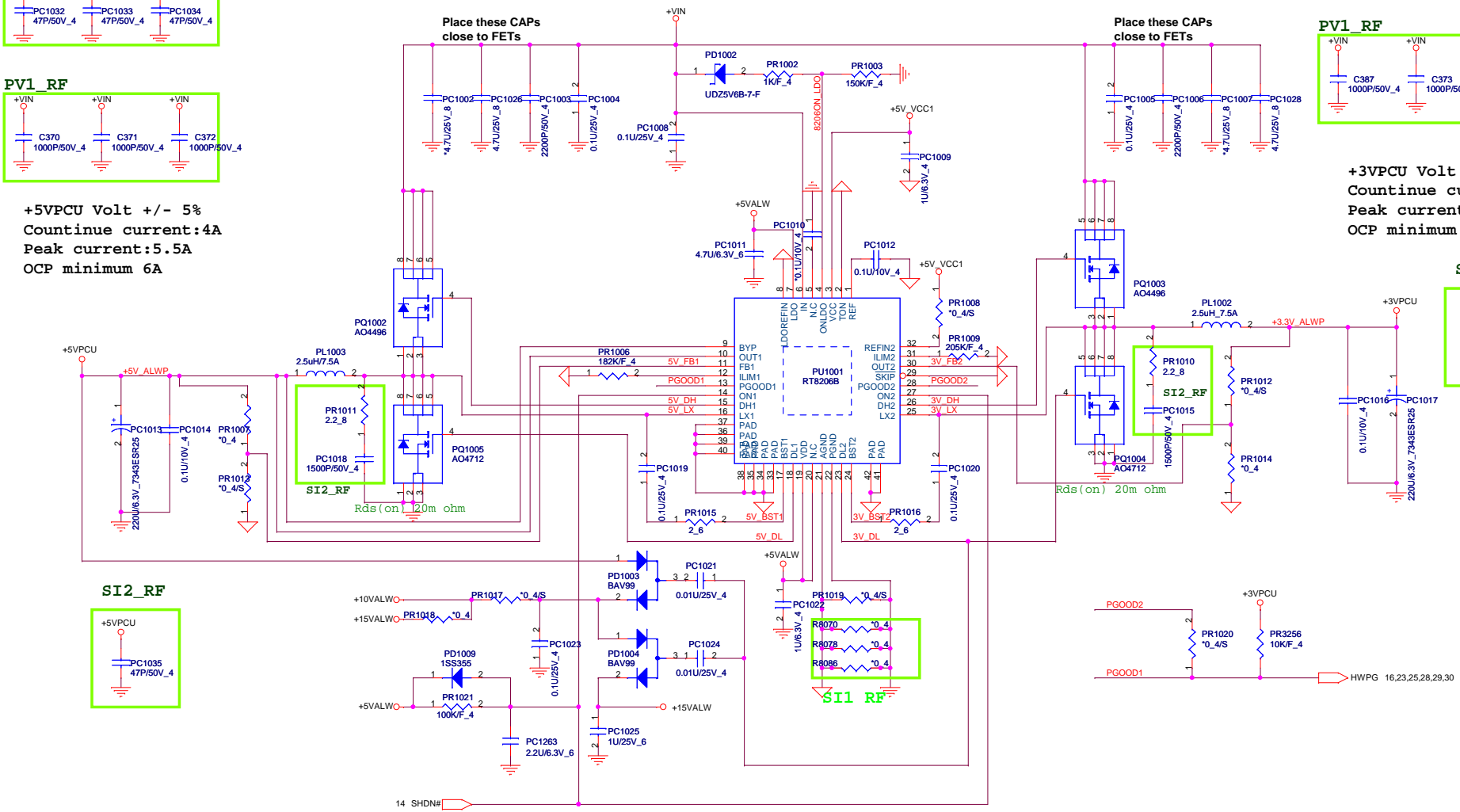
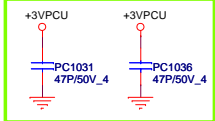


PV1_RF

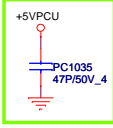


+3VPCU Volt +/- 5%
Countinue current:4A
Peak current:5.5A
OCP minimum 6A

SI2_RF

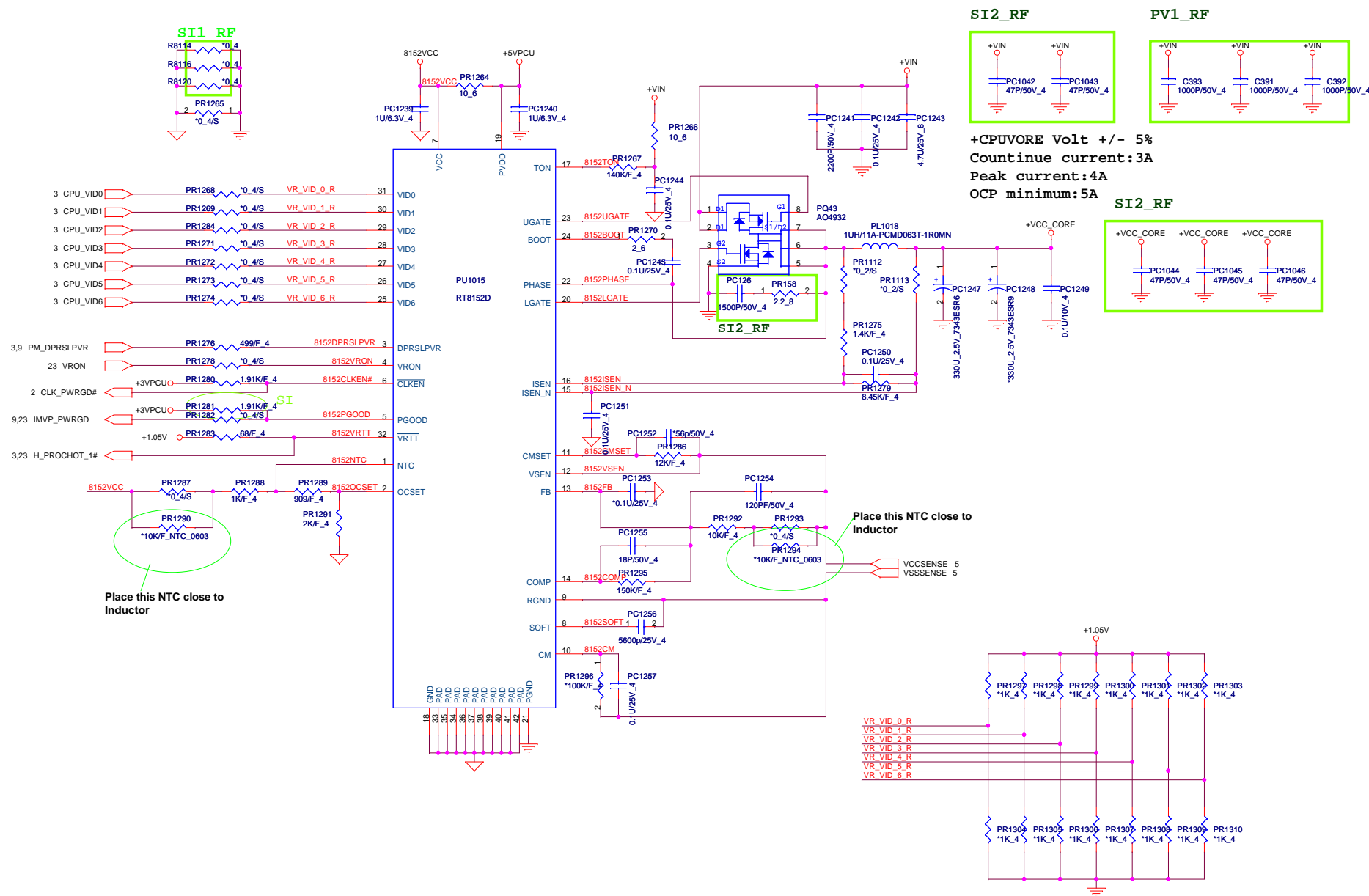


SI2_RF

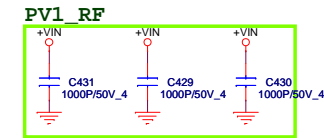
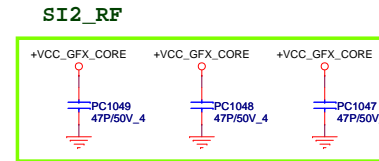
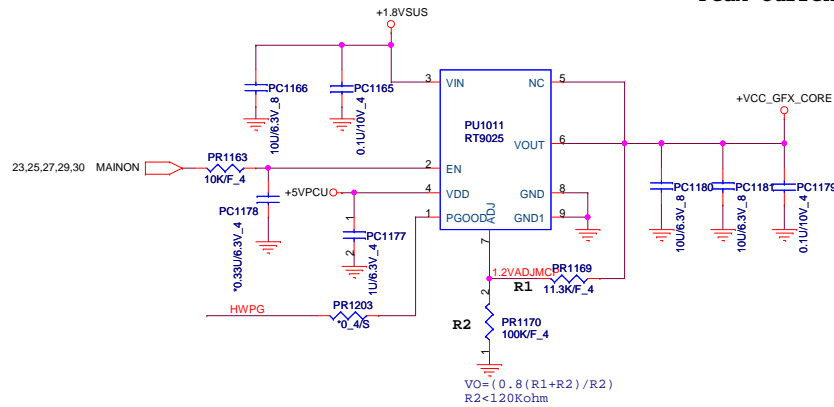
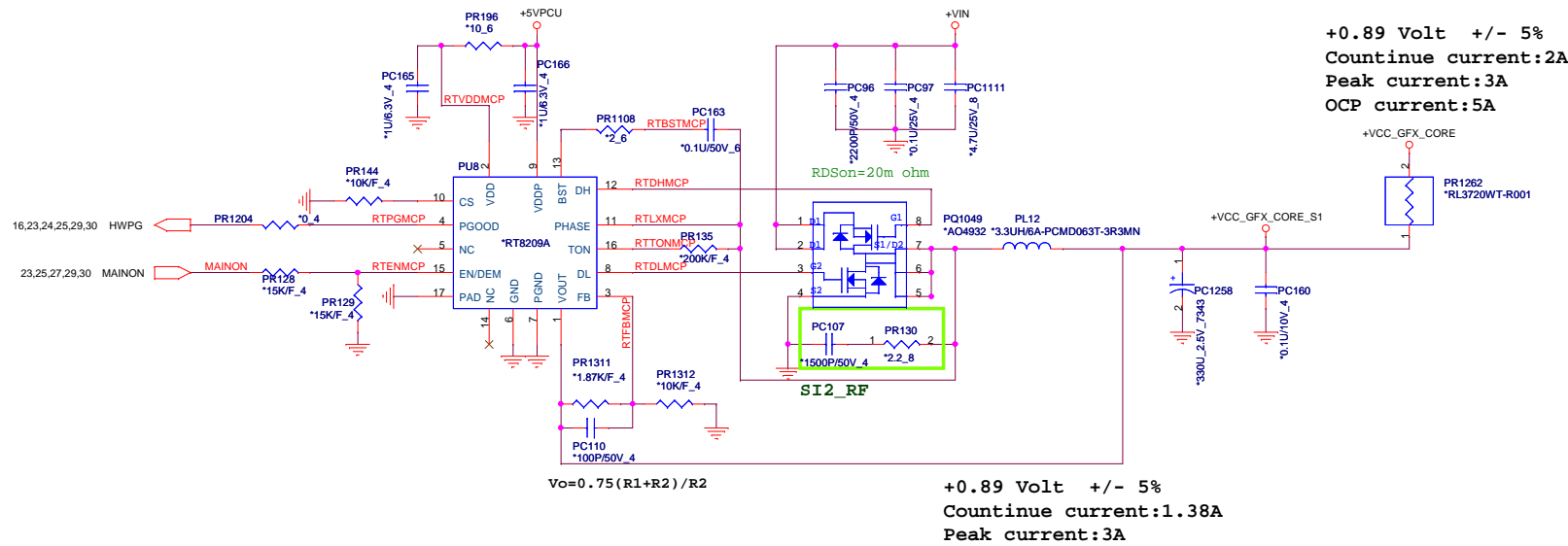


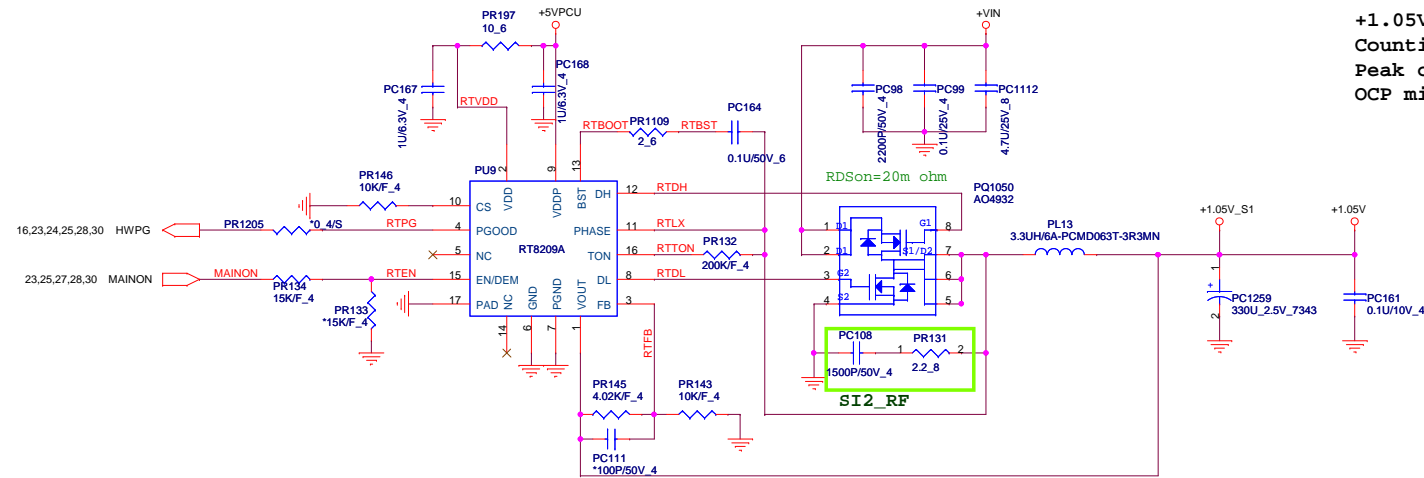
SI1_RF





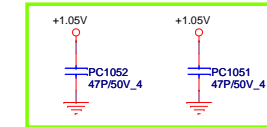




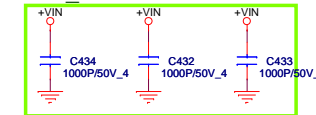


+1.05Volt +/- 5%
 Countinue current:3A
 Peak current:4A
 OCP minimum:5A

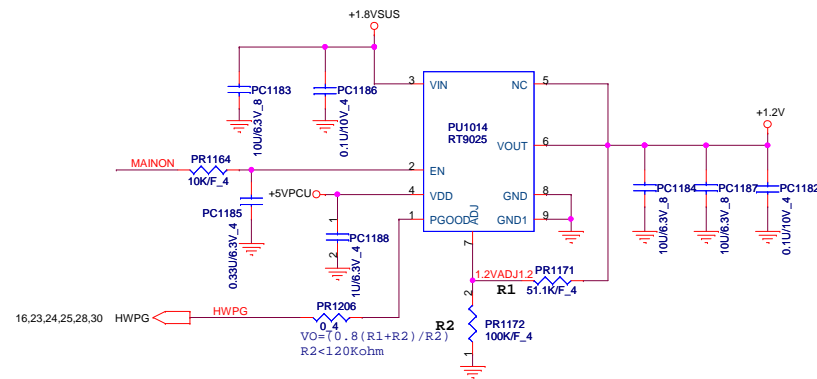
SI2_RF



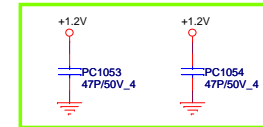
PV1_RF



+1.2 Volt +/- 5%
 Countinue current:0.8A
 Peak current:3A

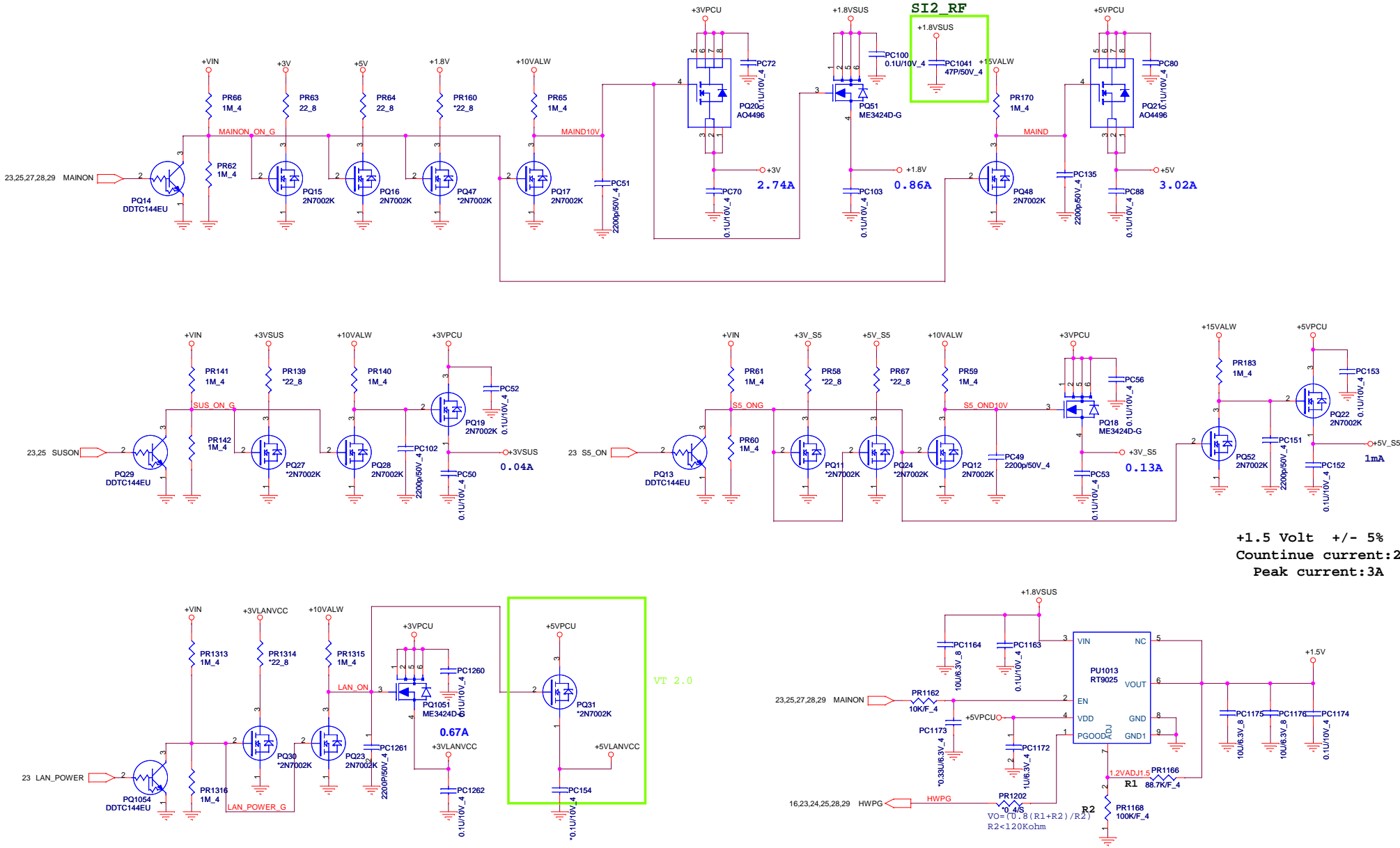


SI2_RF



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 +1.05V (RT8209A)
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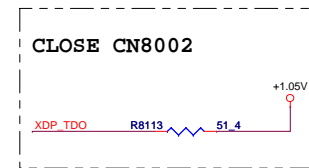
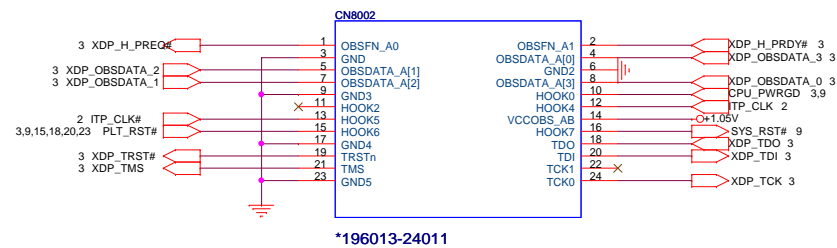
Quanta Computer Inc.

PROJECT : Annika

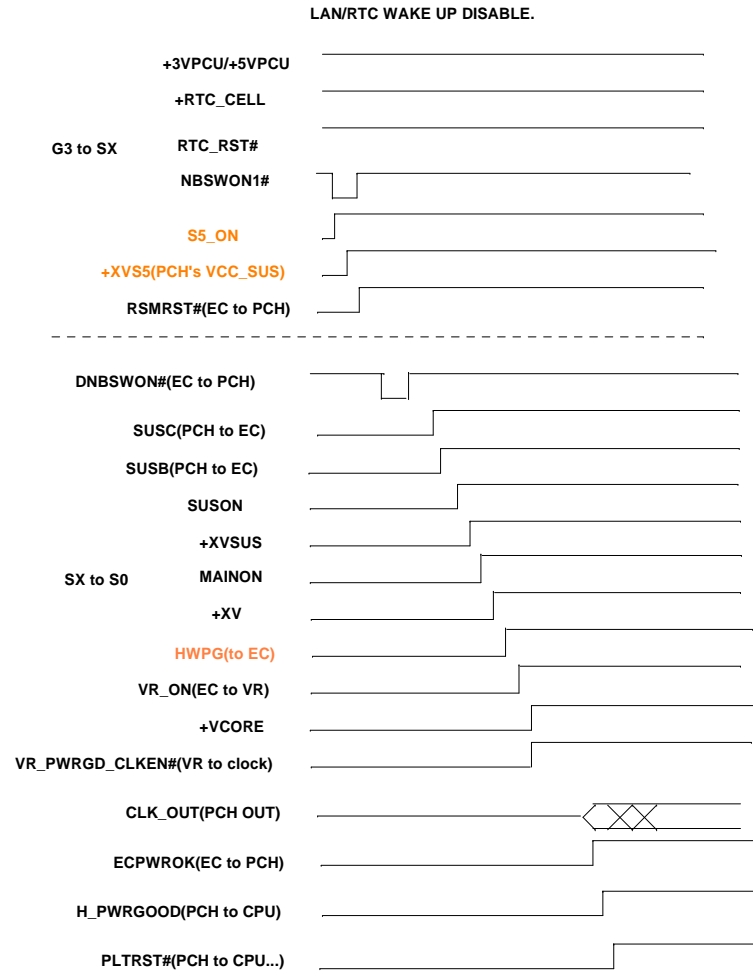
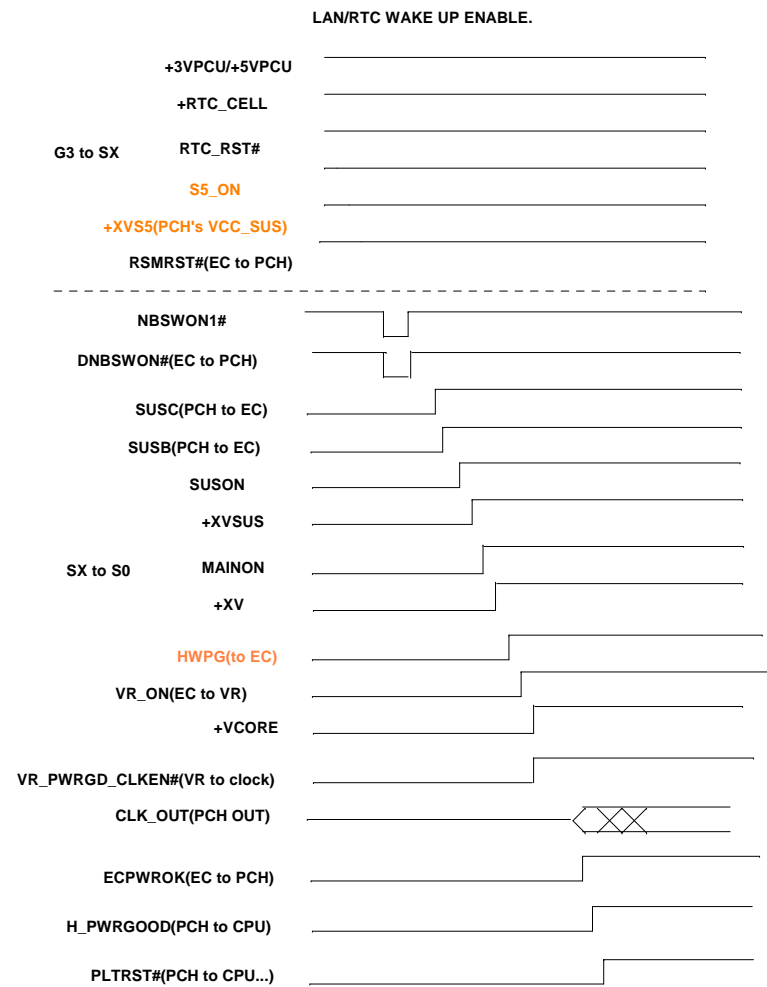
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CPU XDP



Power up sequence



Quanta Computer Inc.

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