

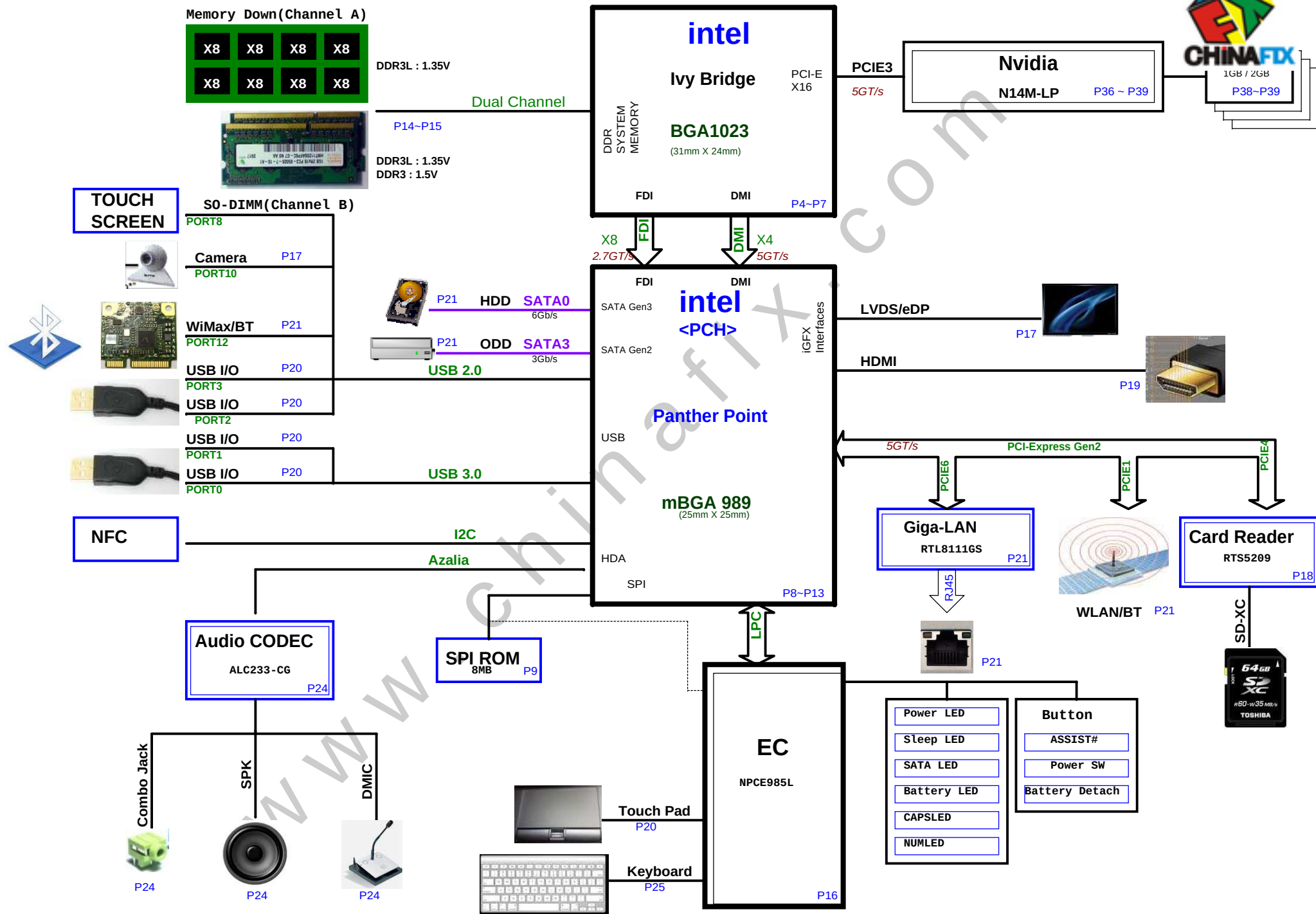
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11	PCH 4/6(GPIO/CPU/STRAP)	1A	
12	PCH 5/6(POWER)	1A	
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14	DDR3L MEMORY DOWN(1RX8)	1A	
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20	USB	1A	
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22	WLAN/KB-BL	1A	
23	HDD/ODD/GSENSOR/TP/FAN	1A	
24	AUDIO(ALC233-CG)	1A	
25	LED/PS	1A	
26	POWER +VCC_CORE (ISL95837)	1A	
27	POWER 3VPCU&RVCC5(TPS51427)	1A	
28	POWER 1.35VSUS/VTT_MEM	1A	
29	POWER +1.05V(G5602R41U) -15A	1A	
30	POWER VCCSA/VCCIO	1A	
31	POWER VCC1.8/Thermal	1A	
32	POWER(BAT IN / ADA IN/ UL)	1A	
33	POWER CHARGER (ISL88731C)	1A	
34	POWER VGA_CORE/1.0(RT8812A)	1A	
35	POWER VCC1.5_VRAM/1.05V	1A	
36	NVIDIA N14 GB2-64 PCIE 1/4	1A	
37	NVIDIA N14 GB2-64 TMDS 2/4	1A	
38	NVIDIA N14 GB2-64 VRAM 3/4	1A	
39	NVIDIA N14 GB2-64 VRAM 4/4	1A	

Page	Title of schematic page	Rev.	Date
40	HOLE/EMI/KB	1A	
41	IO PORT LIST	1A	

* : No mount
E@ : For DIS GFX only
I@ : For INT GFX only



Chief River ULV BLOCK DIAGRAM

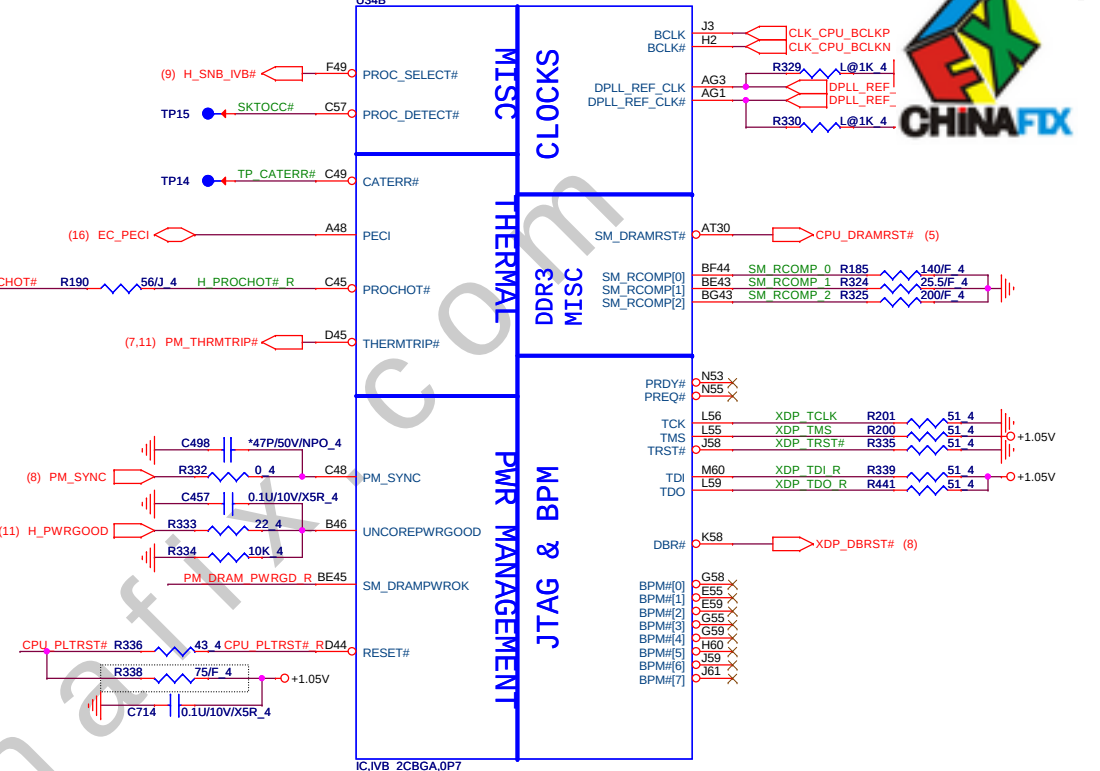
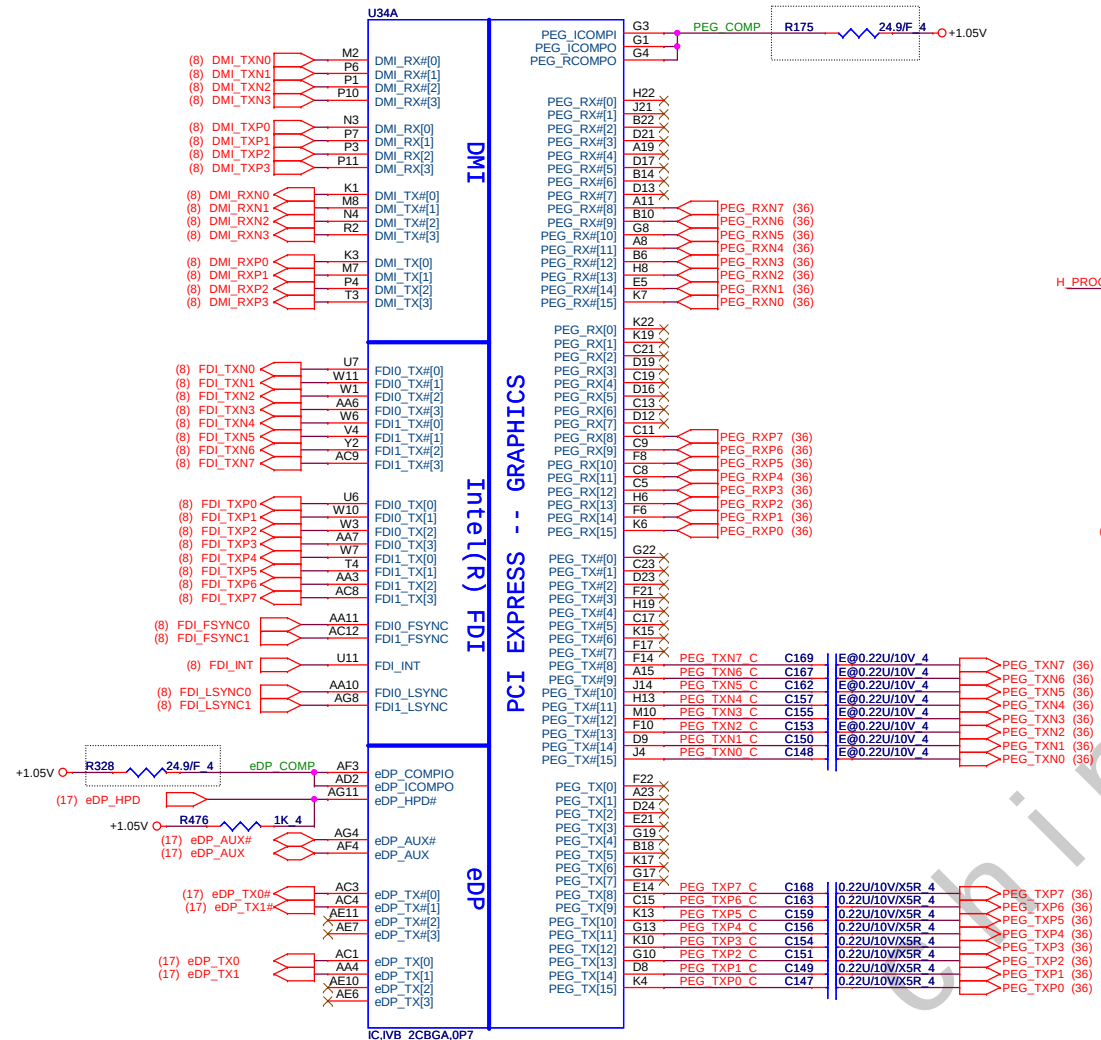




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Ivy Bridge Processor (DMI, PEG, FDI)

Ivy Bridge Processor (CLK, MISC, JTAG)



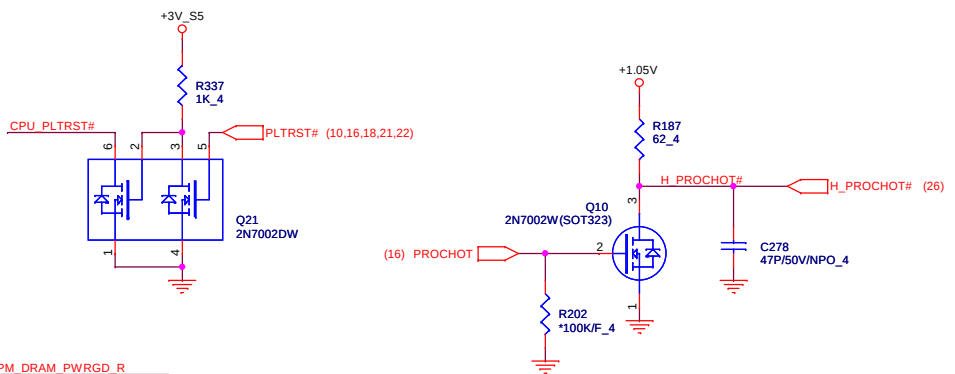
SNB_IVB#:

- It is NC when using Sandy Bridge.(1.05V)
- For next generation processor it will be grounded in package.(1.0V)

FDI Disabling (Discrete Only)

FDI_FSYNC (J18/J17/J19/H17) can gang all these 4 signals together and tie them with only one 1K resistor to GND (DG V0.5 Ch2.2.9).

FDI_INT connect to GND with 1K ohm.



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

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SNB/IVB 1/4

Size Document Number

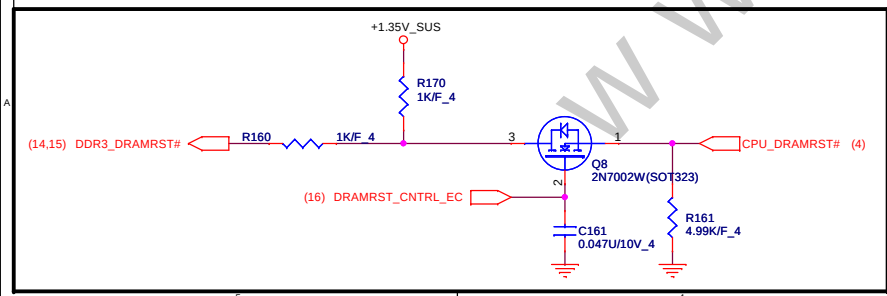
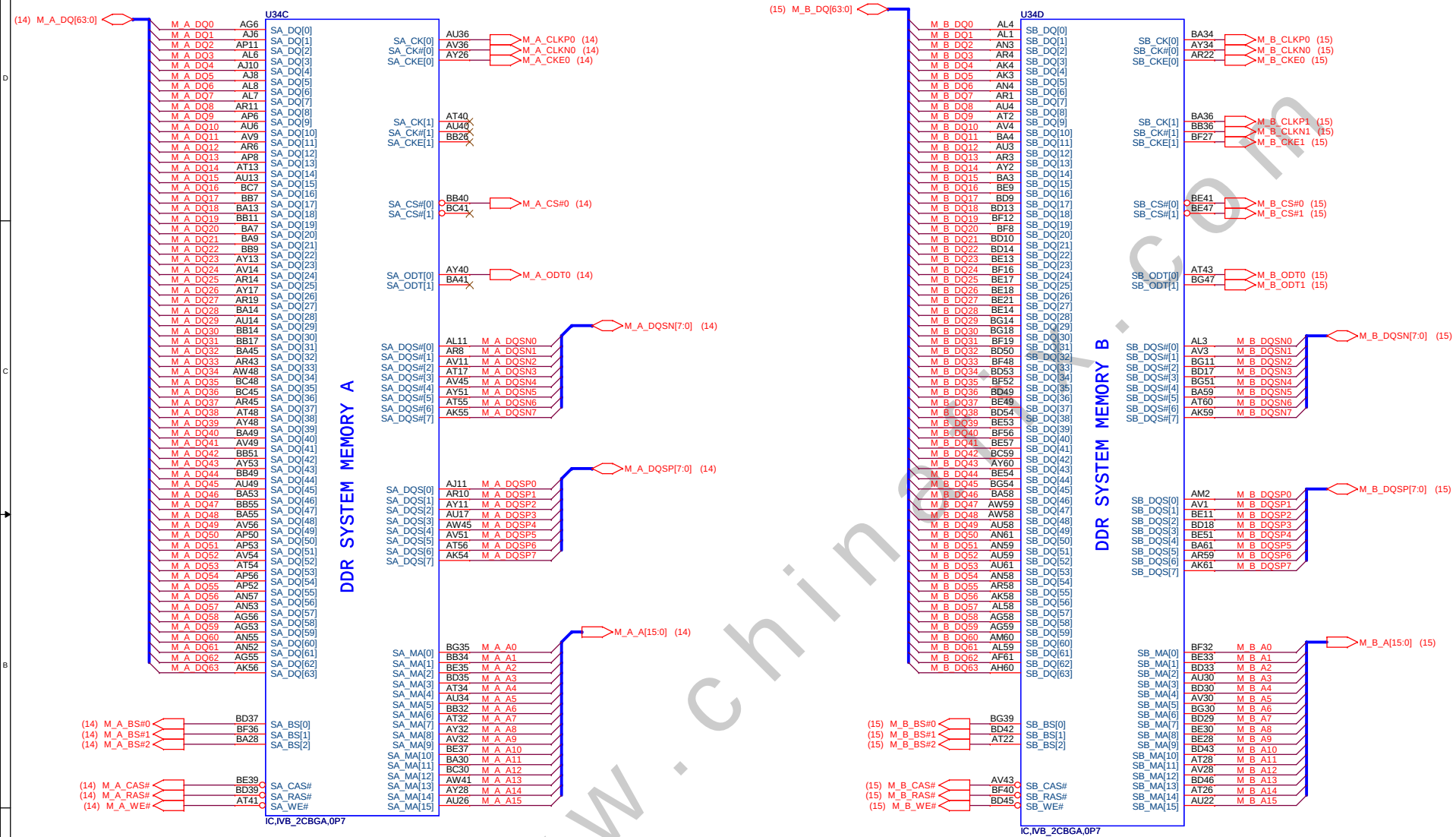
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Rev 1A

Ivy Bridge Processor (DDR3)



5

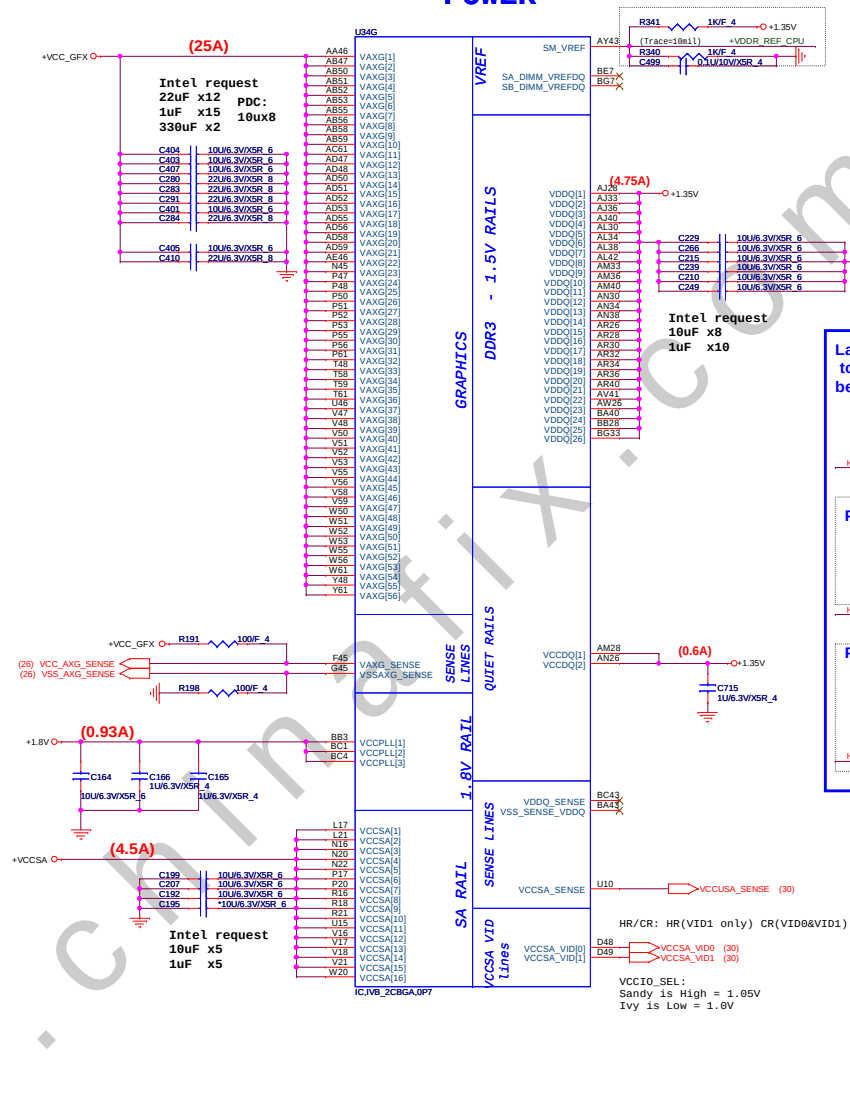


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		1A
SNB/IVB 2/4		
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Ivy Bridge Processor (GRAPHIC POWER)



SVID CLK

Close to VR

SVID DATA

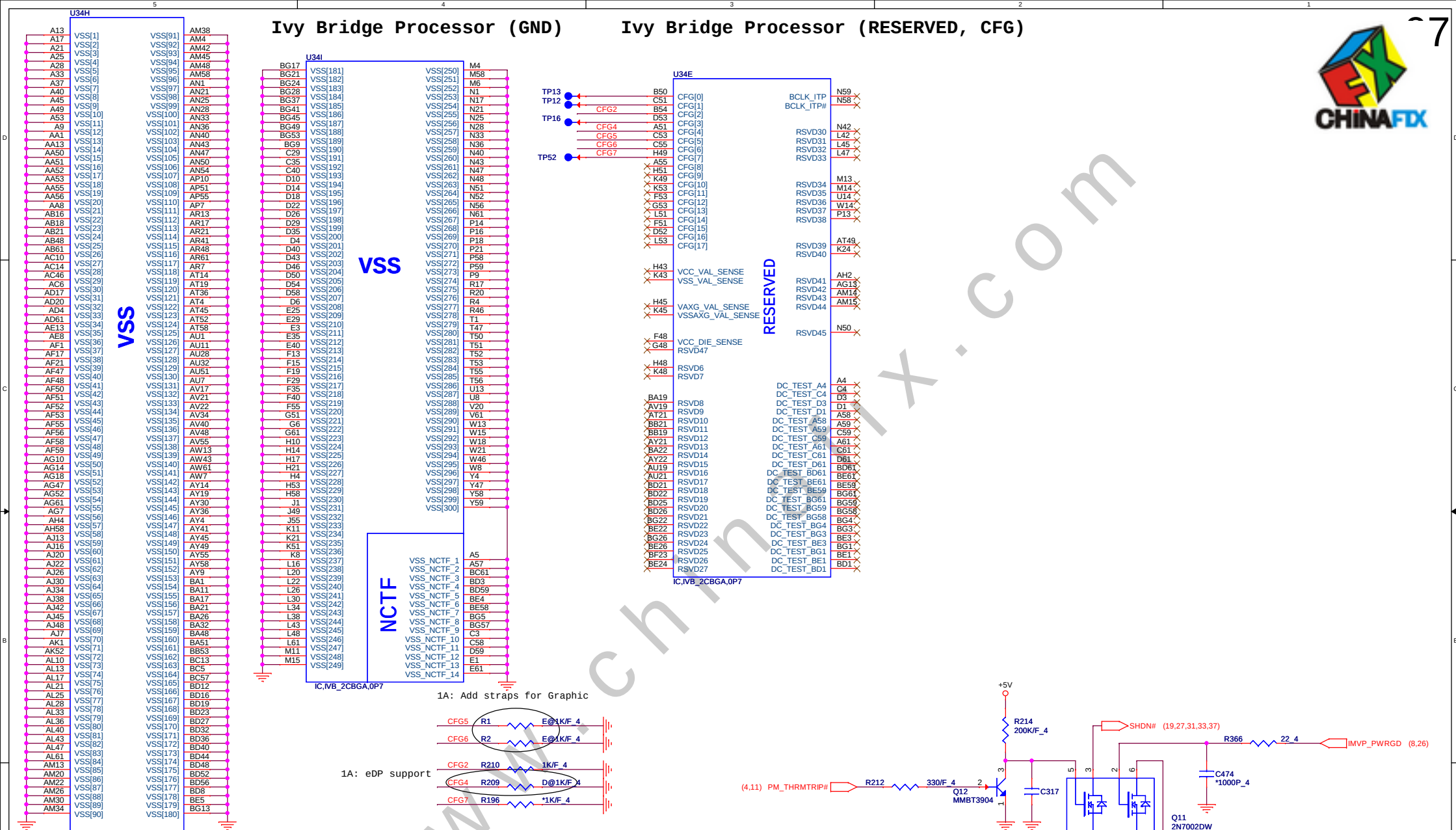
close to VR

SVID ALERT

HR/CR: HR(VID1 only) CR(VID0&VID1)

D48 → VCCSA_VID0 (30)
D49 → VCCSA_VID1 (30)

VCCIO_SEL:
Sandy is High = 1.05V
Ivy is Low = 1.0V

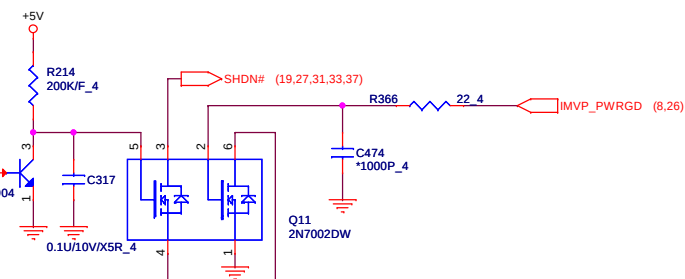


Processor Strapping

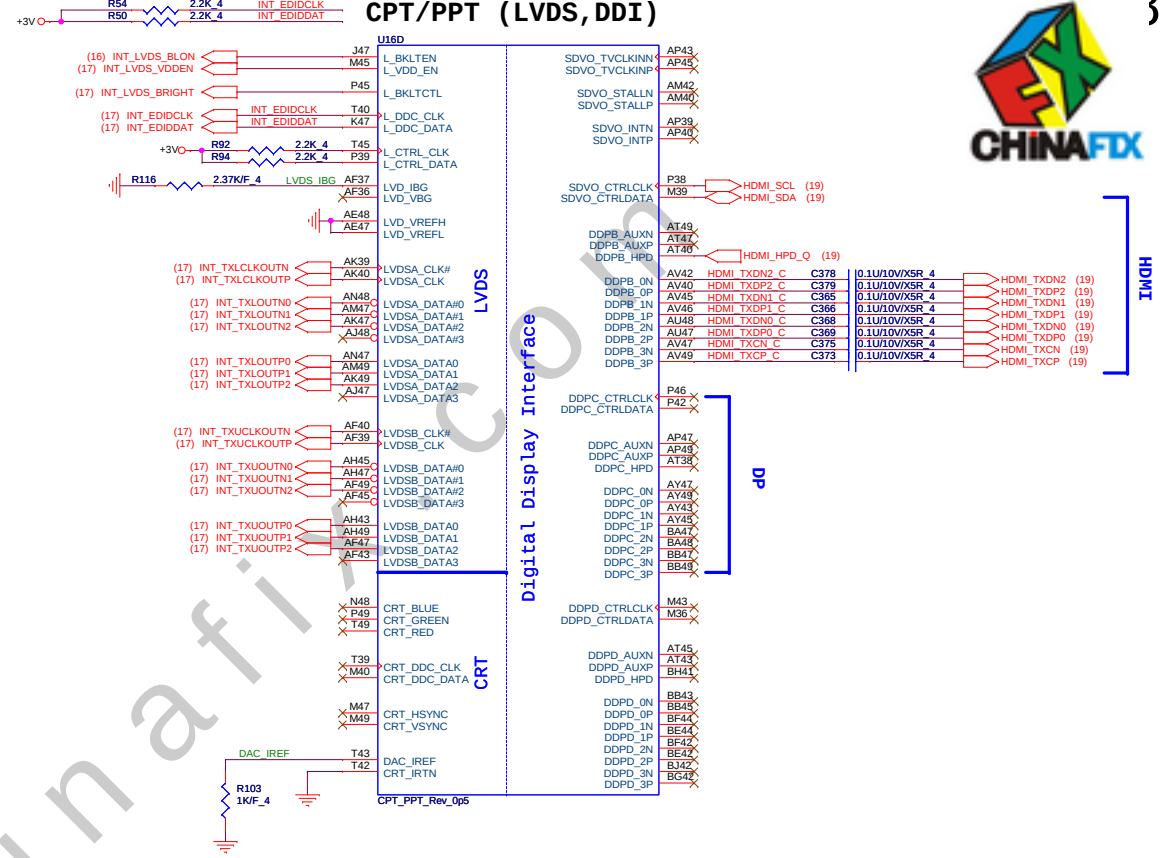
	1	0
CFG2 (PEG Static Lane Reversal)	Normal Operation	Lane Reversed
CFG4 (DP Presence Strap)	Disable; No physical DP attached to eDP	Enable; An ext DP device is connected to eDP
CFG7 (PEG Defer Training)	PEG train immediately following xxRESETB de assertion	PEG wait for BIOS training

CFG[6:5] (PCIe Port Bifurcation Straps)

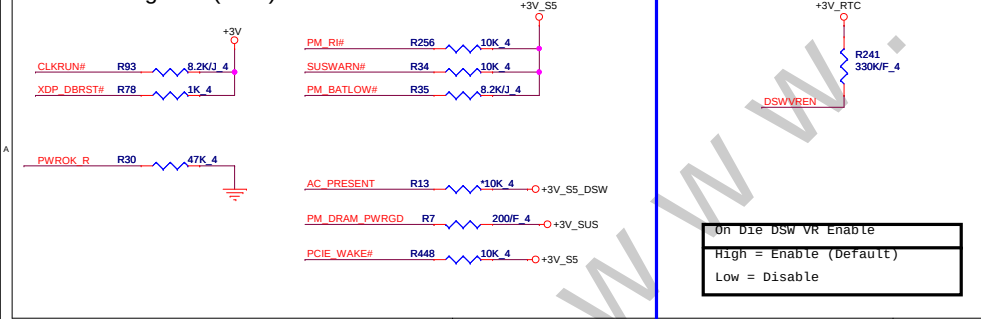
```
11: (Default) x16 - X16 PEG interface
10: PEG x8 x8 bifurcation enableddisabled
01: Reserved - (Device 1 function 1 disabled ; function 2 enabled)
00: x8,x4,x4 - Device 1 functions 1 and 2 enabled
```



CPT/PPT (LVDS, DDI)



On Die DSW VR Enable
High = Enable (Default)
Low = Disable



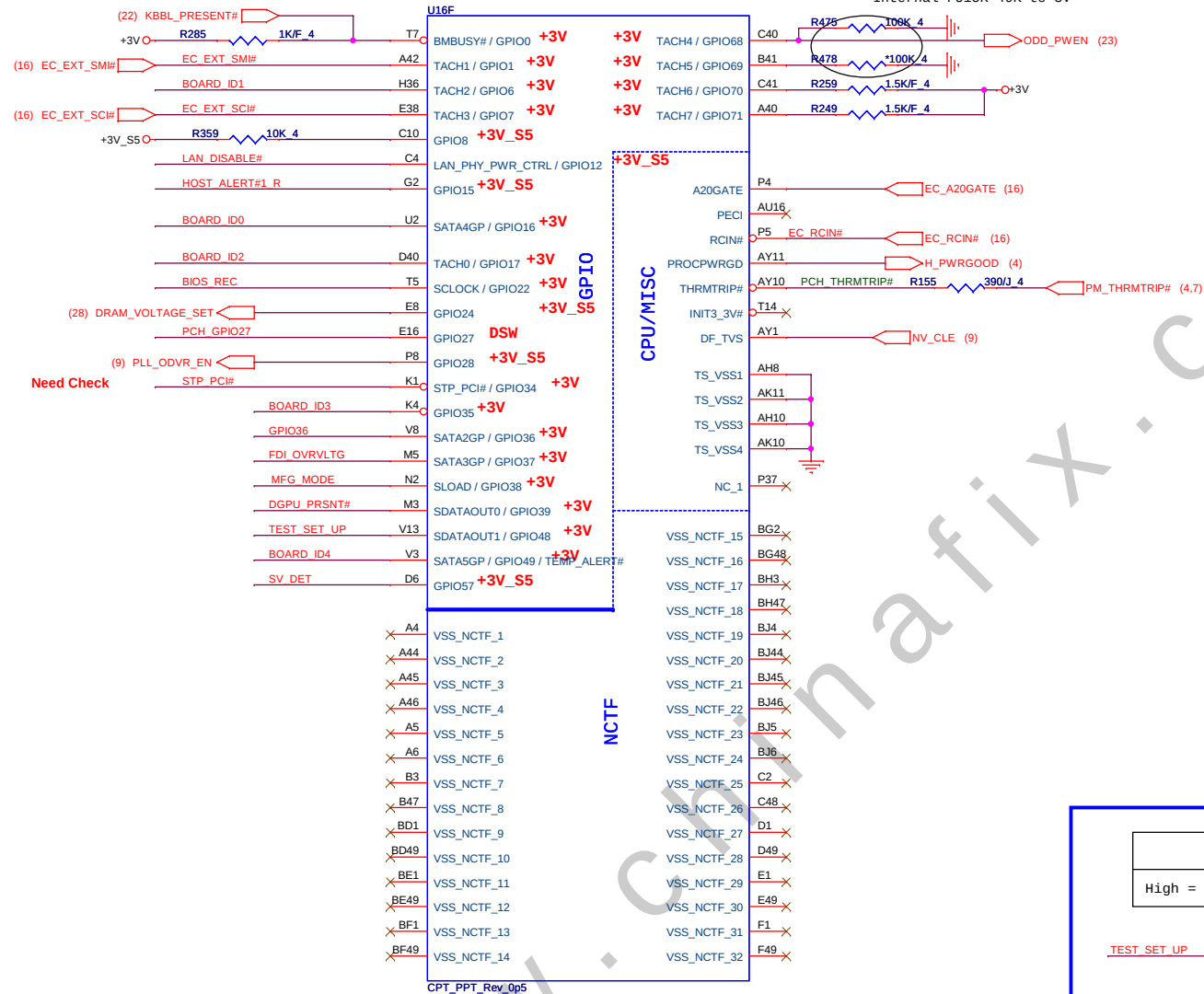
		Quanta Computer Inc. PROJECT : GD5		
		CPT/PTT 1/6		
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CPT/PPT (PCI-E, SMBUS, CLK)



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	CPT/PPT 3/6	1A
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CPT/PPT (GPIO, VSS_NCTF, RSVD)



CPT_PPT_Rev_0p5

FDI TERMINATION
VOLTAGE
OVERRIDE

Low - Tx, Rx terminated
to same voltage

DMI TERMINATION
VOLTAGE OVERRIDE

Low = Tx, Rx terminated to
same voltage (DC Coupling Mode)
(DEFAULT)

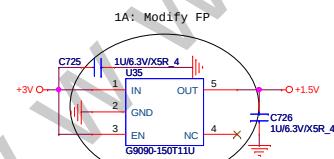
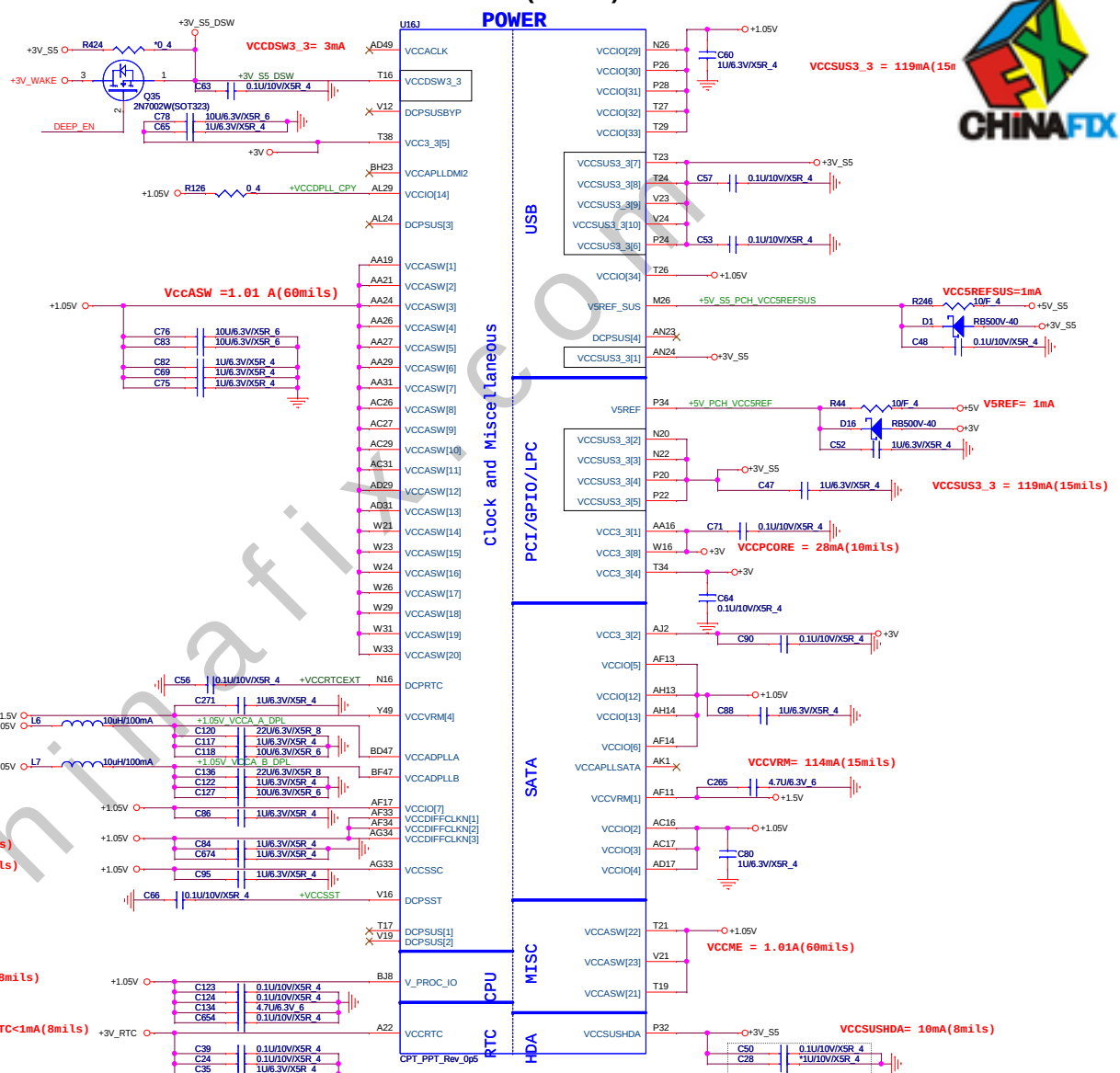
BIOS RECOVERY

High = Disable (Default)
Low = Enable

MFG - TEST

MFG MODE R287

+3V



CPT/PPT (GND)

U16I

H5	VSS[0]	AK38
AA17	VSS[1]	VSS[80]
AA2	VSS[2]	VSS[81]
AA3	VSS[3]	VSS[82]
AA33	VSS[4]	VSS[83]
AA34	VSS[5]	VSS[84]
AB11	VSS[6]	VSS[85]
AB14	VSS[7]	VSS[86]
AB39	VSS[8]	VSS[87]
AB4	VSS[9]	VSS[88]
AB43	VSS[10]	VSS[89]
AB5	VSS[11]	VSS[90]
AB7	VSS[12]	VSS[91]
AC19	VSS[13]	VSS[92]
AC2	VSS[14]	VSS[93]
AC21	VSS[15]	VSS[94]
AC24	VSS[16]	VSS[95]
AC33	VSS[17]	VSS[96]
AC34	VSS[18]	VSS[97]
AC48	VSS[19]	VSS[98]
AD10	VSS[20]	VSS[99]
AD11	VSS[21]	VSS[100]
AD12	VSS[22]	VSS[101]
AD13	VSS[23]	VSS[102]
AD19	VSS[24]	VSS[103]
AD24	VSS[25]	VSS[104]
AD26	VSS[26]	VSS[105]
AD27	VSS[27]	VSS[106]
AD33	VSS[28]	VSS[107]
AD34	VSS[29]	VSS[108]
AD36	VSS[30]	VSS[109]
AD37	VSS[31]	VSS[110]
AD38	VSS[32]	VSS[111]
AD39	VSS[33]	VSS[112]
AD4	VSS[34]	VSS[113]
AD40	VSS[35]	VSS[114]
AD42	VSS[36]	VSS[115]
AD43	VSS[37]	VSS[116]
AD45	VSS[38]	VSS[117]
AD46	VSS[39]	VSS[118]
AD6	VSS[40]	VSS[119]
AE2	VSS[41]	VSS[120]
AE3	VSS[42]	VSS[121]
AF10	VSS[43]	VSS[122]
AF12	VSS[44]	VSS[123]
AD14	VSS[45]	VSS[124]
AD16	VSS[46]	VSS[125]
AF16	VSS[47]	VSS[126]
AF19	VSS[48]	VSS[127]
AF24	VSS[49]	VSS[128]
AF26	VSS[50]	VSS[129]
AF27	VSS[51]	VSS[130]
AF29	VSS[52]	VSS[131]
AF31	VSS[53]	VSS[132]
AF38	VSS[54]	VSS[133]
AF4	VSS[55]	VSS[134]
AF42	VSS[56]	VSS[135]
AF46	VSS[57]	VSS[136]
AF5	VSS[58]	VSS[137]
AF7	VSS[59]	VSS[138]
AF8	VSS[60]	VSS[139]
AG19	VSS[61]	VSS[140]
AG2	VSS[62]	VSS[141]
AG31	VSS[63]	VSS[142]
AG48	VSS[64]	VSS[143]
AH11	VSS[65]	VSS[144]
AH3	VSS[66]	VSS[145]
AH36	VSS[67]	VSS[146]
AH39	VSS[68]	VSS[147]
AH40	VSS[69]	VSS[148]
AH42	VSS[70]	VSS[149]
AH46	VSS[71]	VSS[150]
AH7	VSS[72]	VSS[151]
AJ19	VSS[73]	VSS[152]
AJ21	VSS[74]	VSS[153]
AJ24	VSS[75]	VSS[154]
AJ33	VSS[76]	VSS[155]
AJ34	VSS[77]	VSS[156]
AK12	VSS[78]	VSS[157]
AK3	VSS[79]	VSS[158]

CPT_PPT_Rev_0p5

AY4	VSS[159]	VSS[259]	H46
AY42	VSS[160]	VSS[260]	K18
AY46	VSS[161]	VSS[261]	K26
AY8	VSS[162]	VSS[262]	K39
B11	VSS[163]	VSS[263]	K46
B15	VSS[164]	VSS[264]	K7
B19	VSS[165]	VSS[265]	L18
B23	VSS[166]	VSS[266]	L2
B27	VSS[167]	VSS[267]	L20
B31	VSS[168]	VSS[268]	L26
B35	VSS[169]	VSS[269]	L28
B39	VSS[170]	VSS[270]	L36
B7	VSS[171]	VSS[271]	L48
F45	VSS[172]	VSS[272]	M12
BB12	VSS[173]	VSS[273]	P16
BB16	VSS[174]	VSS[274]	M18
BB20	VSS[175]	VSS[275]	M22
BB22	VSS[176]	VSS[276]	M24
BB24	VSS[177]	VSS[277]	M30
BB28	VSS[178]	VSS[278]	M32
BB30	VSS[179]	VSS[279]	M34
BB38	VSS[180]	VSS[280]	M38
BB4	VSS[181]	VSS[281]	M4
BB46	VSS[182]	VSS[282]	M42
BC14	VSS[183]	VSS[283]	M46
BC18	VSS[184]	VSS[284]	M8
BC2	VSS[185]	VSS[285]	N18
BC22	VSS[186]	VSS[286]	P30
BC26	VSS[187]	VSS[287]	N47
BC32	VSS[188]	VSS[288]	P11
BC34	VSS[189]	VSS[289]	P18
BC36	VSS[190]	VSS[290]	T33
BC40	VSS[191]	VSS[291]	P40
BC42	VSS[192]	VSS[292]	P43
BC48	VSS[193]	VSS[293]	P47
BD46	VSS[194]	VSS[294]	P7
BD5	VSS[195]	VSS[295]	R2
BE22	VSS[196]	VSS[296]	R48
BE26	VSS[197]	VSS[297]	T12
BE40	VSS[198]	VSS[298]	T31
BF10	VSS[199]	VSS[299]	T37
BF12	VSS[200]	VSS[300]	T4
BF16	VSS[201]	VSS[301]	W34
BF20	VSS[202]	VSS[302]	T46
BF22	VSS[203]	VSS[303]	T47
BF24	VSS[204]	VSS[304]	T8
BF26	VSS[205]	VSS[305]	V11
BF28	VSS[206]	VSS[306]	V17
BD3	VSS[207]	VSS[307]	V26
BF30	VSS[208]	VSS[308]	V27
AT28	VSS[209]	VSS[309]	V29
BF40	VSS[210]	VSS[310]	V31
BF8	VSS[211]	VSS[311]	V36
BG17	VSS[212]	VSS[312]	V39
BG21	VSS[213]	VSS[313]	V43
BG33	VSS[214]	VSS[314]	V7
BG44	VSS[215]	VSS[315]	W17
BG8	VSS[216]	VSS[316]	W19
BH11	VSS[217]	VSS[317]	W2
BH15	VSS[218]	VSS[318]	W27
BH17	VSS[219]	VSS[319]	W48
BH19	VSS[220]	VSS[320]	Y12
H10	VSS[221]	VSS[321]	Y38
AV30	VSS[222]	VSS[322]	Y4
BH27	VSS[223]	VSS[323]	Y42
BH31	VSS[224]	VSS[324]	Y46
AV4	VSS[225]	VSS[325]	Y8
BH35	VSS[226]	VSS[326]	BG29
BH39	VSS[227]	VSS[327]	N24
BH43	VSS[228]	VSS[328]	AJ3
AW18	VSS[229]	VSS[329]	AD47
BH7	VSS[230]	VSS[330]	B43
D3	VSS[231]	VSS[331]	BE10
D12	VSS[232]	VSS[332]	BG41
D16	VSS[233]	VSS[333]	G14
D18	VSS[234]	VSS[334]	H16
D22	VSS[235]	VSS[335]	T36
D24	VSS[236]	VSS[336]	BG22
D26	VSS[237]	VSS[337]	BG24
D30	VSS[238]	VSS[338]	C22
D32	VSS[239]	VSS[339]	AP13
D34	VSS[240]	VSS[340]	M14
D38	VSS[241]	VSS[341]	AP3
D42	VSS[242]	VSS[342]	BE16
D8	VSS[243]	VSS[343]	BC16
E18	VSS[244]	VSS[344]	BG28
E26	VSS[245]	VSS[345]	BJ28
G18	VSS[246]	VSS[346]	
G20	VSS[247]	VSS[347]	
G26	VSS[248]	VSS[348]	
G28	VSS[249]	VSS[349]	
G36	VSS[250]	VSS[350]	
G48	VSS[251]	VSS[351]	
H12	VSS[252]	VSS[352]	
H18	VSS[253]		
H22	VSS[254]		
H24	VSS[255]		
H26	VSS[256]		
H30	VSS[257]		
H32	VSS[258]		
H34			
F3			

CPT_PPT_Rev_0p5

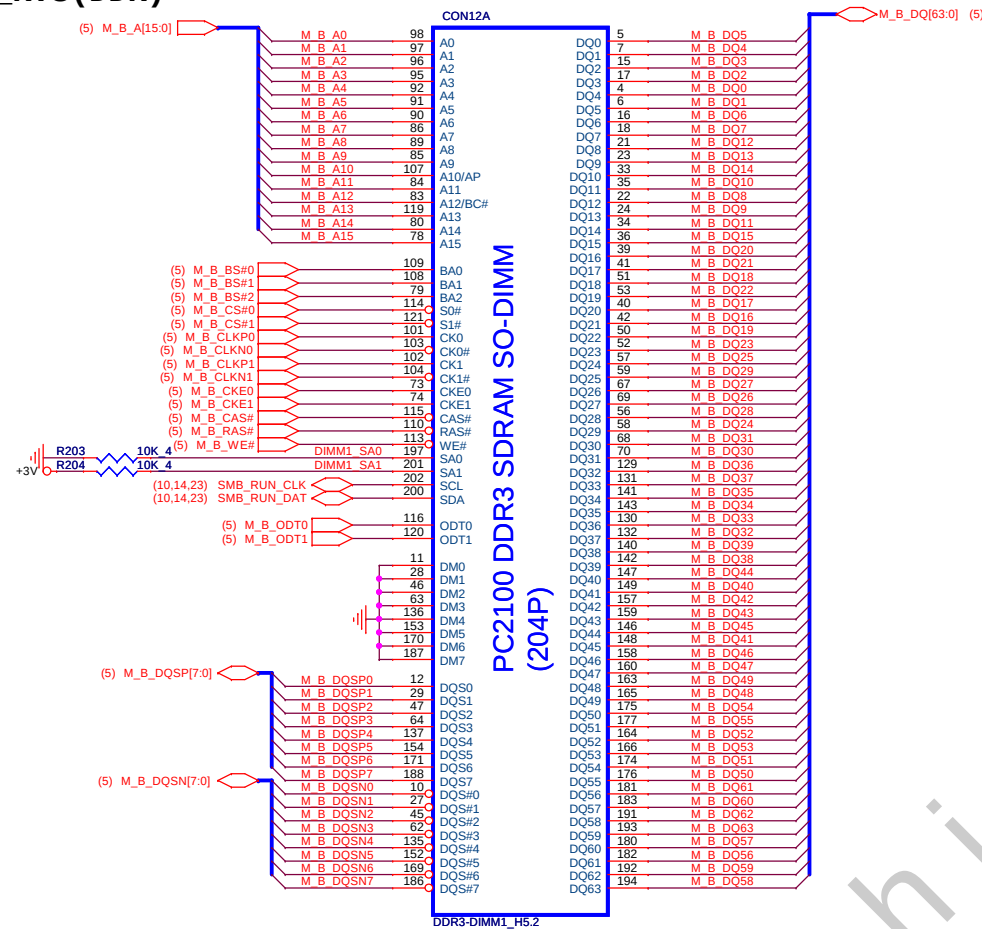


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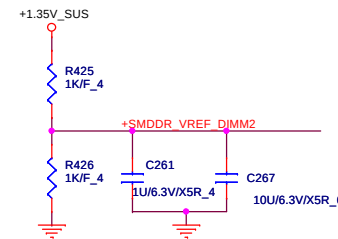
Size	Document Number	Rev
		1A
CPT/PPT 6/6		
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1.Level 1 Environment-related Substances should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

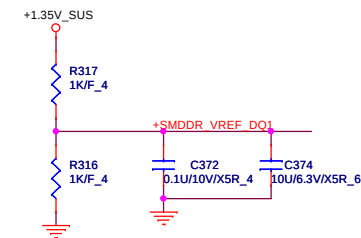
DDR_RVS(DDR)



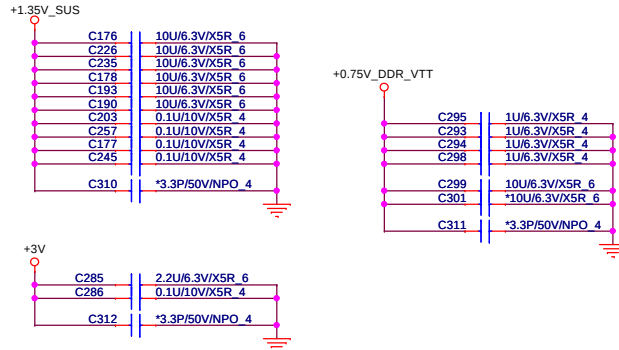
RUV Type



VREF DQ1 M1 Solution



Place these Caps near So-Dimm1.



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Size	Document Number	Rev
	DDR3 SO-DIMM-1	1A
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Since ECSCI is OD, no need for a back-drive protection diode on this signal. But note there is internal PU in chipset at default

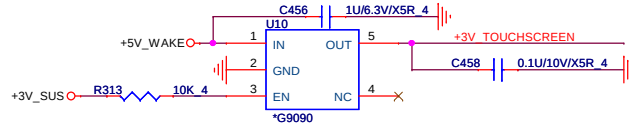
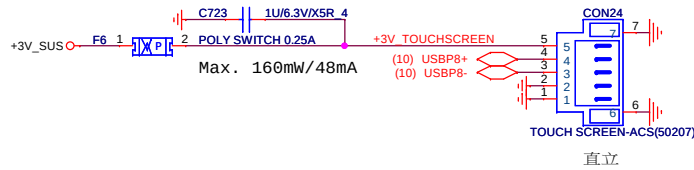


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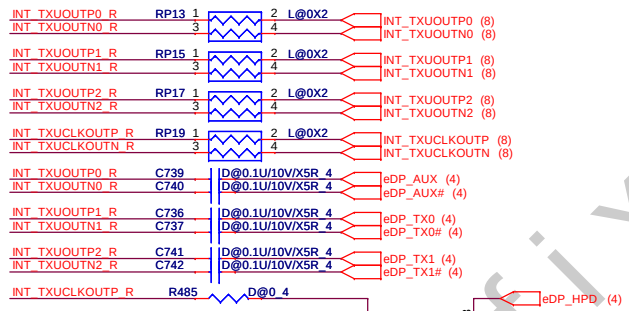
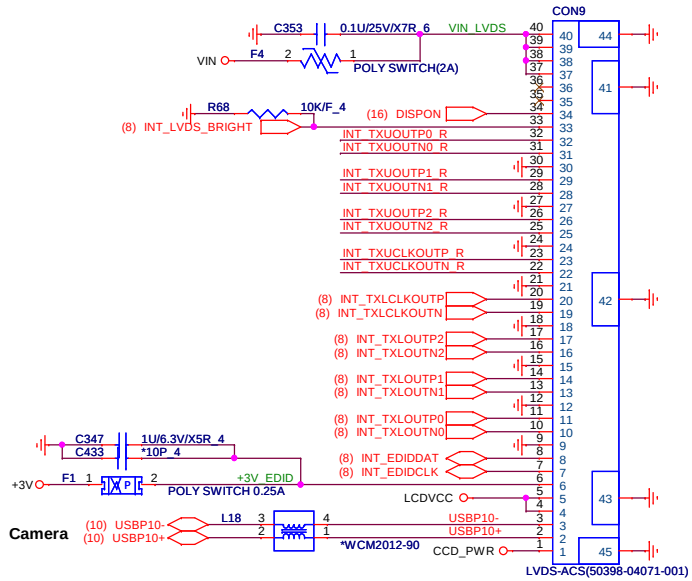
Size	Document Number	Rev
	NPCE985	1A
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Touch Screen

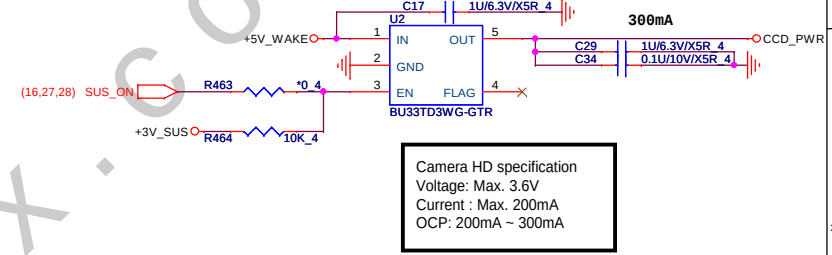


FAST, UL/CSA

LVDS

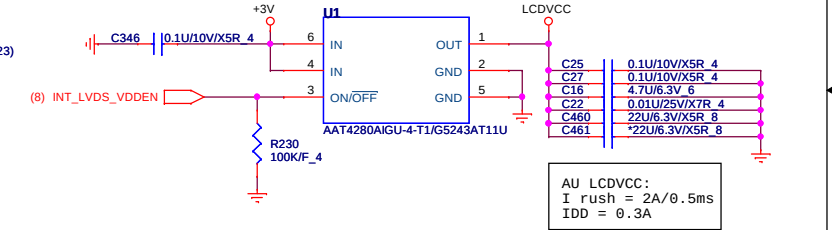


USB Camera Power



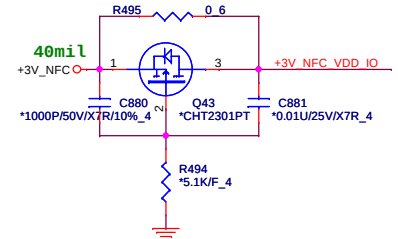
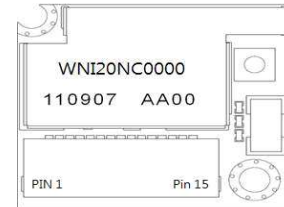
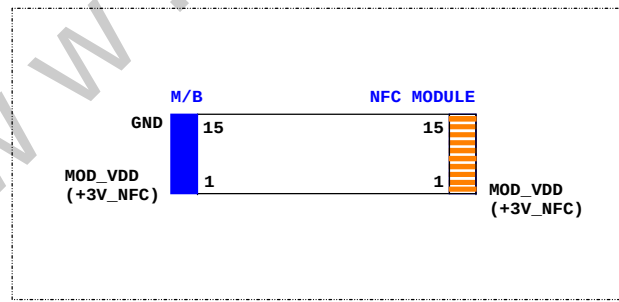
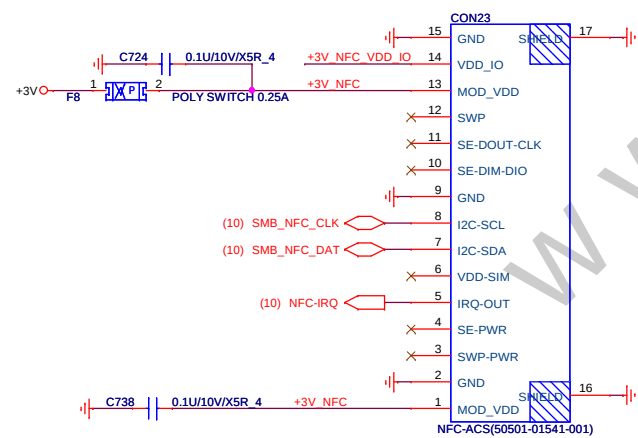
Camera HD specification
Voltage: Max. 3.6V
Current : Max. 200mA
OCP: 200mA ~ 300mA

NB LVDS enable



AU LCDVCC:
I rush = 2A/0.5ms
IDD = 0.3A

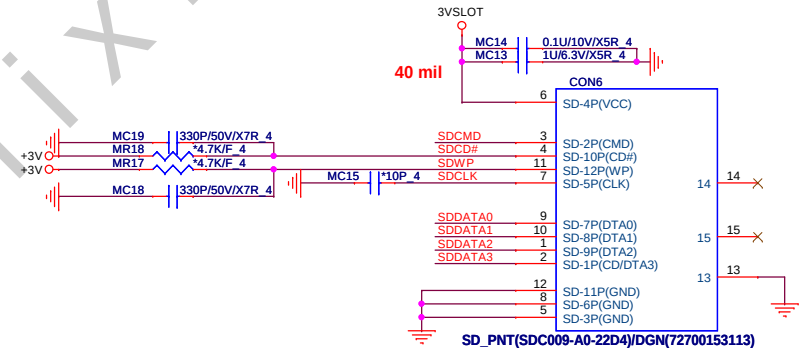
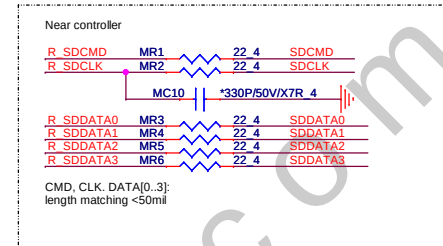
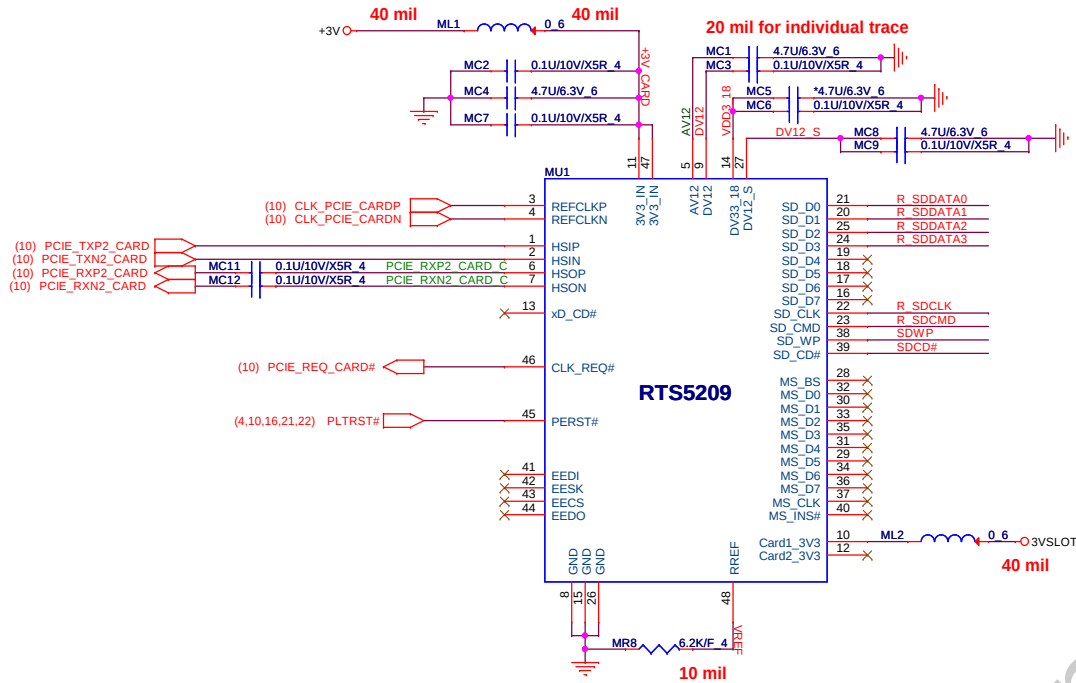
NFC



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Size	Document Number	Rev
		1A
NFC/TOUCH SCREEN/eDP		
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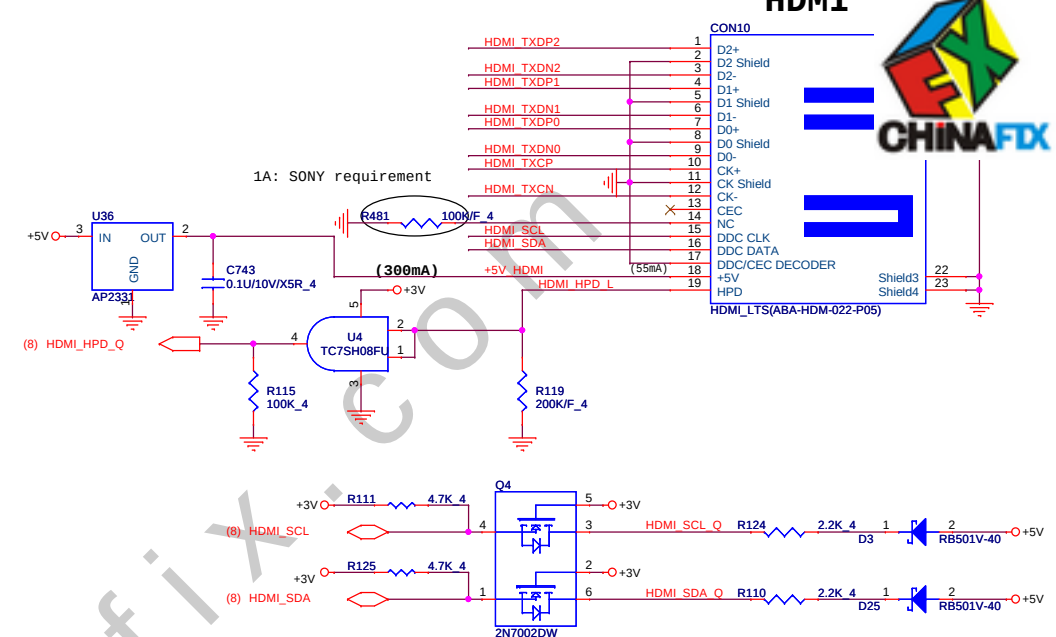
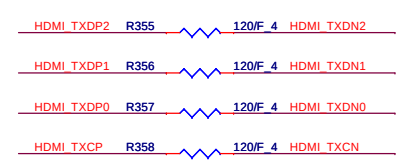
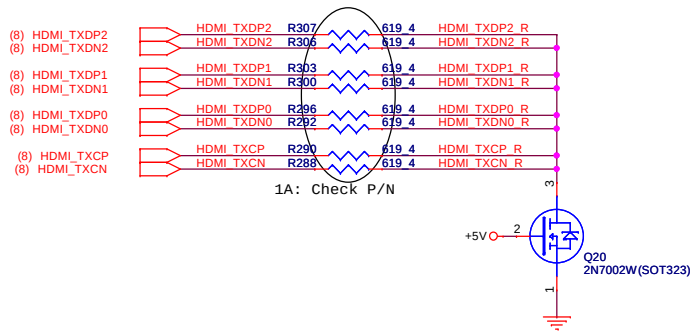


Quanta Computer Inc.
PROJECT : GD5

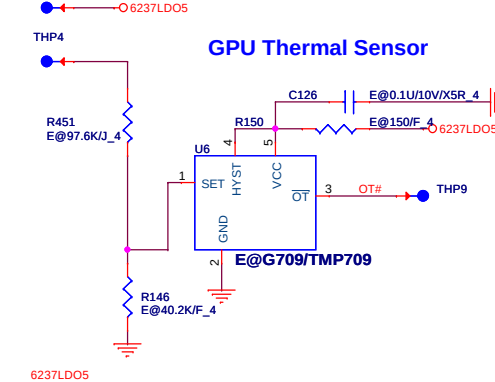
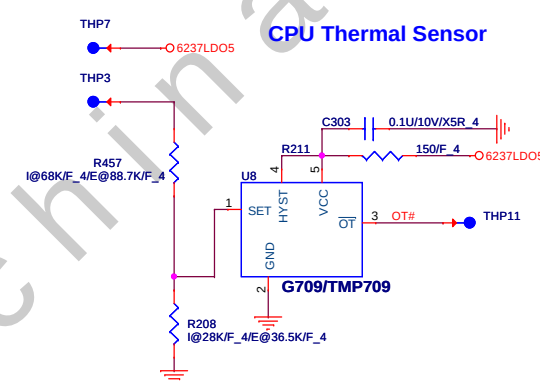
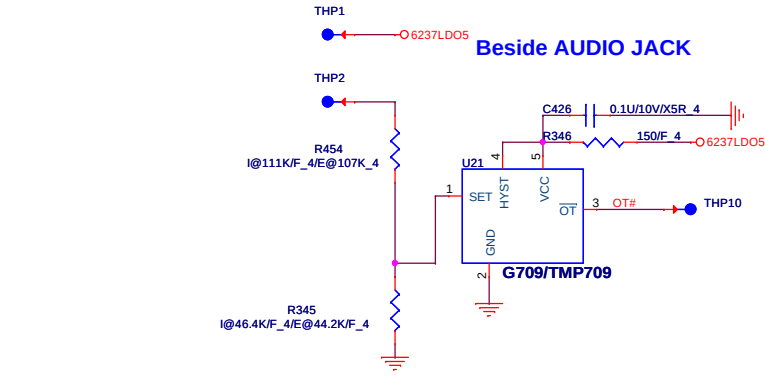
Size	Document Number	Rev
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1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

HDMI



H/W Thermal Protect



RSET(kΩ)= 0.0012T2 - 0.9308T + 96.147

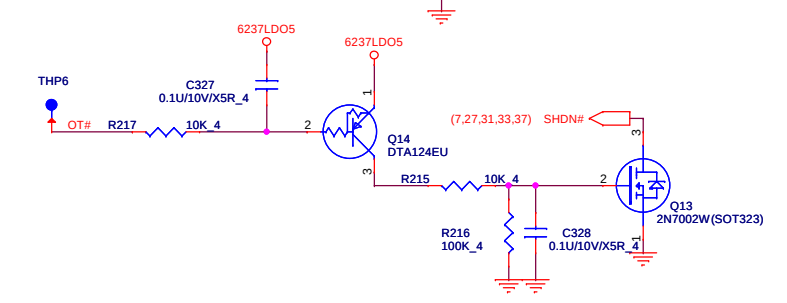
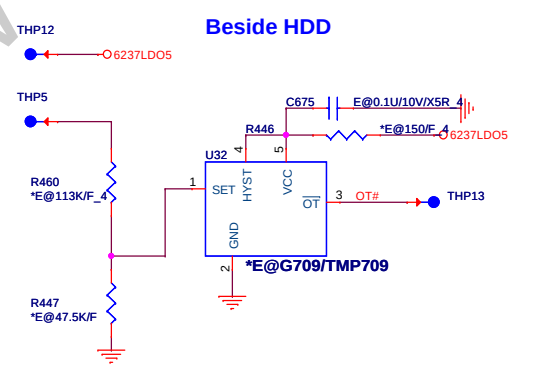
95	18.5K
100	15K
107	10.3K
110	8.2K

DIS SKU

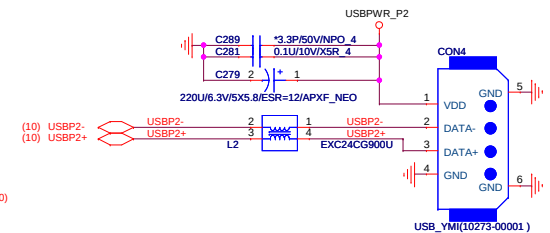
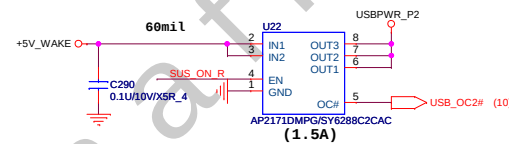
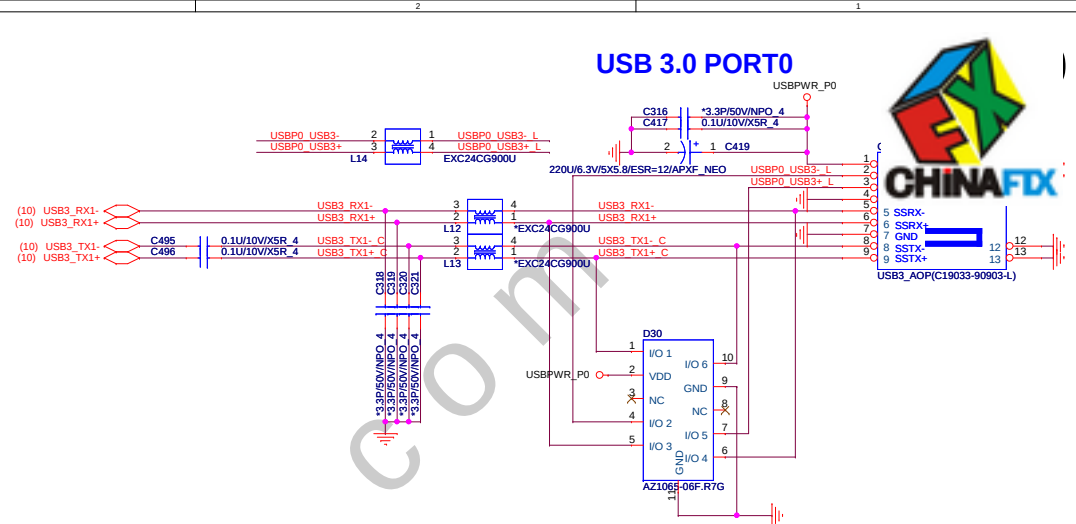
Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	70	R208=36.87K	36.5K	71	70
Near GFX sensor temp	65	R146=40.72K	40.2K	66.3	65.1
Near AUDIO sensor temp	60	R345=44.62K	44.2K	61.2	60

UMA SKU

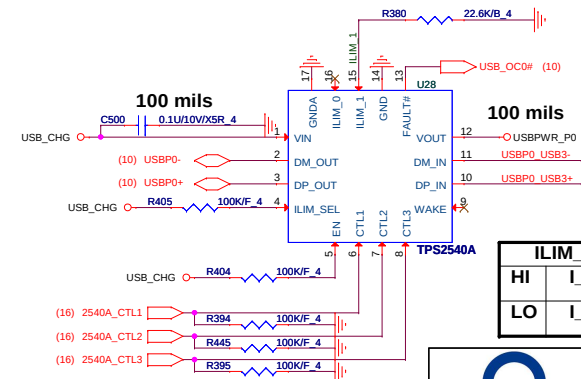
Location of IC	Temp	R-Set	Parts in BOM	Max	Min
Near CPU sensor temp	81	R208=28.63K	28K	82.3	81.4
Near AUDIO sensor temp	58	R345=46.2K	46.4K	58.4	57.1



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.



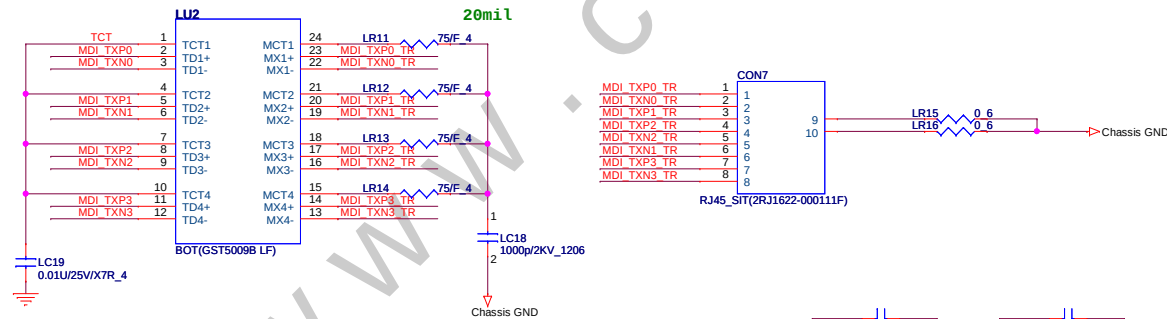
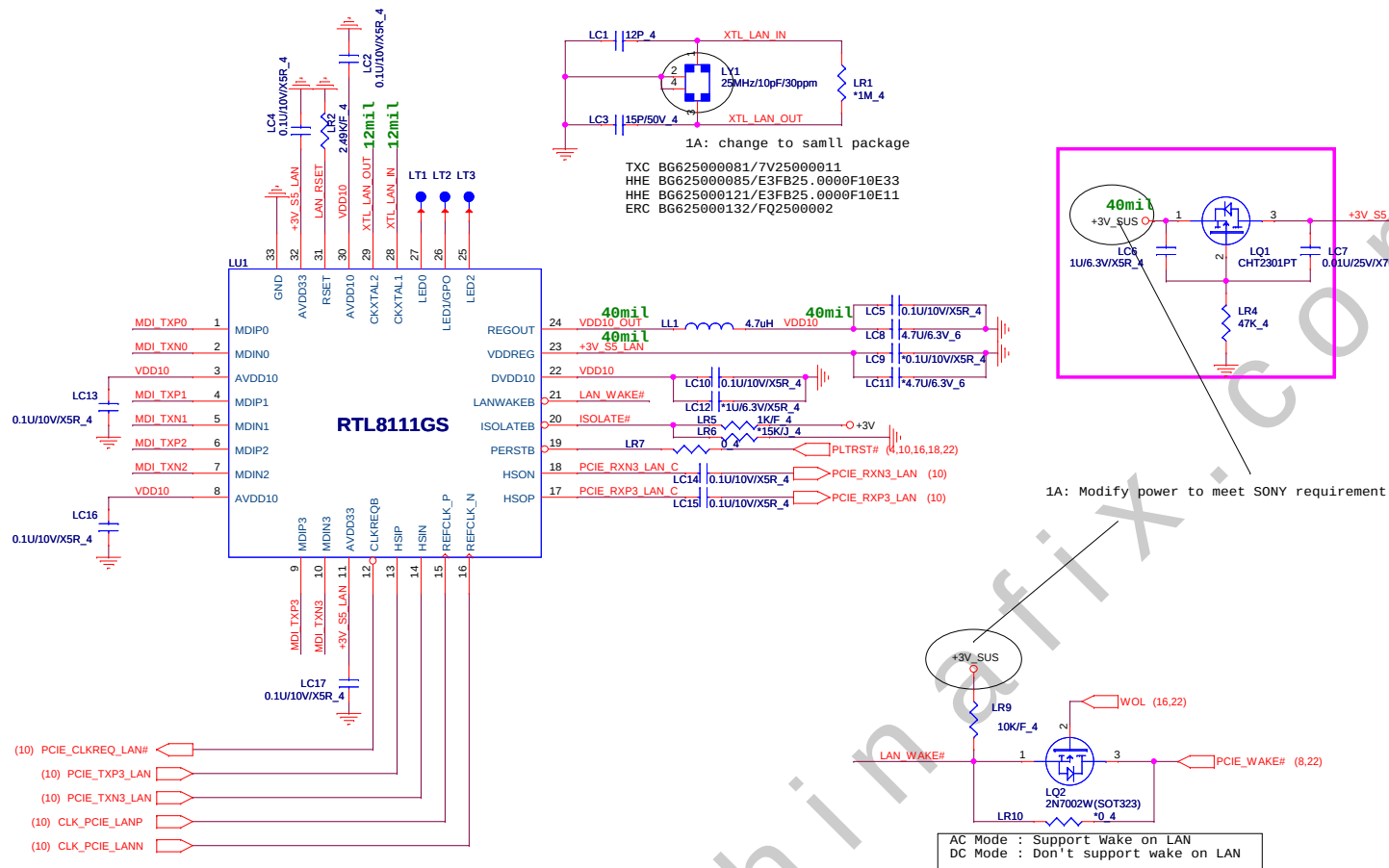
USB Charger



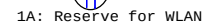
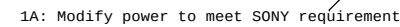
ILIM_SEL (I LIMIT(A)= 48000/R)		
HI	I_LIM_1	
LO	I_LIM_0	48000/22.6K=2.123A

		Quanta Computer Inc. PROJECT : GD5	
Size	Document Number	Rev	
		1A	
USB/USB Charger			
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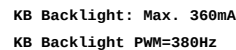
- 1.Level 1 Environment-related Substances Should Never be Used.
- 2.Recycled Resin and Coated Wire should be procured from Green Partners.



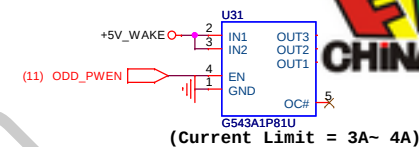
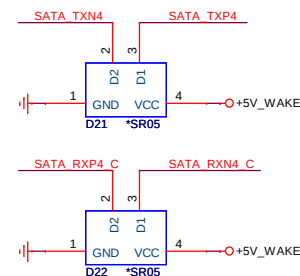
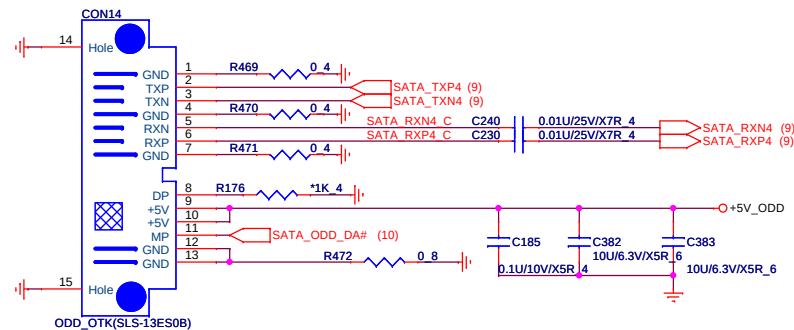
2



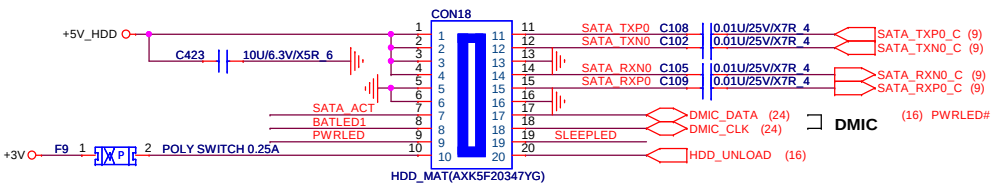
AC Mode : Support Wake on WLAN
DC Mode : Don't support wake on WLAN

[illegible]

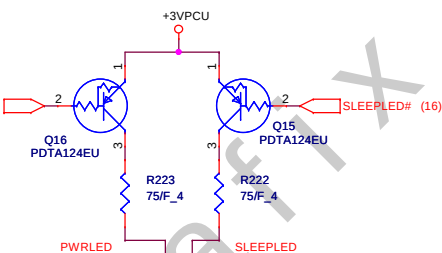
ODD CONNECTOR



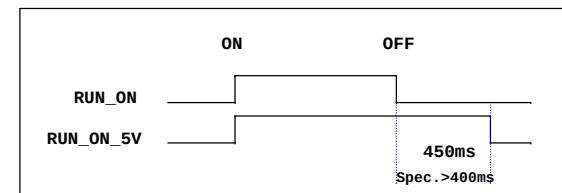
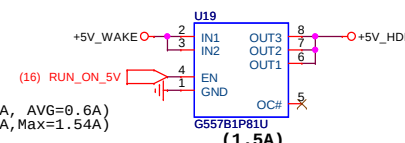
HDD BOARD CONNECTOR



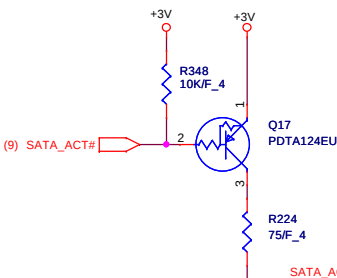
Power/Sleep LED



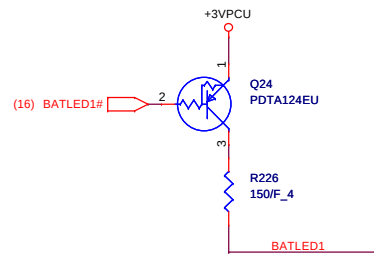
(2.5" HDD VCC5 Max=1.5A, AVG=0.6A)
(2.5" SSD VCC5 typ=0.8A, Max=1.54A)



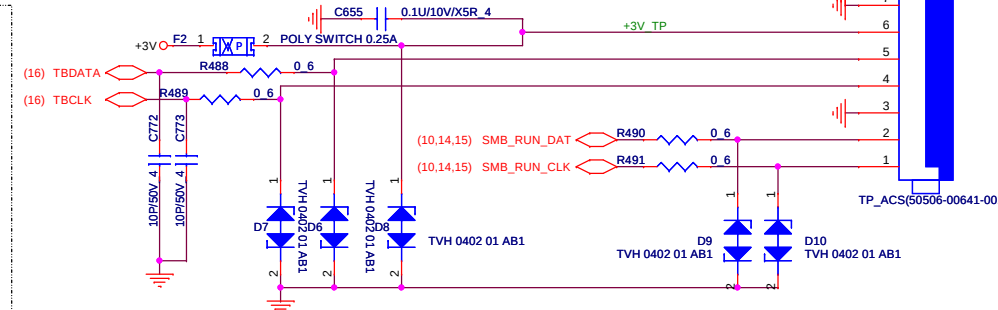
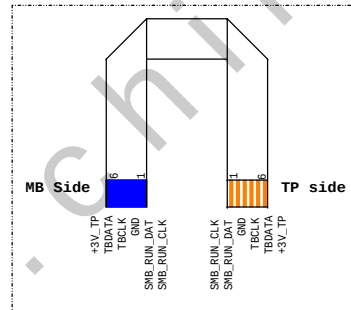
SATA LED



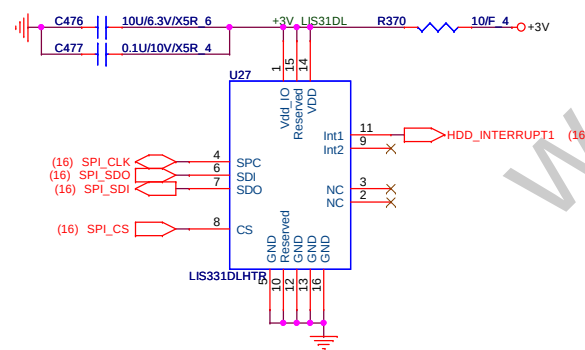
BATTERY LED



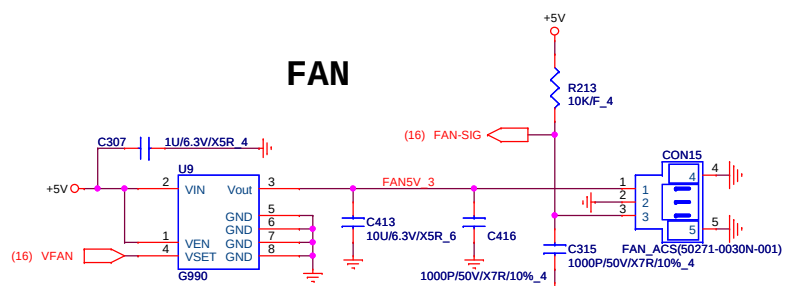
T/P Board to T/P



HDD PROTECT



FAN



1. Level 1 Environment-related Substances Should Never be Used.
2. Recycled Resin and Coated Wire should be procured from Green Partners.



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	HDD/ODD/TP/FAN	1A
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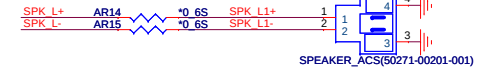
Analog
Digital

80Mils

ALC233-CG

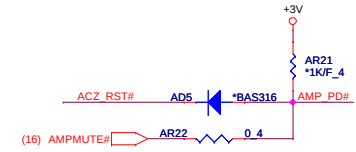
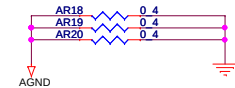
SPK L+ L- R+ R- trace width
Speaker 4 ohm ==> 50 mils

SPEAKER CON

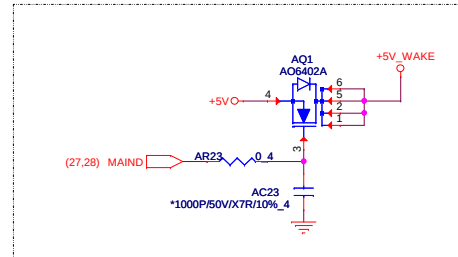


<<Attention>>
Place these EMI components close to codec; For EMI issue.

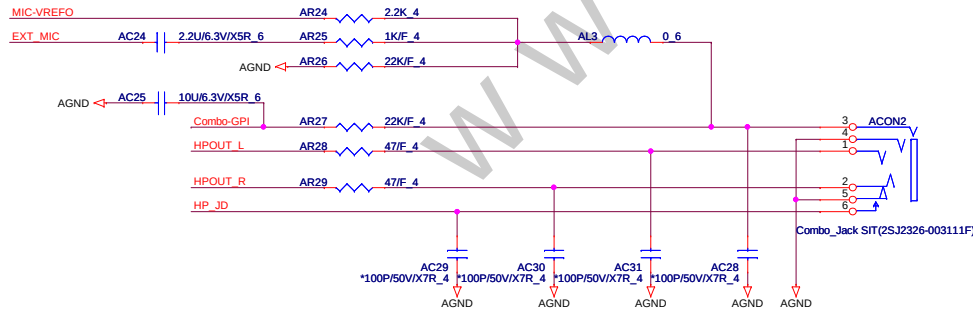
For EMI



Analog
Digital



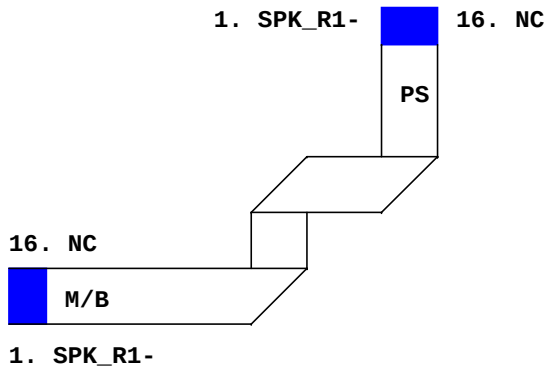
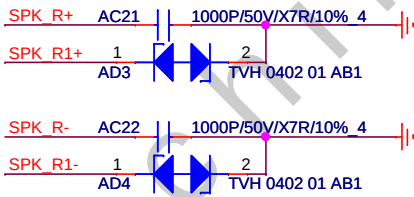
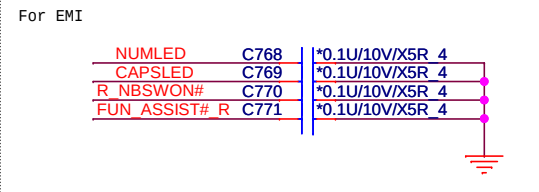
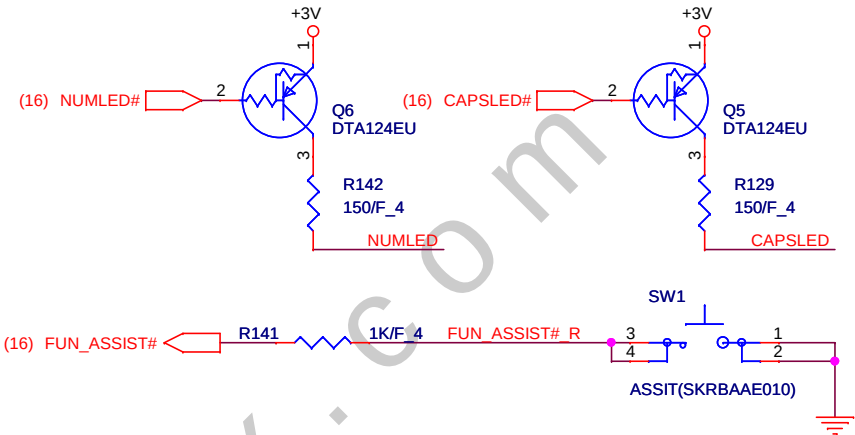
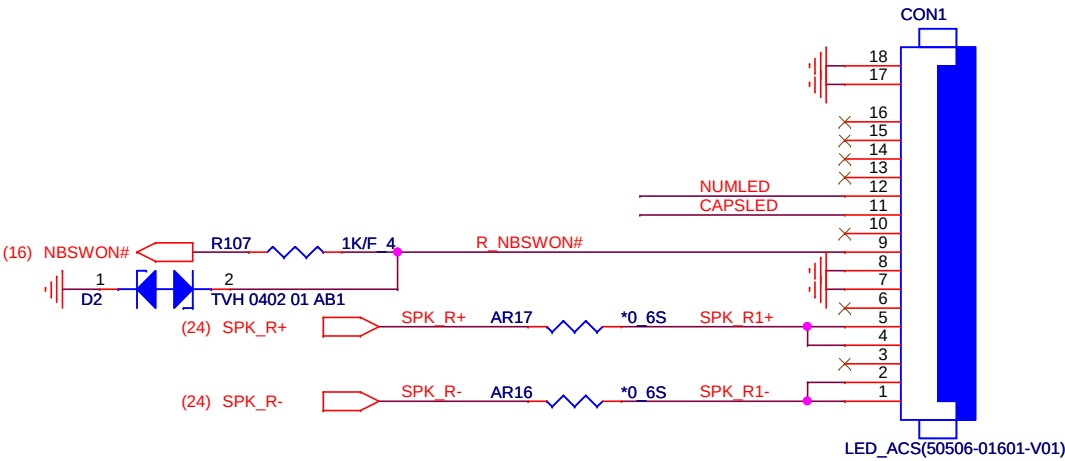
Combo Jack



1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

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Audio Codec ALC233-CG			
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Power SW Board Connector

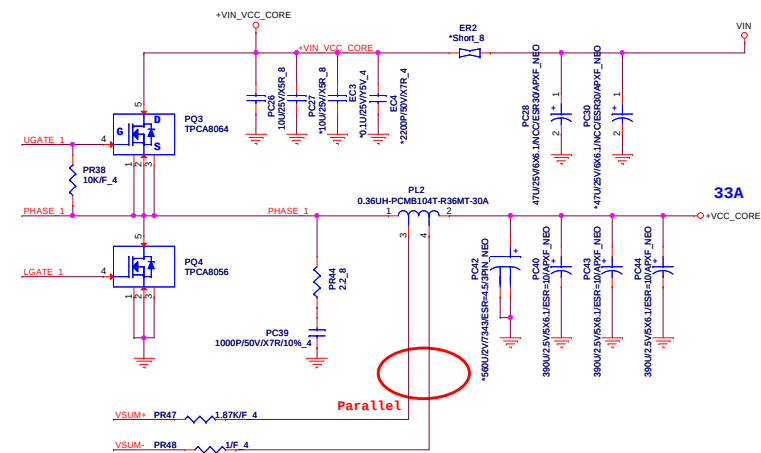
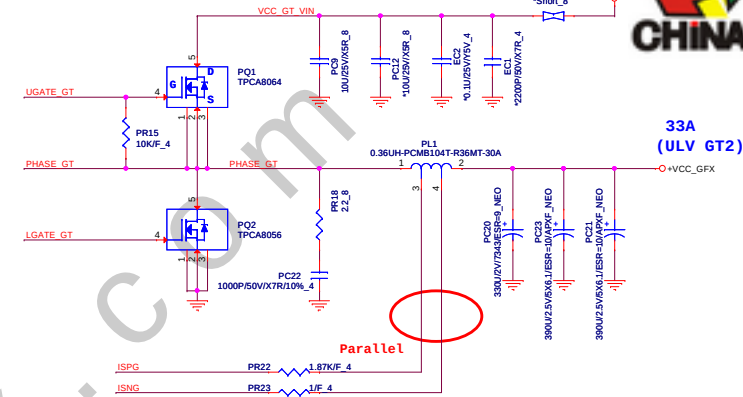
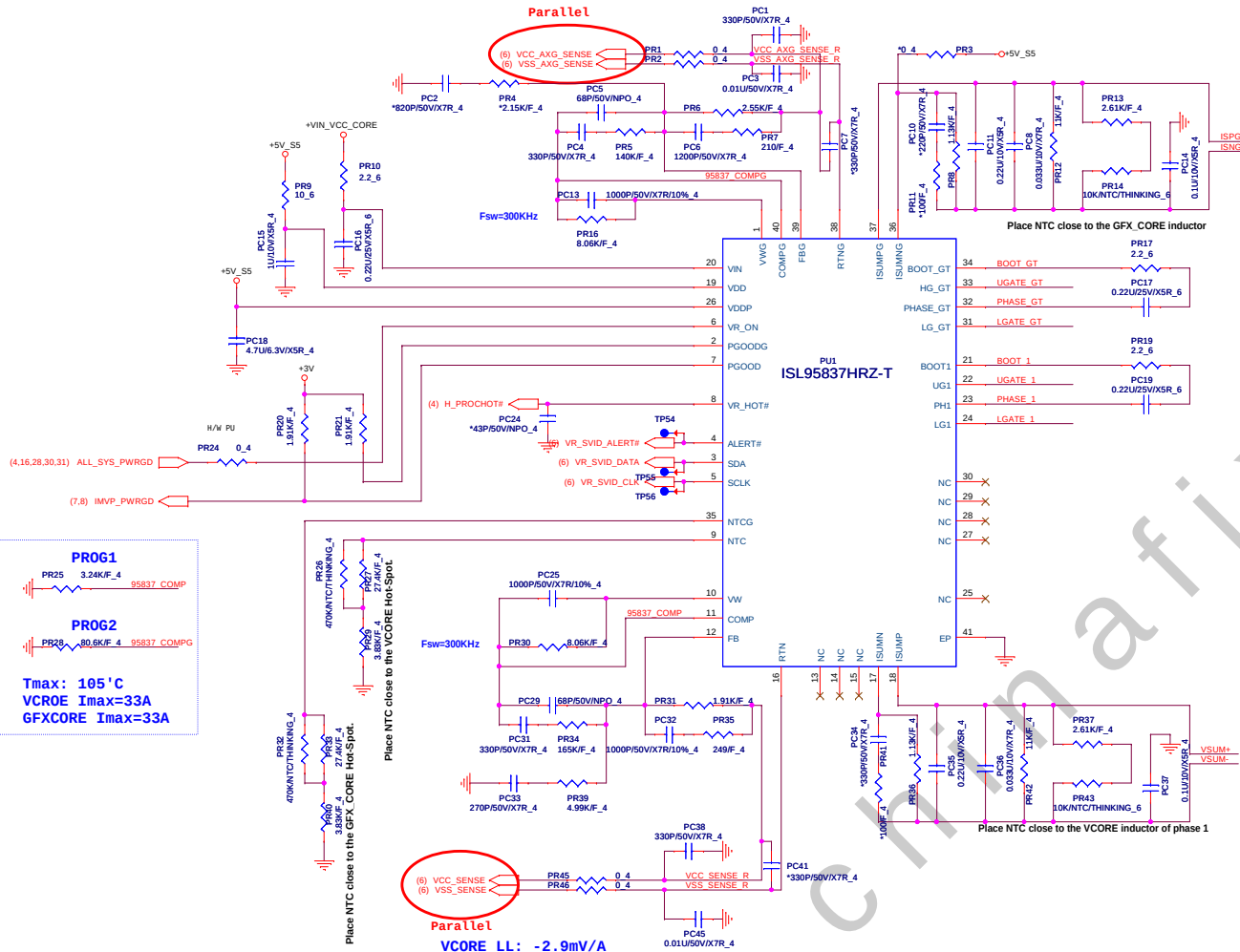


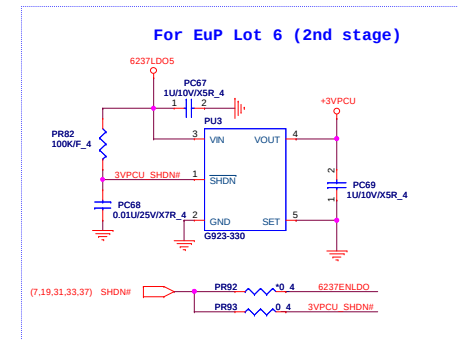
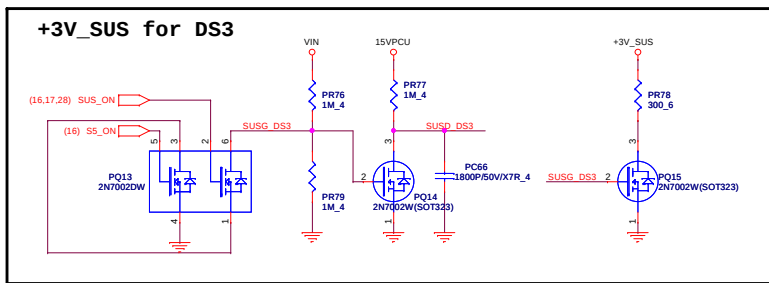
1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.

Quanta Computer Inc.
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	LED/PS	1A
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GFX_CORE LL: -3.9mV/A for GT2



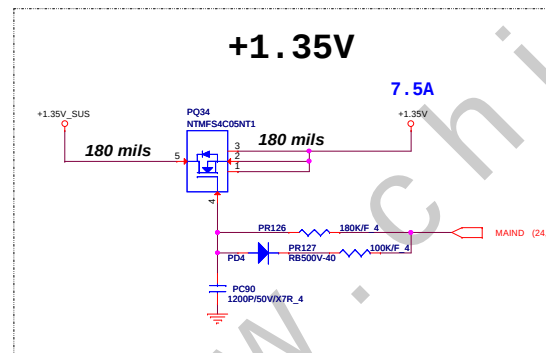
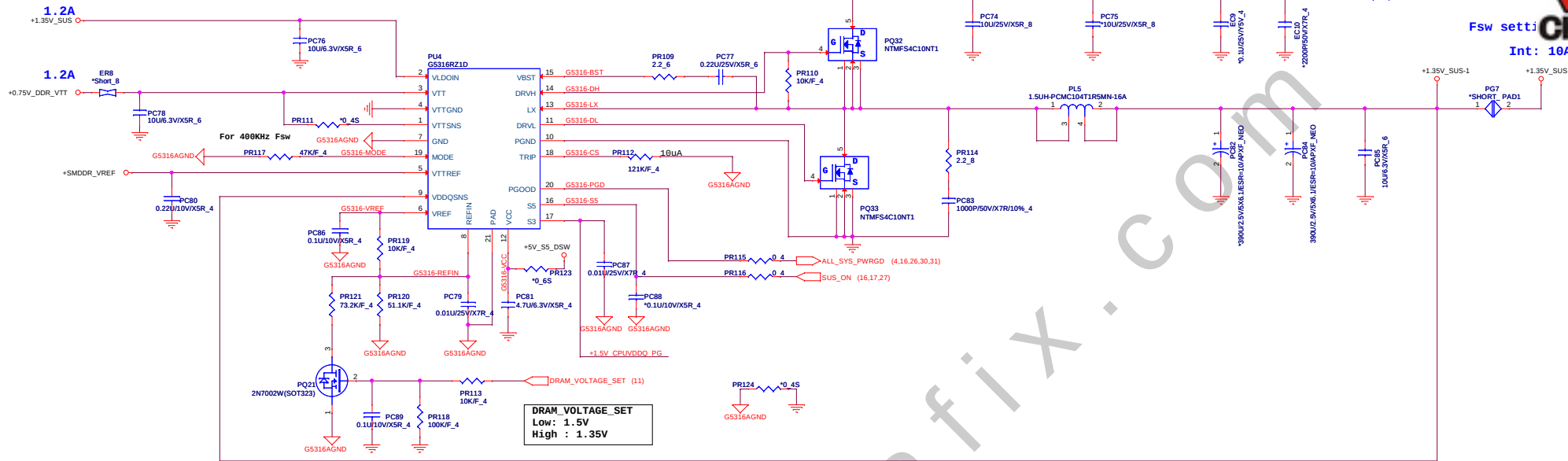


1.35VSUS & VTT_MEM



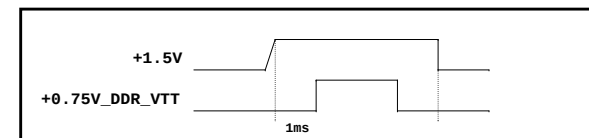
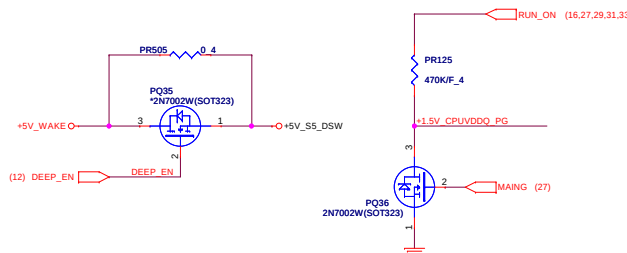
Fsw setti CHINAFOX

Int: 10A

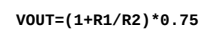


MODE	Resistor on Mode	Fsw	Discharge Mode
3	200Kohm	400KHZ	Tracking discharge
2	100Kohm	300KHZ	
1	60Kohm	300KHZ	Non-tracking discharge
0	47Kohm	400KHZ	

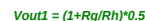
STATE	S3	S5	1.5VSUS	VTTREF	VTT
S0	1	1	On	On	On
S3	0	1	On	On	Off/High Z
S4/S5	0	0	Off	Off	Off



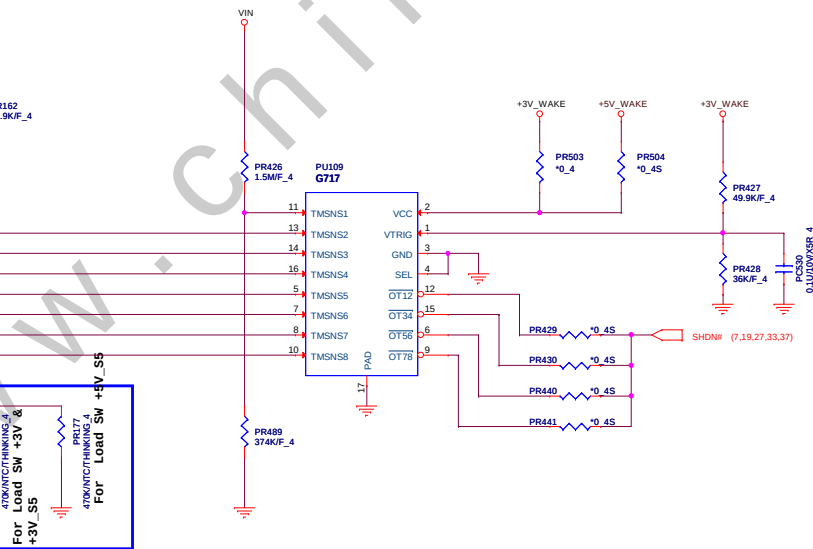
FSW Setting 450KHz

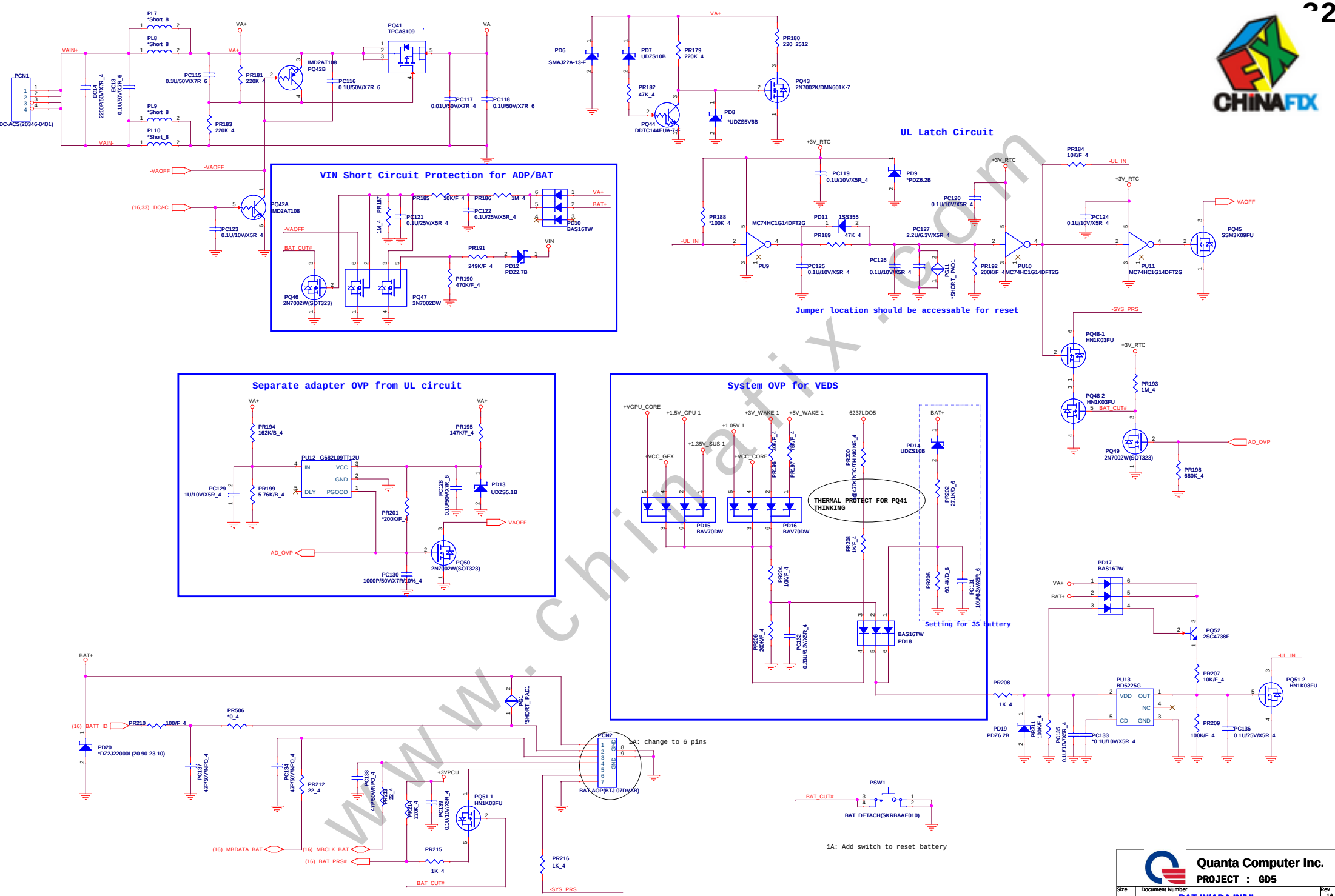

$$R_{ds} \cdot I_{CP} = R_{ILIM} \cdot 20 \mu A$$

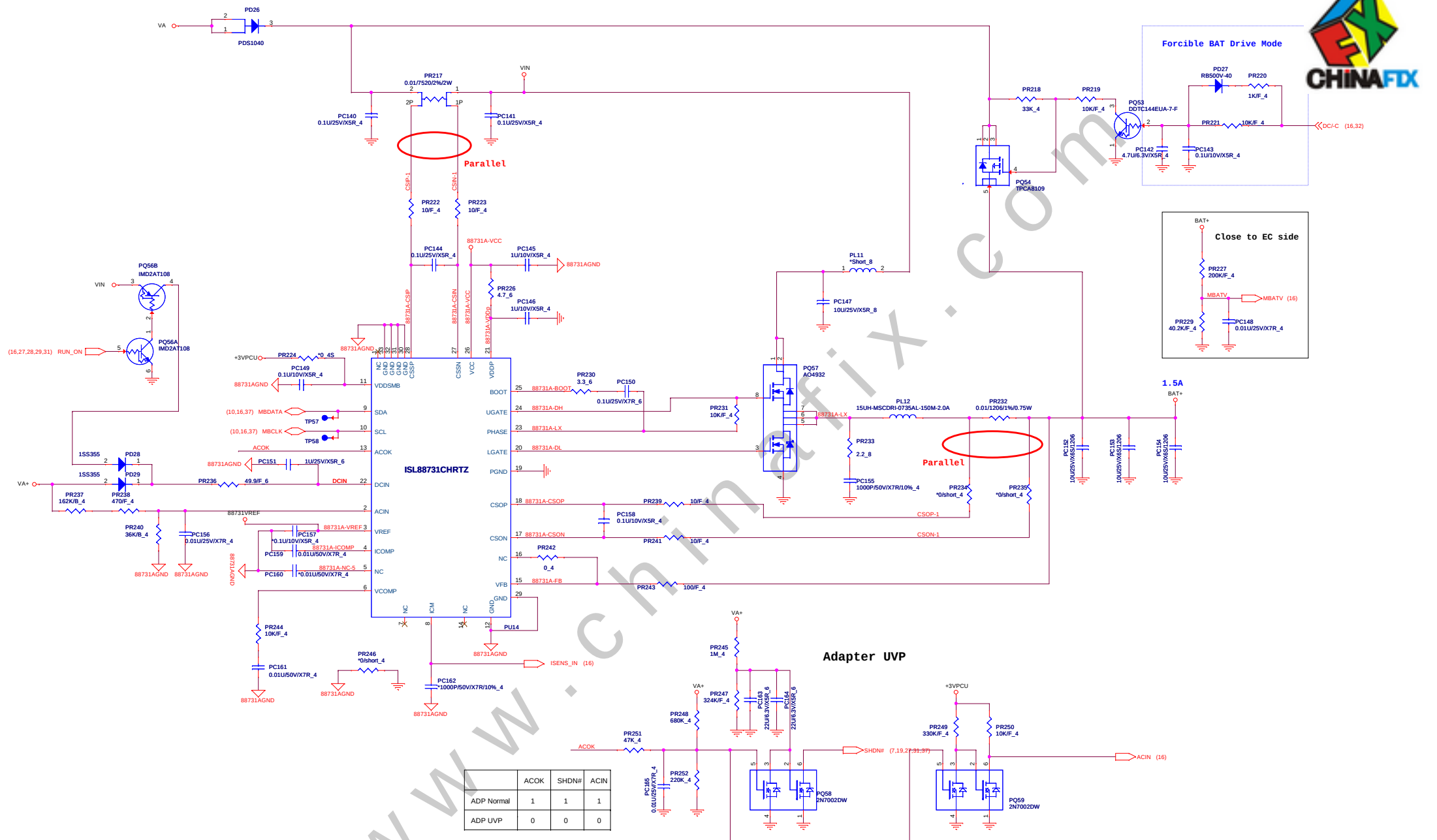
VCC1.8



Thermal Protection and Battery UVP for VEDS Abnormal



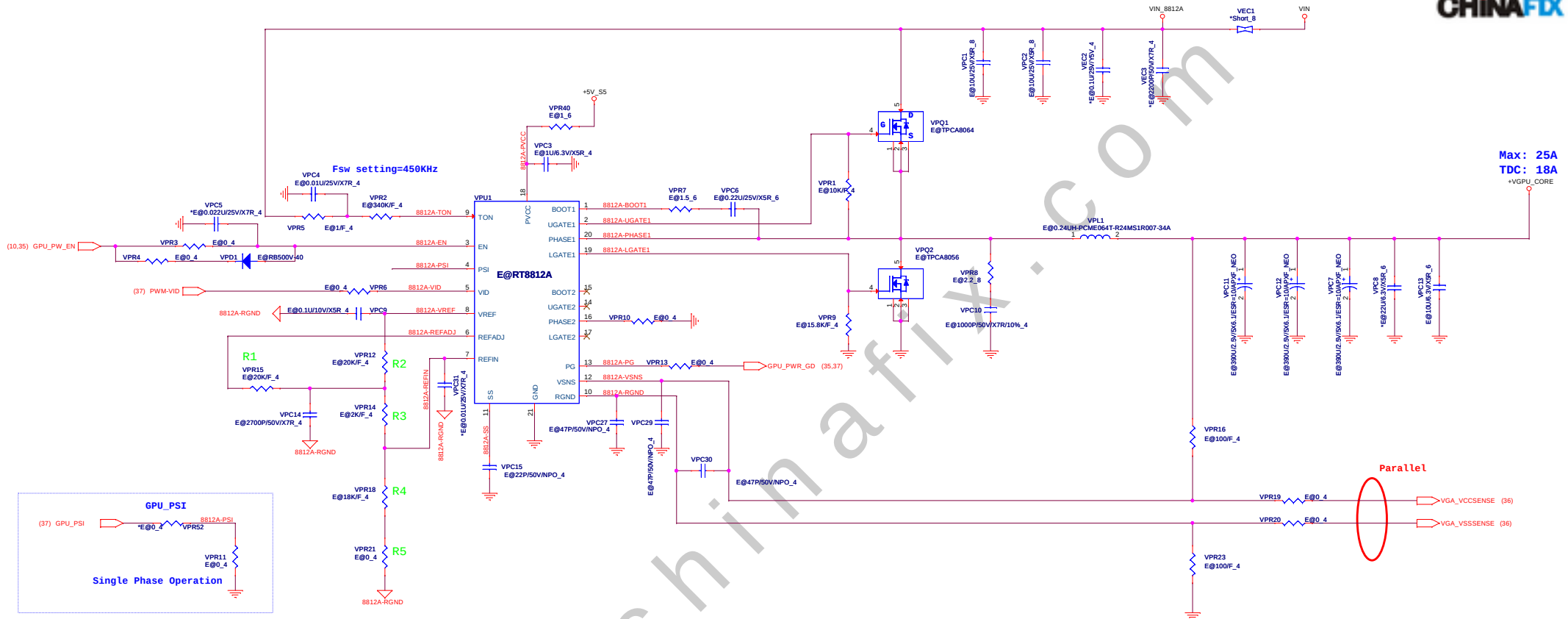




	ACOK	SHDN#	ACIN
ADP Normal	1	1	1
ADP UVP	0	0	0

1. Level 1 Environment-related Substances Should Never be Used.
2. Recycled Resin and Coated Wire should be procured from Green Partners.

VGA-CORE

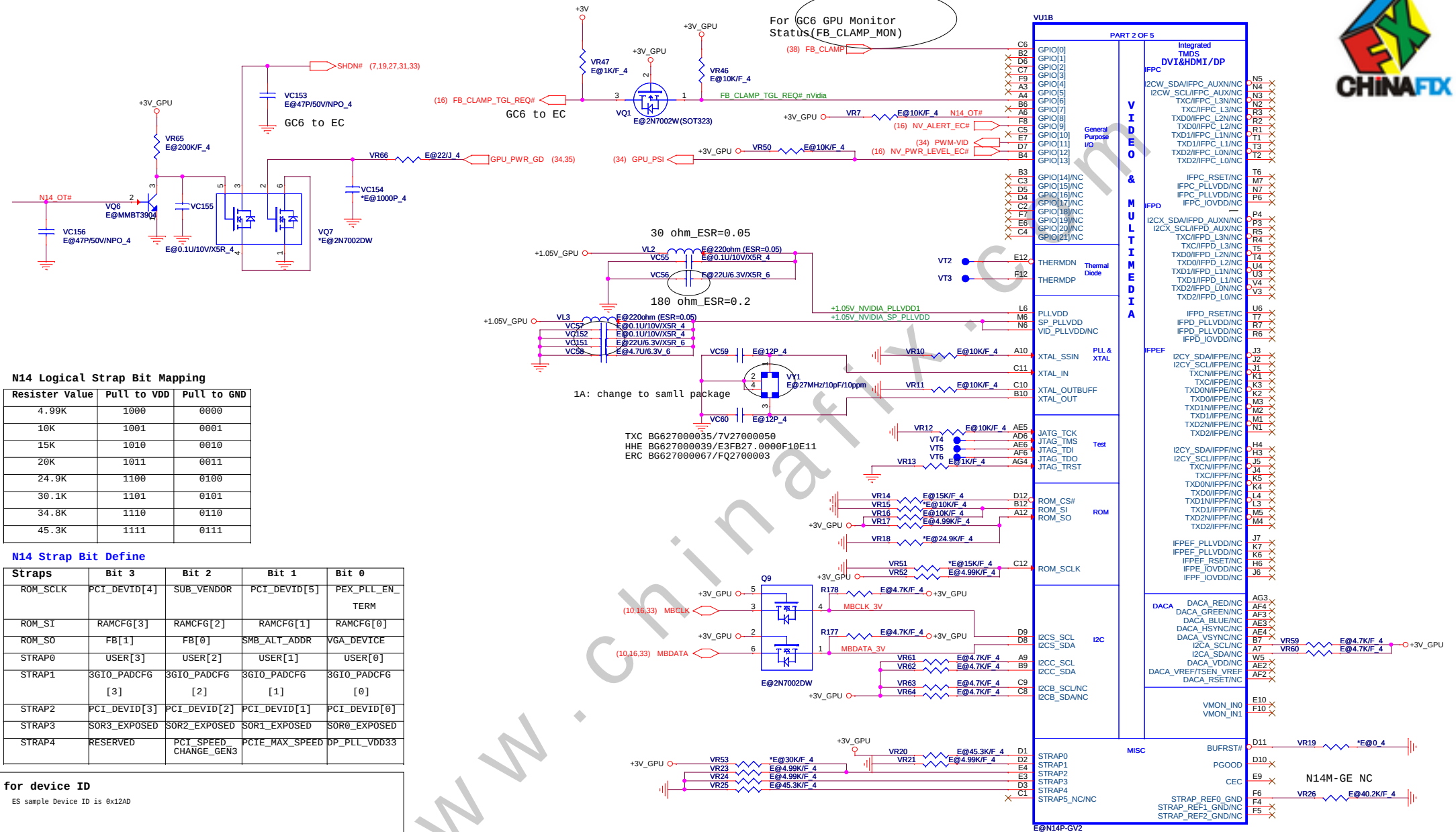


Max: 25A
TDC: 18A

Parallel

1. Level 1 Environment-related Substances Should Never be Used.
2. Recycled Resin and Coated Wire should be procured from Green Partners.





	VRAM Capacity	VRAM Vender	ID	VR35	P/N	P/N
N14M-GS/LP	128Mx16 DDR3	Samsung	0111	PD45.3K	K4W2G1646E-BC1A	
		Micron	0101	PD30.1K	MT41J128M16JT-0936:K	
	256Mx16 DDR3	Samsung	0011	PD20K	K4W4G1646B-HC11	
		Micron	0001	PD10K	MT41K256M16HA-1076:E	

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2.Recycled Resin and Coated Wire should be procured from Green Partners.



Quanta Computer Inc.

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Nvidia 214



Down Side VRAM TOP/BOT

TOP

BOT

VUIC Part 3 of 5

MEMORY INTERFACE

E@N14P-GV2

need find Bead 30 ohm@100MHz
ESR=0.01

100-BALL SDRAM DDR3
E@VRAM_DDR3

100-BALL SDRAM DDR3
E@VRAM_DDR3

Place around VU2 and VU3

Place near VRAM side

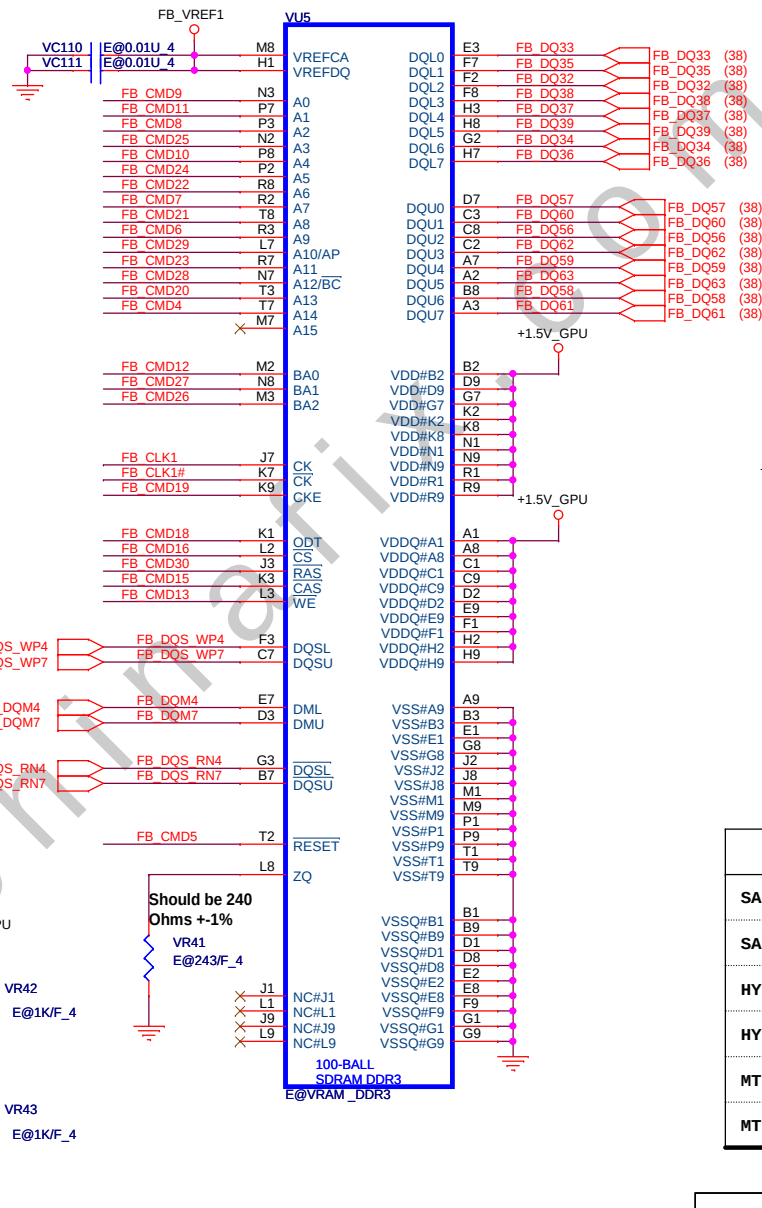


Quanta Computer Inc.
PROJECT : Huron River
VRAM 3/4

1.Level 1 Environment-related Substances Should Never be Used.
2.Recycled Resin and Coated Wire should be procured from Green Partners.



BOT



+1.5V GPU	
VC116	E@1U/6.3V/XSR 4
VC117	E@1U/6.3V/XSR 4
VC118	E@1U/6.3V/XSR 4
VC119	E@1U/6.3V/XSR 4
VC120	E@1U/6.3V/XSR 4
VC121	E@1U/6.3V/XSR 4
VC122	E@1U/6.3V/XSR 4
VC123	E@1U/6.3V/XSR 4
VC125	E@0.1U/10V/XSR 4
VC126	E@0.1U/10V/XSR 4
VC127	E@0.1U/10V/XSR 4
VC128	E@0.1U/10V/XSR 4
VC87	*E@3.3PI50V/NPO 4
VC88	*E@100PI50V/X7R10% 4

	P/N	Vendor P/N
SAM 2G	AKD5MGGT525	K4W2G1646E-BC11
SAM 4G	AKD5MGSTL14	K4W4G1646B-HC11
HYN 2G		
HYN 4G		
MTI 2G	AKD5MGWT525	MT41J128M16JT-107G:K
MTI 4G	AKD5PGSTL07	MT41K256M16HA-107G:E



Quanta Computer Inc.
PROJECT : Huron River

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- 1.Level 1 Environment-related Substances Should Never be Used.
- 2.Recycled Resin and Coated Wire should be procured from Green Partners.

USB PORT Architecture	
PORT 0	USB3.0
PORT 1	USN2.0
PORT 2	USN2.0
PORT 3	USB2.0
PORT 4	N/A
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A
PORT 9	WiMax/BT
PORT 10	Camera
PORT 11	N/A
PORT 12	N/A
PORT 13	N/A

PCIE BUS	
PORT 1	WLAN Port
PORT 2	CARD READER
PORT 3	GLAN(RTL8111E)
PORT 4	N/A
PORT 5	N/A
PORT 6	N/A
PORT 7	N/A
PORT 8	N/A

SATA BUS	
PORT 0	HDD
PORT 1	N/A
PORT 2	N/A
PORT 3	N/A
PORT 4	ODD
PORT 5	N/A

SM BUS	MBCLK/MBDATA	WRITE	READ	Function
ISL88731CHRTZ	0001 001X	0001 0010	0001 0011	Charger
AMD Thames	0100 0001	-	0100 0001	Graphice
LIS331DL	0011 101X	0011 1010	0011 1011	G Sensor

SM BUS	MBCLK_BAT/MBDATA_BAT	WRITE	READ	Function
VGP-BPS26	0001 011X	0001 0110	0001 0111	Battery

SM BUS	SMB_PCH_CLK/SMB_PCH_DAT	WRITE	READ	Function
DIMM Module0	1010 000X	1010 0000	1010 0001	DDRIII
DIMM Module 1	1010 010X	1010 0100	1010 0101	DDRIII
Synaptics	0010 110X	0010 1100	0010 1101	Click PAD



	R363(High) R362(Low)	R294(High) R297(Low)
	Board ID3	Board ID0
14"/HK6	0	0
15"/HK5	0	1
17"/HK7	1	0

Board ID1 (VRAM Vendor)	Samaung(1)	Hynix(0)
R47(High)	Stuff	No Stuff
R48(Low)	No Stuff	Stuff

Board ID2		
14" 4PCS	1G	512M
15" 8PCS	1G	2G
R39(High)	Stuff	No Stuff
R27(Low)	No Stuff	Stuff

PCBA SKU	Discrete	UMA
R277(Pull High)	Stuff	No Stuff
R275(Pull Low)	No Stuff	Stuff

	S0	S3	DS3	S4	S5 (Charger Enable)	S5 (Charger Disable)
RUN_ON	H	L	L	L	L	L
+3V	H	L	L	L	L	L
+5V	H	L	L	L	L	L
+0.75V_DDR_VTT	H	L	L	L	L	L
+1.05V	H	L	L	L	L	L
+0.85V	H	L	L	L	L	L
+1.5V	H	L	L	L	L	L
+1.8V	H	L	L	L	L	L
+1.8V_GPU	H	L	L	L	L	L
+1.0V_GPU	H	L	L	L	L	L
+VGPU_CORE	H	L	L	L	L	L
+VCC_GFX	H	L	L	L	L	L
+VCC_CORE	H	L	L	L	L	L
SUS_ON	H	H	H	L	L	L
+1.5V_SUS	H	H	H	L	L	L
S5_ON	H	H	L	H	L	L
+5V_S5	H	H	L	H	L	L
+3V_S5	H	H	L	H	L	L
EC_WAKE_ON	H	H	H	H	H	L
+3V_WAKE	H	H	H	H	H	L
+5V_WAKE	H	H	H	H	H	L
DEEP_EC_EN	H	H	H	H	L	L
+3V_S5_DSW	H	H	H	H	L	L
+3V_SUS	H	H	L	L	L	L