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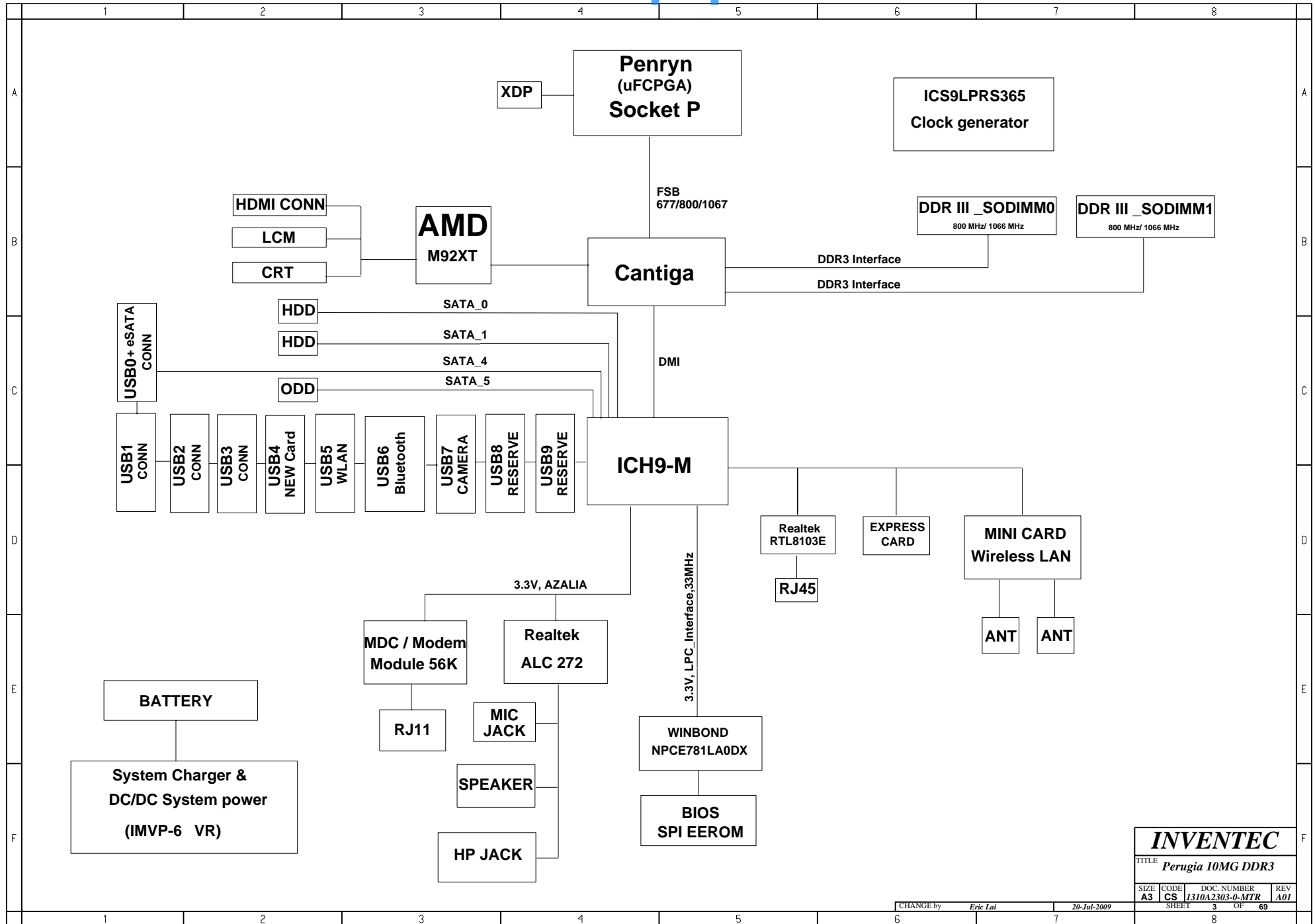
INVENTEC

TITLE **Perugia 10MG DDR3**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	J310A2303-0-MTR	A01

CHANGE by **Eric Lai** 20-Jul-2009

SHEET 2 OF 69



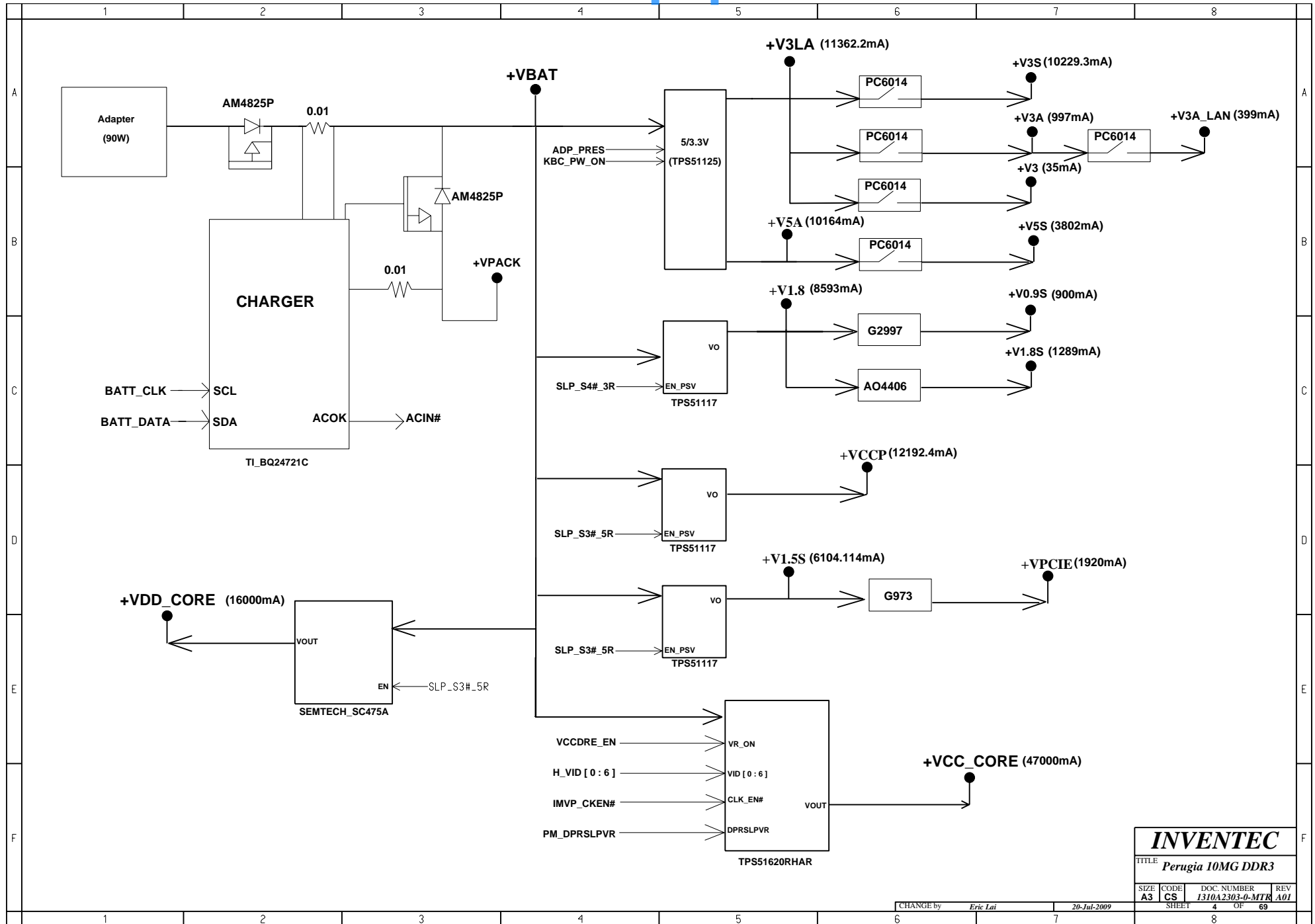
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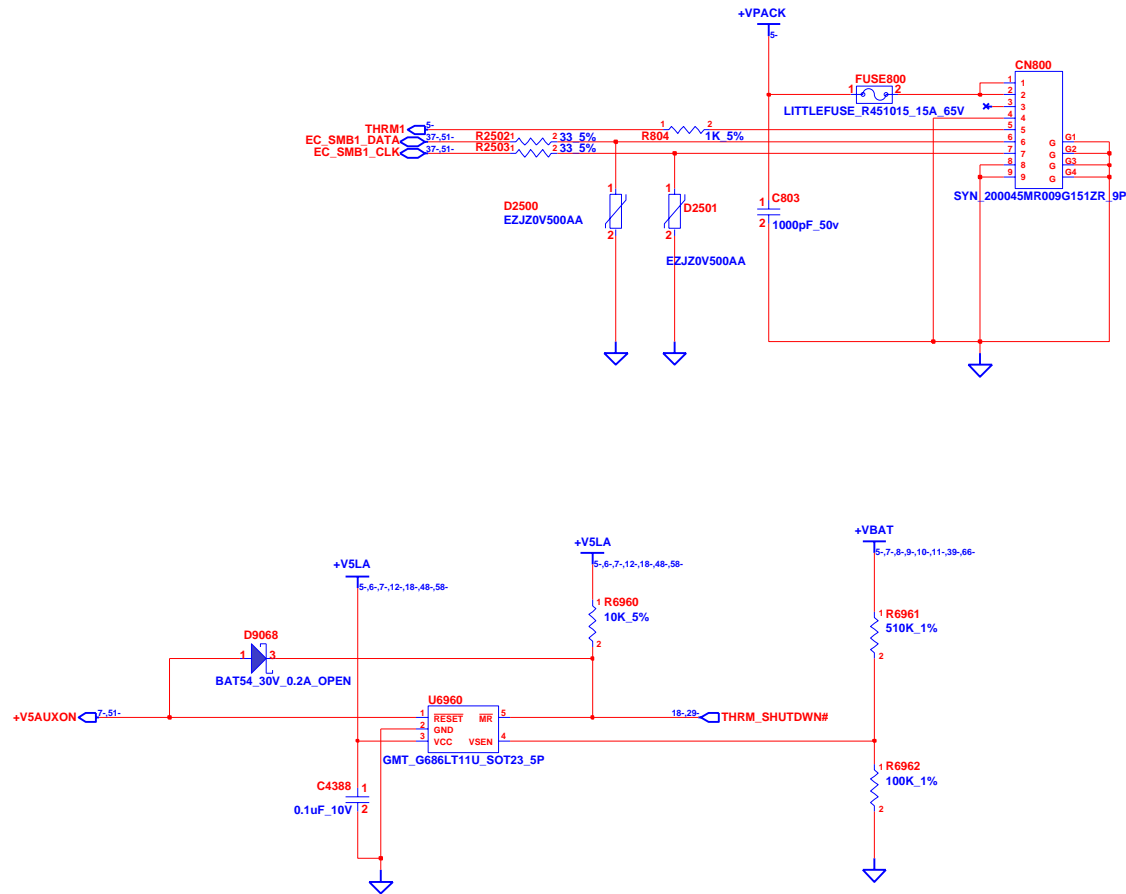
TITLE: **Perugia 10MG DDR3**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	J310A2303-0-MTR	A01

CHANGE by: *Eric Lai* 20-Jul-2009

SHEET 3 OF 69





INVENTEC

TITLE			
SELECT & BATTERY CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01

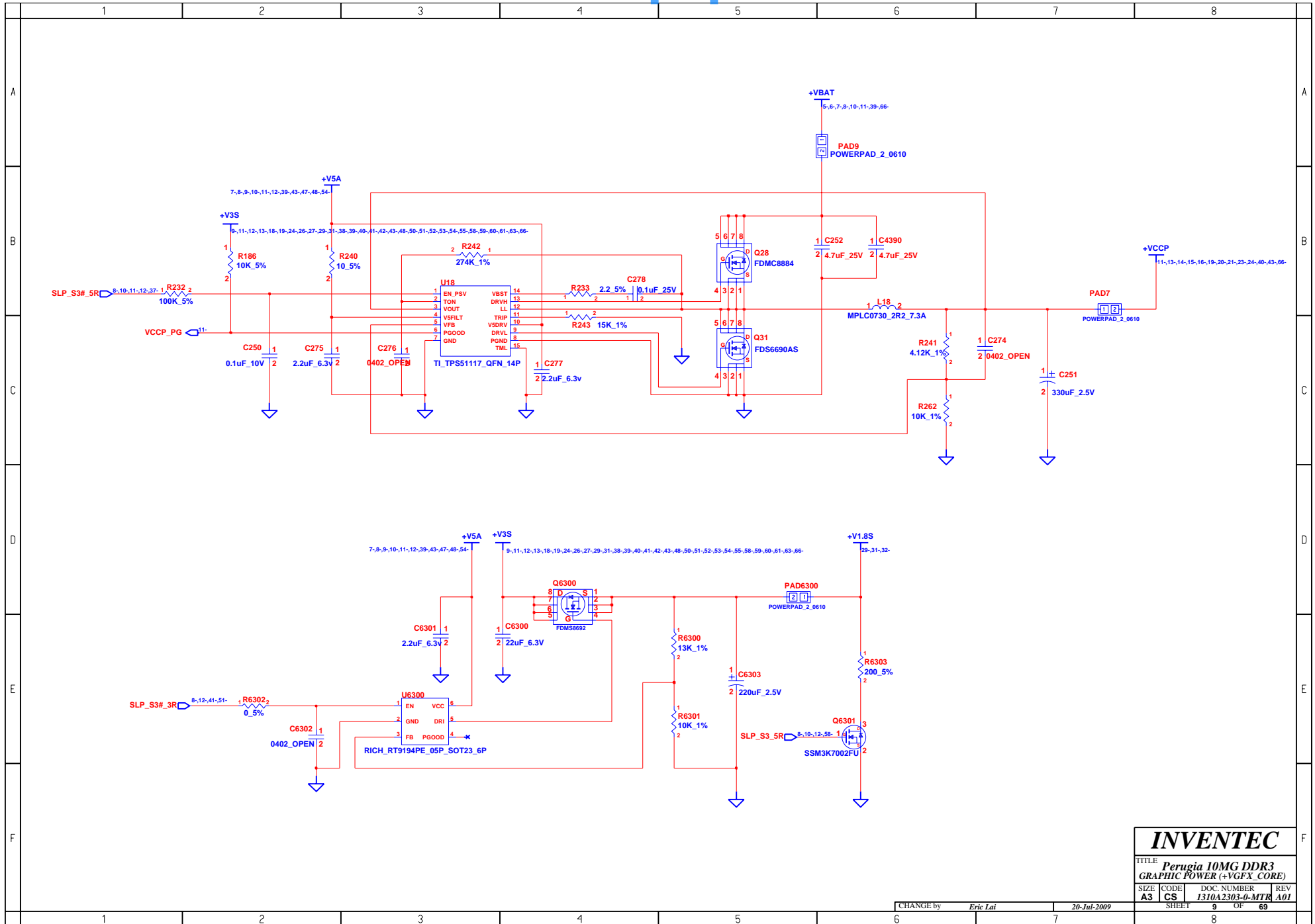
CHANGE by Eric Lai 20-Jul-2009

SHEET 6 OF 69

TITLE Perugia 10MG DDR3 SYSTEM POWER(+V1.5/+V0.75S)			
SIZE A3	CODE CS	DOC. NUMBER 1310A2303-0-MTR	REV A01
SHEET		8	OF 69

CHANGE by	<i>Eric Lai</i>	20-Jul-2009
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CS	1310A2303-0-MTR	A01
SHEET	8	OF 69

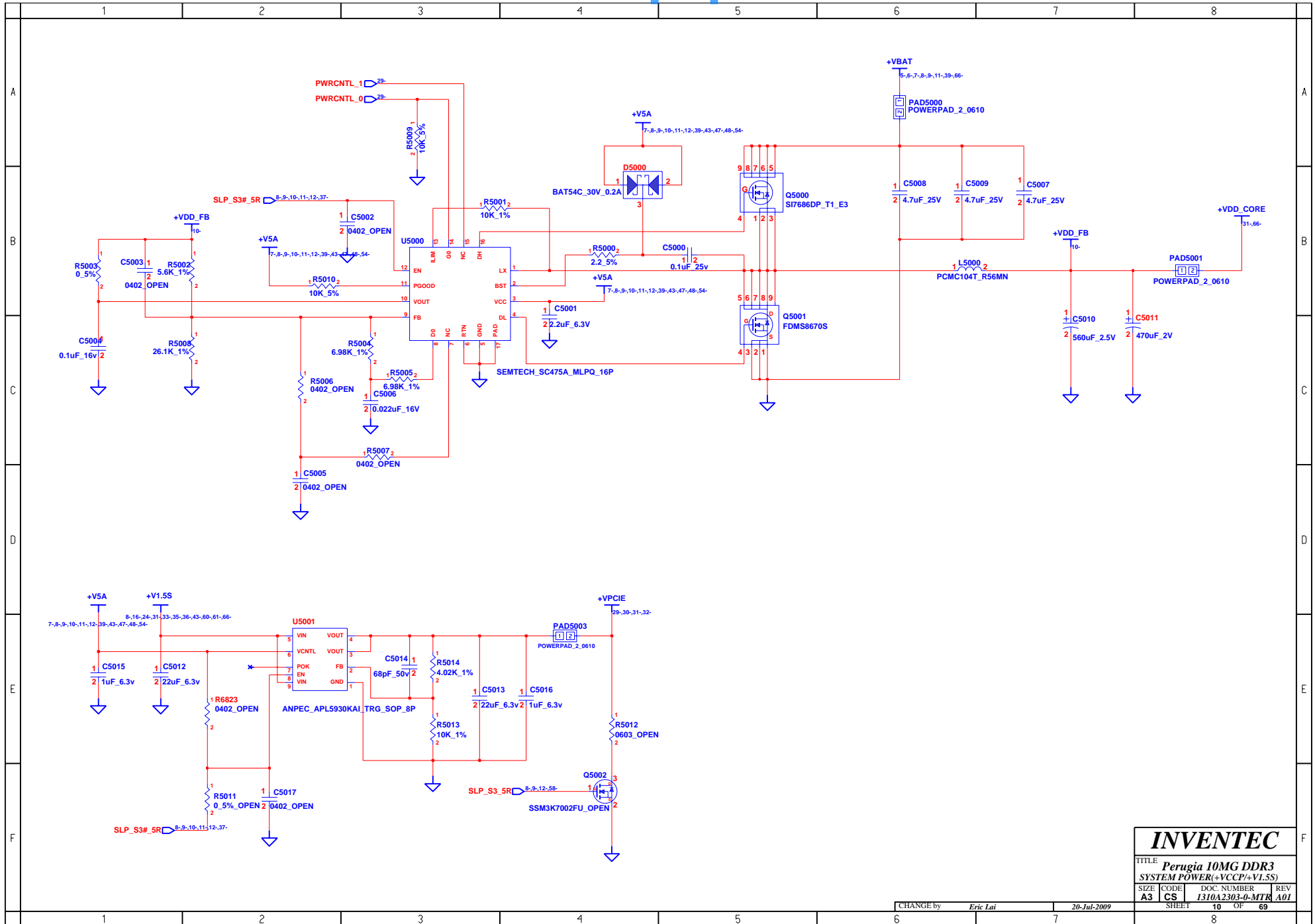


INVENTEC

TITLE			
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01

CHANGE by Eric Lai 20-Jul-2009

SHEET 9 OF 69



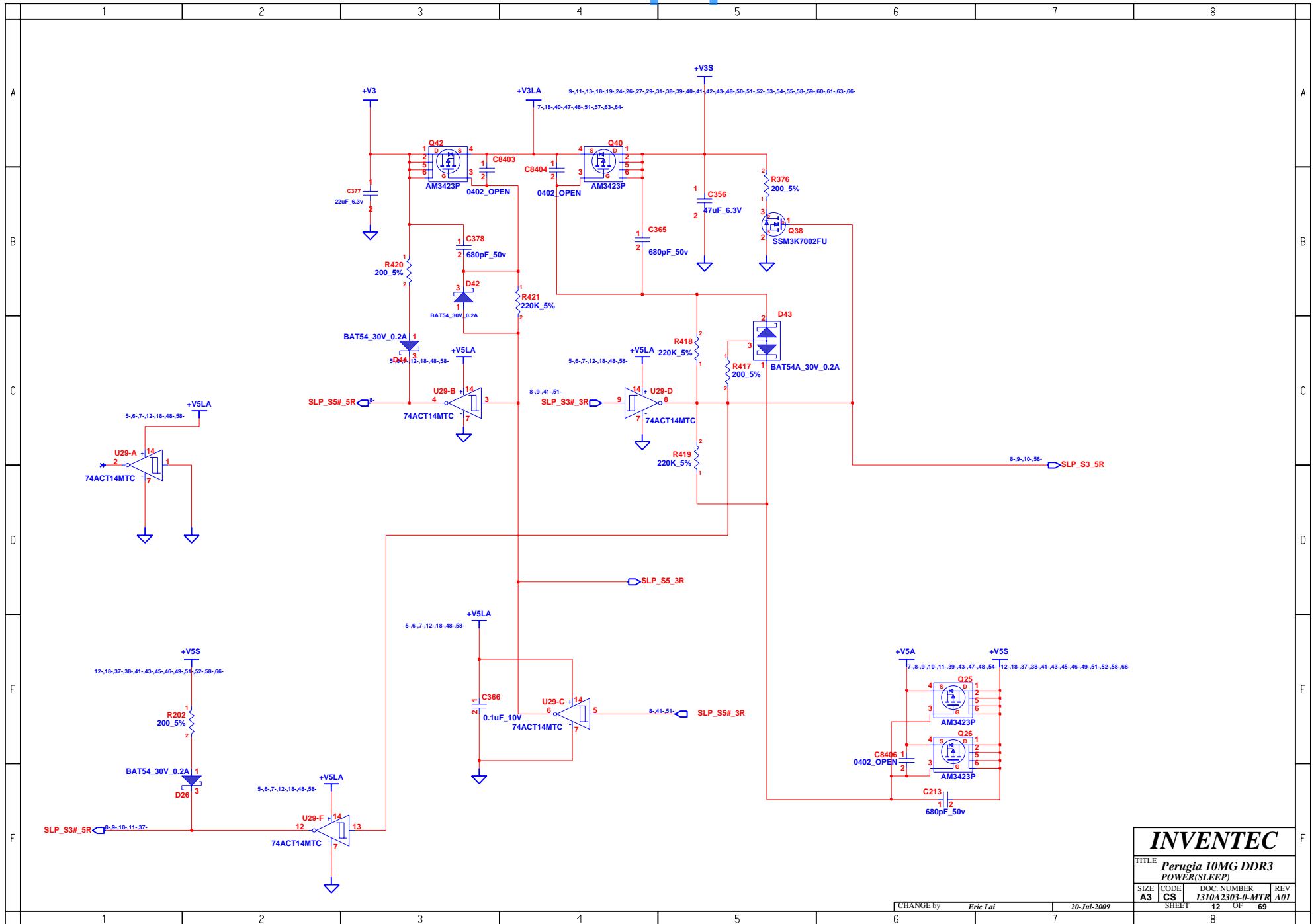
INVENTEC

TITLE: **Perugia 10MG DDR3**
SYSTEM POWER(+VCCP/+V1.5S)

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01

CHANGE by: **Eric Lai** 20-Jul-2009

SHEET 10 OF 69



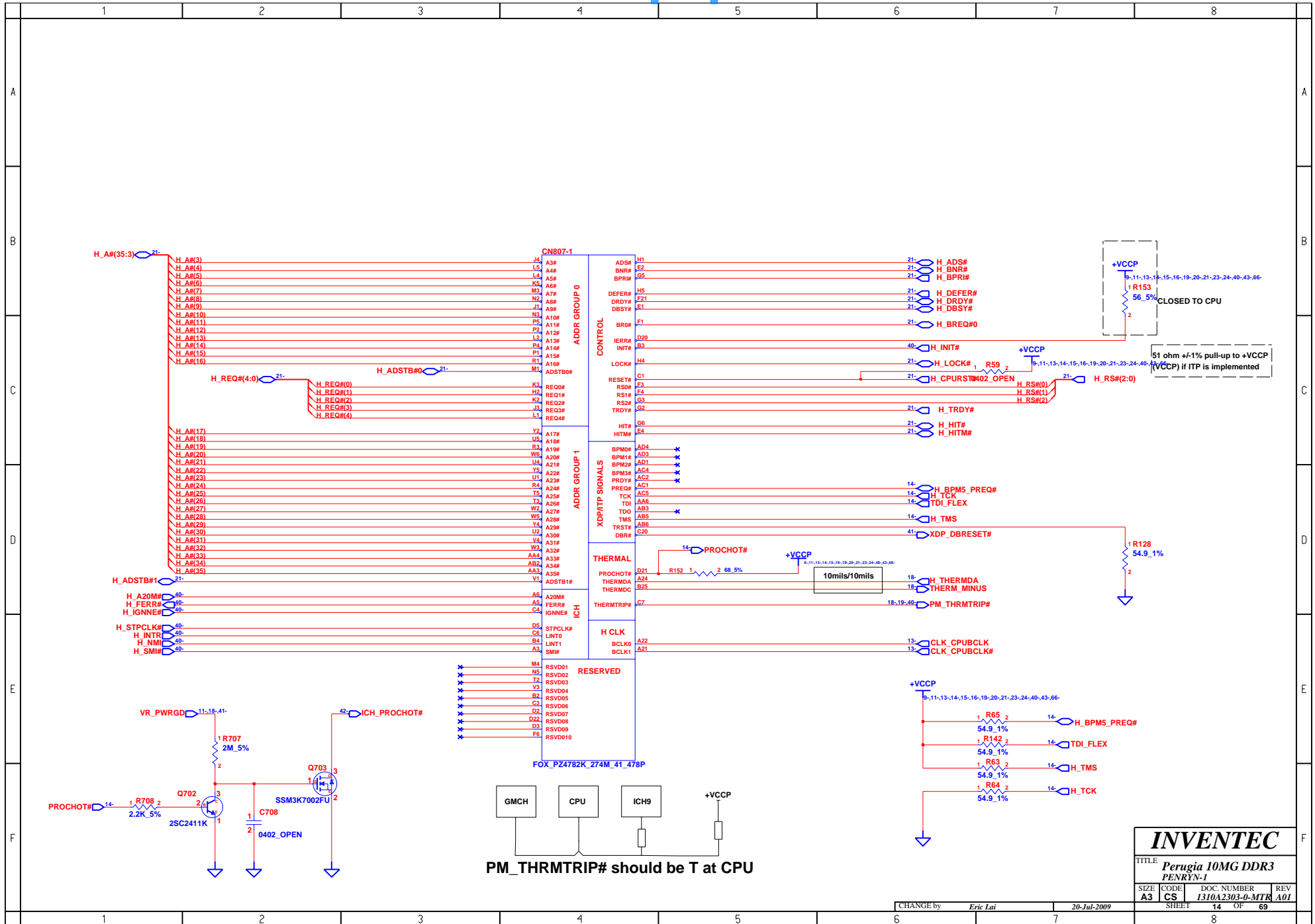
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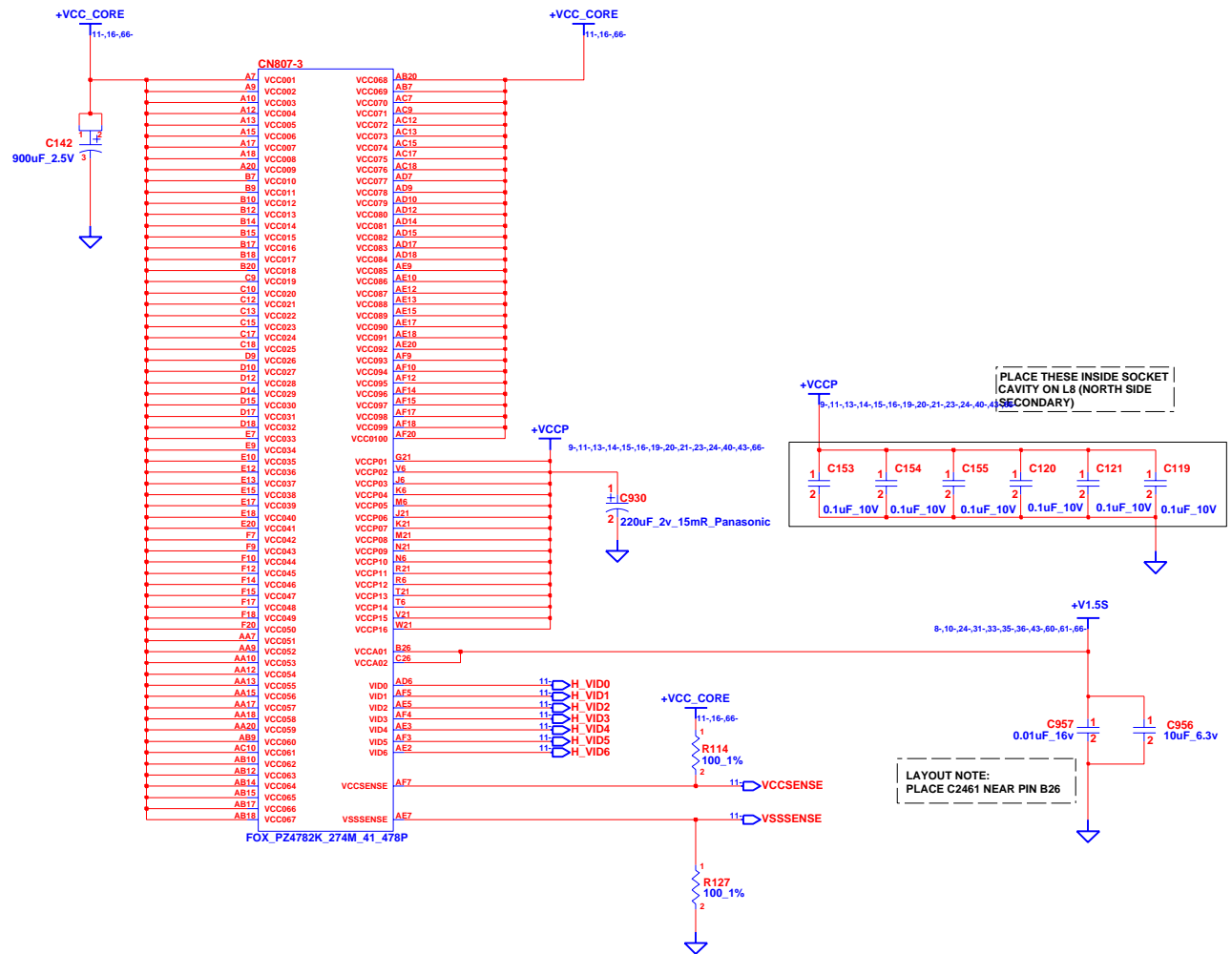
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**Perugia 10MG DDR3
POWER(SLEEP)**

SIZE A3	CODE CS	DOC. NUMBER I310A2303-0-MTR	REV A01
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CHANGE by **Eric Lai** 20-Jul-2009

SHEET 12 OF 69





PLACE THESE INSIDE SOCKET CAVITY ON L8 (NORTH SIDE SECONDARY)

LAYOUT NOTE: PLACE C2461 NEAR PIN B26

LAYOUT NOTE: ROUTE VCCSENSE AND VSSSENSE TRACE AT 24.7 OHM WITH 50 MIL SPACING PLACE PU AND PD WITHIN 1 INCH OF CPU

INVENTEC

TITLE			
Perugia 10MG DDR3			
PENRYN-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01
CHANGE by		20-Jul-2009	
SHEET		16	OF 69

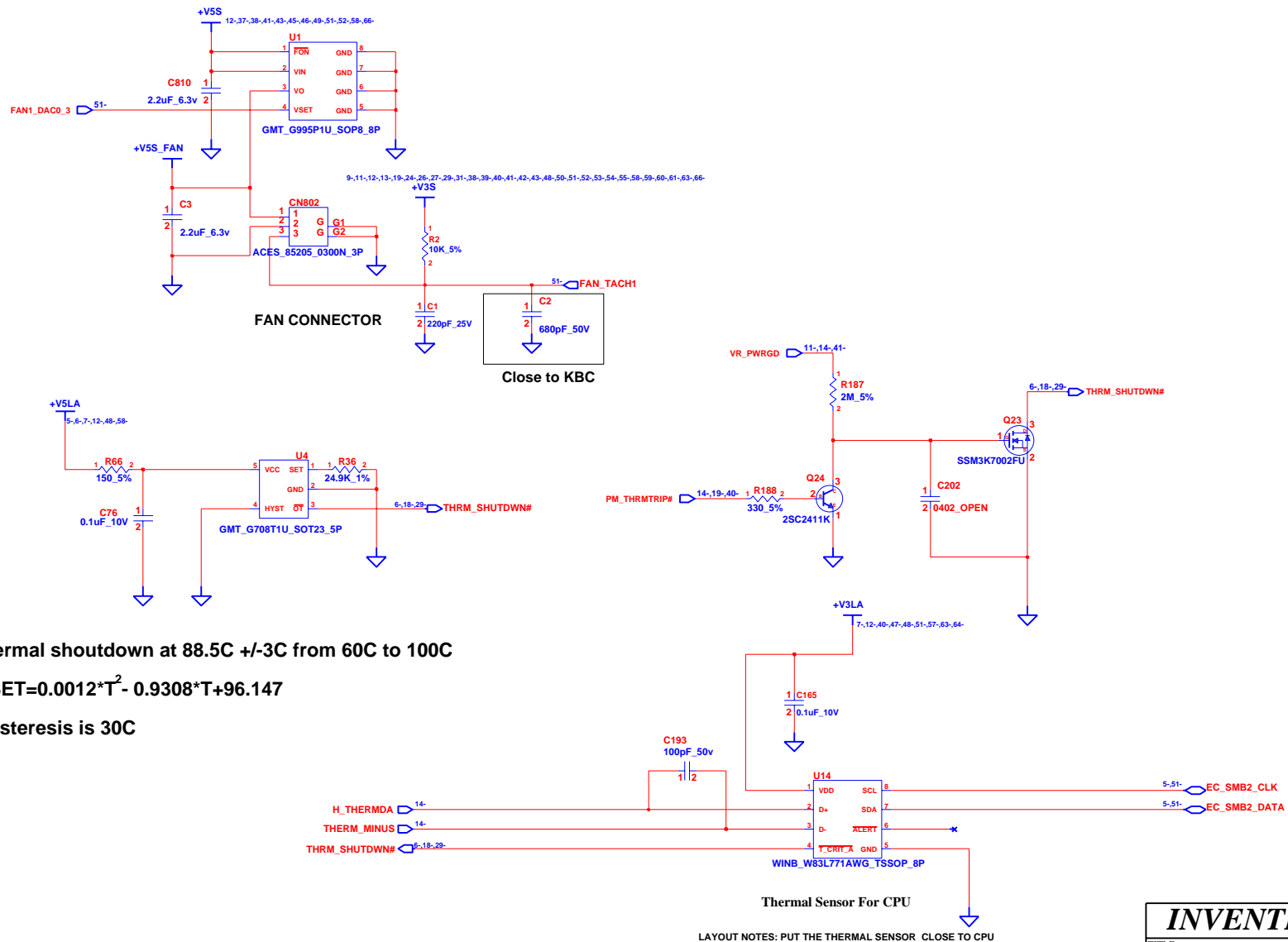
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A									A
B									B
C									C
D									D
E									E
F									F

CN807-4

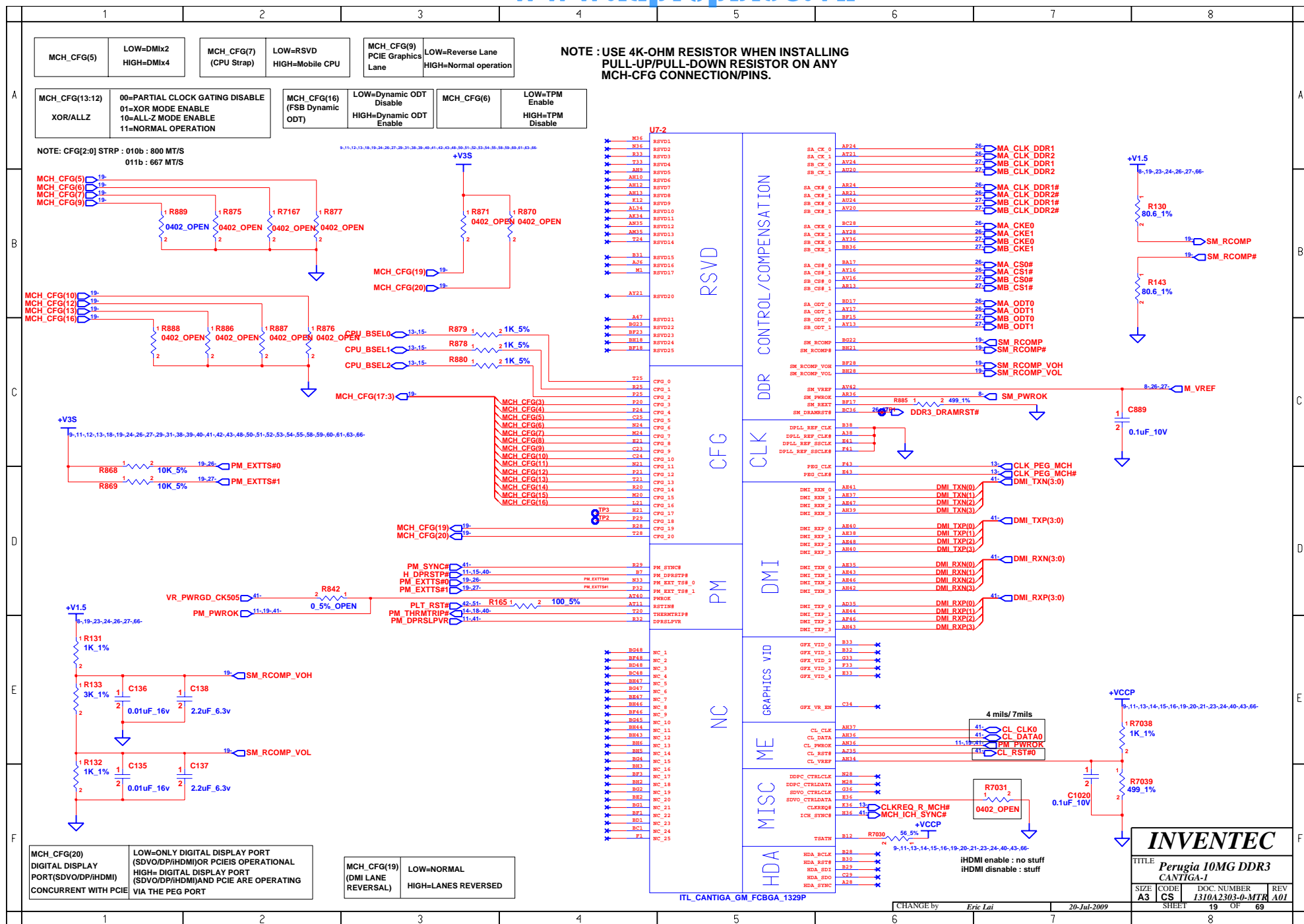
A4	VSS001	VSS002	P6
A8	VSS002	VSS003	P21
A11	VSS003	VSS004	P24
A14	VSS004	VSS005	R2
A16	VSS005	VSS006	R5
A19	VSS006	VSS007	R22
A23	VSS007	VSS008	R25
AF2	VSS008	VSS009	T1
B6	VSS009	VSS010	T4
B8	VSS010	VSS011	T23
B11	VSS011	VSS012	T26
B13	VSS012	VSS013	U3
B16	VSS013	VSS014	U6
B19	VSS014	VSS015	U21
B21	VSS015	VSS016	U24
B24	VSS016	VSS017	V2
C5	VSS017	VSS018	V5
C8	VSS018	VSS019	V22
C11	VSS019	VSS020	V25
C14	VSS020	VSS021	W1
C16	VSS021	VSS022	W4
C19	VSS022	VSS023	W23
C2	VSS023	VSS024	W26
C22	VSS024	VSS025	Y3
C25	VSS025	VSS026	Y6
D1	VSS026	VSS027	Y21
D4	VSS027	VSS028	Y24
D8	VSS028	VSS029	AA2
D11	VSS029	VSS030	AA5
D13	VSS030	VSS031	AA8
D16	VSS031	VSS032	AA11
D19	VSS032	VSS033	AA14
D23	VSS033	VSS034	AA16
D26	VSS034	VSS035	AA19
E3	VSS035	VSS036	AA22
E6	VSS036	VSS037	AA25
E8	VSS037	VSS038	AB1
E11	VSS038	VSS039	AB4
E14	VSS039	VSS040	AB8
E16	VSS040	VSS041	AB11
E19	VSS041	VSS042	AB13
E21	VSS042	VSS043	AB16
E24	VSS043	VSS044	AB18
F5	VSS044	VSS045	AB23
F8	VSS045	VSS046	AB26
F11	VSS046	VSS047	AC3
F13	VSS047	VSS048	AC6
F16	VSS048	VSS049	AC8
F19	VSS049	VSS050	AC11
F2	VSS050	VSS051	AC14
F22	VSS051	VSS052	AC16
F25	VSS052	VSS053	AC19
G4	VSS053	VSS054	AC21
G1	VSS054	VSS055	AC24
G23	VSS055	VSS056	AD2
G26	VSS056	VSS057	AD5
H3	VSS057	VSS058	AD8
H6	VSS058	VSS059	AD11
H21	VSS059	VSS060	AD13
H24	VSS060	VSS061	AD16
J4	VSS061	VSS062	AD19
J5	VSS062	VSS063	AD22
J22	VSS063	VSS064	AD25
J25	VSS064	VSS065	AE1
K1	VSS065	VSS066	AE4
K4	VSS066	VSS067	AE8
K23	VSS067	VSS068	AE11
K26	VSS068	VSS069	AE14
L3	VSS069	VSS070	AE16
L6	VSS070	VSS071	AE19
L21	VSS071	VSS072	AE23
M2	VSS072	VSS073	AE26
M5	VSS073	VSS074	A2
M22	VSS074	VSS075	AF6
M25	VSS075	VSS076	AF8
N1	VSS076	VSS077	AF11
N4	VSS077	VSS078	AF13
N23	VSS078	VSS079	AF16
N26	VSS079	VSS080	AF19
P3	VSS080	VSS081	AF21
			AF25

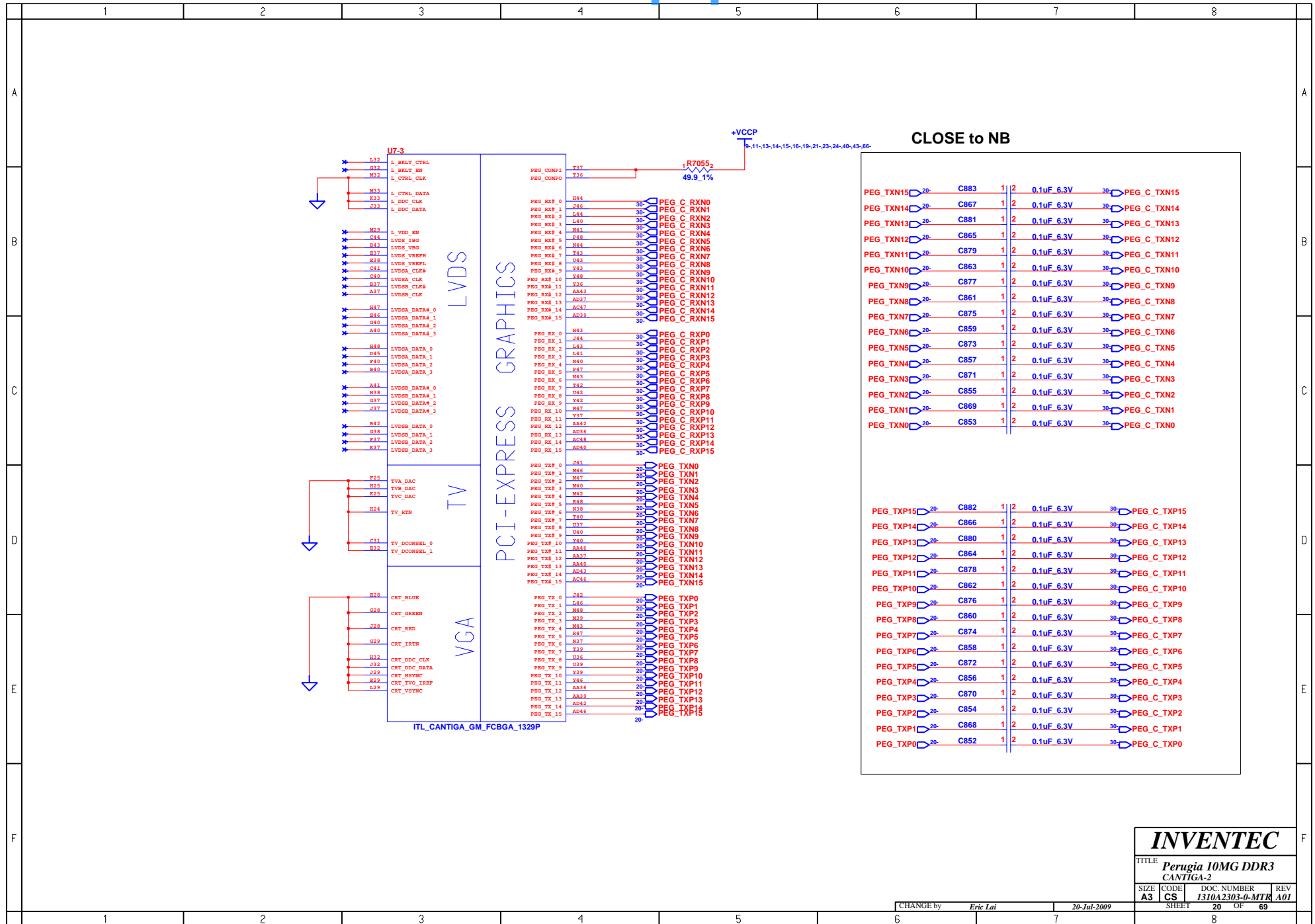
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INVENTEC			
TITLE			
Perugia 10MG DDR3			
PENRYN-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01
CHANGE by		Eric Lai	20-Jul-2009
SHEET		17	OF 69



INVENTEC			
TITLE: Perugia 10MG DDR3 THERMAL&FAN CONTROLLER			
SIZE A3	CODE CS	DOC. NUMBER I310A2303-0-MTR	REV A01
CHANGE by Eric Lai		20-Jul-2009	SHEET 18 OF 69



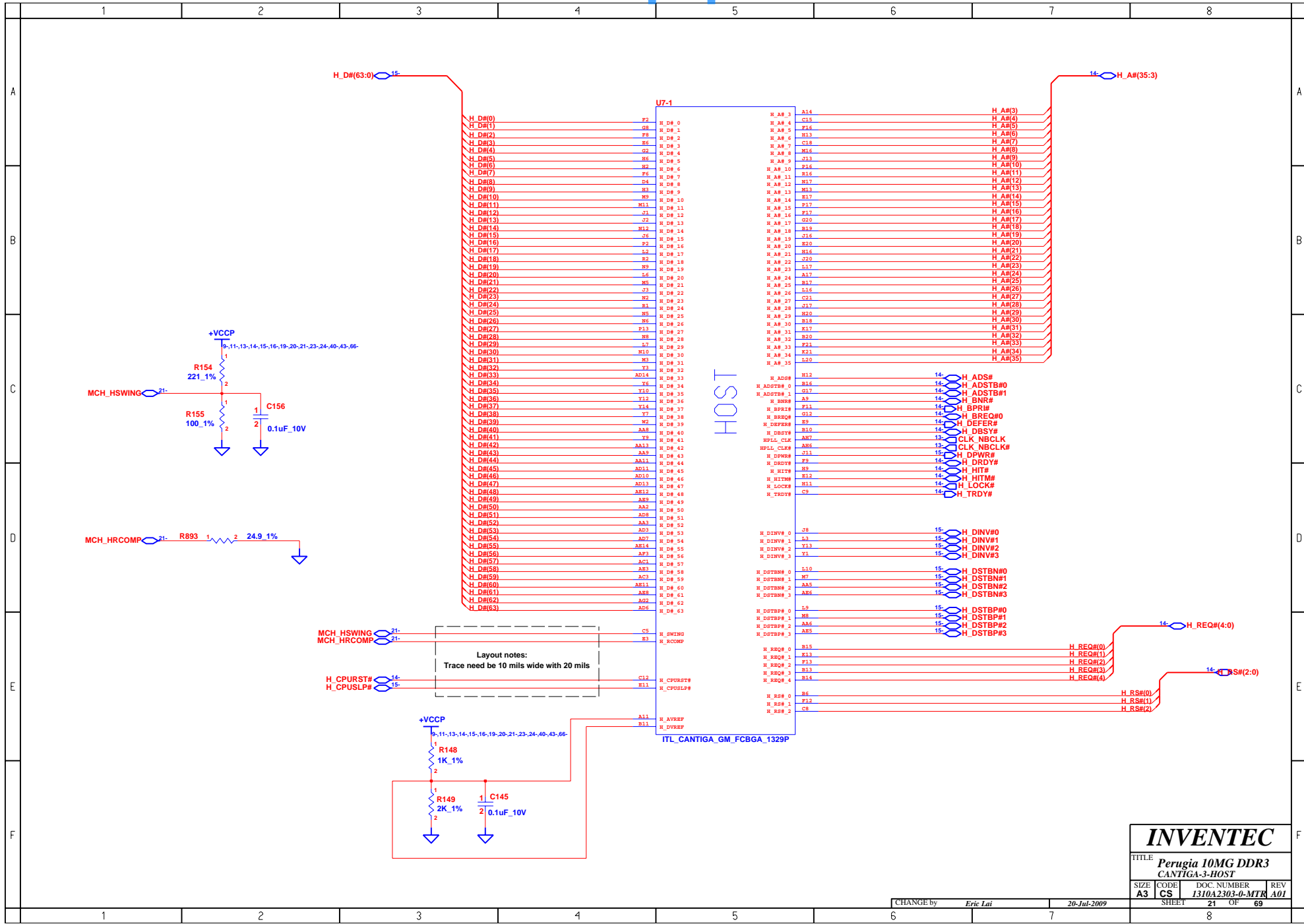


INVENTEC

TITLE
Perugia 10MG DDR3
CANTIGA-2SIZE CODE DOC. NUMBER REV
A3 CS 1310A2303-0-MTR A01

CHANGE by Eric Lai 20-Jul-2009

SHEET 20 OF 69



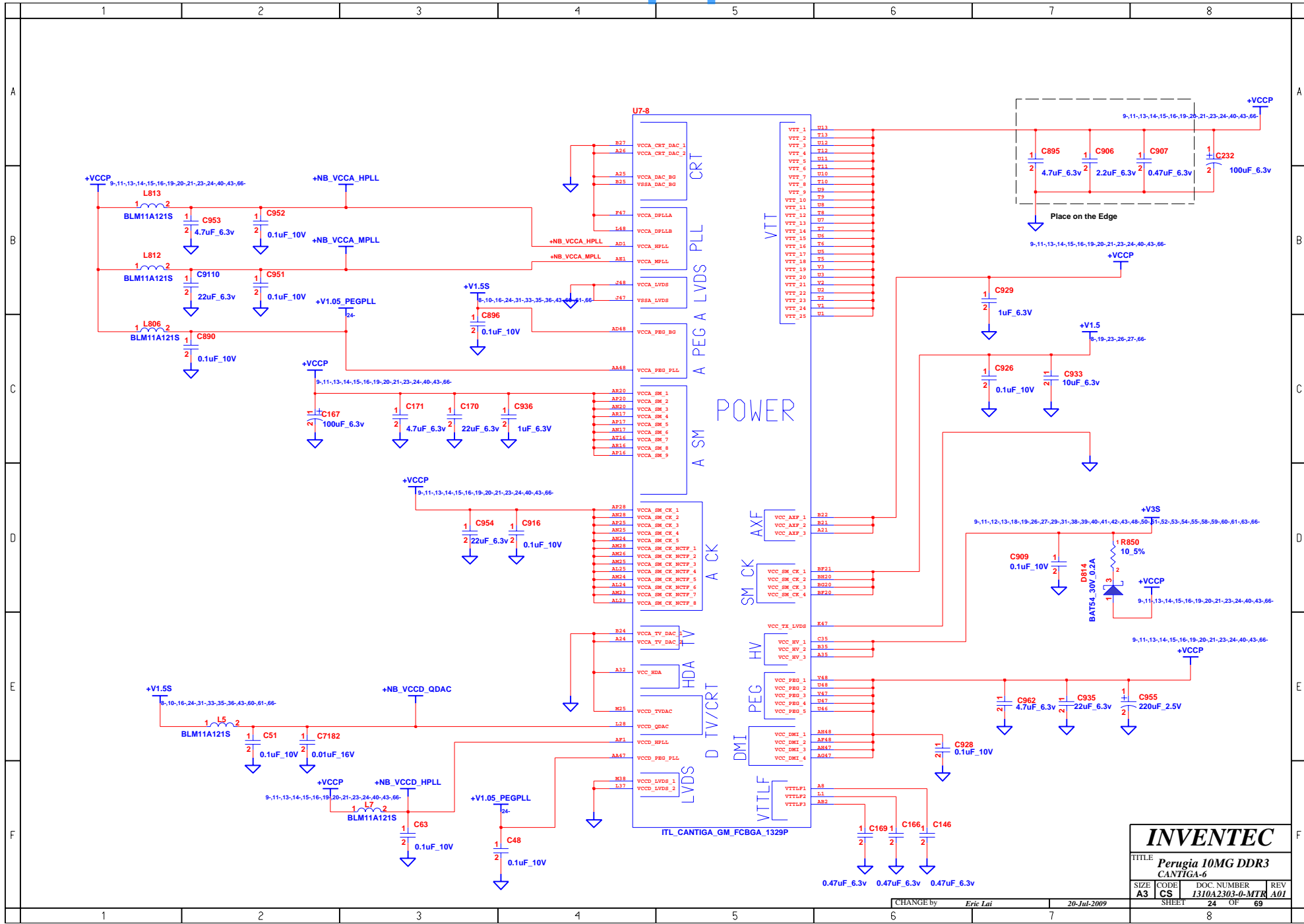
INVENTEC

TITLE: **Perugia 10MG DDR3 CANTIGA-3-HOST**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01

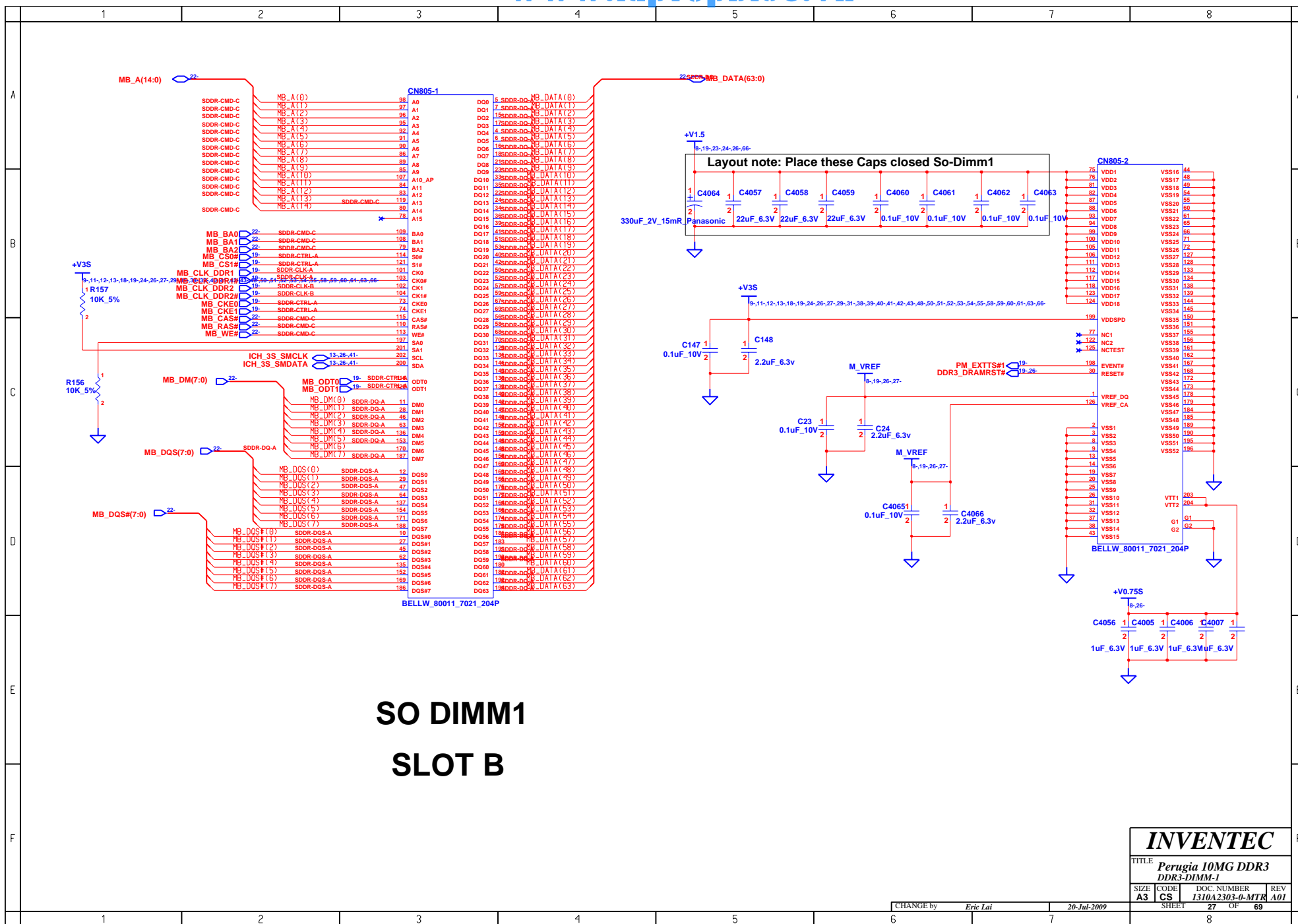
CHANGE by: **Eric Lai** 20-Jul-2009

SHEET 21 OF 69



INVENTEC			
TITLE Perugia 10MG DDR3 CANTIGA-6			
SIZE A3	CODE CS	DOC. NUMBER I310A2303-0-MTR	REV A01
CHANGE by Eric Lai		20-Jul-2009	
SHEET 24		OF 69	

SIZE A3	CODE CS	DOC. NUMBER <i>1310A2303-0-MTR</i>	RE AC
SHEET		26	OF 69



INVENTEC

TITLE
**Perugia 10Mg DDR3
DDR3-DIMM-1**

SIZE CODE DOC. NUMBER REV
A3 CS 1310A2303-0-MTR A01

CHANGE by **Eric Lai** 20-Jul-2009

SHEET 27 OF 69

	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC

TITLE
Perugia 10MG DDR3
DDR2-DAMPING

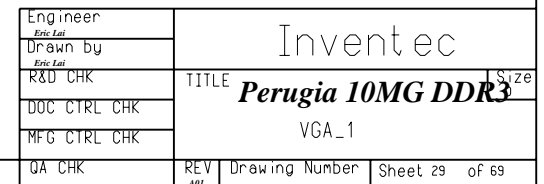
SIZE
A3

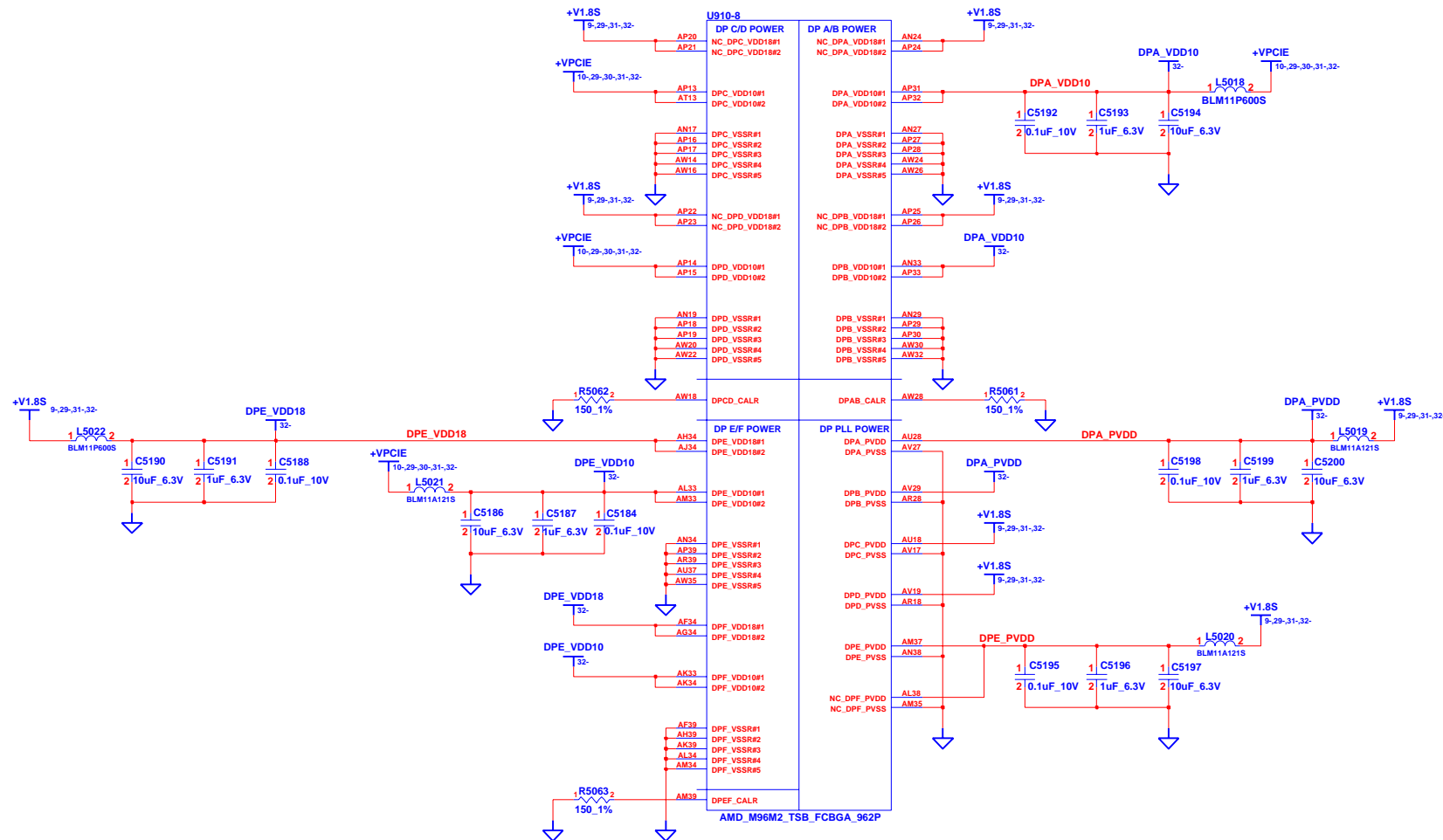
CODE
CS

DOC. NUMBER
1310A2303-0-MTR

REV
A01

SHEET 28 OF 69





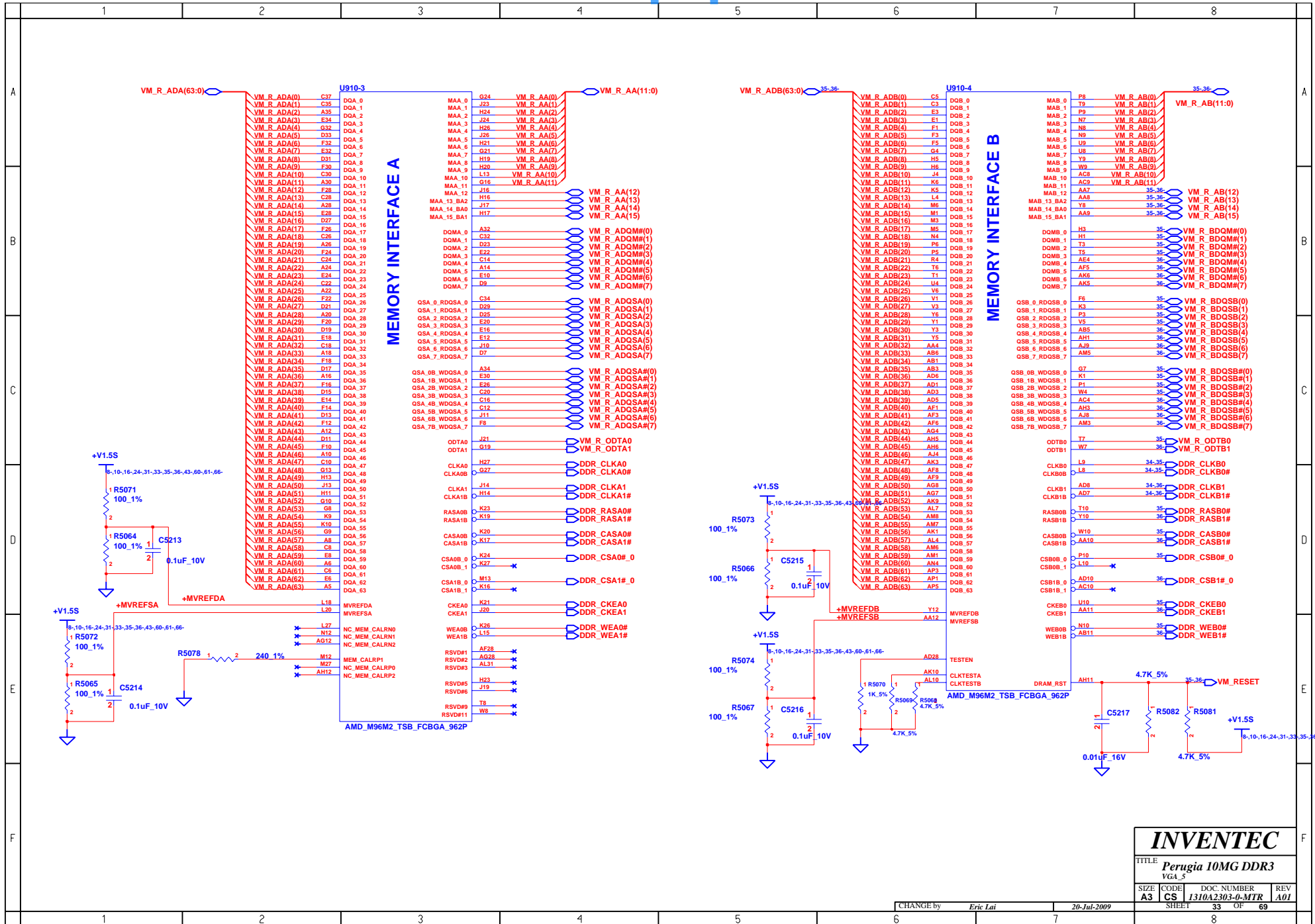
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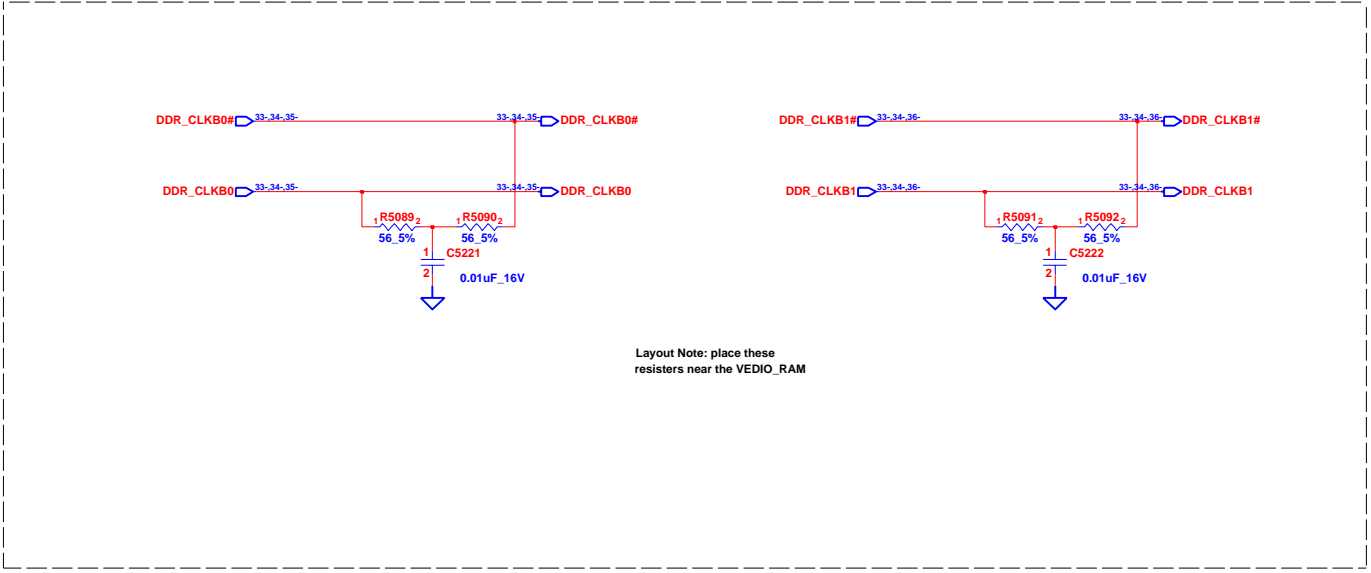
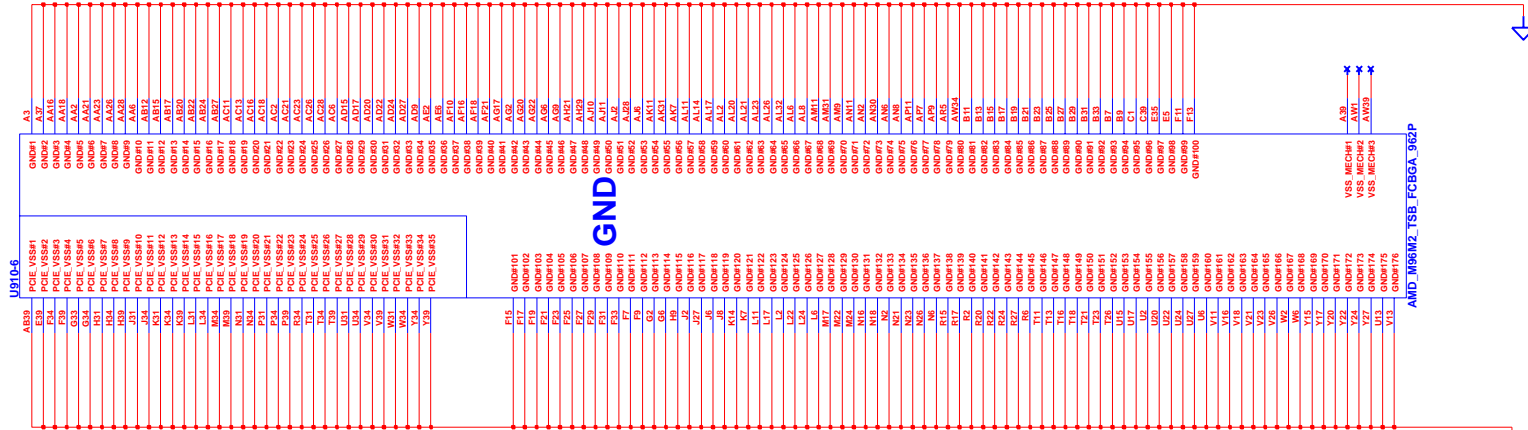
TITLE
VGA_4
Perugia 10MG DDR3

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01

CHANGE by Eric Lai 20-Jul-2009

SHEET 32 OF 69





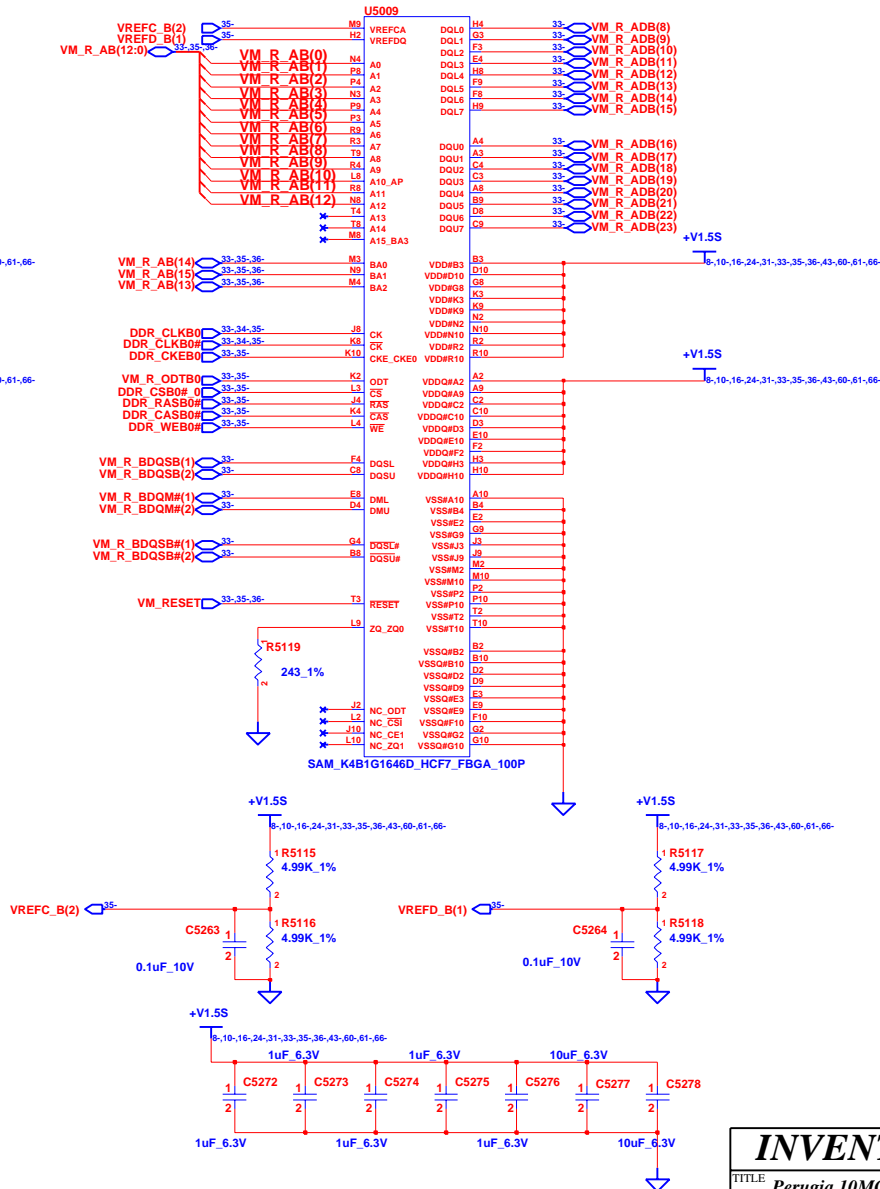
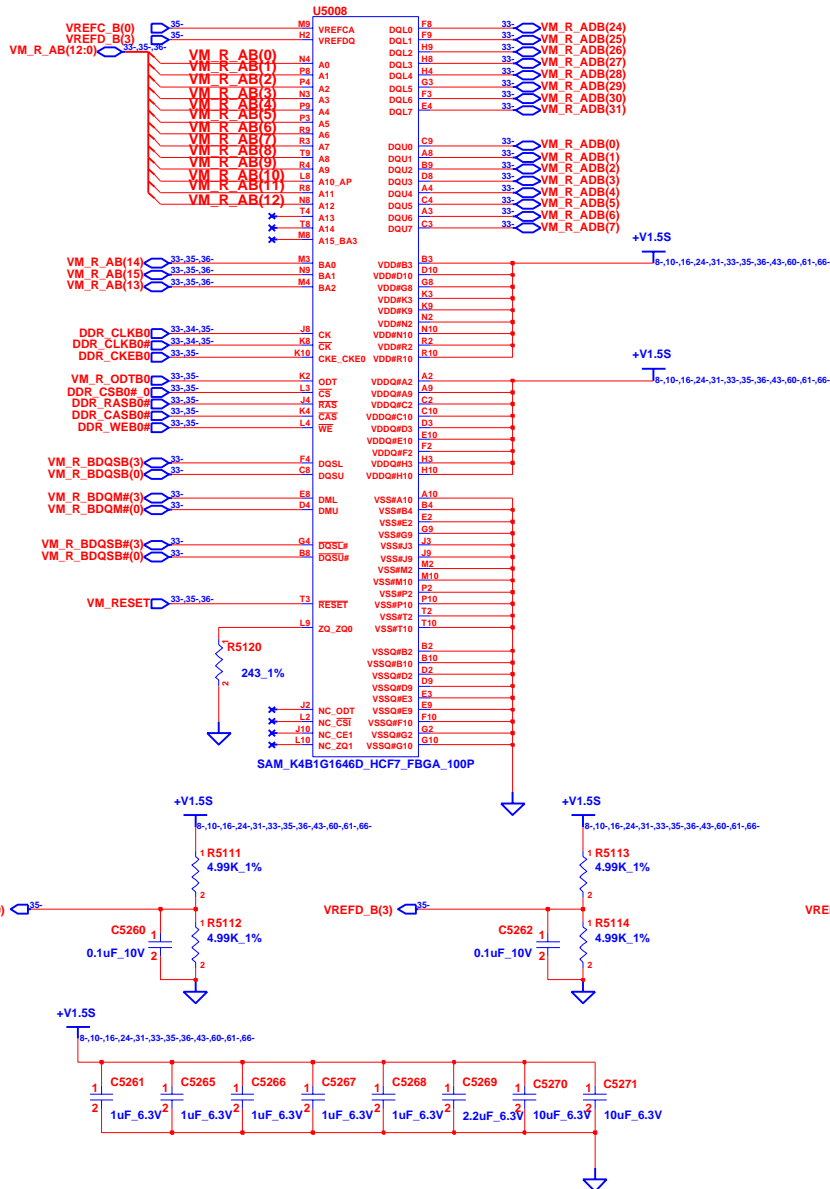
Layout Note: place these resistors near the VEDIO_RAM

INVENTEC

TITLE Perugia 10MG DDR3 VGA_6			
SIZE A3	CODE CS	DOC. NUMBER 1310A2303-0-MTR	REV A01
SHEET		34	OF 69

CHANGE by Eric Lai 20-Jul-2009

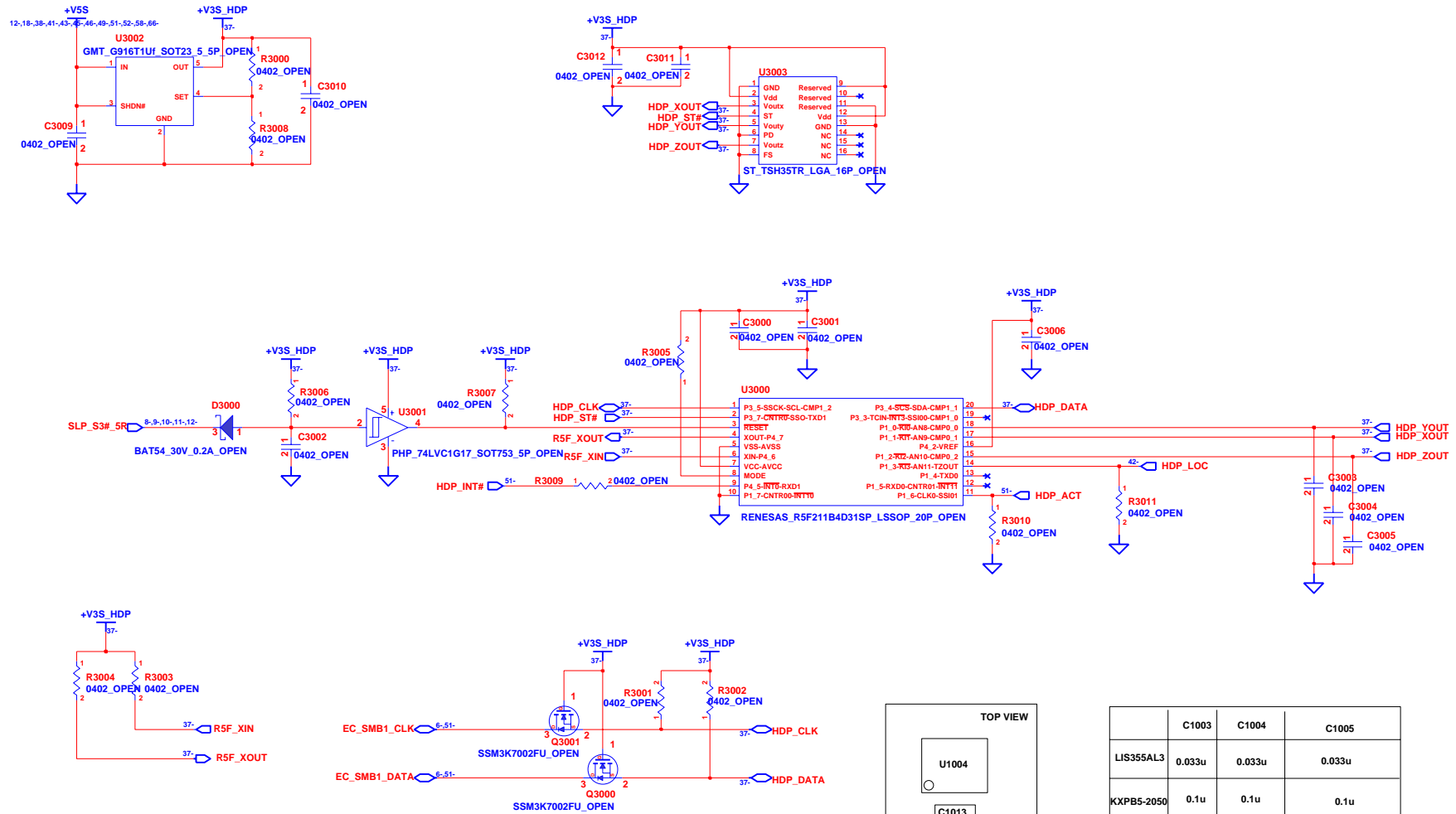
M92 Use channel B



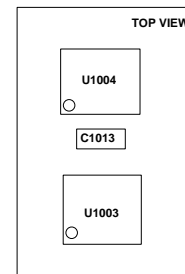
INVENTEC

TITLE			
Perugia 10MG DDR3			
VRAM_0			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01
SHEET		35	OF 69

CHANGE by Eric Lai 20-Jul-2009



	C1003	C1004	C1005
LIS355AL3	0.033u	0.033u	0.033u
KXPB5-2050	0.1u	0.1u	0.1u



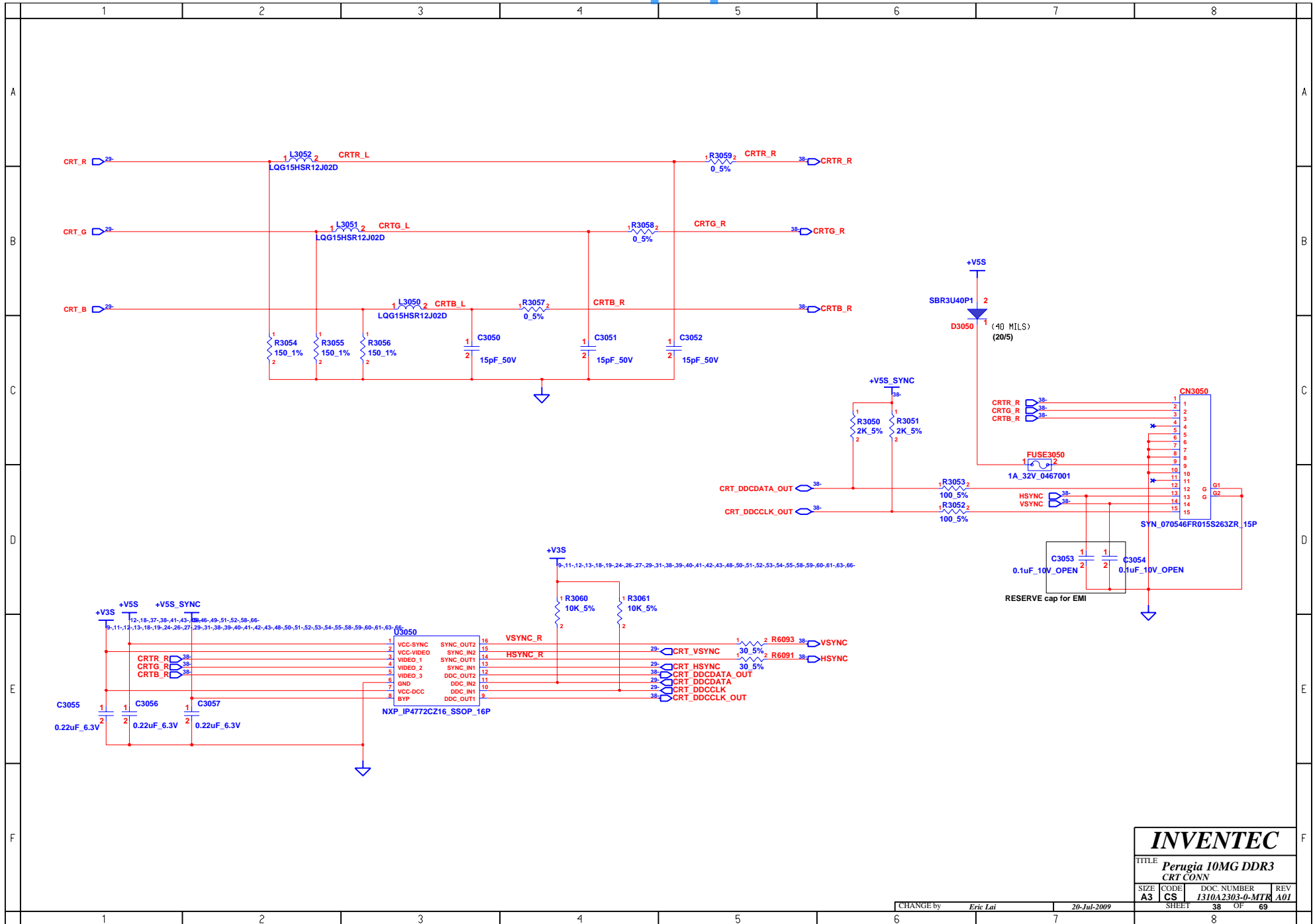
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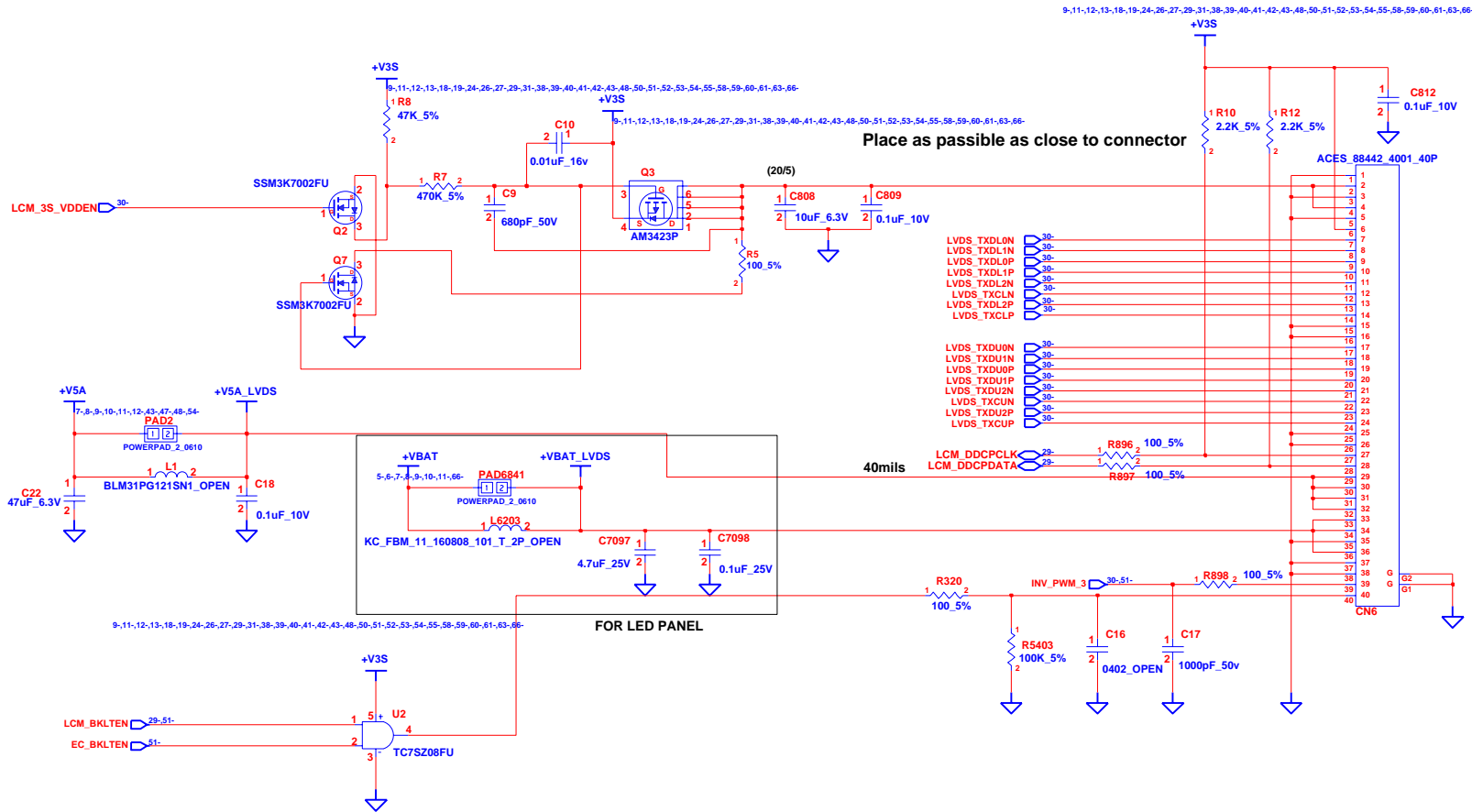
TITLE
Perugia 10MG DDR3
3D Sensor

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01

CHANGE by *Eric Lai* 20-Jul-2009

SHEET 37 OF 69





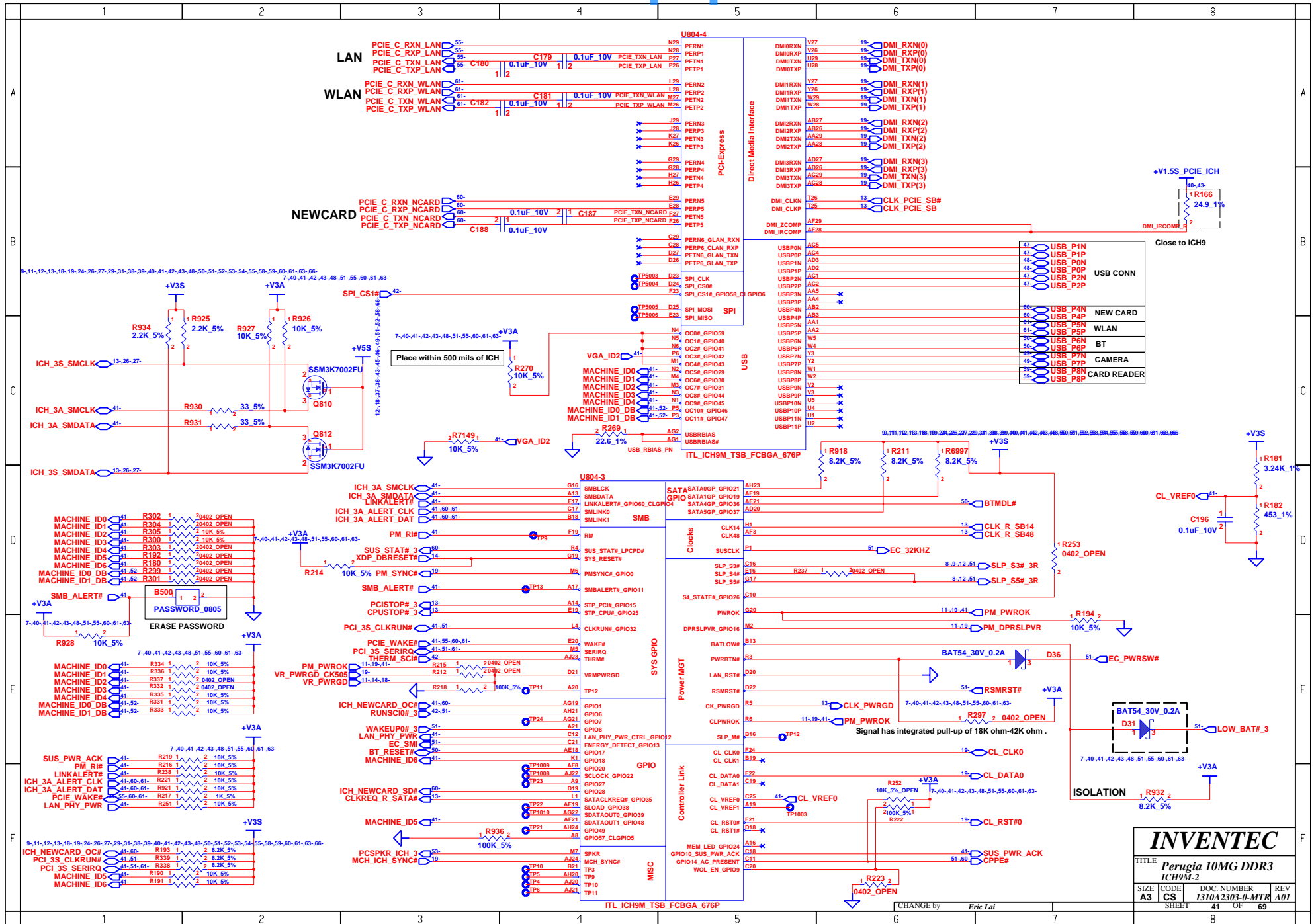
INVENTEC

TITLE
**Perugia 10MG DDR3
LCD CONN**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01

CHANGE by *Eric Lai* 20-Jul-2009

SHEET 39 OF 69



	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

U804-5

AA26 VSS001 H5

AA27 VSS002 J23

AA28 VSS003 J26

AA29 VSS004 J27

AB1 VSS005 AC22

AA23 VSS006 K28

AB26 VSS007 K29

AB29 VSS008 L13

AB4 VSS009 L15

AB5 VSS010 L2

AC17 VSS011 L26

AC26 VSS012 L27

AC27 VSS013 L5

AC3 VSS014 L7

AD1 VSS015 M12

AD10 VSS016 M13

AD12 VSS017 M14

AD13 VSS018 M15

AD14 VSS019 M16

AD17 VSS020 M17

AD18 VSS021 M23

AD21 VSS022 M28

AD25 VSS023 M29

AD23 VSS024 N11

AD4 VSS025 N12

AD5 VSS026 N13

AD6 VSS027 N14

AD7 VSS028 N15

AD9 VSS029 N16

AE13 VSS030 N17

AE13 VSS031 N18

AE14 VSS032 N26

AE16 VSS033 N27

AE17 VSS034 P12

AE3 VSS035 P13

AE20 VSS036 P14

AE24 VSS037 P15

AE3 VSS038 P16

AE4 VSS039 P17

AE5 VSS040 P2

AE9 VSS041 P23

AF13 VSS042 P28

AF16 VSS043 P29

AF18 VSS044 P4

AF24 VSS045 P7

AH26 VSS046 R11

AF28 VSS047 R12

AF27 VSS048 R13

AF3 VSS049 R14

AF7 VSS050 R15

AF9 VSS051 R16

AG13 VSS052 R17

AG16 VSS053 R18

AG18 VSS054 R28

AG20 VSS055 T12

AG23 VSS056 T13

AG3 VSS057 T14

AG5 VSS058 T15

AG9 VSS059 T16

AH12 VSS060 T17

AH14 VSS061 T23

AH17 VSS062 B26

AH19 VSS063 U12

AH2 VSS064 U13

AH22 VSS065 U14

AH23 VSS066 U15

AH28 VSS067 U16

AH5 VSS068 U17

AH6 VSS069 AD23

AJ12 VSS070 U26

AJ14 VSS071 U27

AJ17 VSS072 U3

AJ8 VSS073 V1

B11 VSS074 V13

B14 VSS075 V15

B17 VSS076 V23

B3 VSS077 V28

B20 VSS078 V29

B23 VSS079 V4

B5 VSS080 V5

B5 VSS081 W26

C26 VSS082 W27

C27 VSS083 W3

E11 VSS084 Y1

E14 VSS085 Y28

E18 VSS086 Y29

E2 VSS087 Y4

E21 VSS088 Y5

E24 VSS089 AG28

E5 VSS090 AH6

E8 VSS091 AF2

F16 VSS092 B25

F28 VSS093

F29 VSS094 VSS_NCTF1 A1

G12 VSS095 VSS_NCTF2 A2

G14 VSS096 VSS_NCTF3 A28

G19 VSS097 VSS_NCTF4 A29

G21 VSS098 VSS_NCTF5 AH1

G24 VSS099 VSS_NCTF6 AH29

G25 VSS100 VSS_NCTF7 AJ1

G27 VSS101 VSS_NCTF8 AJ2

G8 VSS102 VSS_NCTF9 AJ28

H3 VSS103 VSS_NCTF10 AJ29

H28 VSS104 VSS_NCTF11 B1

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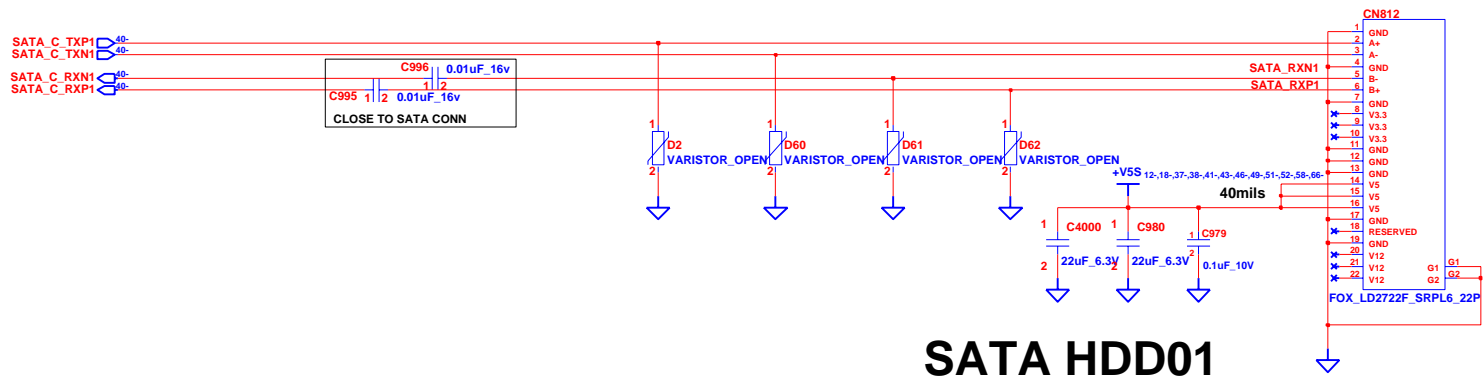
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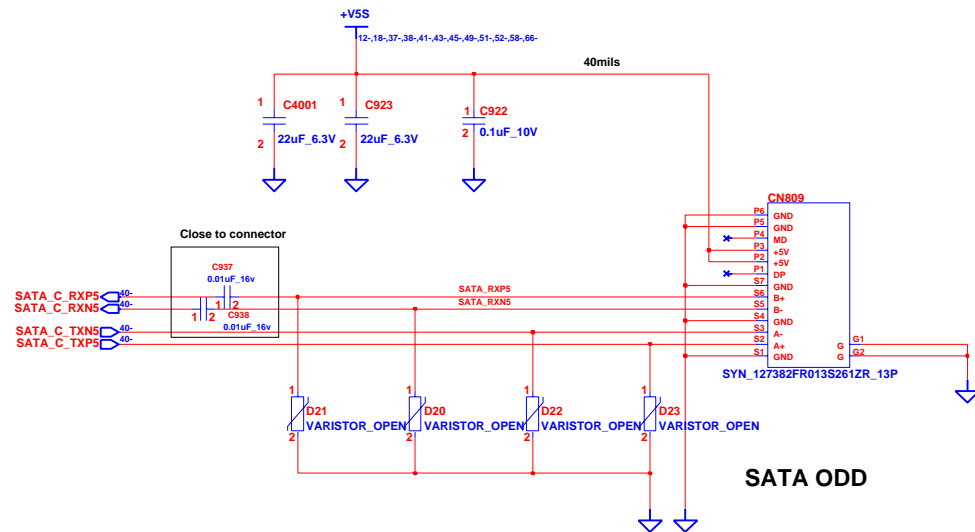
CHANGE by Eric Lai 20-Jul-2009

SIZE A3 CODE CS DOC. NUMBER 1310A2303-0-MTR REV A01

SHEET 44 OF 69

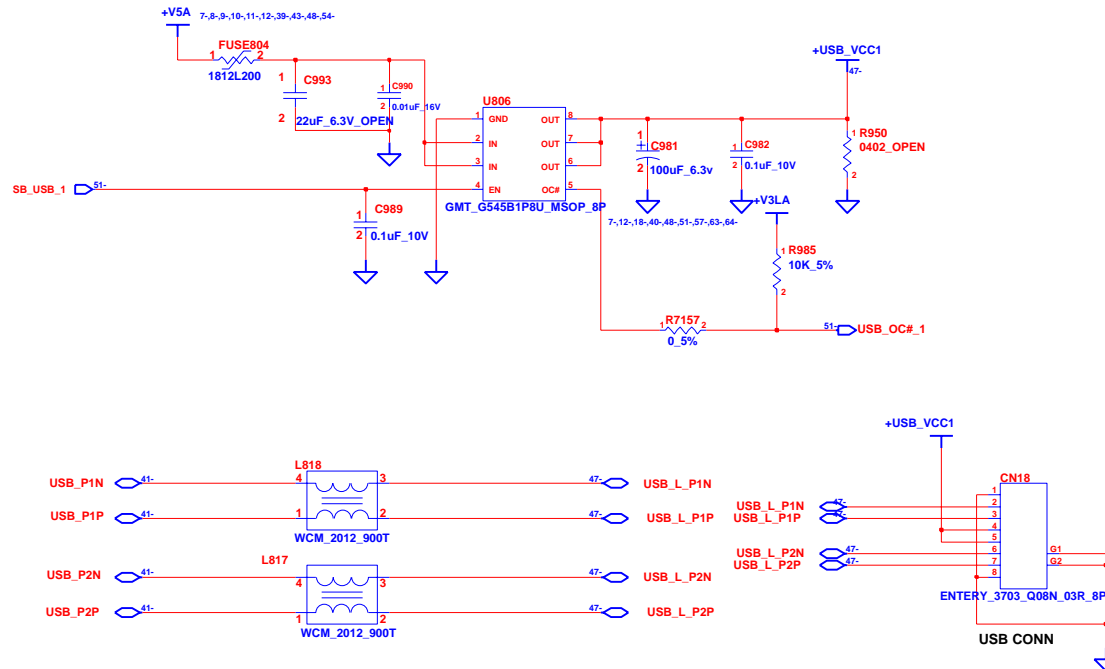


INVENTEC			
TITLE			
Perugia 10MG DDR3 HDD CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01
CHANGE by		SHEET	
Eric Lai		45 OF 69	
20-Jul-2009		8	



SATA ODD

INVENTEC			
TITLE Perugia 10MG DDR3 SATA ODD			
SIZE A3	CODE CS	DOC. NUMBER 1310A2303-0-MTR	REV A01
CHANGE by Eric Lai		20-Jul-2009	
SHEET 46		OF 69	



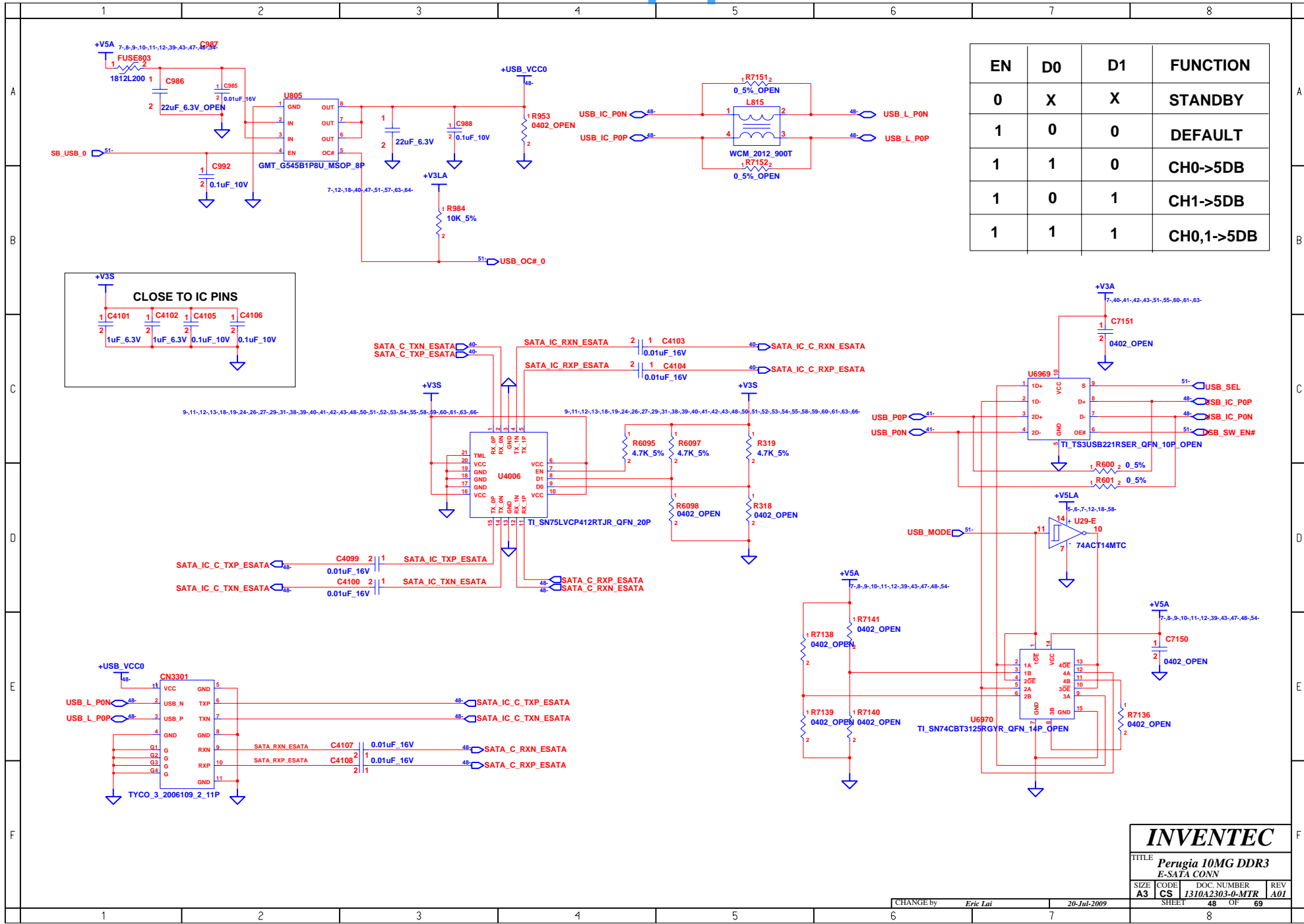
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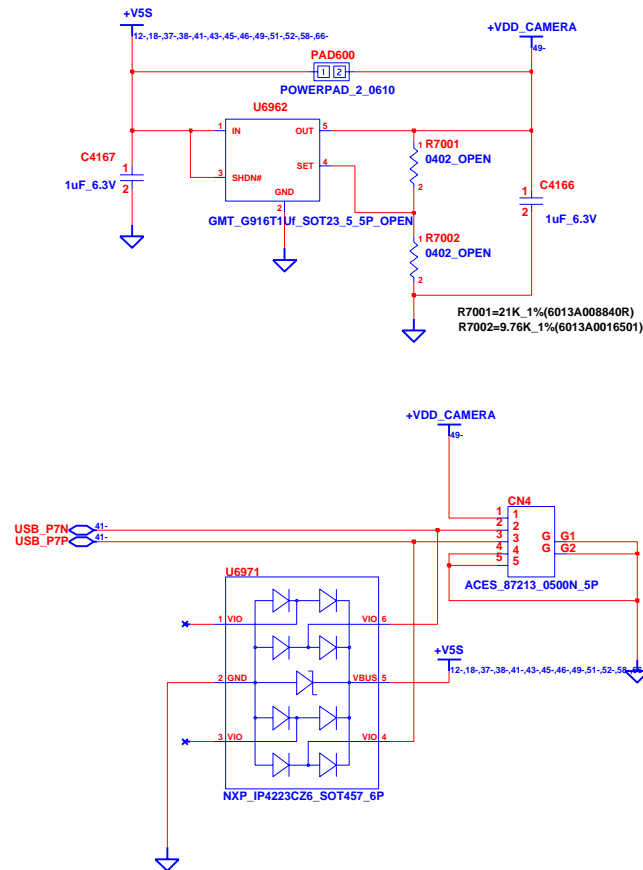
TITLE
Perugia 10MG DDR3
USB CONN/USB(DB)

SIZE A3	CODE CS	DOC. NUMBER I310A2303-0-MTR	REV A01
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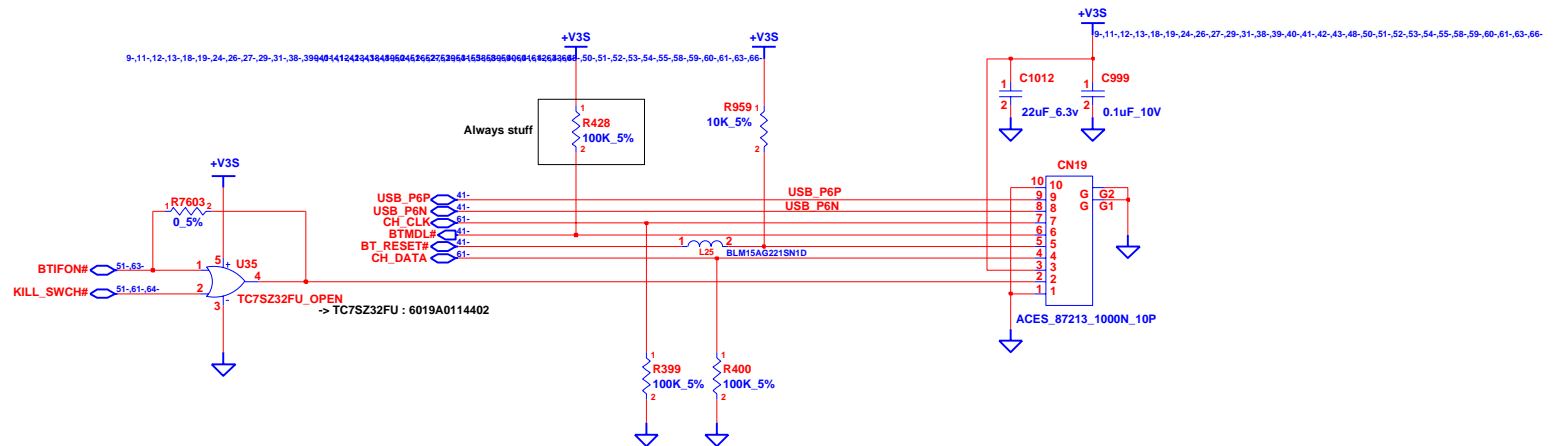
CHANGE by *Eric Lai* 20-Jul-2009

SHEET 47 OF 69



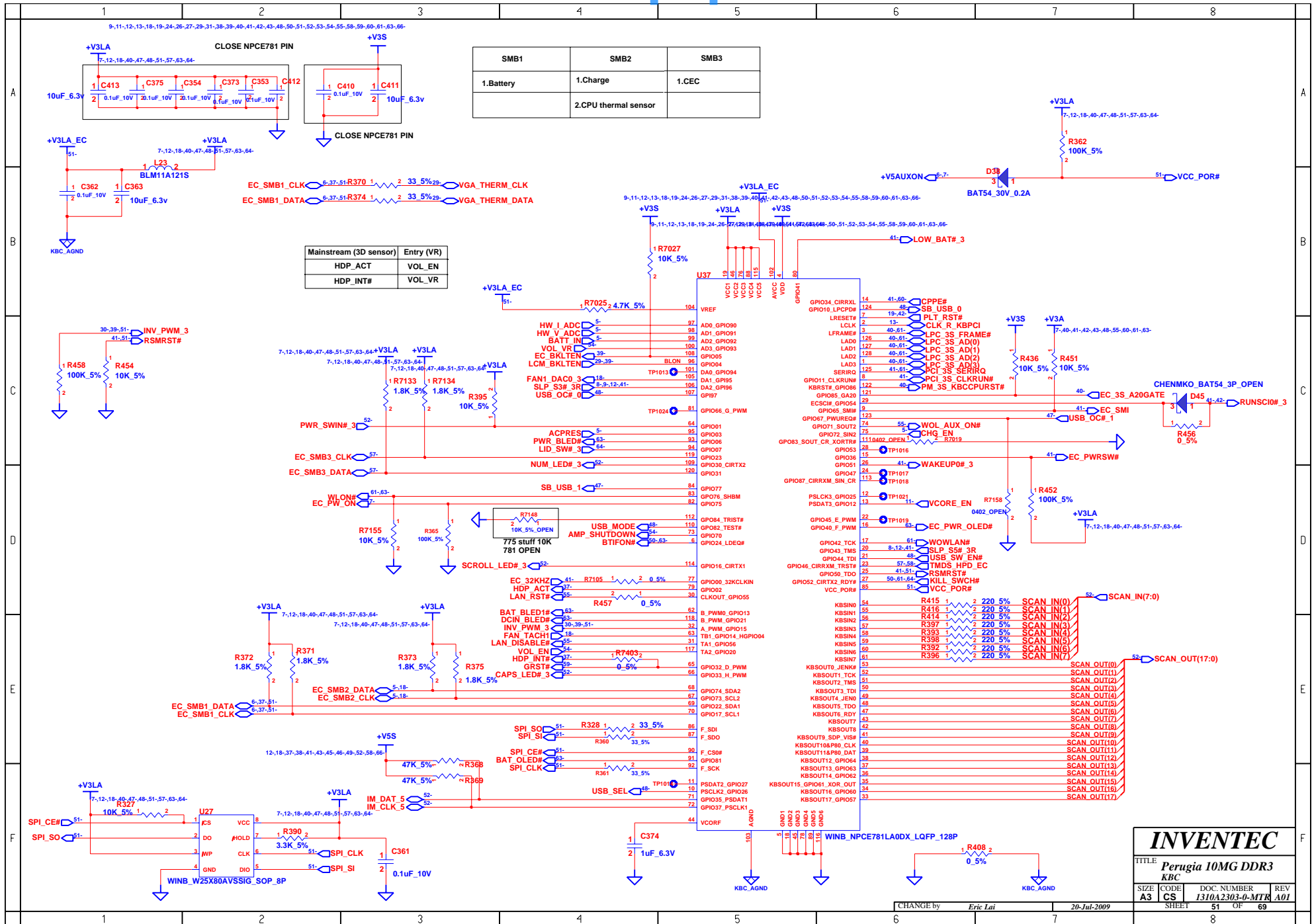


INVENTEC			
TITLE			
Perugia 10MG DDR3 Camera			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01
CHANGE by		20-Jul-2009	
SHEET		49	OF 69

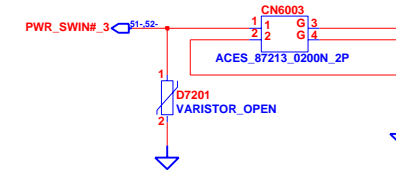
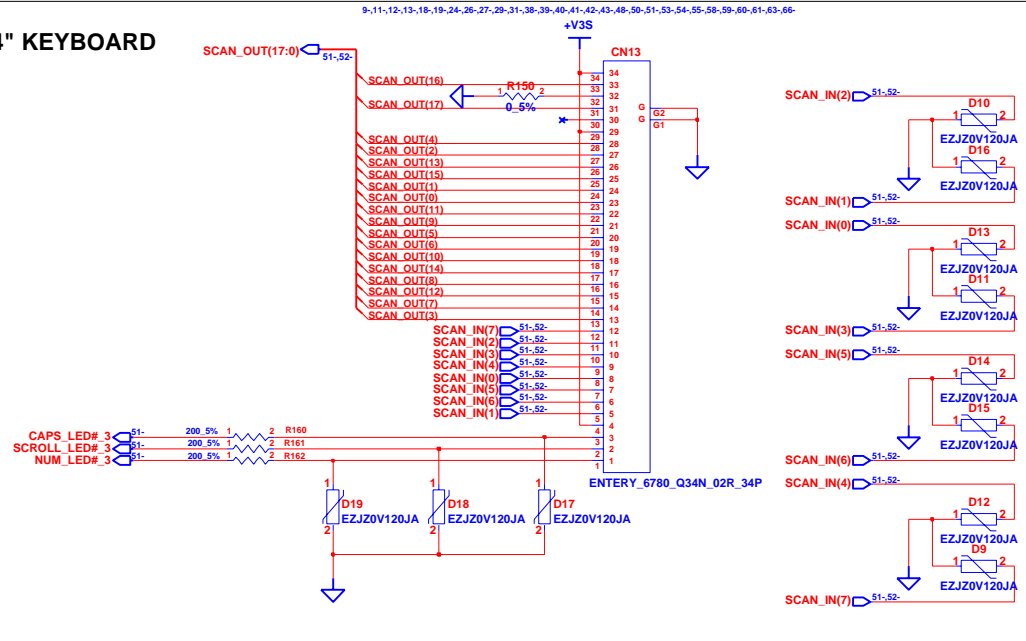


INVENTEC			
TITLE: Perugia 10MG DDR3 Bluetooth			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A2303-0-MTR	REV: A01
SHEET: 50		OF: 69	

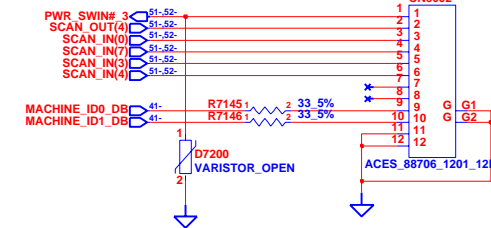
CHANGE by: Eric Lai 20-Jul-2009



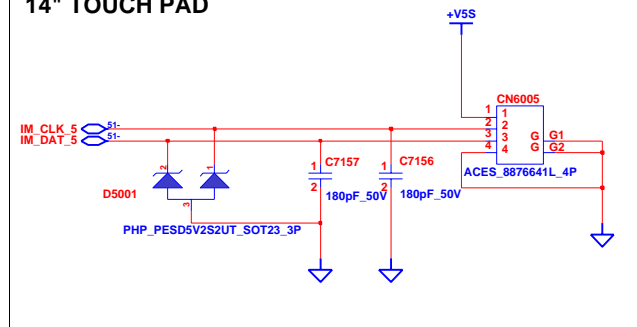
14" KEYBOARD



Hotkey



14" TOUCH PAD



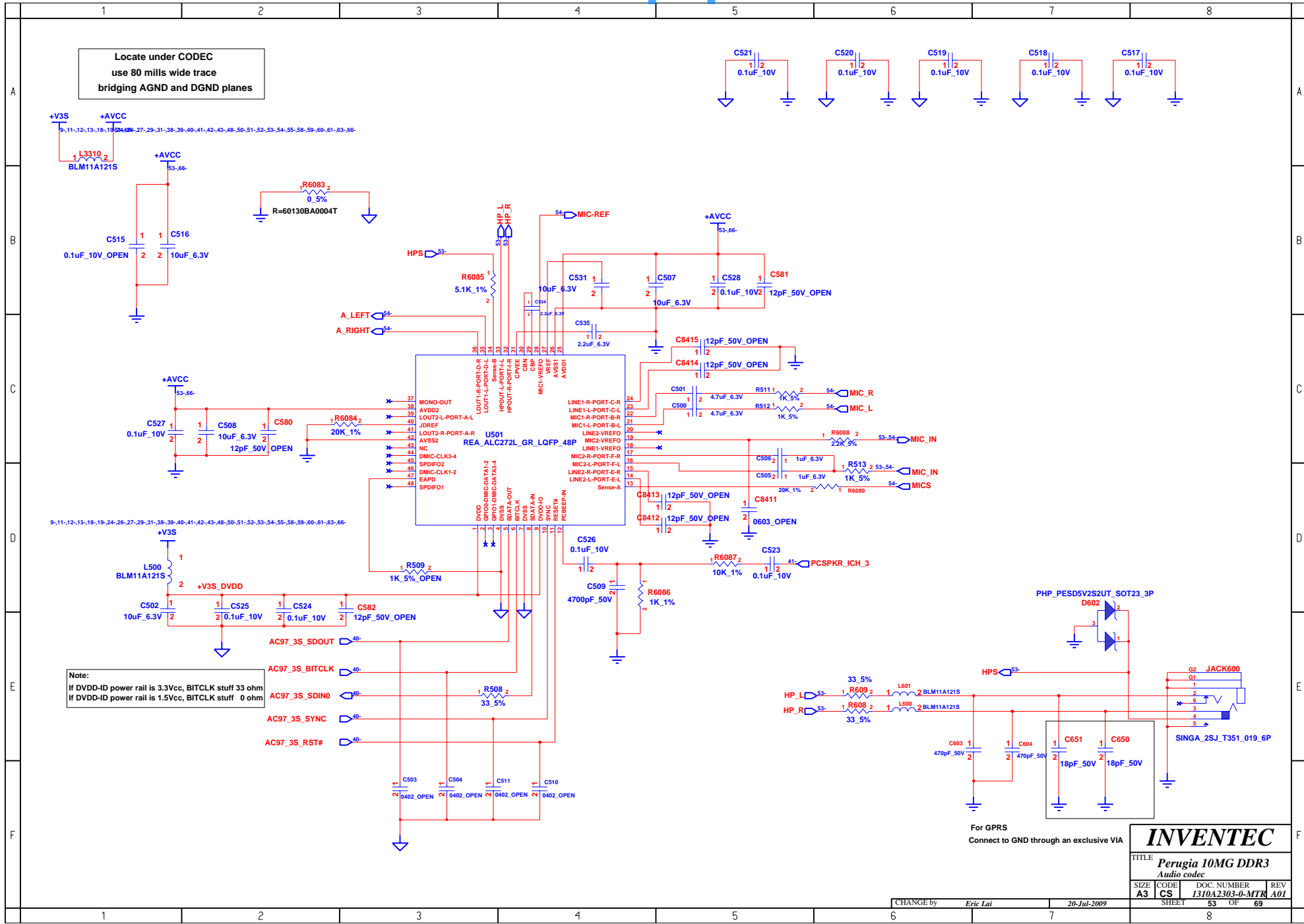
INVENTEC

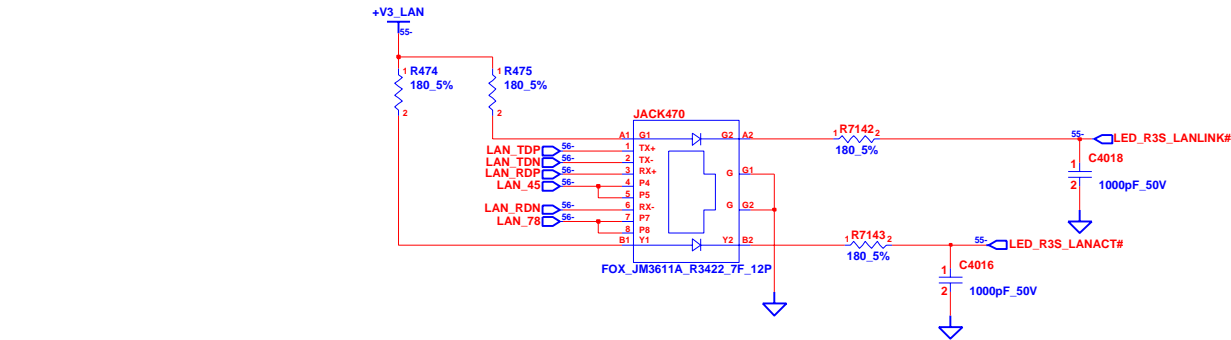
TITLE: **Perugia 10MG DDR3
K/B & TP/B CONN**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01

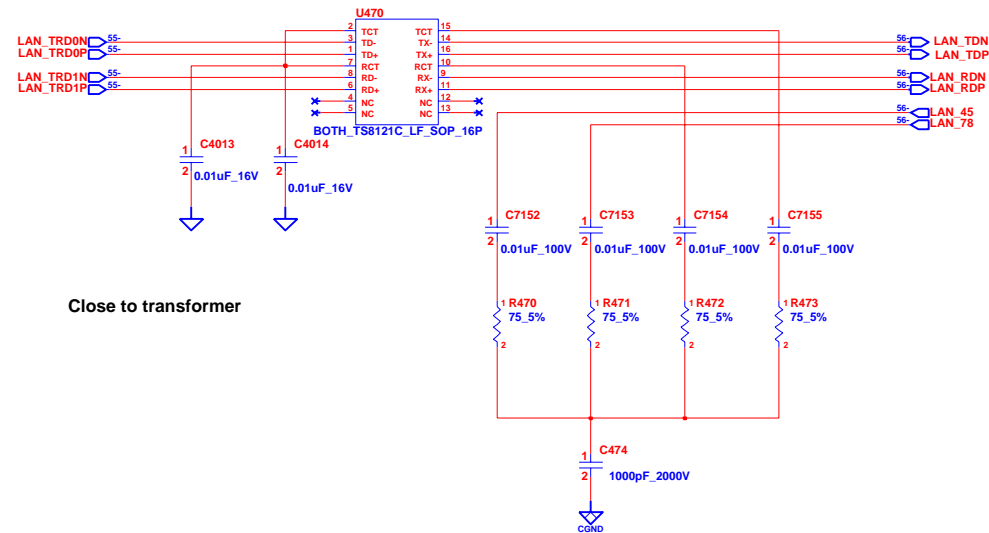
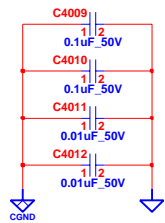
CHANGE by: **Eric Lai** 20-Jul-2009

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LEDS1-0	00	* 01	10	11
LED0	Tx/Rx	Tx/Rx	Tx	LINK10 ACT
LED1	LINK100	LINK 10/100/1000	LINK	LINK100 ACT
LED2	LINK10	LINK10/100	Rx	ULL
LED3	LINK1000	LINK1000	ULL	LINK1000 ACT



Close to transformer

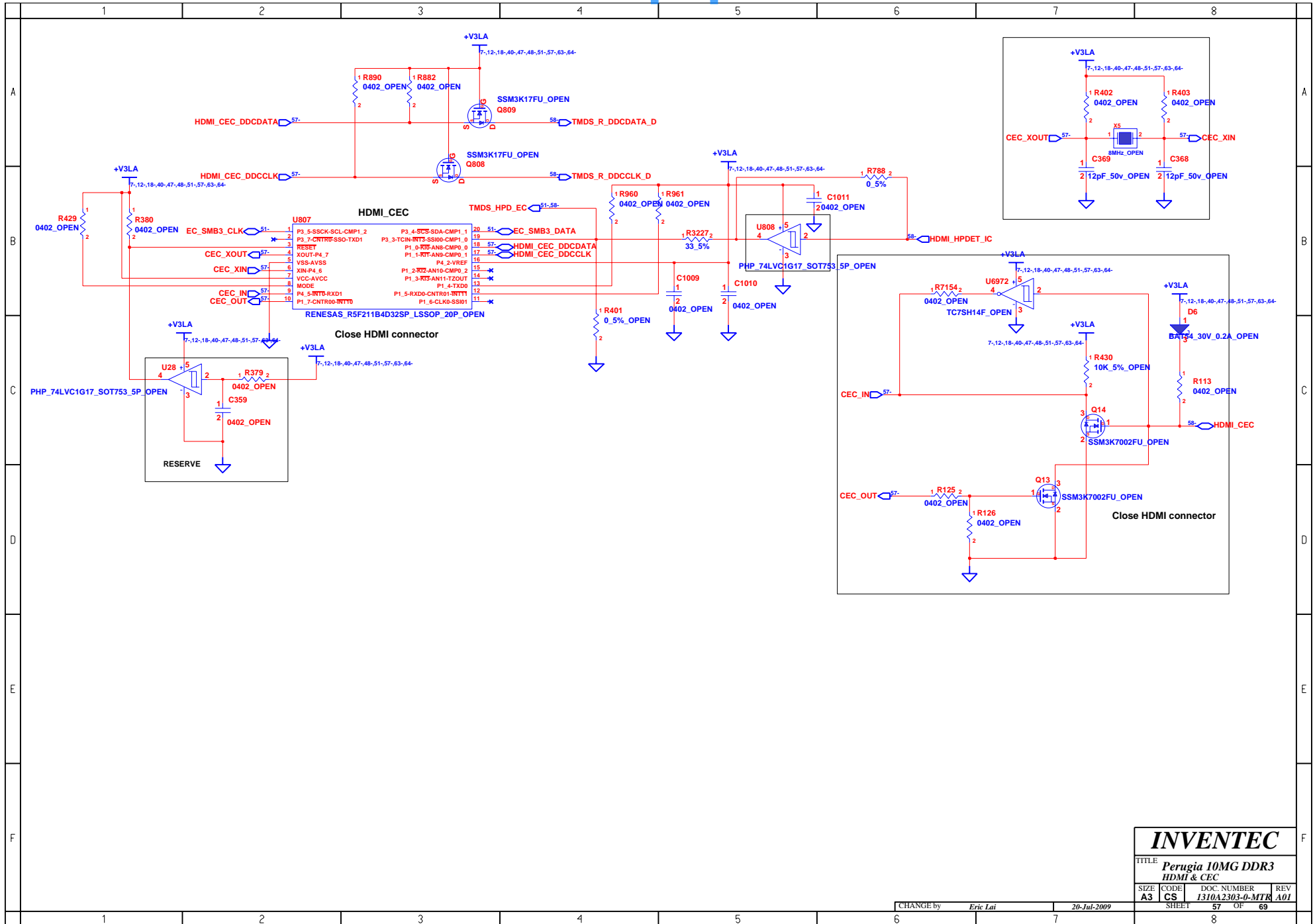
INVENTEC

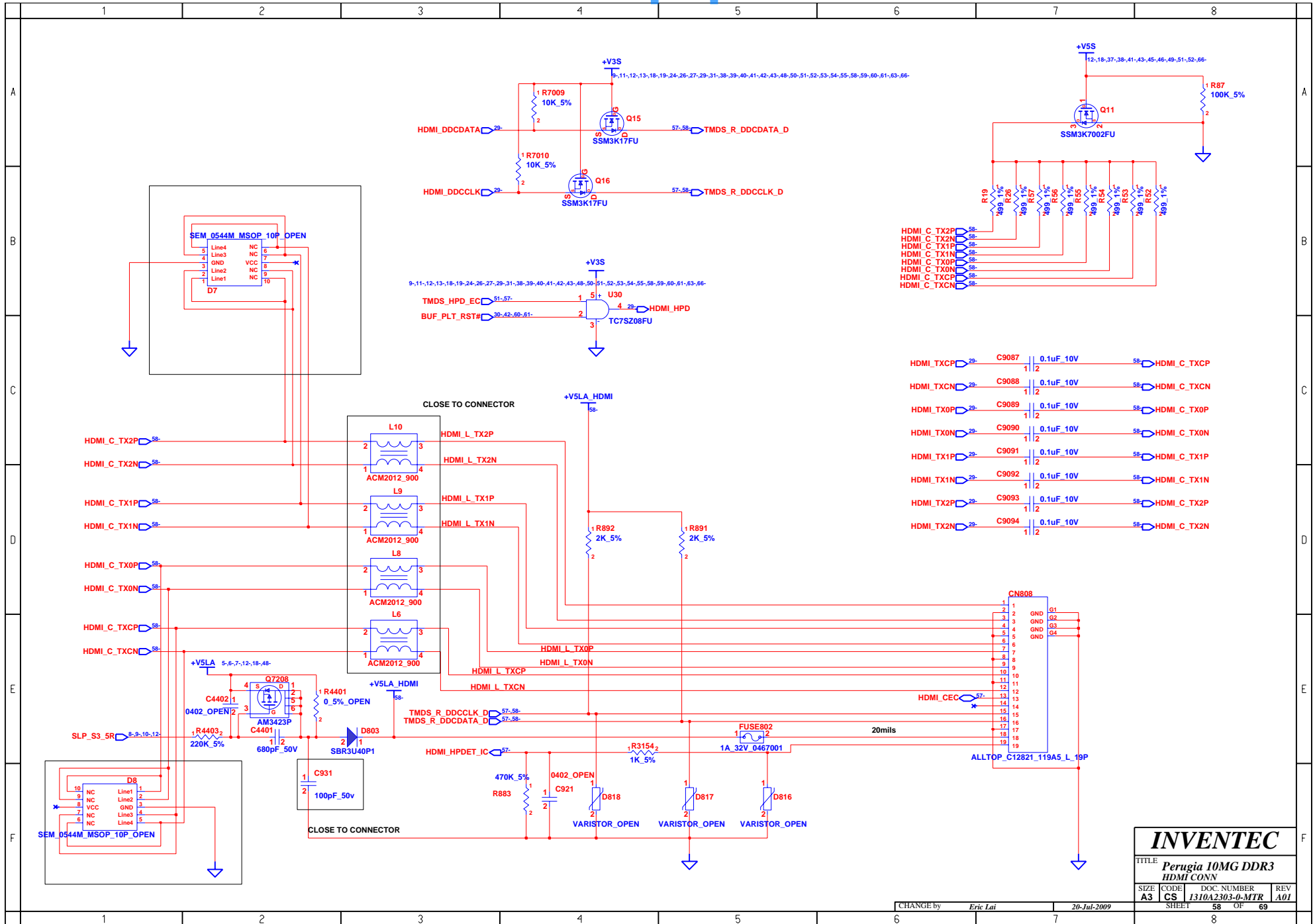
TITLE
Perugia 10MG DDR3
Transformer&RJ45

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01

CHANGE by Eric Lai 20-Jul-2009

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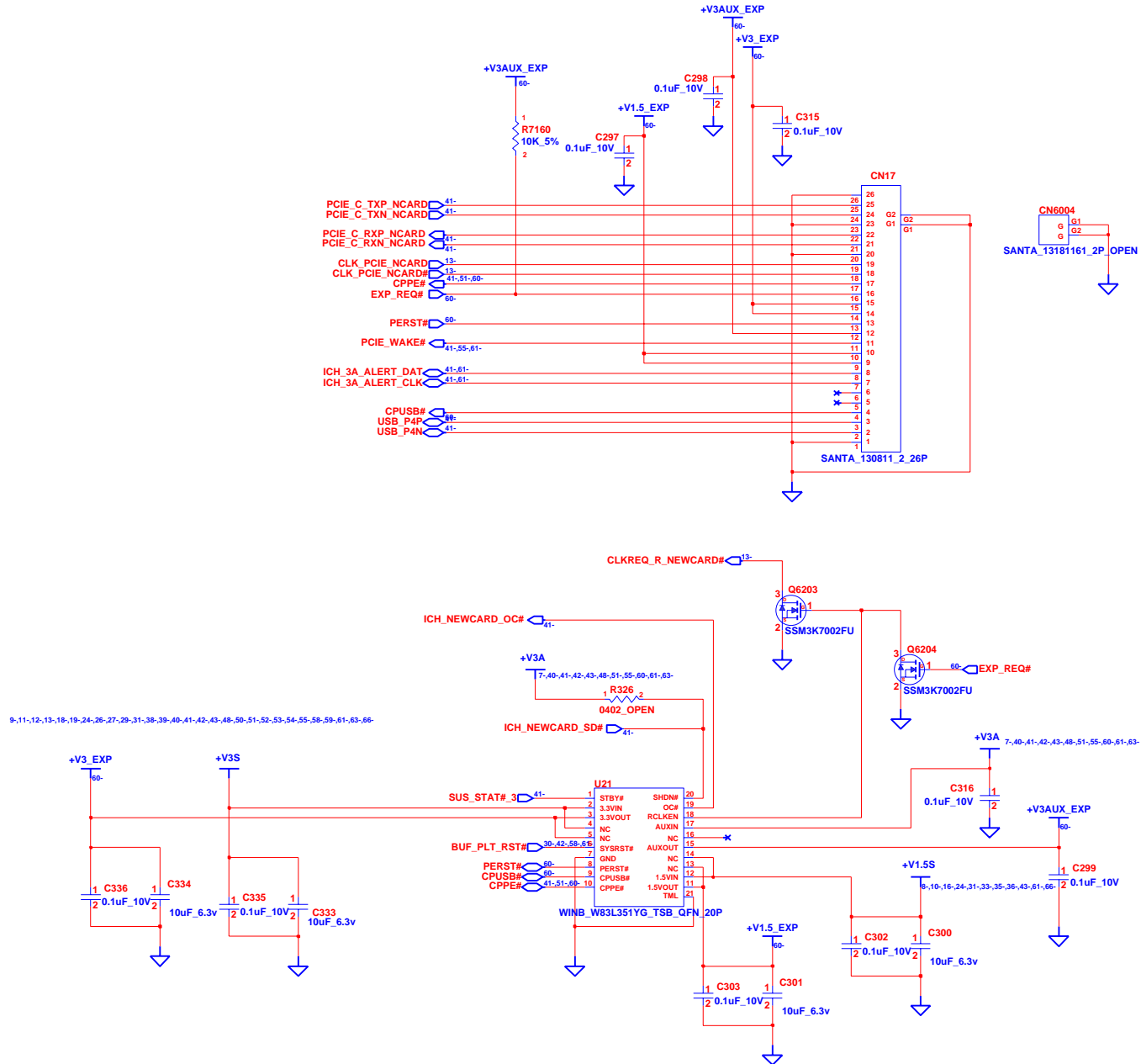




INVENTEC

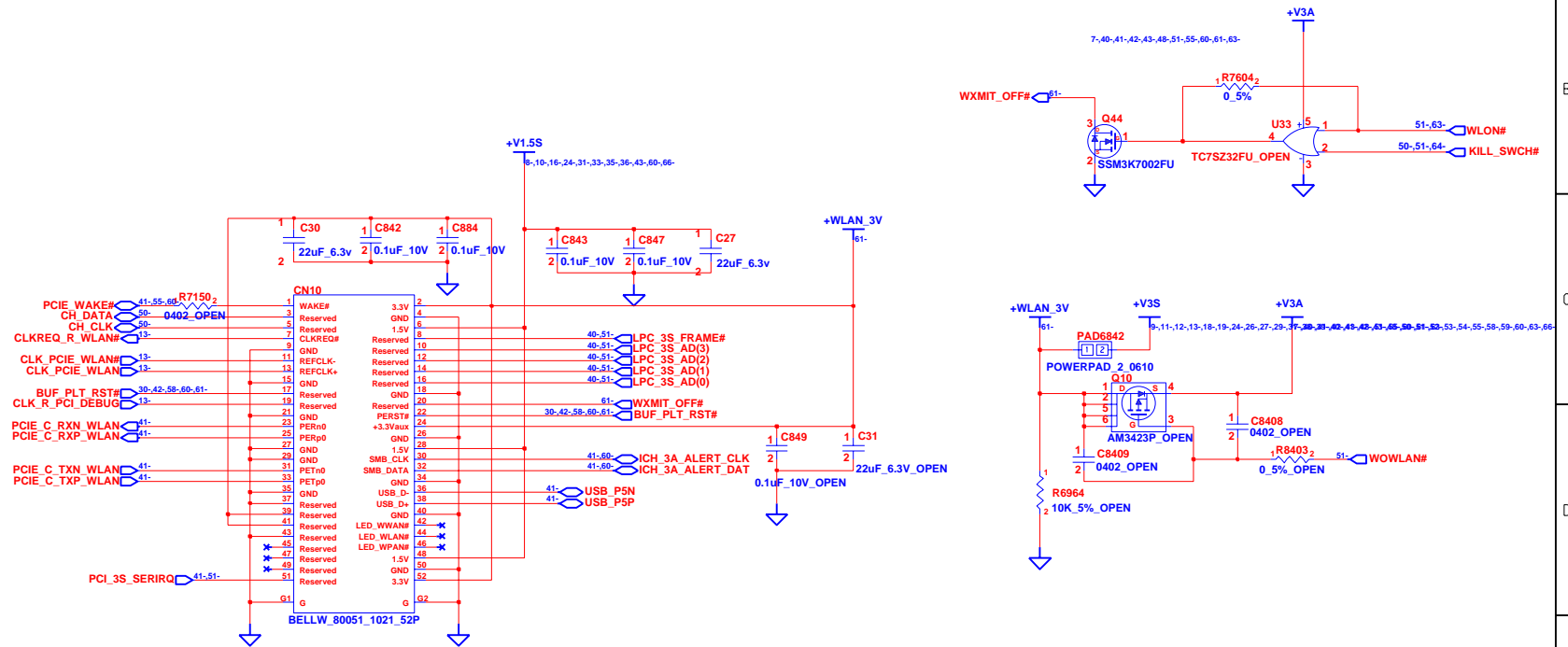
TITLE			
Perugia 10MG DDR3			
HDMI CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01
SHEET		58	OF 69

CHANGE by Eric Lai 20-Jul-2009



INVENTEC			
TITLE			
Perugia 10MG DDR3			
Express Card CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A2303-0-MTR	A01
CHANGE by		20-Jul-2009	
SHEET		60	OF 69

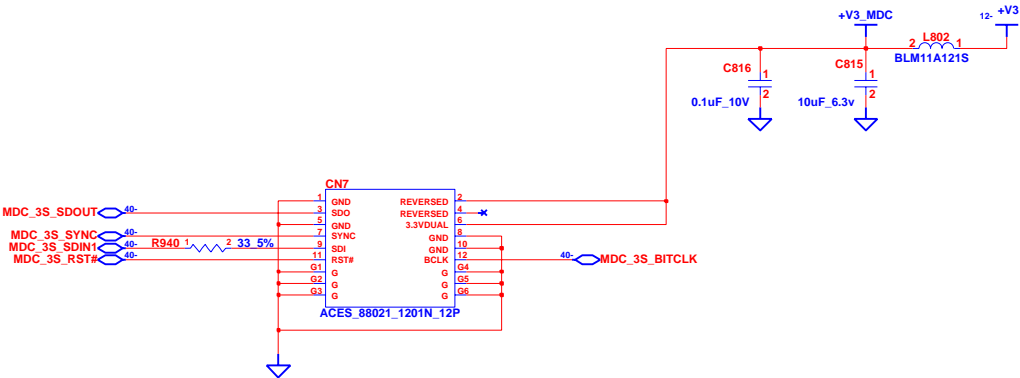
Wireless & Debug card



INVENTEC

TITLE			
Perugia 10MG DDR3			
Wireless & debug card			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01
SHEET		61	OF 69

CHANGE by Eric Lai 20-Jul-2009



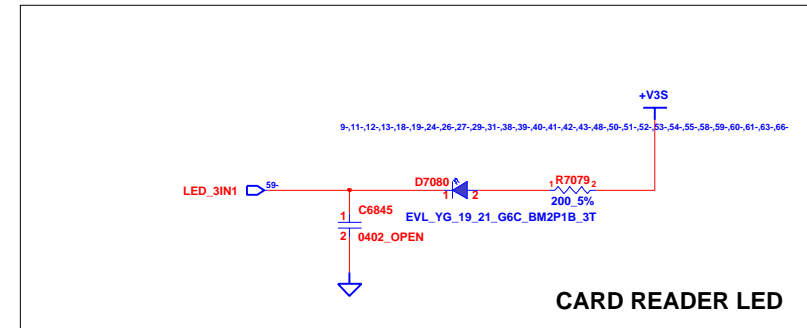
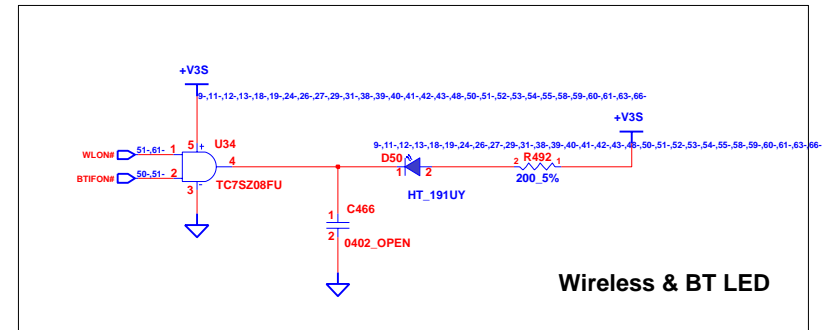
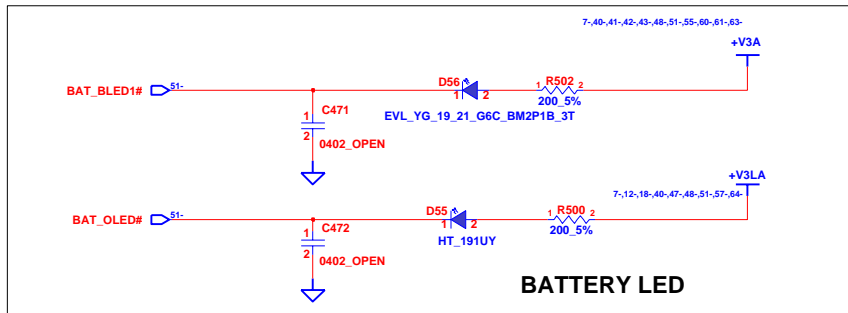
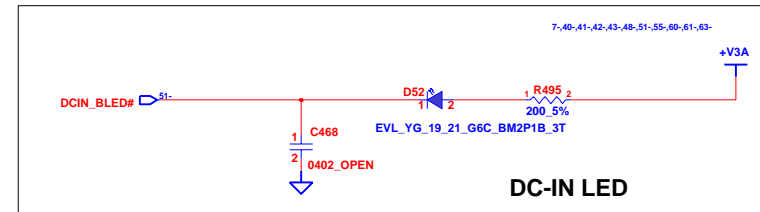
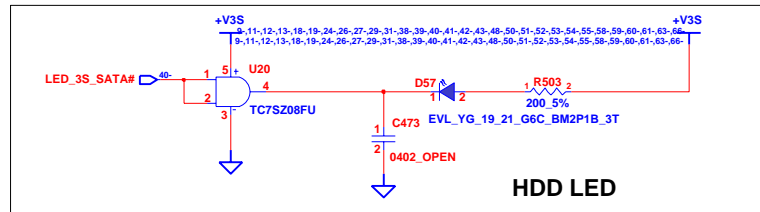
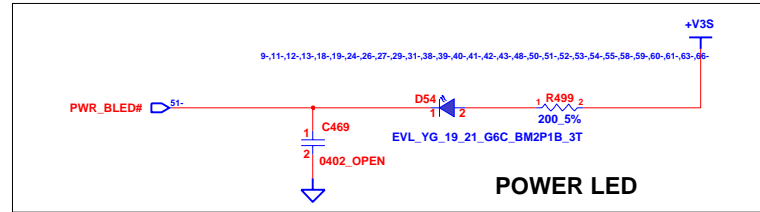
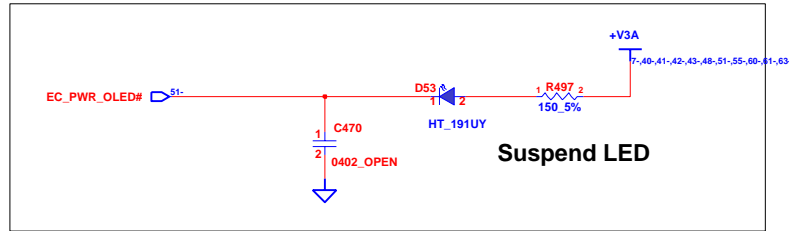
INVENTEC

TITLE
Perugia 10MG DDR3
MDC CONN

SIZE A3	CODE CS	DOC. NUMBER I310A2303-0-MTR	REV A01
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CHANGE by *Eric Lai* 20-Jul-2009

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INVENTEC

TITLE
**Perugia 10MG DDR3
LED & Hotkey CONN**

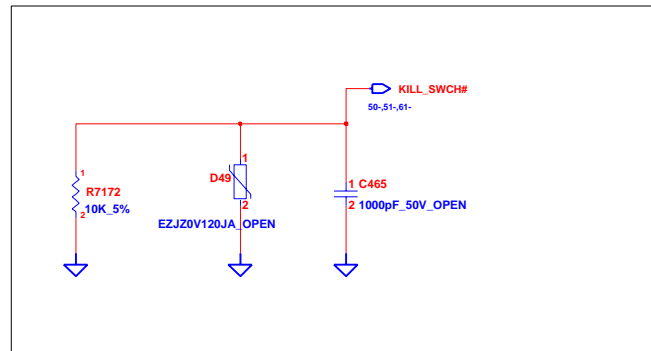
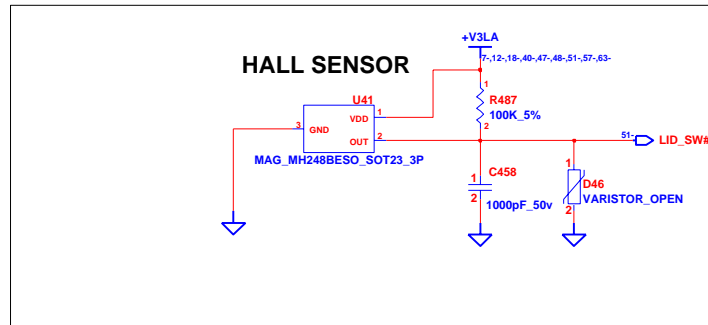
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01

CHANGE by Eric Lai 20-Jul-2009

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KILL SWITCH

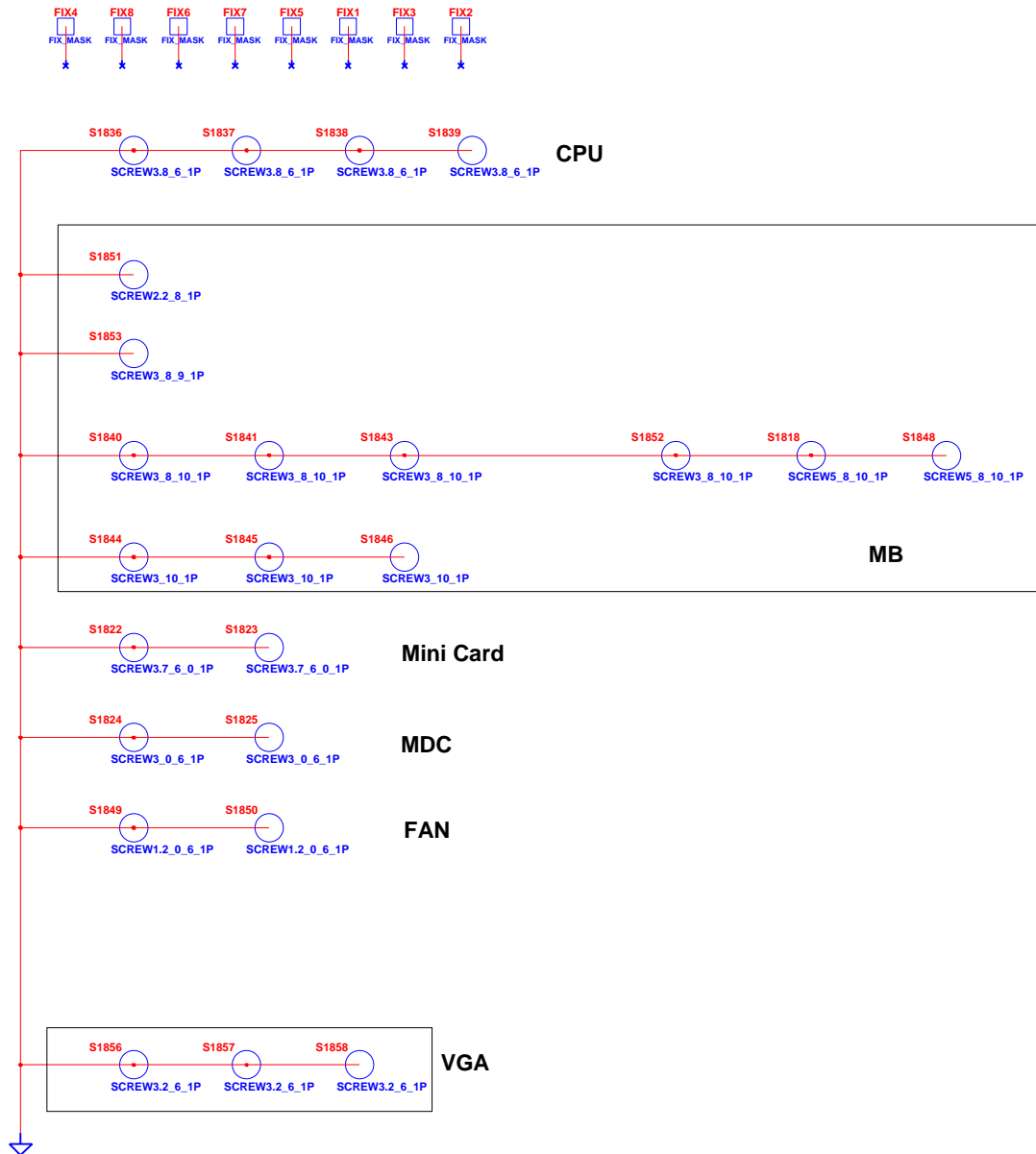
WLAN ON: KILL_SWCH# -> Low
WLAN OFF: KILL_SWCH# -> High

**HALL SENSOR****INVENTEC**

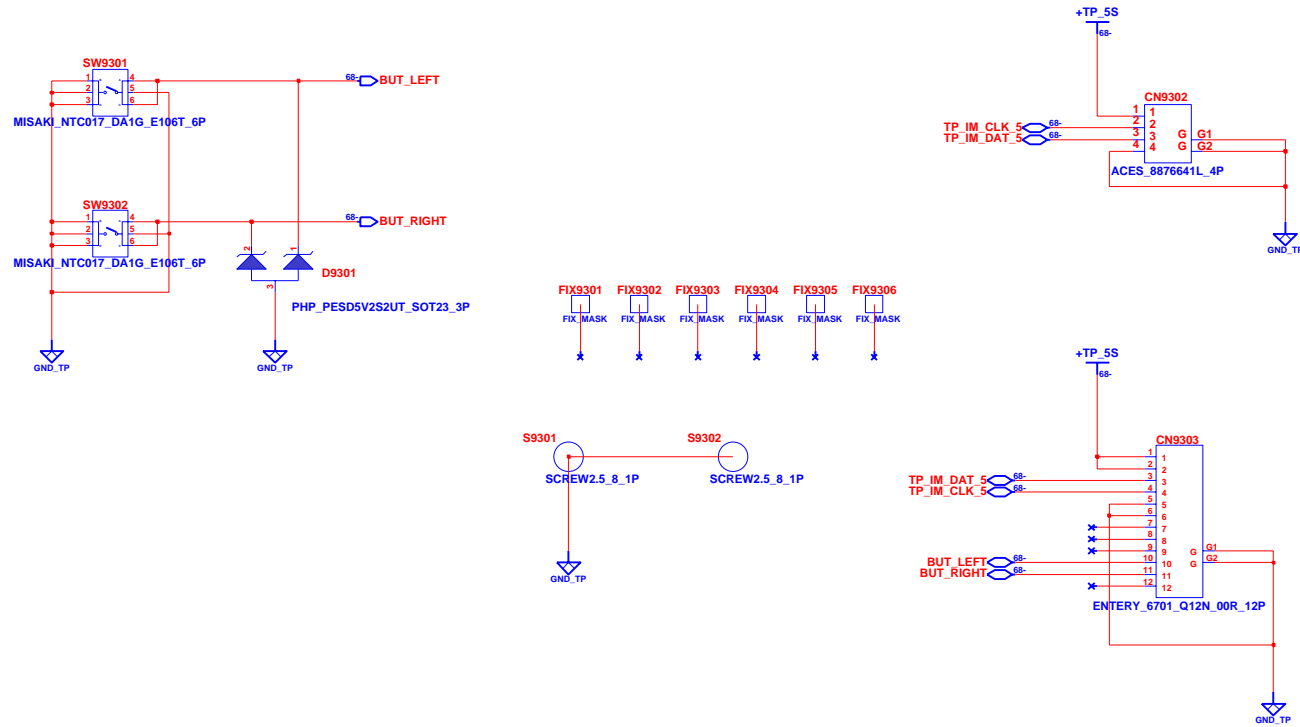
TITLE			
Perugia 10MG DDR3 Kill Swich & Hall Sensor			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01

CHANGE by Eric Lai 20-Jul-2009

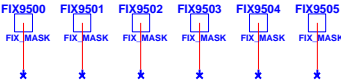
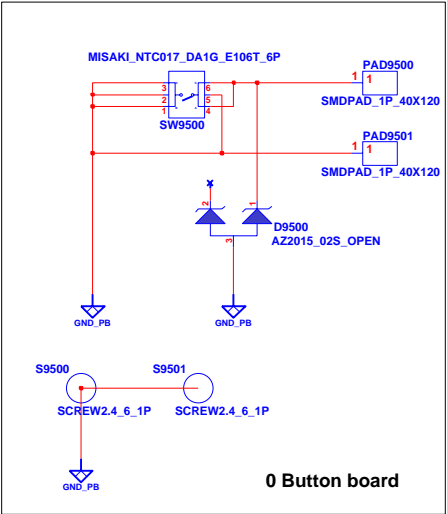
SHEET 64 OF 69



INVENTEC			
TITLE: Perugia 10MG DDR3			
Screw			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	I310A2303-0-MTR	A01
CHANGE by		20-Jul-2009	
SHEET		65	OF 69



INVENTEC			
TITLE: Perugia 10MG DDR3 Touch pad board			
SIZE: A3	CODE: CS	DOC. NUMBER: I310A2303-0-MTR	REV: A01
CHANGE by: Eric Lai		20-Jul-2009	
SHEET: 68		OF: 69	



INVENTEC

TITLE
Perugia 10MG DDR3
Touch pad board

SIZE A3	CODE CS	DOC. NUMBER I310A2303-0-MTR	REV A01
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CHANGE by *Eric Lai* 20-Jul-2009

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