

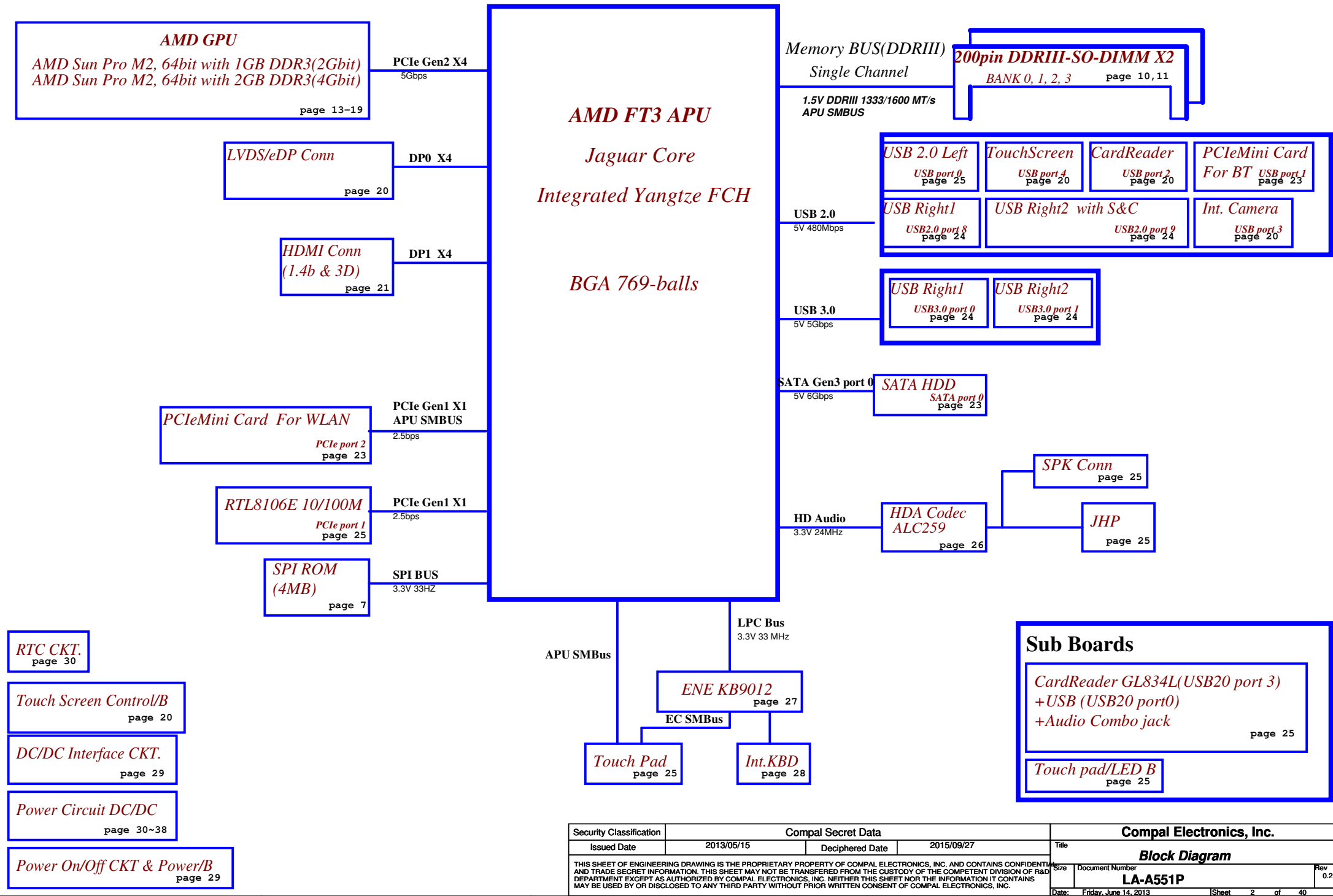
ZRMAE/ZEMAE

Juno/Iakros 10AN/10ANG

LA-A551P REV 0.2 Schematic

AMD KABINI Quad Core 25W for UMA+DIS
AMD KABINI Quad Core 15W
2013-06-07 Rev 0.2

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Cover Page	
				Size	Document Number
				LA-A551P	
				Date: Friday, June 14, 2013	Rev 0.2
				Sheet 1	of 40

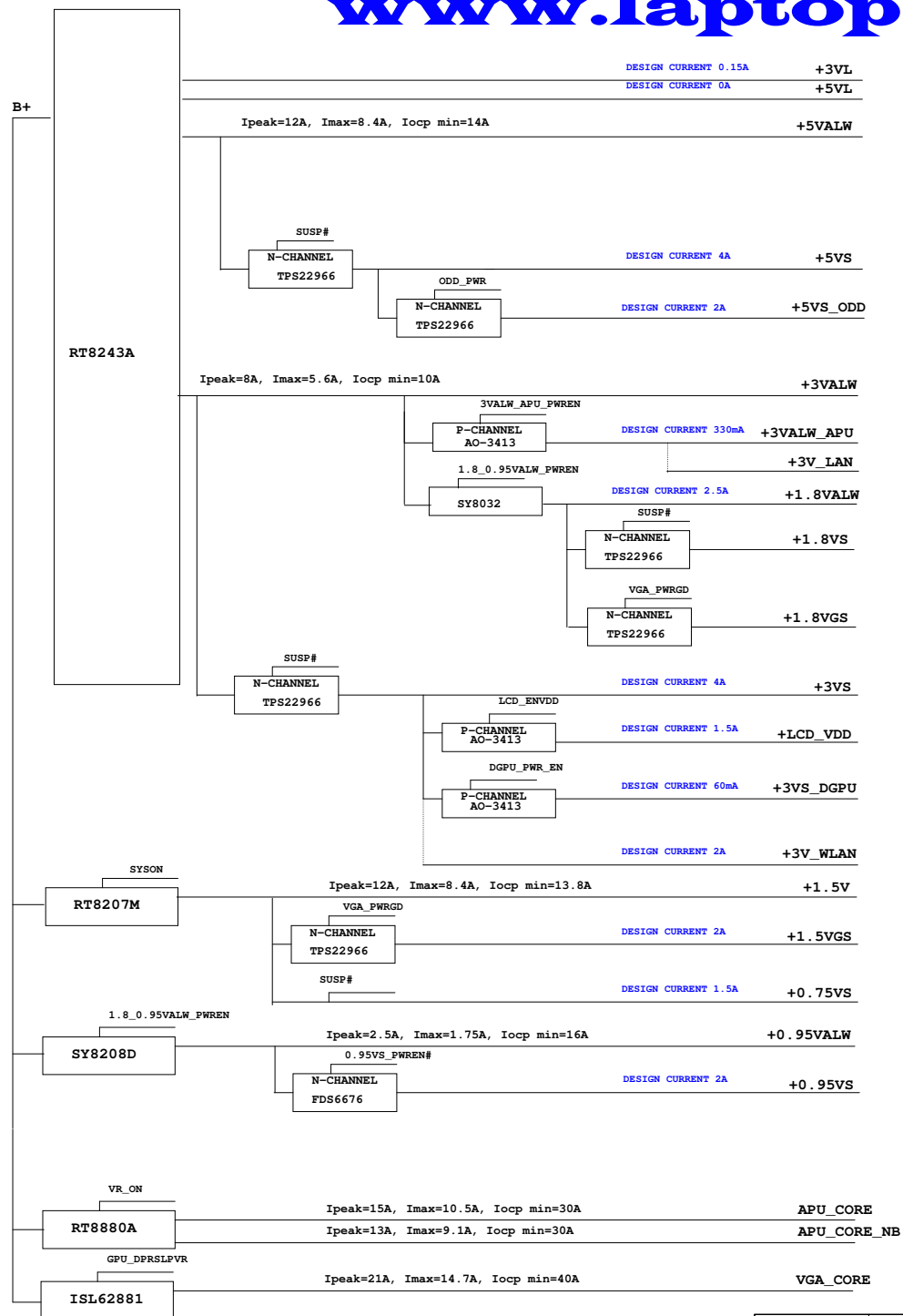


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					LA-A551P
				Date	Friday, June 14, 2013
				Sheet	2 of 40

Block Diagram

Rev 0.2

B+



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				LA-A551P	Rev 0.2
				Date: Friday, June 14, 2013	Sheet 3 of 40

Voltage Rails (O MEANS ON X MEANS OFF)

power plane State	+RTCVCC	B+	+5VL +3VL	+5VALW +3VALW +1.8VALW +0.95VALW +VSB	+1.5V	+5VS +3VS +0.95VS +1.8VS +1.5VS +0.75VS +APU_CORE +APU_CORE_NB
S0	O	O	O	O	O	O
S1	O	O	O	O	O	O
S3	O	O	O	O	O	X
S5 S4/AC	O	O	O	O	X	X
S5 S4/ Battery only	O	O	O	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X	X

UMA

BTO Option Table

Function	APU	
description	CPU A4-5000	CPU A6-5200
explain	15W 4C	25W 4C
BTO	A4R1@	A6R1@

Function	GPU	EC			LVDS-eDP		Camera & Mic			KB Light
description	Sun-Pro M2	9012	885		LVDS-eDP		Camera & Mic			KB Light
explain	VGA	9012	w/	w/ EMI	LVDS	eDP	Camera & Mic			KB Light
BTO	VGA@	9012@	885@	885_EMI@	LVDS@	IEDP@	CAM@	CAM@EMI@	@CAM@EMI@	KBL@

Function	LAN	S&C		Size		Codec	Touch Screen	
description	8106E	TI solution		Size		ALC259	Touch Screen	
explain	8106E	TPS2546	TPS2544	14"	15"	ALC259	W/ Touch	W/O EMI Touch
BTO	8106E@	2546@	2544@	14@	15@	259@	Touch_EMI@	@Touch_EMI@

Function	EMI/ESD/RF part				
description	EMI/ESD/RF part				
explain	EMI/ESD/RF part				
BTO	EMI@	@EMI@	ESD@	@ESD@	@RF@

APU SM Bus Address (SCL0/SDA0)

Power	Device	HEX	Address
+3VS	DDR SO-DIMM A	A0H	1010 0000 b
+3VS	DDR SO-DIMM B	A2H	1010 0010 b
+3VS	WLAN		

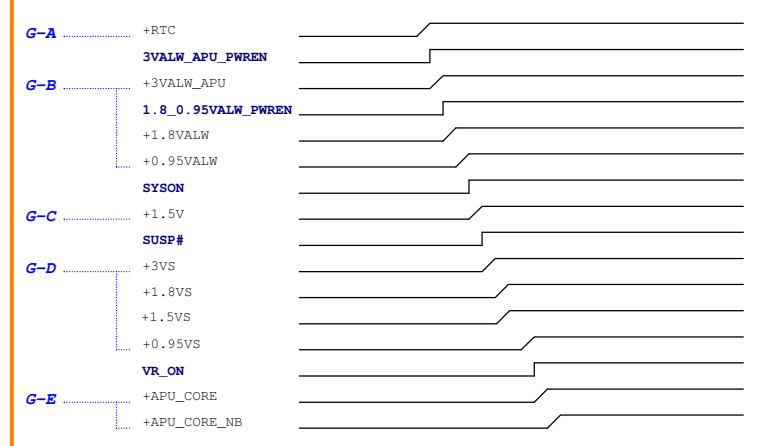
EC SM Bus1 Address

EC SM Bus2 Address

Power	Device	HEX	Address	Power	Device	HEX	Address
+3VL	Smart Battery	16H	0001 0110 b	+3VS	VGA thermal	82H	1000 0010 b
+3VL	Charger	12H	0001 0010 b	+3VS	APU thermal	98H	1001 1000 b

STATE	SIGNAL	SLP_S3#	SLP_S5#
Full ON		HIGH	HIGH
S1 (Power On Suspend)		HIGH	HIGH
S3 (Suspend to RAM)		LOW	HIGH
S4 (Suspend to Disk)		LOW	HIGH
S5 (Soft OFF)		LOW	LOW
G3		LOW	LOW

APU POWER SEQUENCE





THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

EDP_LCD_TXOUT0+_R 1 2 0.1U_0402_16V7K EDP_LCD_TXOUT2+_R 1 2 0.1U_0402_16V7K
 EDP_LCD_TXOUT0+_R 1 2 0.1U_0402_16V7K EDP_LCD_TXOUT2+_R 1 2 0.1U_0402_16V7K
 CC109 EDP@ CC110 EDP@

HDMI

EDP/LVDS

DISPLAY

MISC

TEST

MISC

FT3_BGA769

LCD_ENBKL : APU to EC to LCD
 LVDS_CLK&LVDS_DATA layout follow EDP AUX route 85 ohm

EDP/LVDS

HDMI

EDP/LVDS

EDP Cap co-layer

CC101 EDP@ CC103 EDP@
 0.1U_0402_16V7K 0.1U_0402_16V7K

EDP LVDS HPD RC44 2 EDP@ 1 100K 0402 5%
 LCD_ENBKL RC19 2 1 100K 0402 5%
 LCD INT PWM RC20 2 1 100K 0402 5%

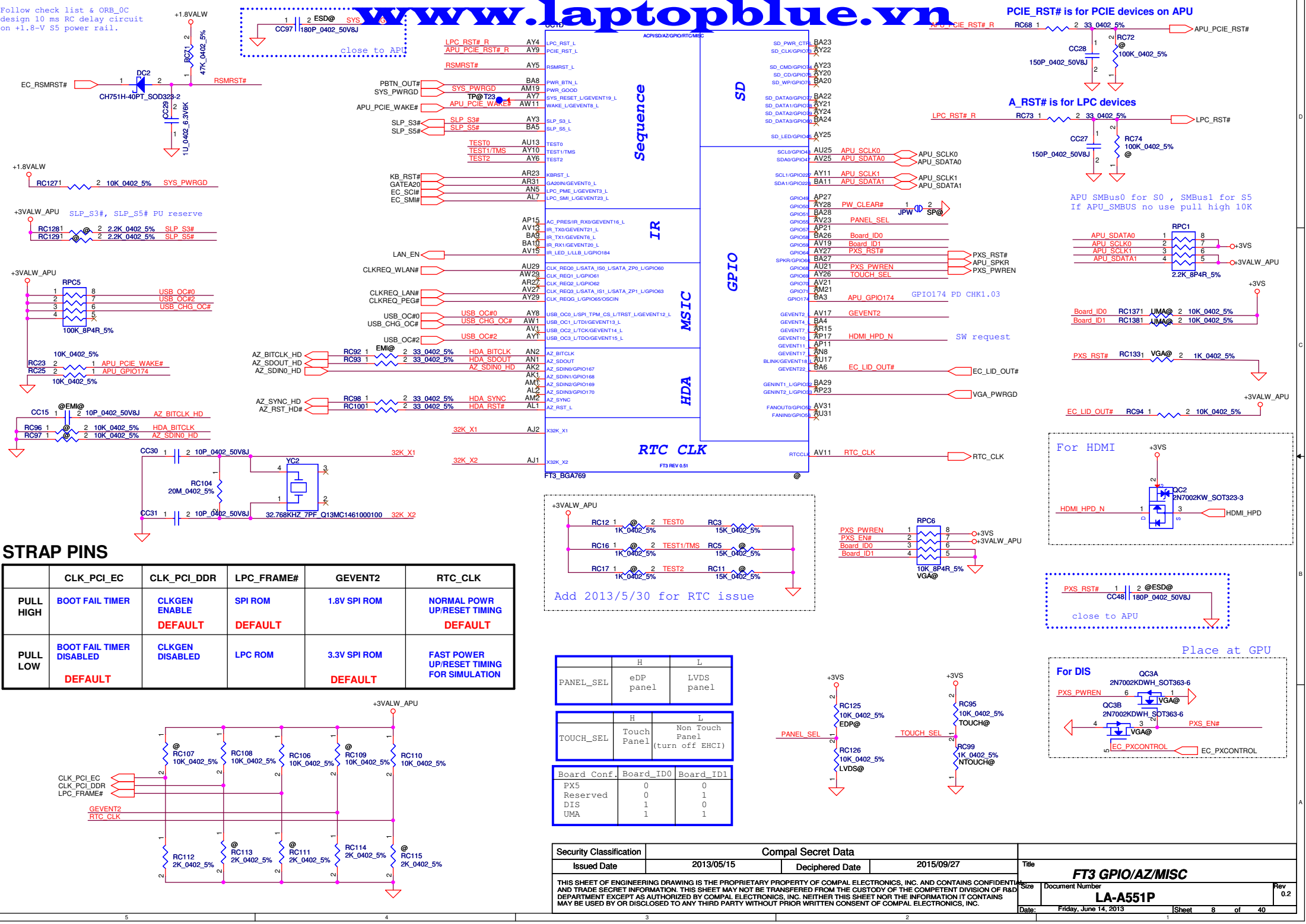
TEST25_L RC35 1 2 510 0402 1%
 TEST36 RC37 1 2 1K 0402 5%
 TEST37 RC39 1 2 1K 0402 5%
 DP STEREO SYNC RC36 1 2 1K 0402 5%
 TEST36 RC41 1 2 1K 0402 5%
 TEST37 RC46 1 2 1K 0402 5%
 TEST25_H RC43 1 2 510 0402 1%

APU TMS 1 8
 APU TCK 2 7
 TEST19 3 6
 TEST18 4 5
 1K_6P4R_5%

NOTE: DP_STEREO SYNC
 Used to align shutter glasses with the interleaved video frame
 INTERNAL(HDMI enable), DP_STEREO SYNC &
 APU_HSYNC PD FOR CUSTOMER(HDMI disable)

Security Classification	Compal Secret Data			Title	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Rev	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-A551P	0.2
				Date	Sheet
				Friday, June 14, 2013	6 of 40

Follow check list & ORB_OC design 10 ms RC delay circuit on +1.8-V S5 power rail.



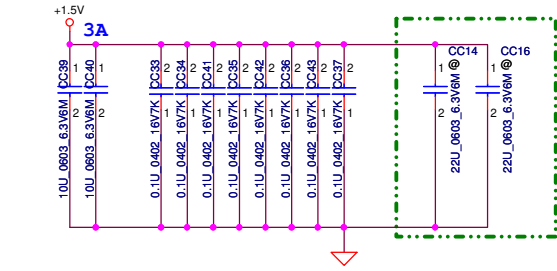
STRAP PINS

	CLK_PCI_EC	CLK_PCI_DDR	LPC_FRAME#	GEVENT2	RTC_CLK
PULL HIGH	BOOT FAIL TIMER	CLKGEN ENABLE	SPI ROM	1.8V SPI ROM	NORMAL POWER UP/RESET TIMING
		DEFAULT	DEFAULT		DEFAULT
PULL LOW	BOOT FAIL TIMER DISABLED	CLKGEN DISABLED	LPC ROM	3.3V SPI ROM	FAST POWER UP/RESET TIMING FOR SIMULATION
	DEFAULT			DEFAULT	

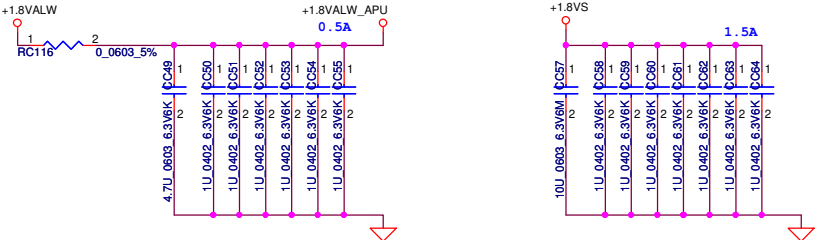
PANEL_SEL	H	L
	eDP panel	LVDS panel
TOUCH_SEL	H	L
	Touch Panel	Non Touch Panel (turn off EHCI)
Board Conf.	Board_ID0	Board_ID1
PX5	0	0
Reserved	0	1
DIS	1	0
UMA	1	1

Security Classification	Compal Secret Data			Title	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Size	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-A551P	0.2
				Date: Friday, June 14, 2013	Sheet 8 of 40

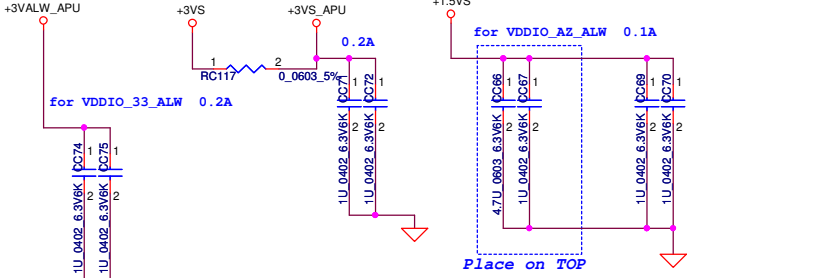
1.5V OF APU



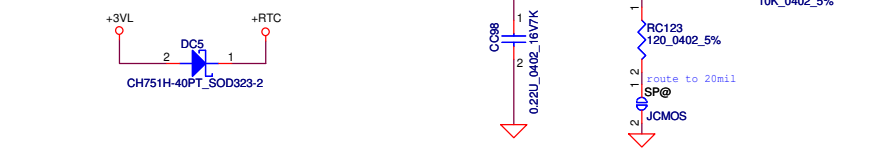
1.8VALW & 1.8VS OF APU



3.3VALW & 3.3VS OF APU

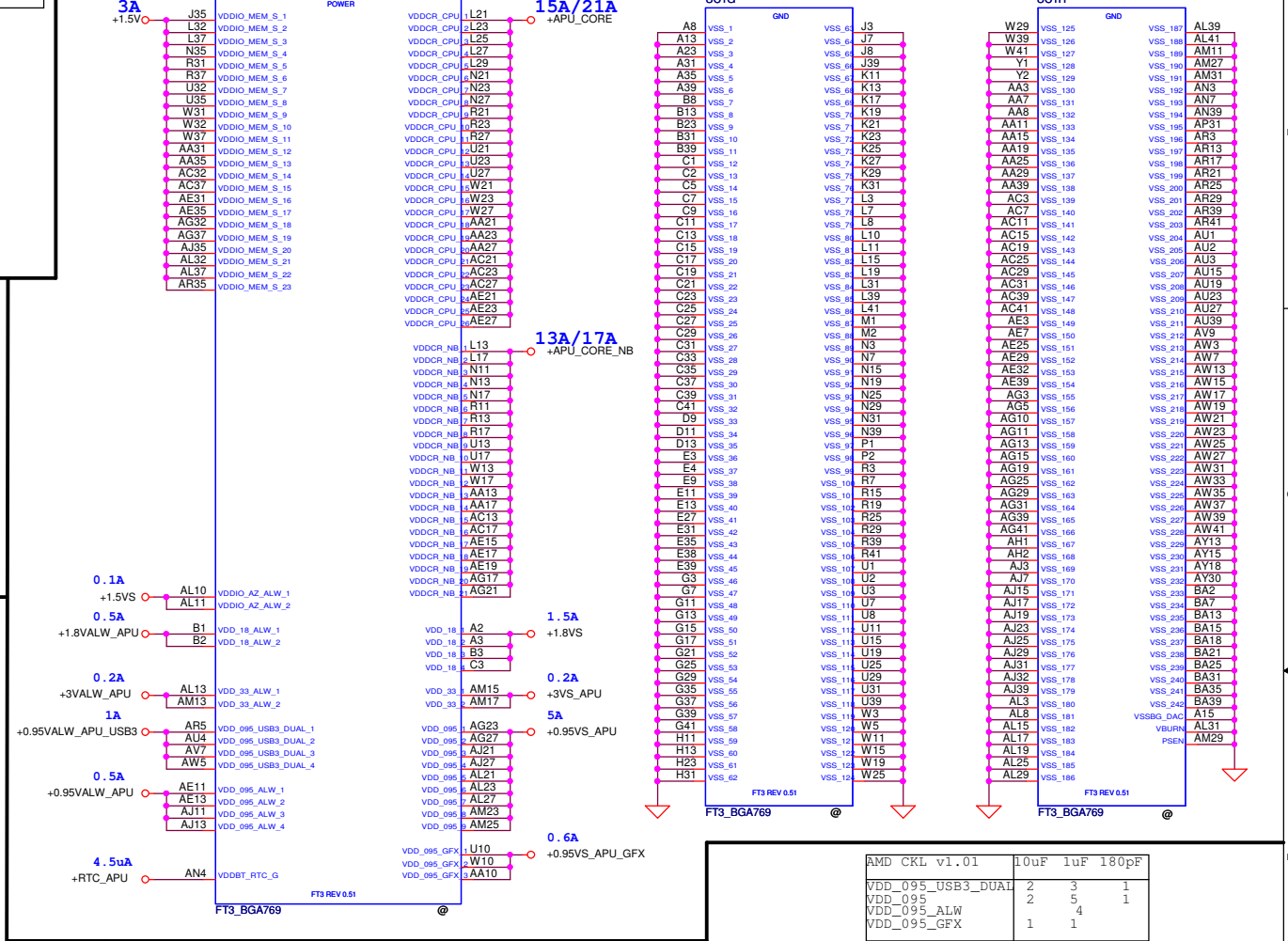


RTC OF APU

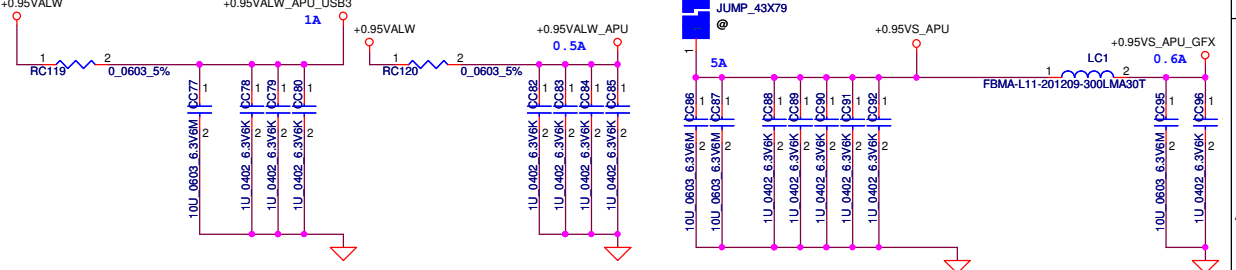


AMD CKL v1.01	10uF	1uF	180pF
VDDIO_MEM_S	2	8	4
VDDIO_MEM_S	2	8	4
VDDIO_MEM_S	2	8	4

www.laptopblue.vn



0.95VALW & 0.95VS OF APU

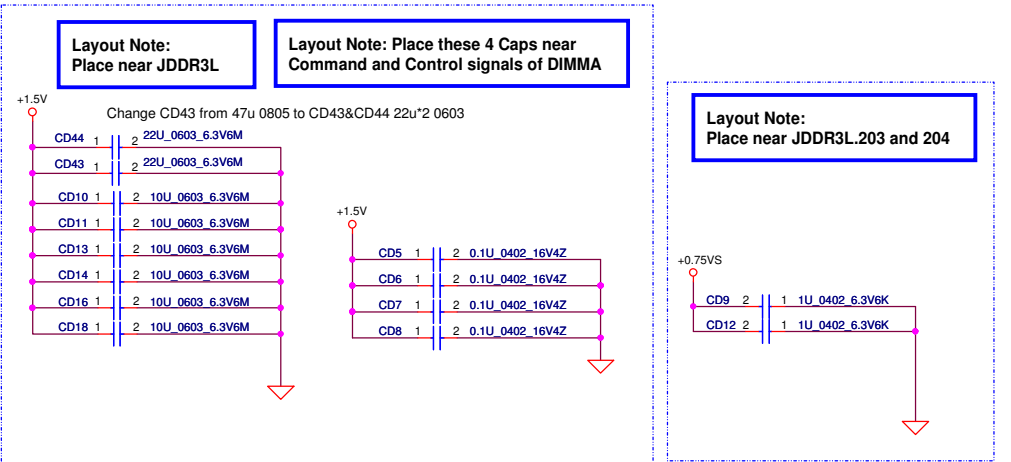
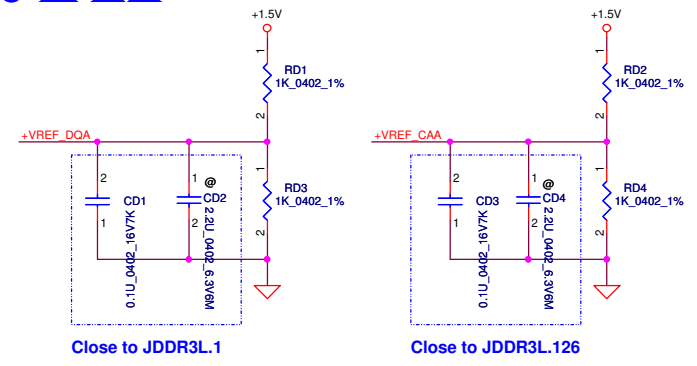
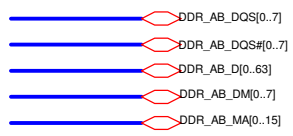
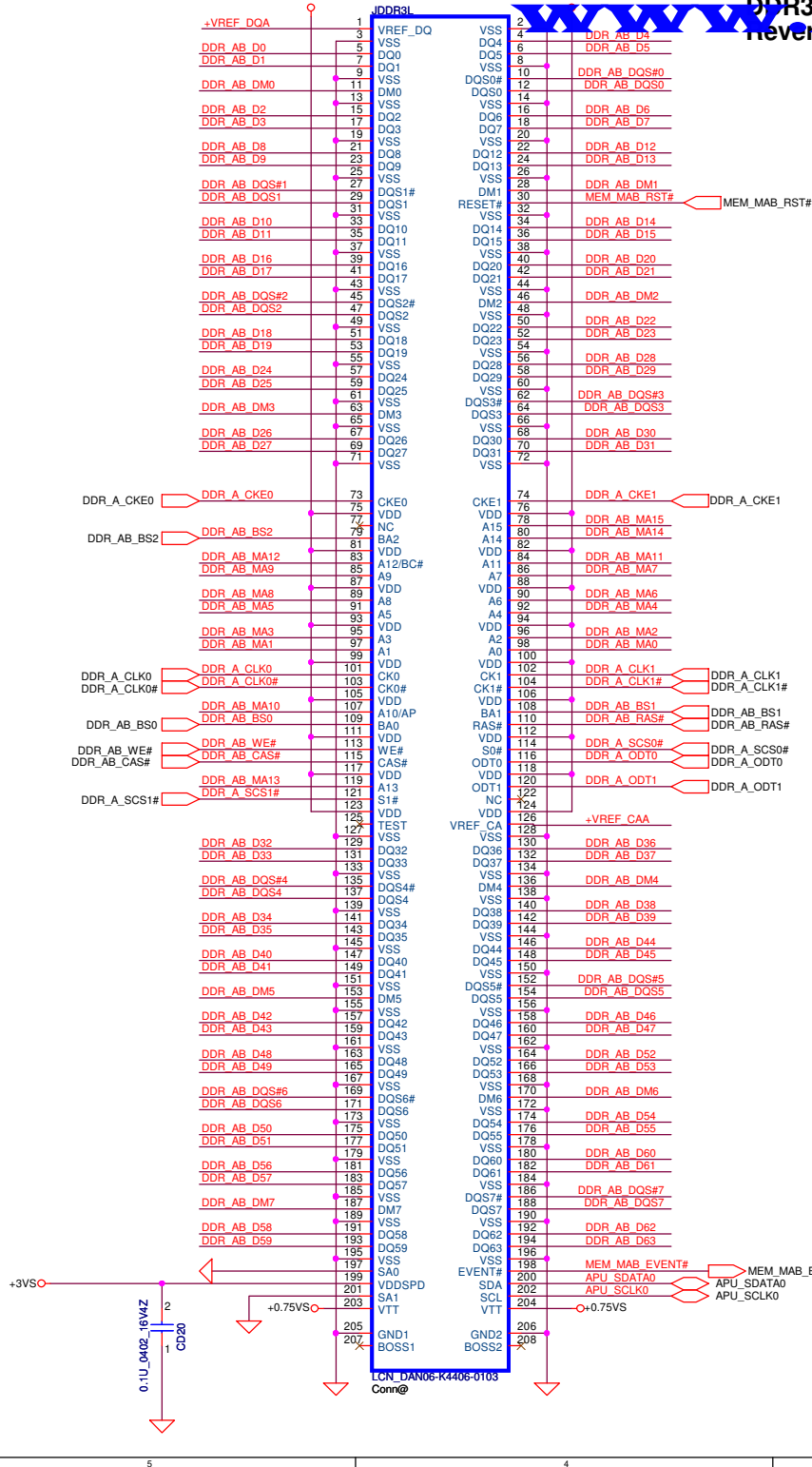


Security Classification	Compal Secret Data			Title	FT3 PWR/GND	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Size	Document Number	Rev
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				LA-A551P		
Date				Friday, June 14, 2013	Sheet	9 of 40

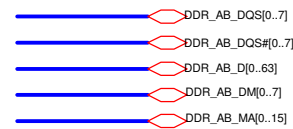
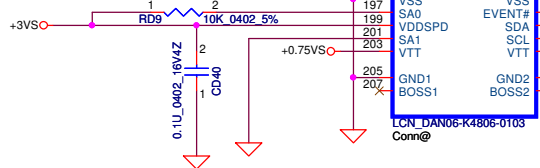
DDR3 SO-DIMM A

Reverse Type

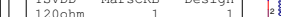
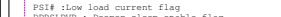
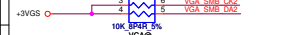
SO-DIMM VREF



Security Classification		Compal Secret Data		Compal Electronics, Inc.							
Issued Date		2013/05/15		Deciphered Date		2015/09/27		Title			
								DDR3II-SODIMMA			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size		Document Number		Rev			
						LA-A551P		0.2			
				Date		Friday, June 14, 2013		Sheet		10 of 40	



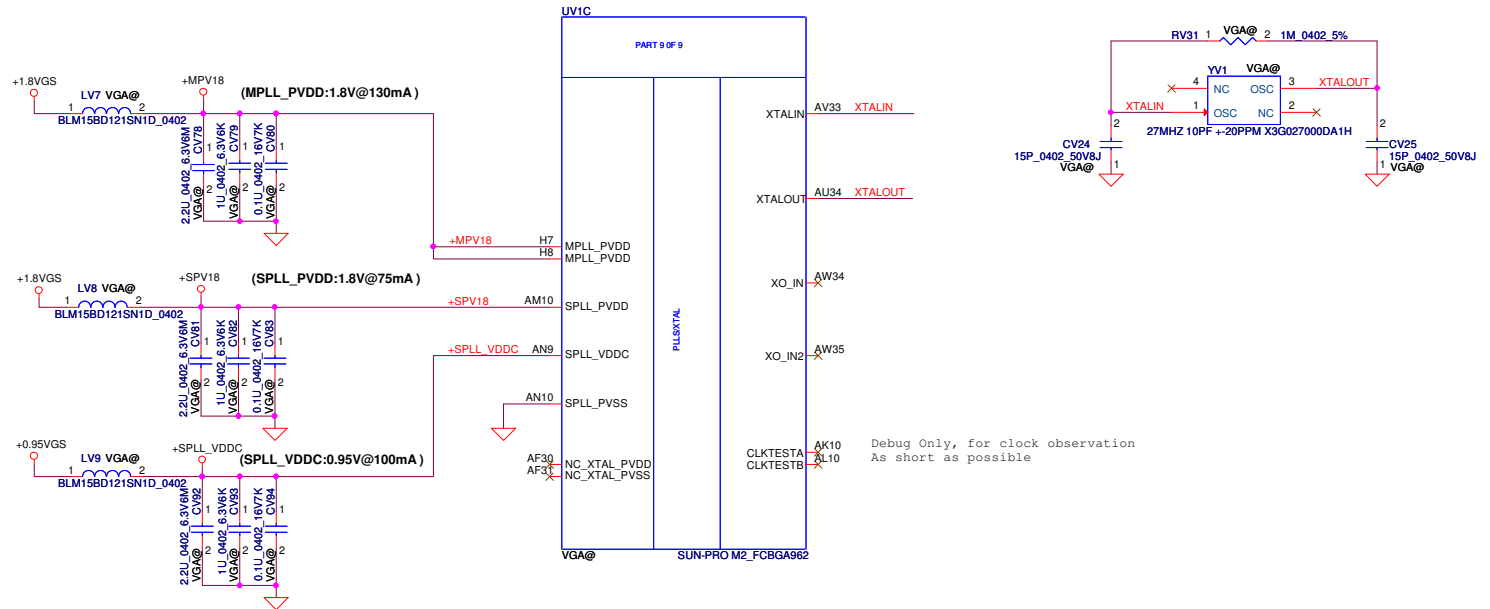
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.



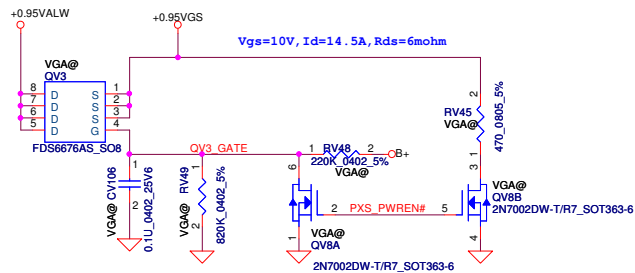
MPLL_PVDD	MarsCRB	Design
220ohm	1	1
0.1u	1	1
1u	1	1
2.2u	1	1

SPLL_PVDD	MarsCRB	Design
120ohm	1	1
0.1u	1	1
1u	1	1
2.2u	1	1

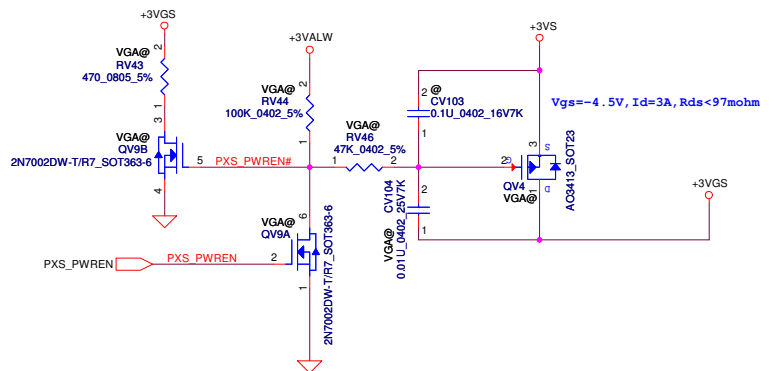
SPLL_VDDC	MarsCRB	Design
120ohm	1	1
0.1u	1	1
1u	1	1
2.2u	1	1



+0.95VS to +0.95VGS



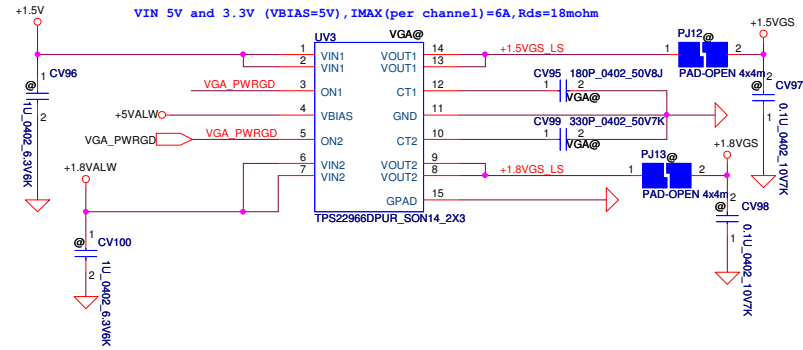
+3VS to +3VGS



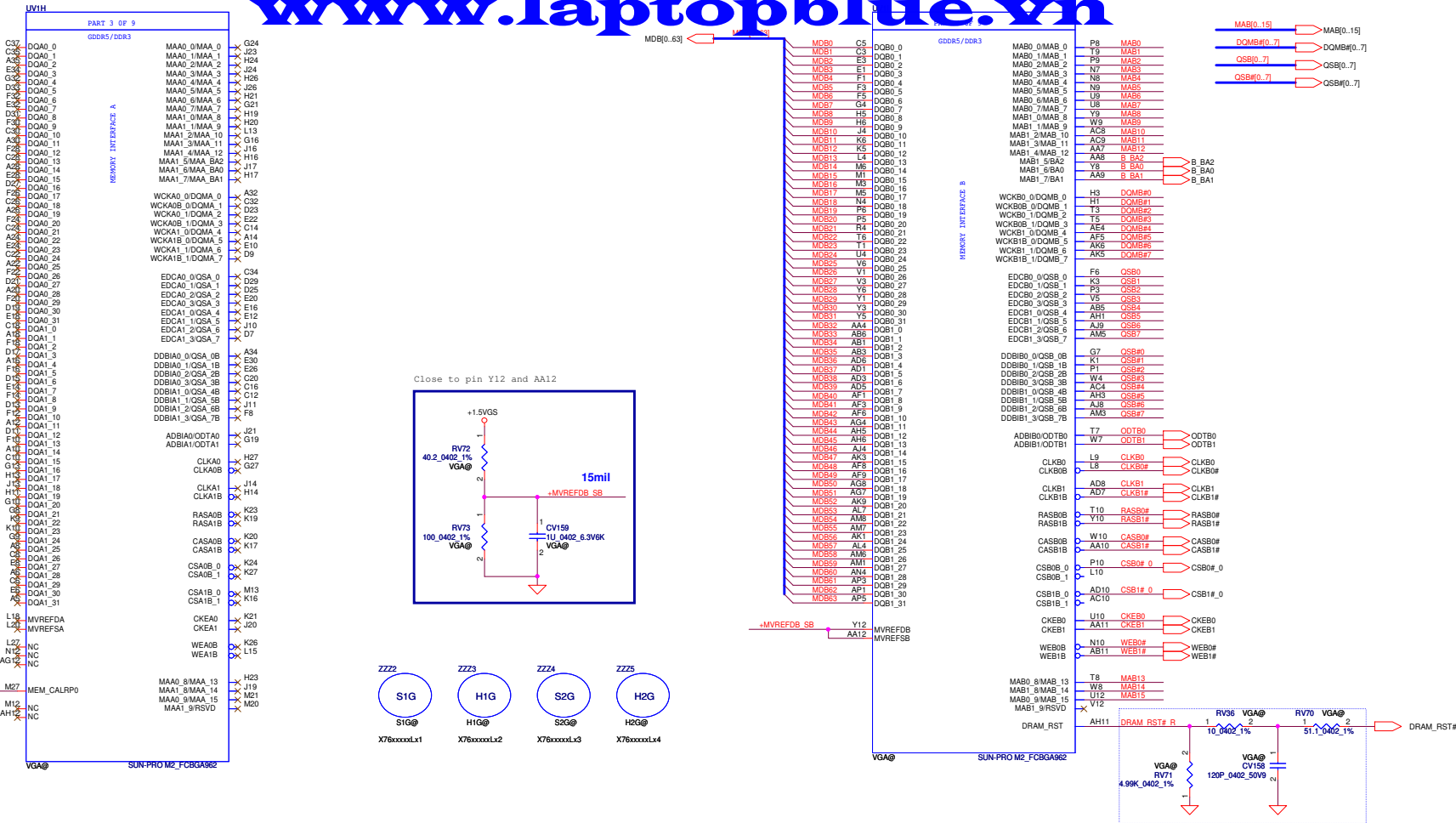
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	BACO POWER	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF PRODUCT DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	LA-A551P	Rev 0.2
				Date	Friday, June 14, 2013	Sheet 14 of 40

Only for Kabini

+1.8VALW to +1.8VGS
+1.5V to +1.5VGS

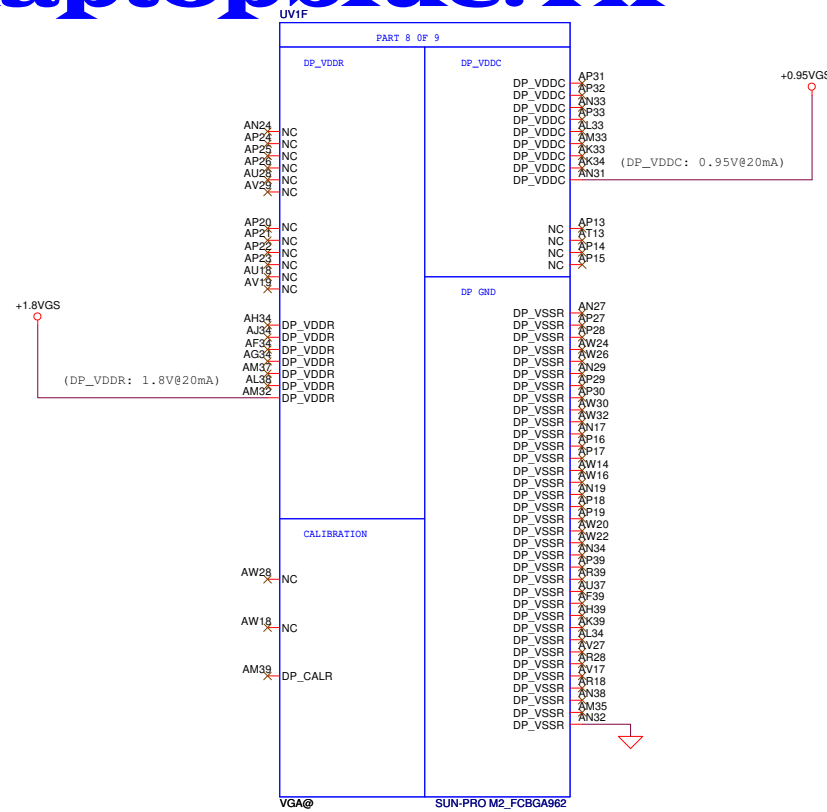
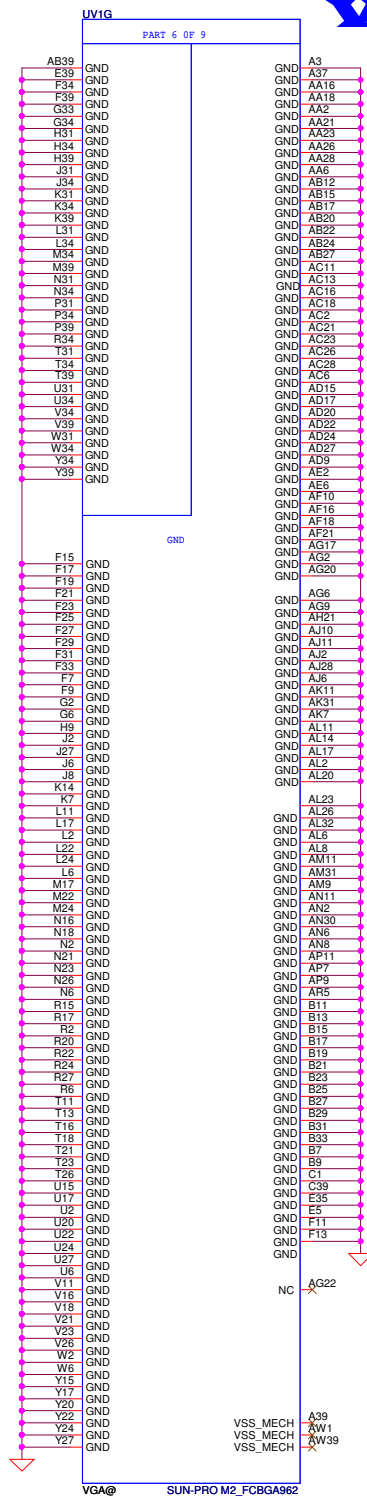


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	BACO POWER
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF FIRST DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	LA-A551P
				Date	Friday, June 14, 2013
				Sheet	15 of 40
				Rev	0.2

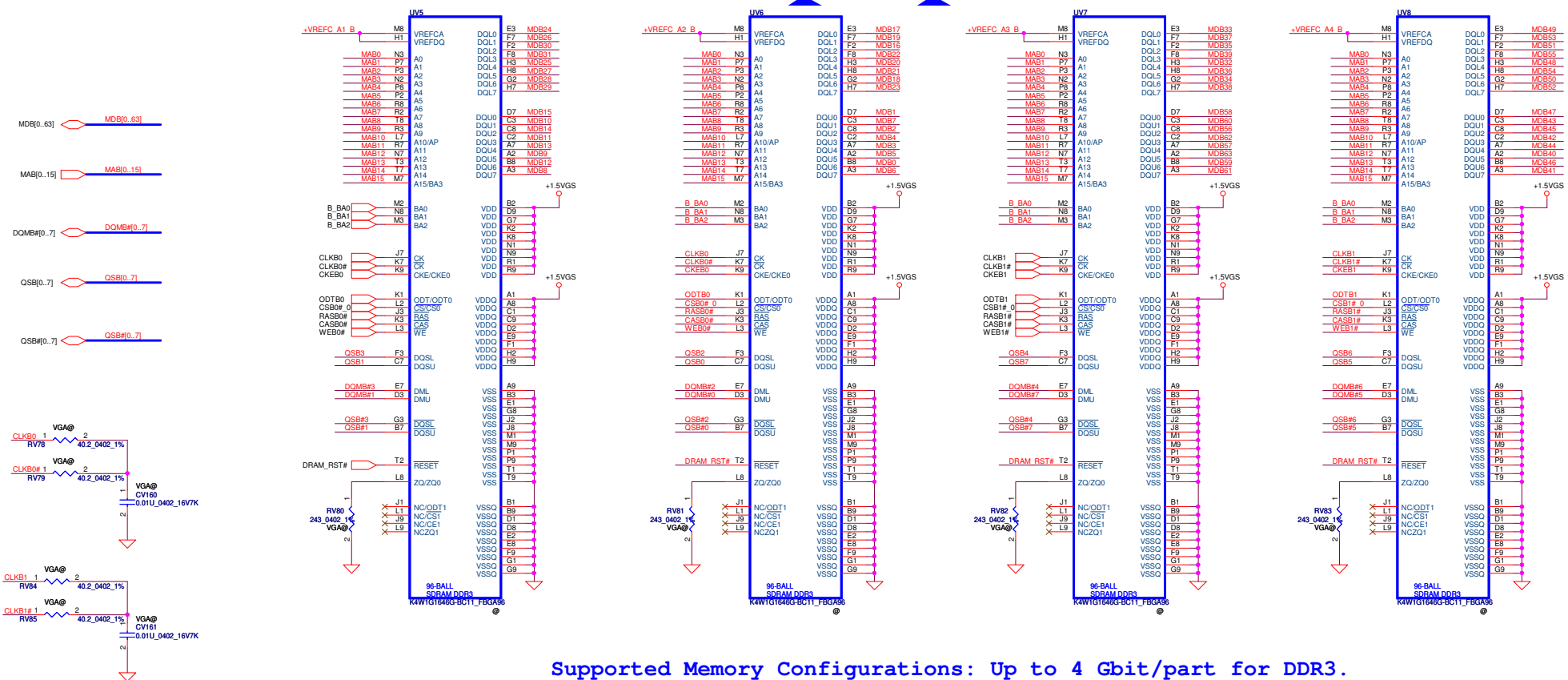


Memory clock 900MHz

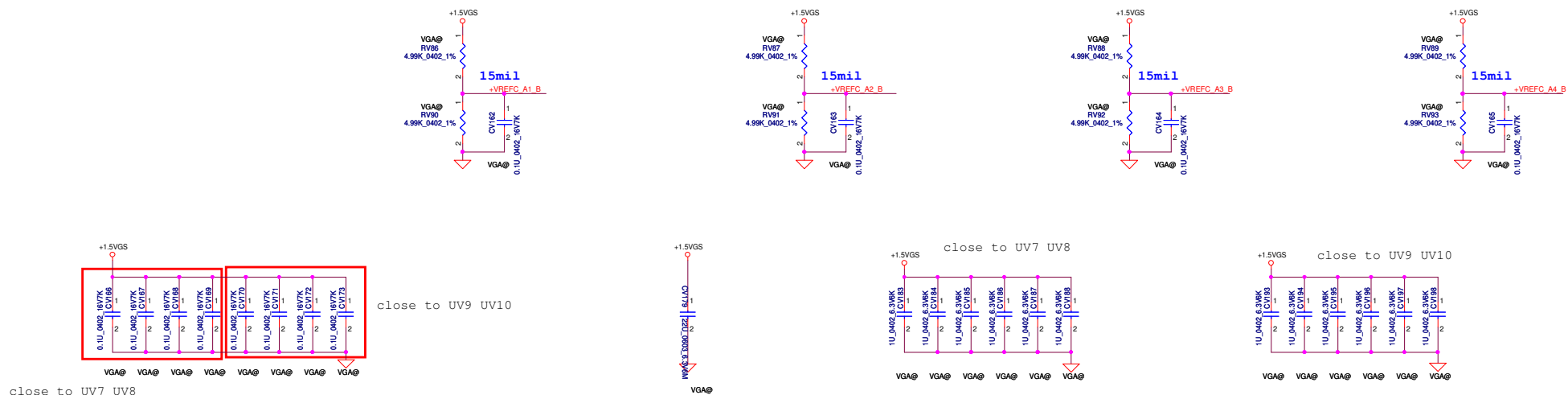
GPU Type	Memory Bus Width	VRAM Vendor	Compal P/N	Manufacturer P/N	X76 P/N	Size per part	Configuration	Total Memory Size/Qty	PS_3[3]	PS_3[2]	PS_3[1]	R_pu	R_pd
SUN PRO-M2	64bit	Samsung	SA000068U20	K4W2G1646E-BC1A		2Gbit	128M*16	1GB/4pcs	0	0	0	RV20	RV27
SUN PRO-M2	64bit	Micron	SA00005XB00	MT41K128M16JT-107G:K		2Gbit	128M*16	1GB/4pcs	0	0	1	RV20	RV27
SUN PRO-M2	64bit	Samsung	SA00005SH00	K4W2G1646E-BC11		2Gbit	128M*16	1GB/4pcs	0	1	0	RV20	RV27
SUN PRO-M3	64bit	Micron	SA000065D30	MT41K256M16HA-107G:E		4Gbit	128M*16	2GB/4pcs	1	1	0	RV20	RV27
SUN PRO-M4	64bit	Samsung	SA000068R20	K4W4G1646B-HC11		4Gbit	128M*16	2GB/4pcs	1	1	1	RV20	RV27



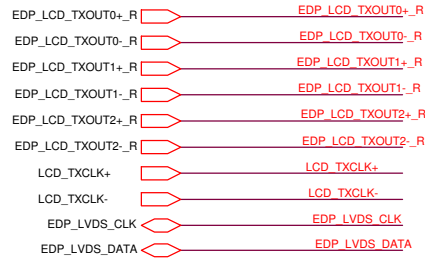
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	PWR_GND
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT TO ANY OTHER DIVISION OF COMPAL ELECTRONICS, INC. WITHOUT THE WRITTEN CONSENT OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				Custom	0.2
Date: Friday, June 14, 2013				Sheet	18 of 40



Supported Memory Configurations: Up to 4 Gbit/part for DDR3.



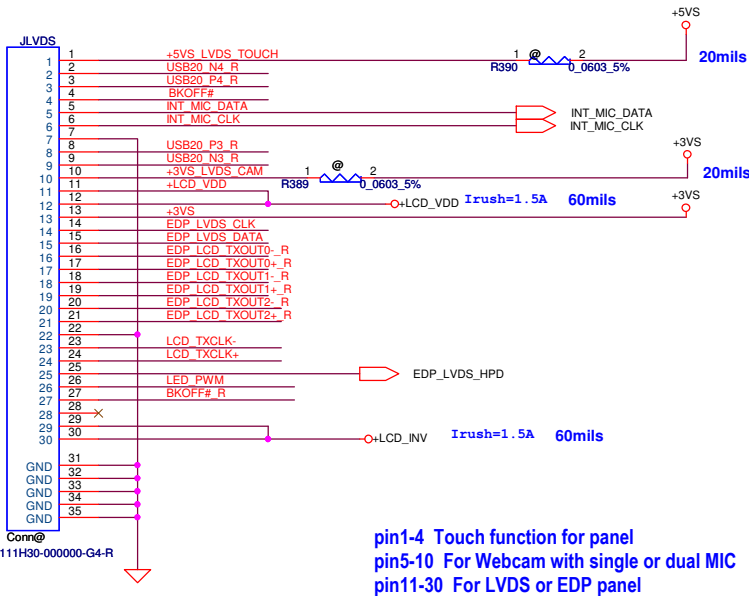
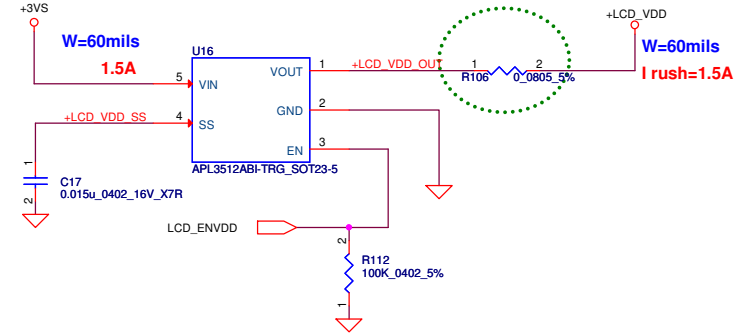
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	VRAM Channel B
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size C	Document Number
					LA-A551P
				Date	Friday, June 14, 2013
				Sheet	19 of 40



If it's EPD, they're become
 LCD_TXOUT2+_R = EDP_TX0+
 LCD_TXOUT2-_R = EDP_TX0-
 LCD_TXOUT1+_R = EDP_TX1+
 LCD_TXOUT1-_R = EDP_TX1-
 LVDS_CLK = EDP_AUXP
 LVDS_DATA = EDP_AUXN

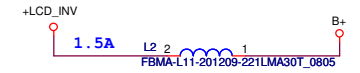
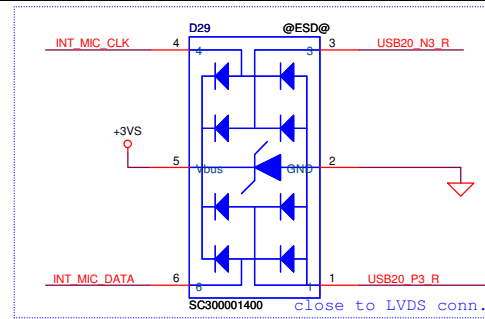
Need check eDP&LVDS both 3V power rail.

Reserve for power consumption
 Remove on PVT phase

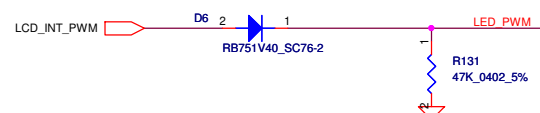


pin1-4 Touch function for panel
 pin5-10 For Webcam with single or dual MIC
 pin11-30 For LVDS or EDP panel

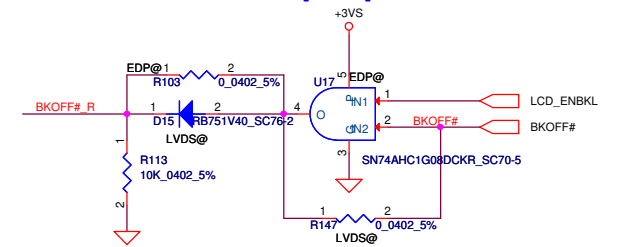
LCD_INV



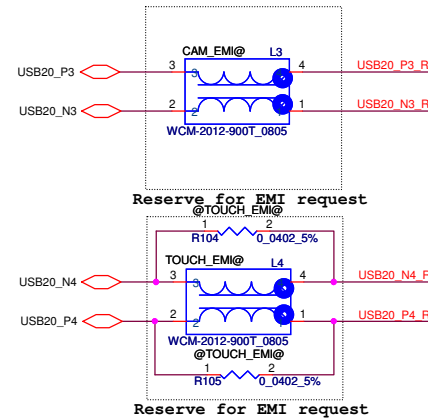
LCD Control



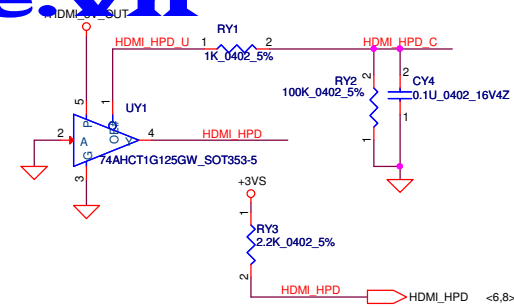
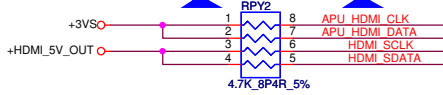
Reserve for eDP panel potential issue



Camera & Touch Screen

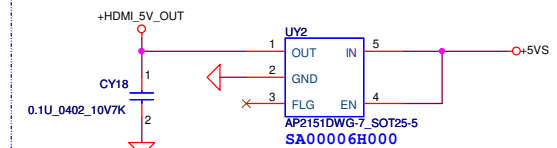


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	LVDS/EDP W/ CAMERA	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
					LA-A551P	0.2
				Date:	Friday, June 14, 2013	Sheet 20 of 40



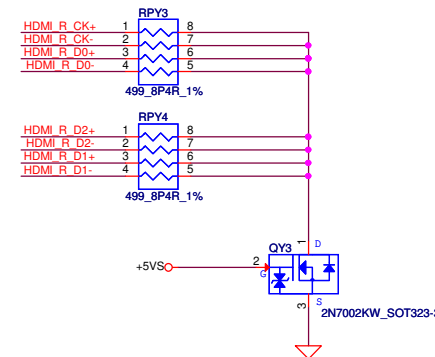
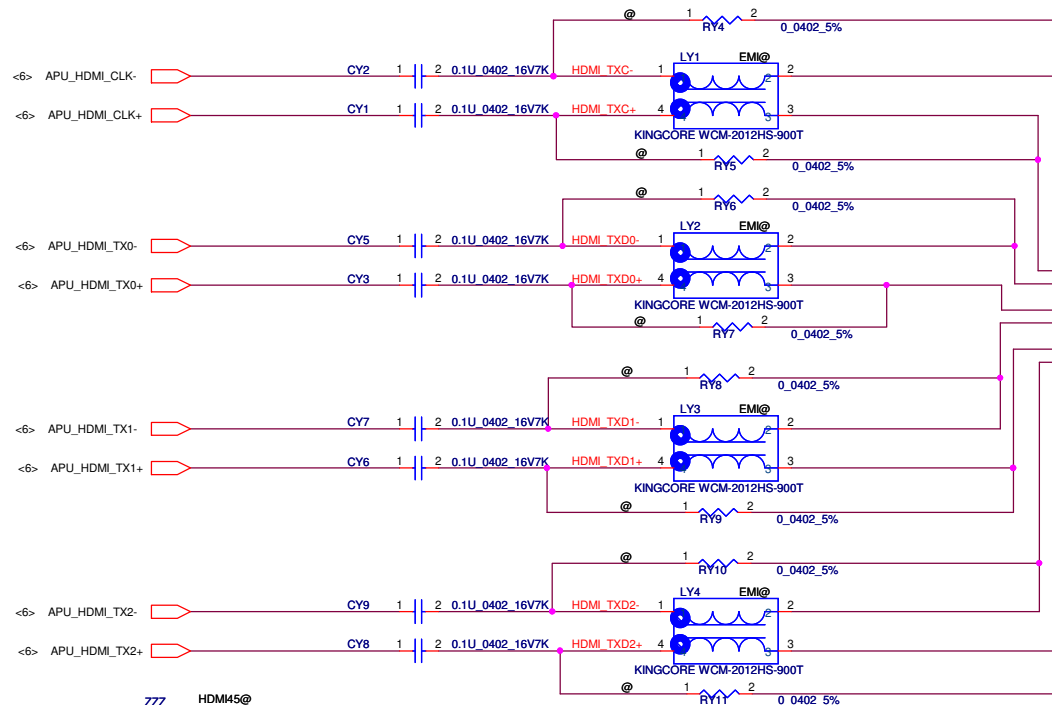
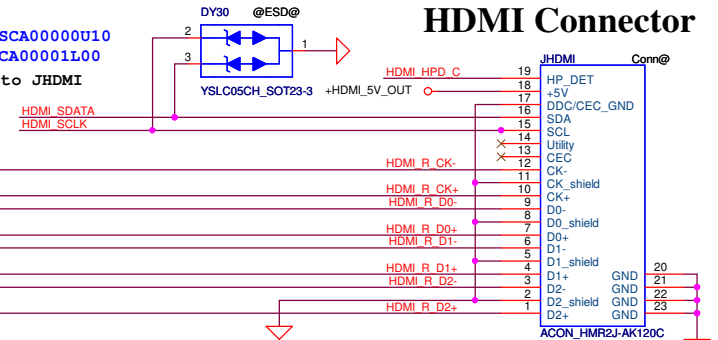
HDMI POWER CIRCUIT

VIN = 5V, IOUT = 0.5A, RDS(ON) TYP=95m ; MAX=115m
Current Limit: TYP=0.8A ; MAX=1A



HDMI Connector

Main: SCA00000U10
2nd: SCA00001L00
Close to JHDMI



HDMI Royalty
HDMI45@
RO0000003HM

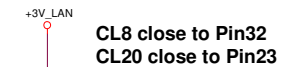
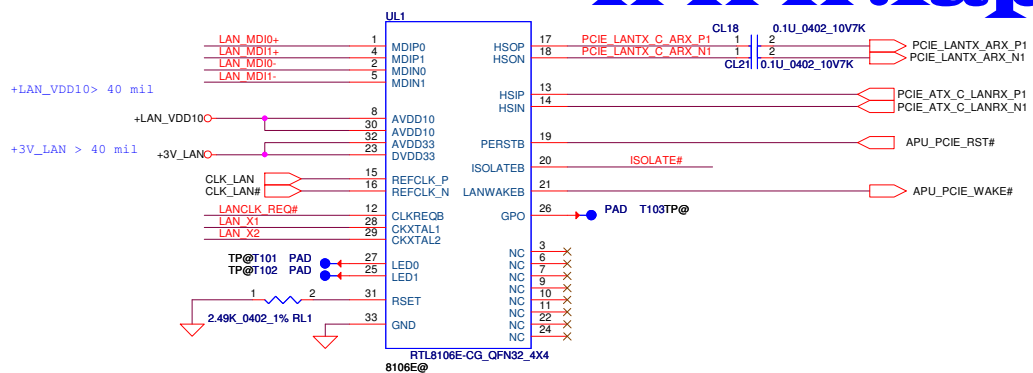
HDMI W/O Logo: RO0000001HM
HDMI W/Logo: RO0000002HM
HDMI W/Logo + HDCP: RO0000003HM

please manually load
this virtual material to 45@ BOM

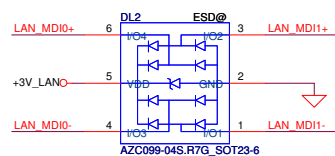
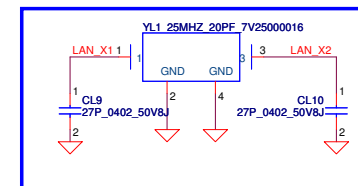
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2013/05/15		Deciphered Date		2015/09/27		Title	
										HDMI W/O CEC	
										LA-A551P	
										Date: Friday, June 14, 2013	
										Sheet 21 of 40	

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

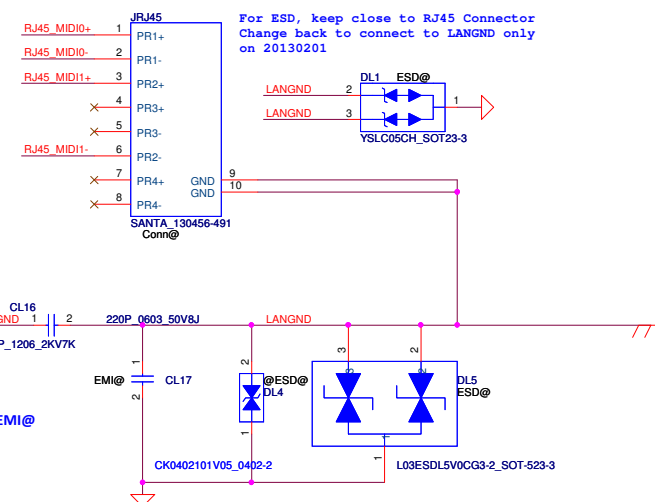
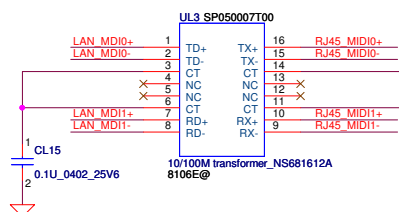
Size Document Number Rev 0.2
Date: Friday, June 14, 2013 Sheet 21 of 40



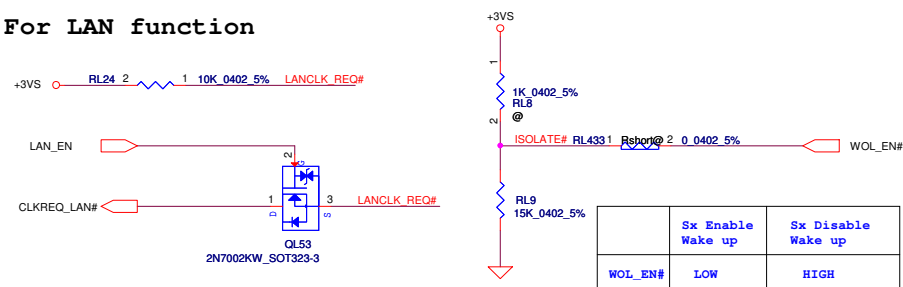
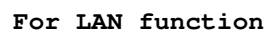
Keep de-coupling capacitors close to
RTL8106E within 200 mil



```
main: SP050007T00
2nd:  SP050007K00
3nd:  SP050006H00
```



2013/5/3 EMI request change CL17 BOM structure to EMI@
2013/6/7 ESD request reserve DL14
2013/6/7 ESD request add DLS

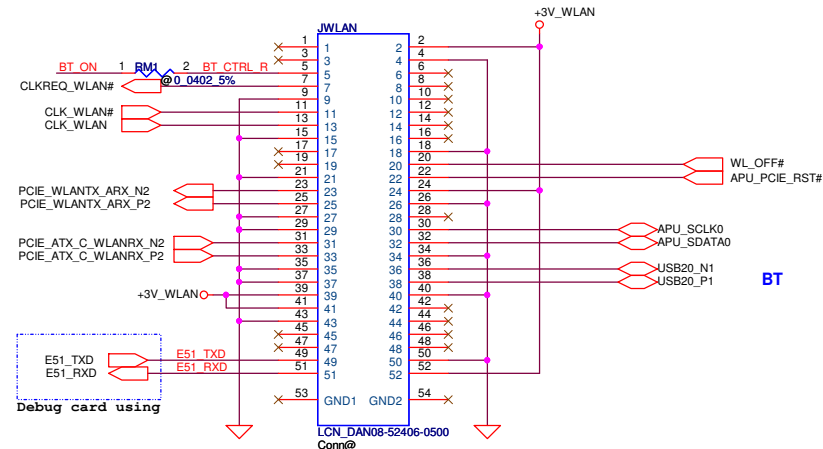
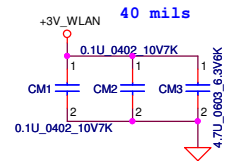


LAN	WOL	LAN_EN		ISOLATED	
		S0	Sx	S0	Sx
0	0	0	0	1	1
0	1	0	0	1	1
1	0	1	1	1	1
1	1	1	1	1	0*

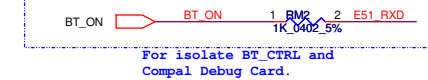
```
*
S3:  after SUSP# assert low over 100ms
S4/S5: after SYSON assert low over 100ms
```

+3V LAN rising time (10%~90%) need > 1ms and <100ms.

Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date		2013/05/15	Deciphered Date		2015/09/27	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF THE DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				PCle-LAN-RTL8106E		
				Document Number		
				LA-A551P		
Date:				Friday, June 14, 2013	Sheet 22 of 40	Rev 0.2



From EC



ACES_50208-00801-003

10
9
8
7
6
5
4
3
2
1

GND
GND

+5VS

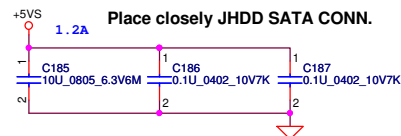
SATA ATX C DRX P0
SATA ATX C DRX N0
SATA DTX ARX N0
SATA DTX ARX P0

C194 1 2 0.01U 0402 25V7K
C195 1 2 0.01U 0402 25V7K
C196 1 2 0.01U 0402 25V7K
C198 1 2 0.01U 0402 25V7K

SATA_ATX_DRX_P0
SATA_ATX_DRX_N0
SATA_DTX_C_ARX_N0
SATA_DTX_C_ARX_P0

Conn@JHDD

Close to JHDD

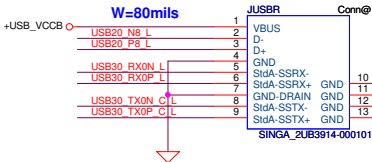
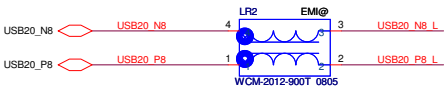
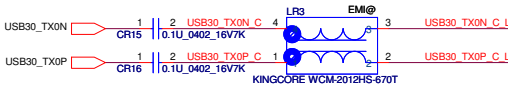
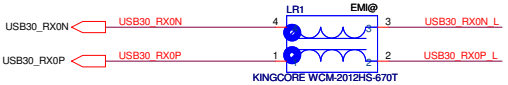


USB Sleep & Charge

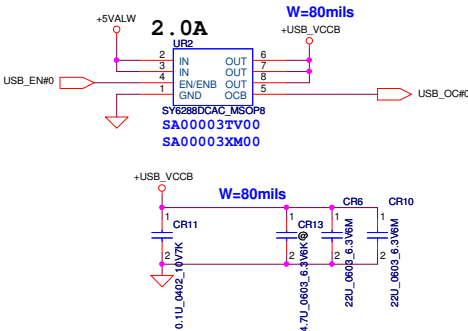
State table for TPS2546RTER

CB0	CB1	CB2	ILIM_SEL	Mode	STATUS
0	1	1	1	Auto/Alternate	Auto-detection charger mode for Apple device(2A,1A). Resistor dividers are connected to DP/DM. Including DCP
1	1	1	0	SDP	USB pass-through mode.DP/DM are connected to TDP/TDM
1	1	1	1	CDP	USB pass-through mode with CDP emulation. DP/DM are connected to TDP/TDM

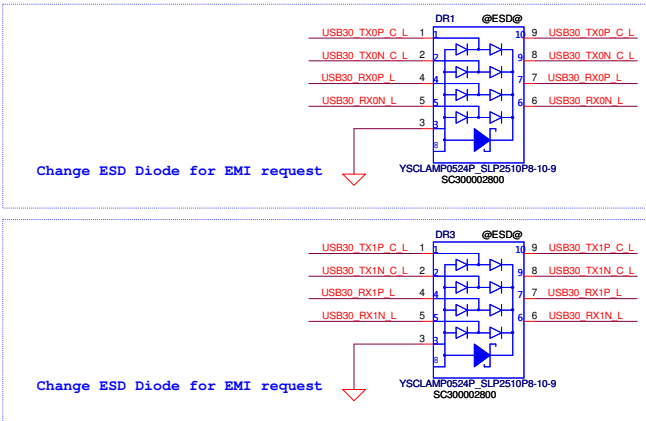
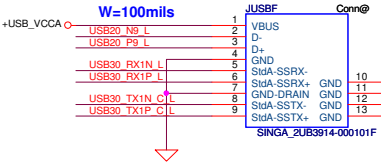
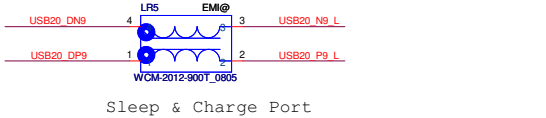
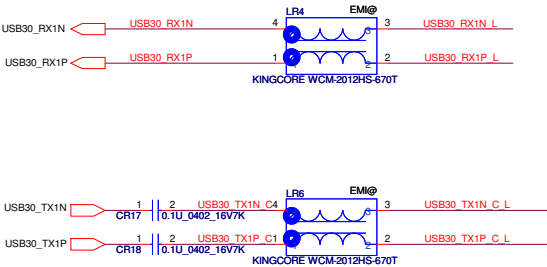
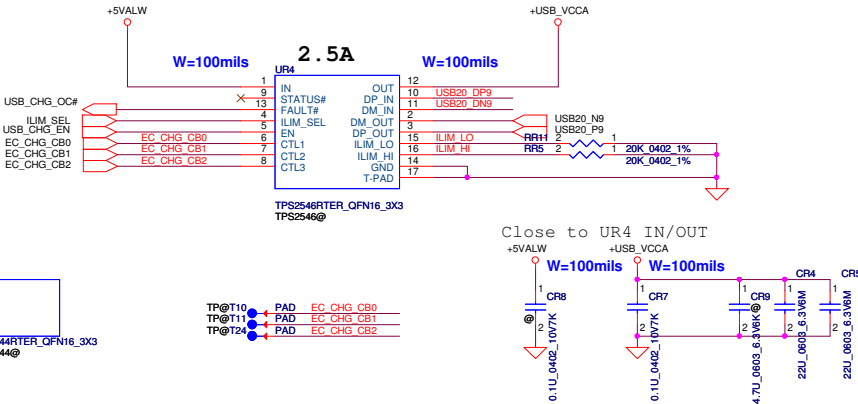
Right side USB 3.0 x 1



USB POWER SWITCH



Right side USB 3.0 x 1 W/ Sleep&Charge



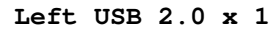


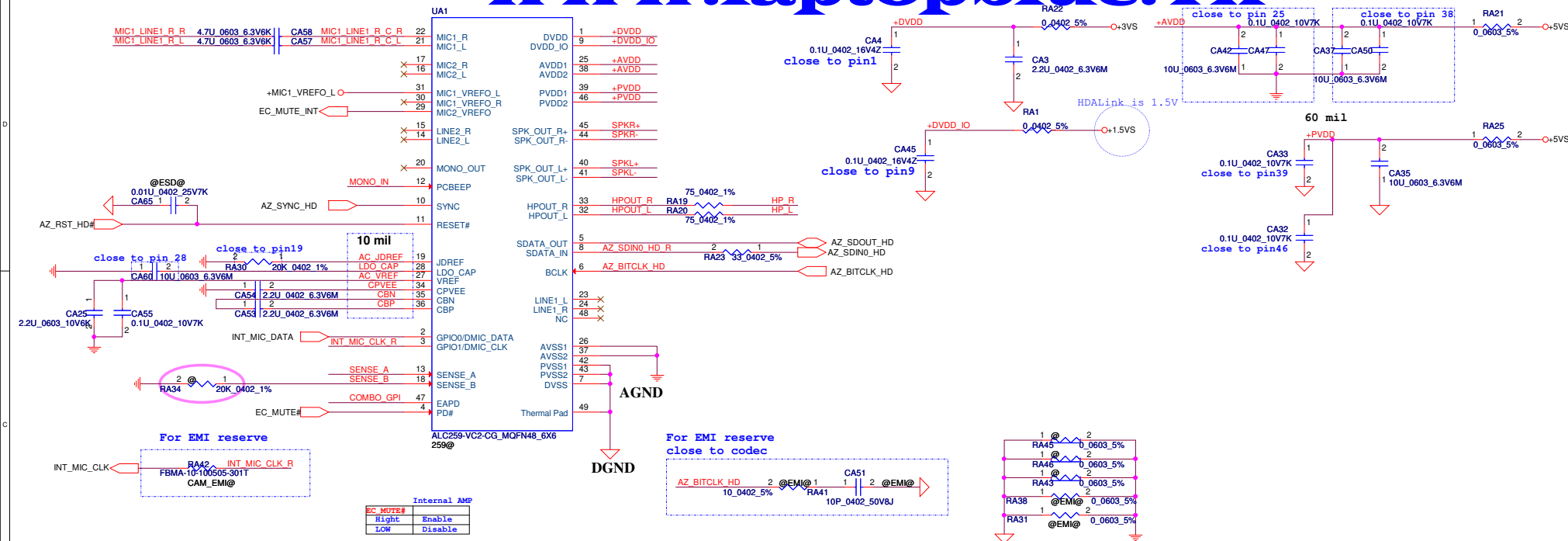
Diagram showing the pin connections for the JSPK connector:

- Pin 1: SPK_L1
- Pin 2: SPK_L2
- Pin 3: SPK_R1
- Pin 4: SPK_R2
- Pin 5: G1
- Pin 6: G2

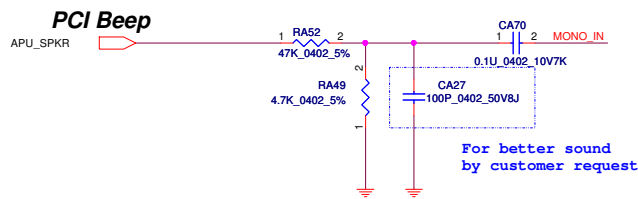
Ground symbol connected to Pin 6.

Connector: E&T_3802-F04N-01R Conn@

Security Classification		Compal Secret Data		Compal Electronics, Inc. USB-CardReader Genesys GL834L	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Document Number	
				LA-A551P	0.2
Date:	Friday, June 14, 2013	Sheet	25	of	40

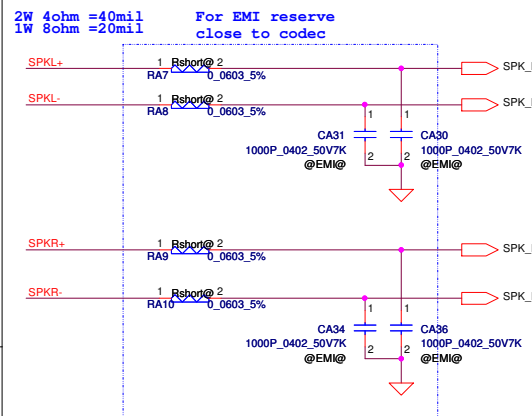


Beep sound

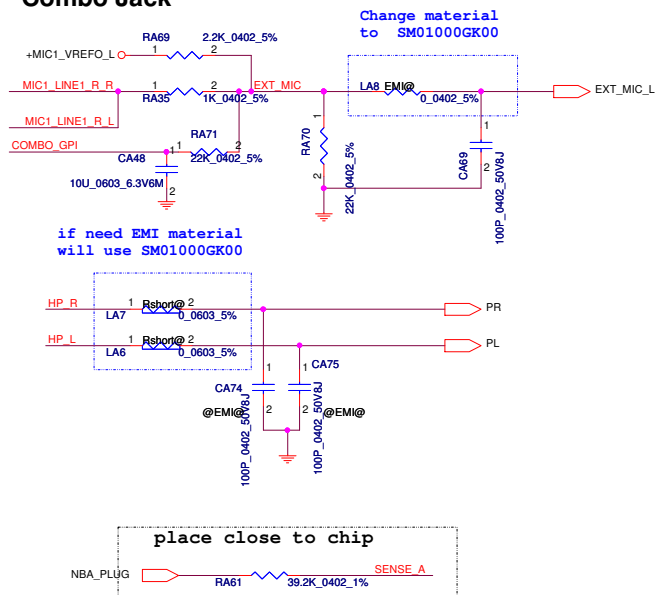


SPK

2W 4ohm =40mil
1W 8ohm =20mil



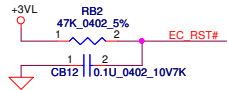
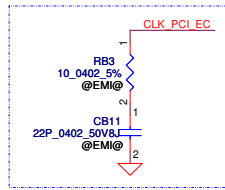
Combo Jack



Sense Pin	Impedance	Codec Signals	Function
SENSE A	39.2K	PORT-I (PIN 32, 33)	Headphone out
	20K	PORT-B (PIN 21, 22)	Ext. MIC
	10K	PORT-C (PIN 23, 24)	
	5.1K	(PIN 48)	
SENSE B	39.2K	PORT-E (PIN 14, 15)	
	20K	PORT-F (PIN 16, 17)	
	10K	PORT-H (PIN 20)	

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title	Cover Sheet
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF FACTORY DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev 0.2
				Date: Friday, June 14, 2013	Sheet 26 of 40

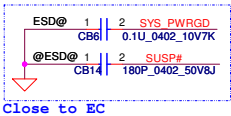
For EMI



KS[0..7] → KS[0..7]
KSQ[0..17] → KSQ[0..17]

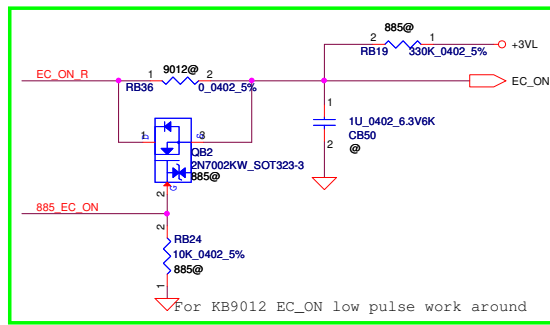
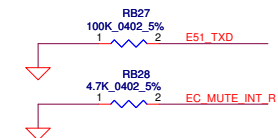
SMBUS1 → BATT, Smart Charger
SMBUS2 → G-Sensor, GPU Thermal Sensor,
APU Thermal Sensor

EC SMBus2 for S0, SMBus1 for S5



P.32_SYS_PWRGD OD/L
for 1.8V PU APU

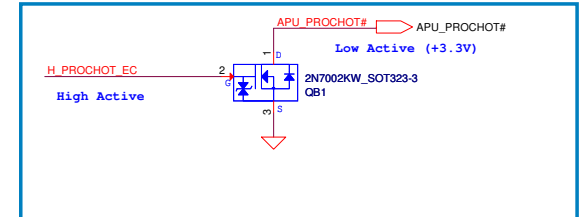
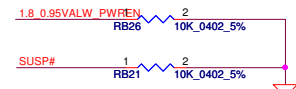
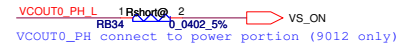
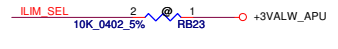
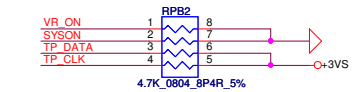
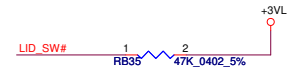
Close to EC



For KB9012 EC_ON low pulse work around

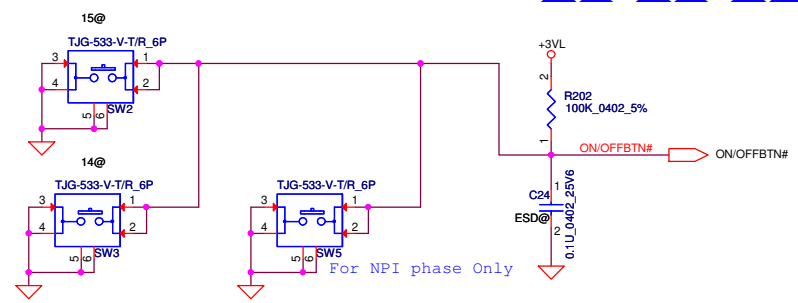
Voltage Comparator Pins FOR 9012 A3

VIN0 pin109	>1.2V	<1.2V
VOUT0 pin104	HIGH	LOW
VOUT1 pin103	LOW	HIGH



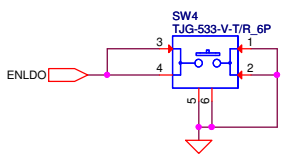
Security Classification		Compal Secret Data		Title	
Issued Date	2013/05/15	Deciphered Date	2015/09/27	LPC-EC-KB9012	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF HEADQUARTERS OR DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-A551P	0.2
				Date: Friday, June 14, 2013	Sheet 27 of 40

Power Button

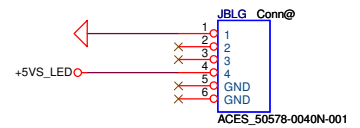
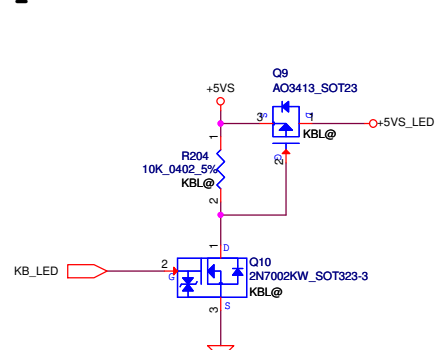


www.laptopblue.vn

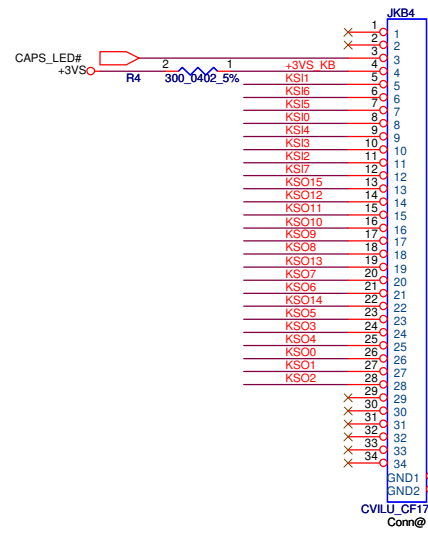
Battery Reset



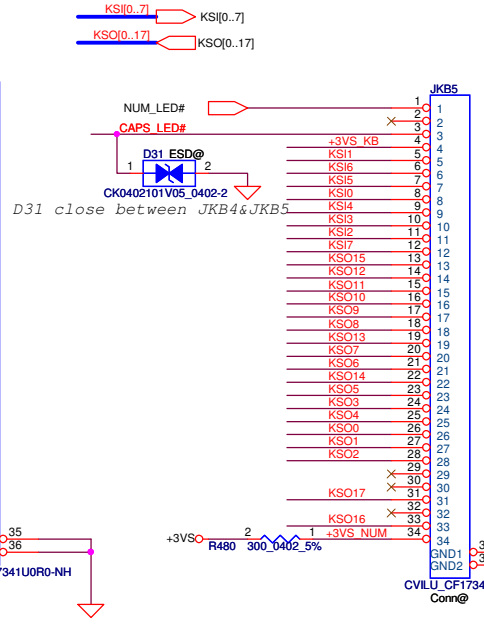
Keyboard LED



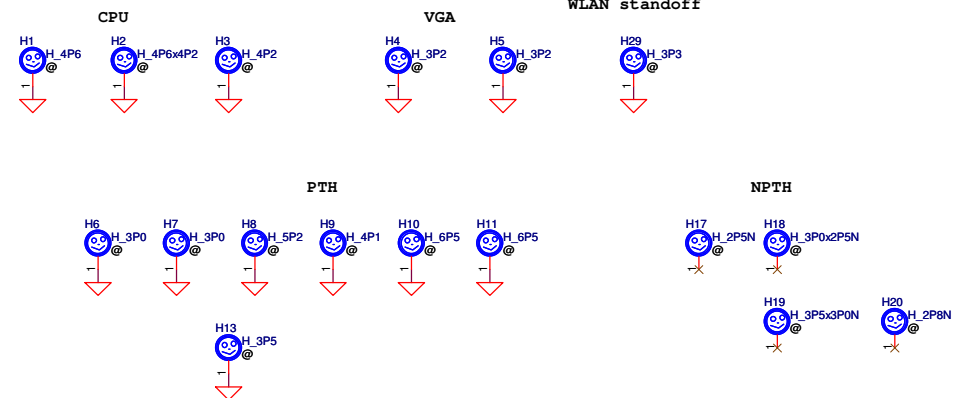
14" KEYBOARD CONN.



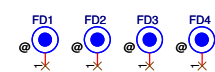
15" KEYBOARD CONN.



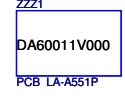
Screw Hole



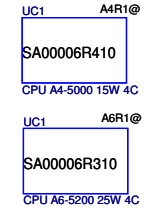
PCB Fedical Mark PAD



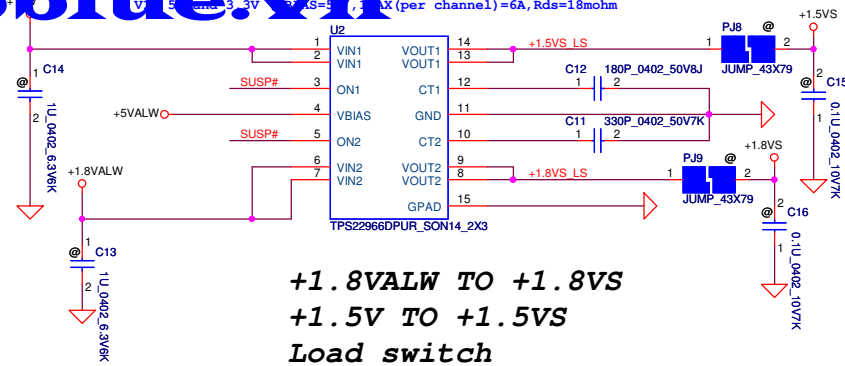
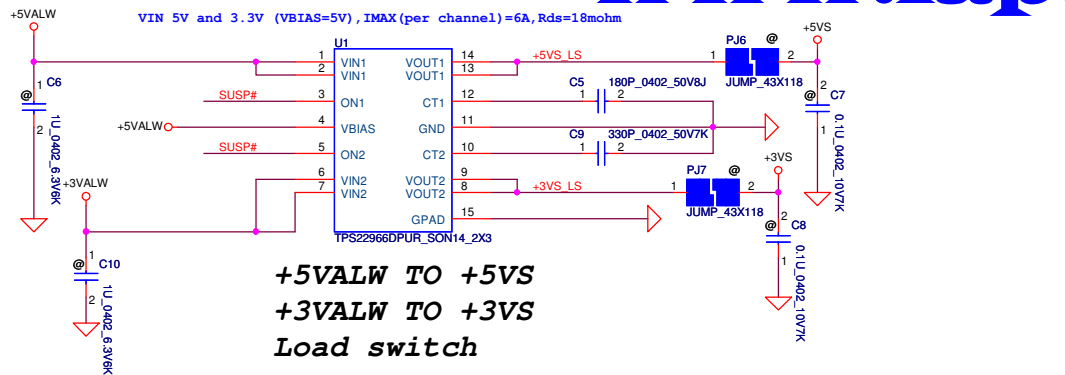
ISPD



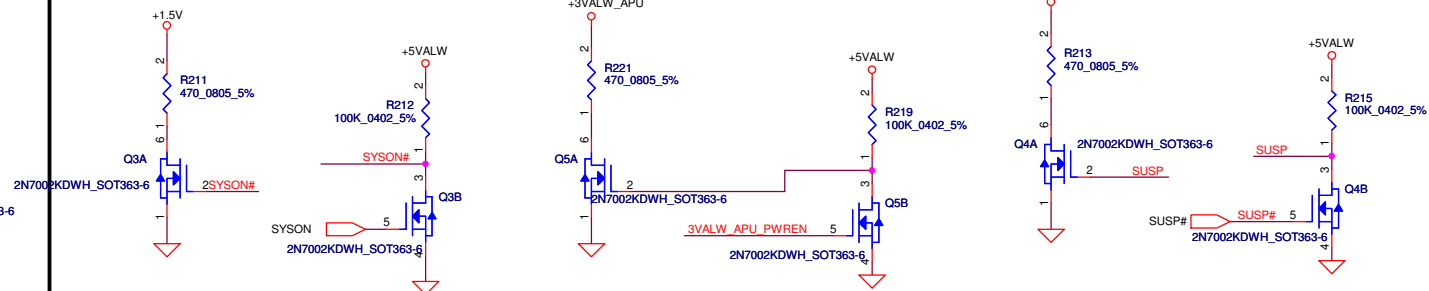
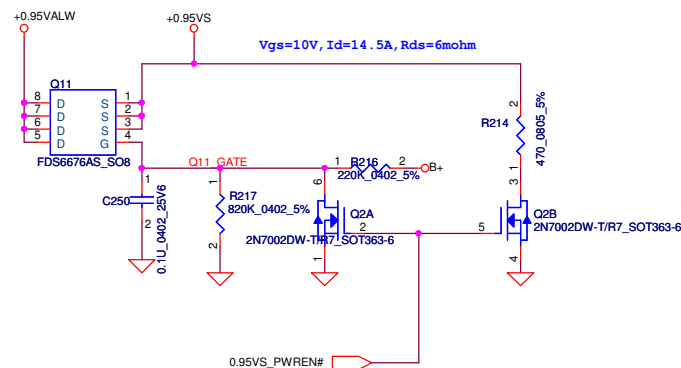
APU PR sample



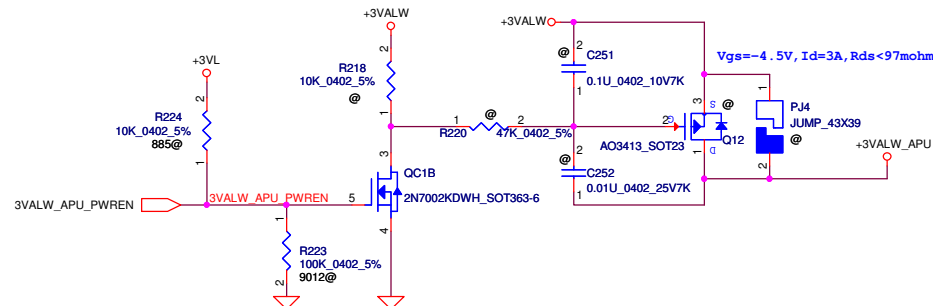
Security Classification		Compal Secret Data		Compal Electronics, Inc.															
Issued Date		2013/05/15		Deciphered Date		2015/09/27		Title											
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								KB/TP/LED/LID/DEBUG/ISPD											
								Size		Document Number		Rev							
										LA-A551P		0.2							
3		1		2		Date:		Friday, June 14, 2013		1		Sheet		28		of		40	



+0.95VALW to +0.95VS



+3VALW to +3VALW_FCH

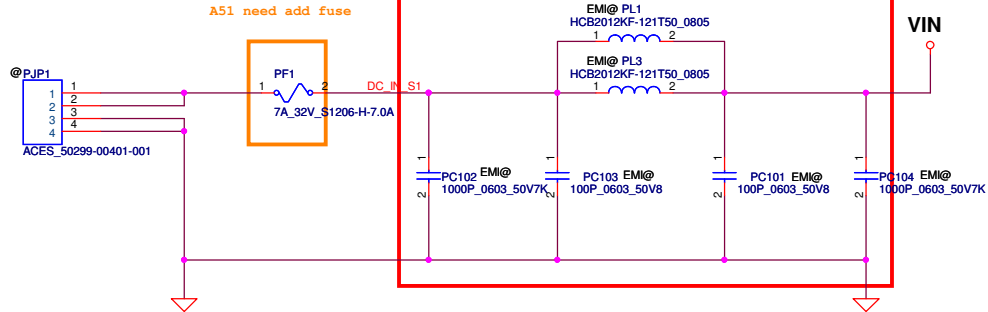


Security Classification		Compal Secret Data				Compal Electronics, Inc.				
Issued Date		2013/05/15		Deciphered Date		2015/09/27		Title		
								DC TO DC INTERFACE		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						Size		Document Number	Rev	
								LA-A551P		0.2
								Date: Friday, June 14, 2013		Sheet 29 of 40

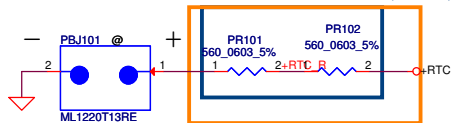
EMI Part (47.1)

Other component (37.1)

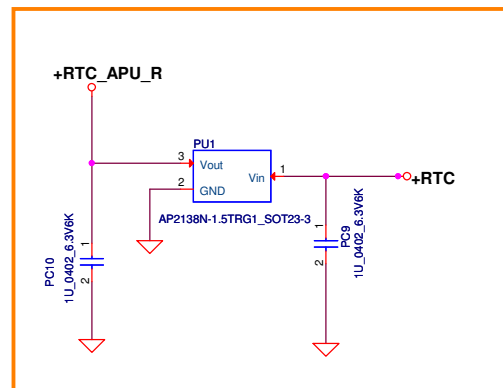
A51 need add fuse



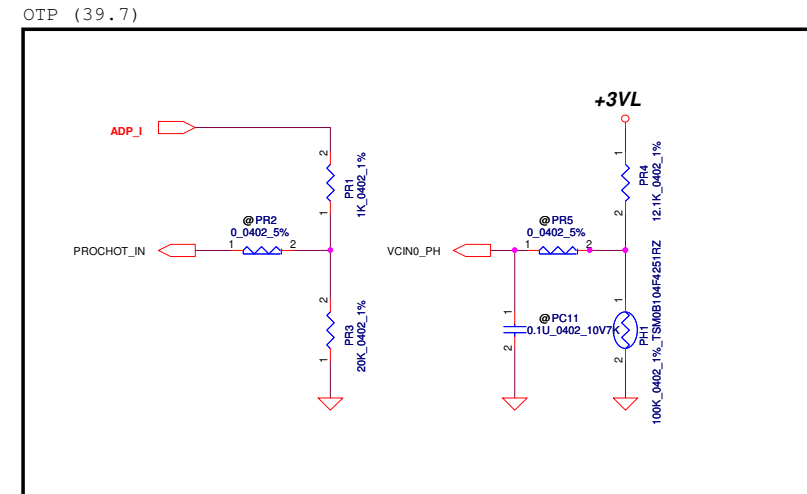
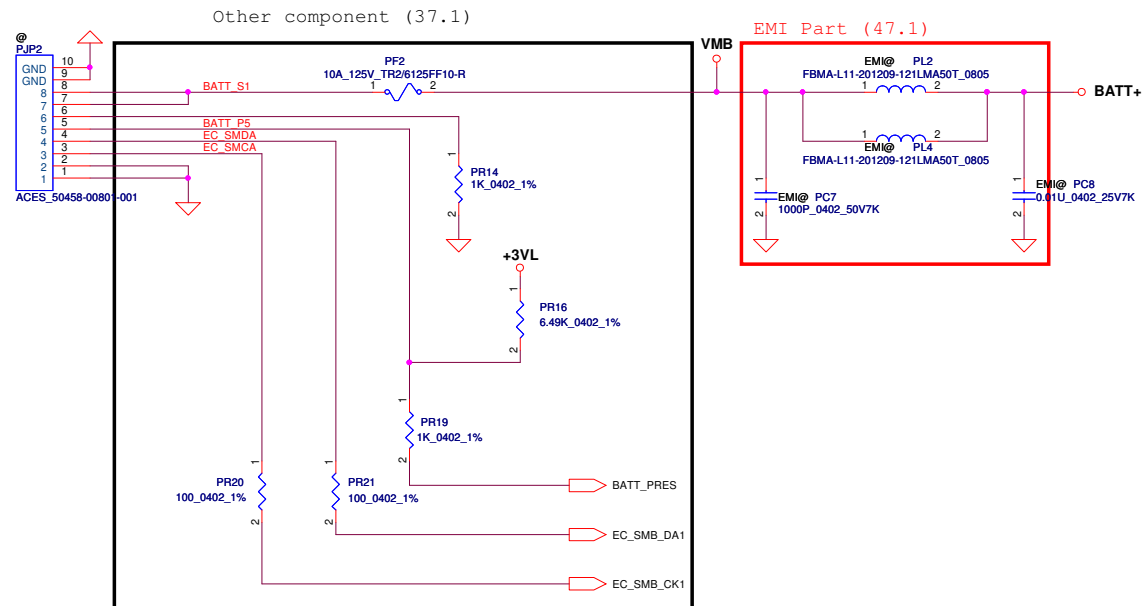
For ML1220 RTC (38.2)



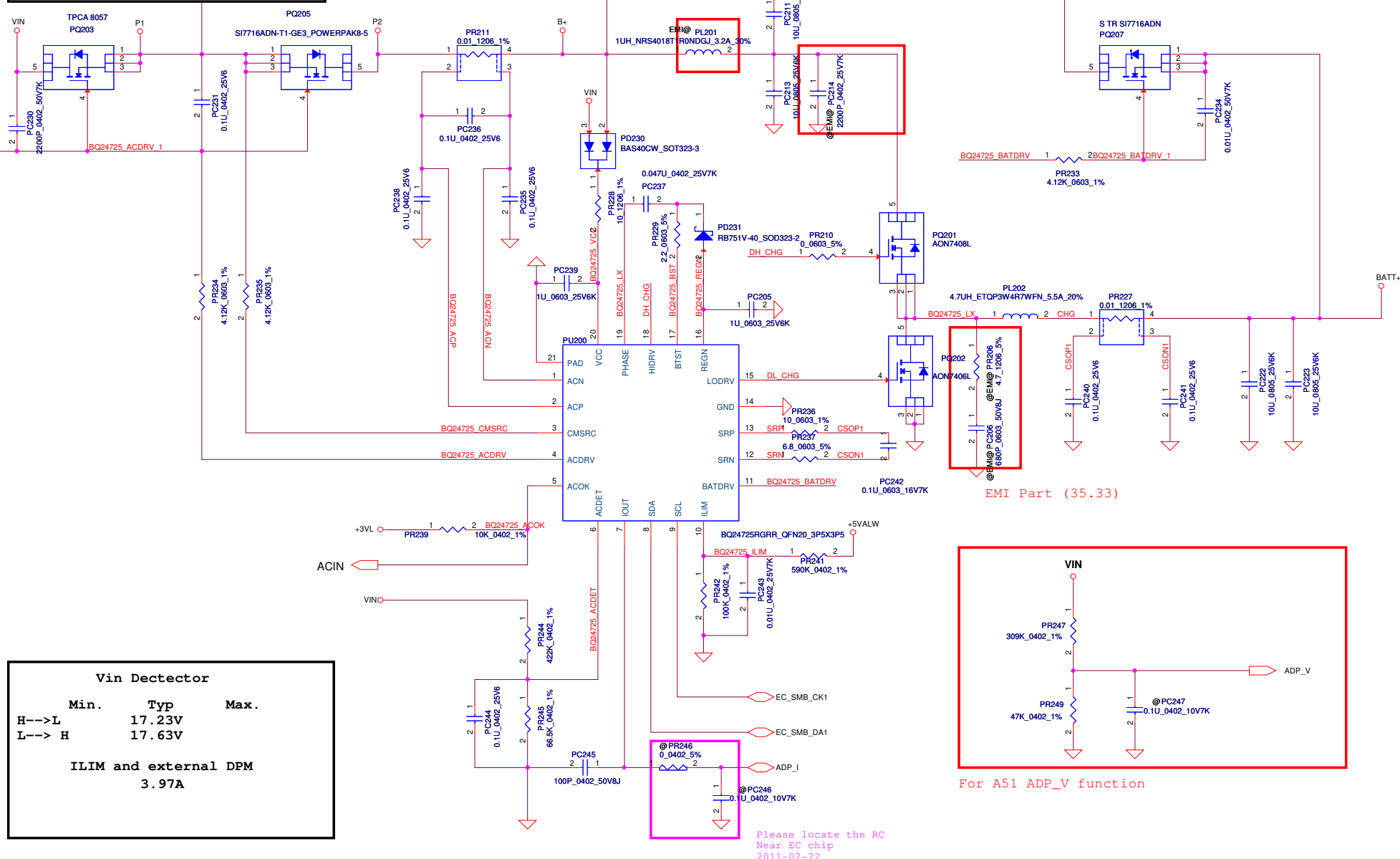
For RTC (38.2)



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/27	Deciphered Date	2015/09/27	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				DCIN/PRECHARGE
Date:		Document Number		Rev
		LA-A551P		0.1
Sheet		30		of 40



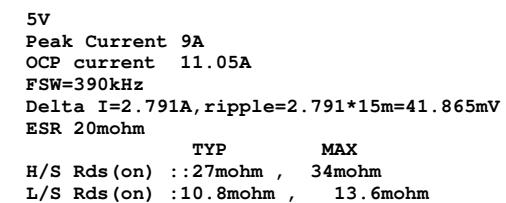
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/09/27	Deciphered Date	2015/09/27	Title	BATTERY CONN / OTP
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Custom
				Document Number	LA-A551P
				Date:	Rev 0.1
				Sheet	31 of 40



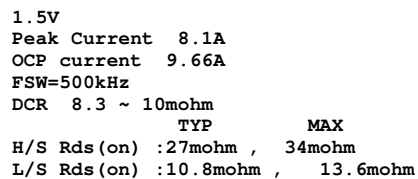
Vin Detector		
Min.	Typ	Max.
H-->L	17.23V	
L--> H	17.63V	
ILIM and external DPM		
3.97A		

Please locate the RC
Near EC chip
2011-02-22

Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2012/09/27	Deciphered Date	2015/09/27	Title	CHARGER	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	LA-A551P	0.1
				Date:	Sheet 32 of 40	



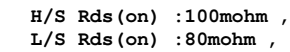
LA-A551P	Compal Secret Data			Compal Electronics, Inc.		
Security Classification	2011/06/24		Deciphered Date	2012/07/12	Title	
Issued Date					3VALW/5VALW	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					Size	Document Number
					Custom	LA-A551P
					Date:	Sheet 33 of 40
					Rev	0.1



STATE	S3	S5	1.5VP	VTT_REFP	0.75VSP
S0	Hi	Hi	On	On	On
S3	Lo	Hi	On	On	Off (Hi-Z)
S4/S5	Lo	Lo	Off (Discharge)	Off (Discharge)	Off (Discharge)

Note: S3 - sleep ; S5 - power off

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2011/06/24	Deciphered Date	2012/07/12	Title	1.5VP/0.75VSP/1.8VSP	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	LA-A551P	0.1
Date:	Sheet		34	of	40	



EMI Part (47.1)

0.95V
Peak Current 7.1A
OCP current 16A
FSW=800kHz

H/S Rds(on) :22mohm
L/S Rds(on) :11mohm

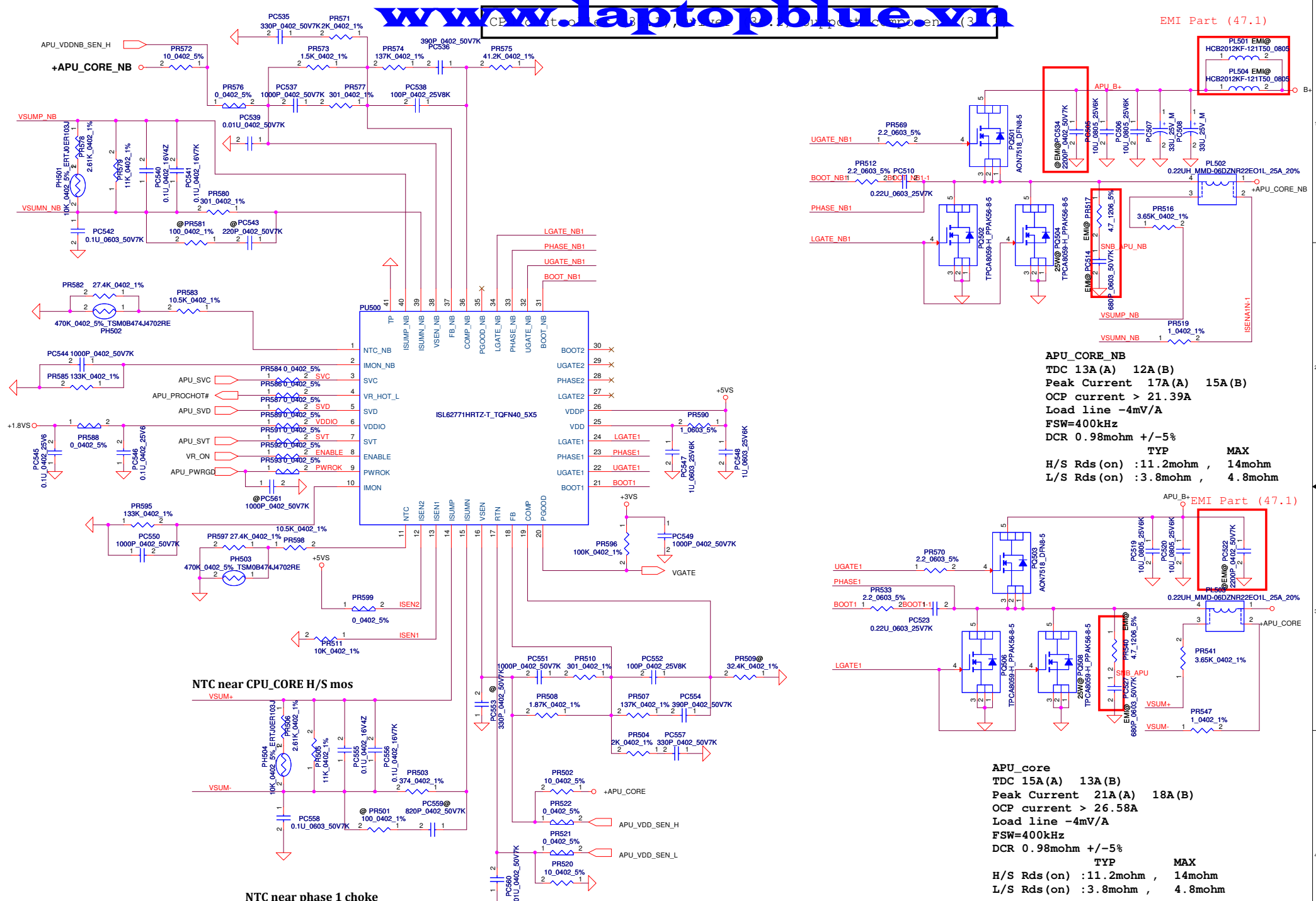
EMI Part (47.1)

+0.95VALWP

+0.95VALWP

H/S Rds (on) : 22mohm
L/S Rds (on) : 11mohm

Security Classification		Compal Secret Data				Compal Electronics, Inc.									
Issued Date		2012/09/27		Deciphered Date		2015/09/27		Title							
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.								+1.8VALWP/+0.95VALWP							
								Size	Document Number	ZRMAE					Rev
								Custom							0.1
								Date:		Friday, June 14, 2013		Sheet		35 of 40	



EMI Part (47.1)

APU_CORE_NB
TDC 13A(A) 12A(B)
Peak Current 17A(A) 15A(B)
OCP current > 21.39A
Load line -4mV/A
FSW=400kHz
DCR 0.98mohm +/-5%

TYP MAX
H/S Rds(on) :11.2mohm , 14mohm
L/S Rds(on) :3.8mohm , 4.8mohm

EMI Part (47.1)

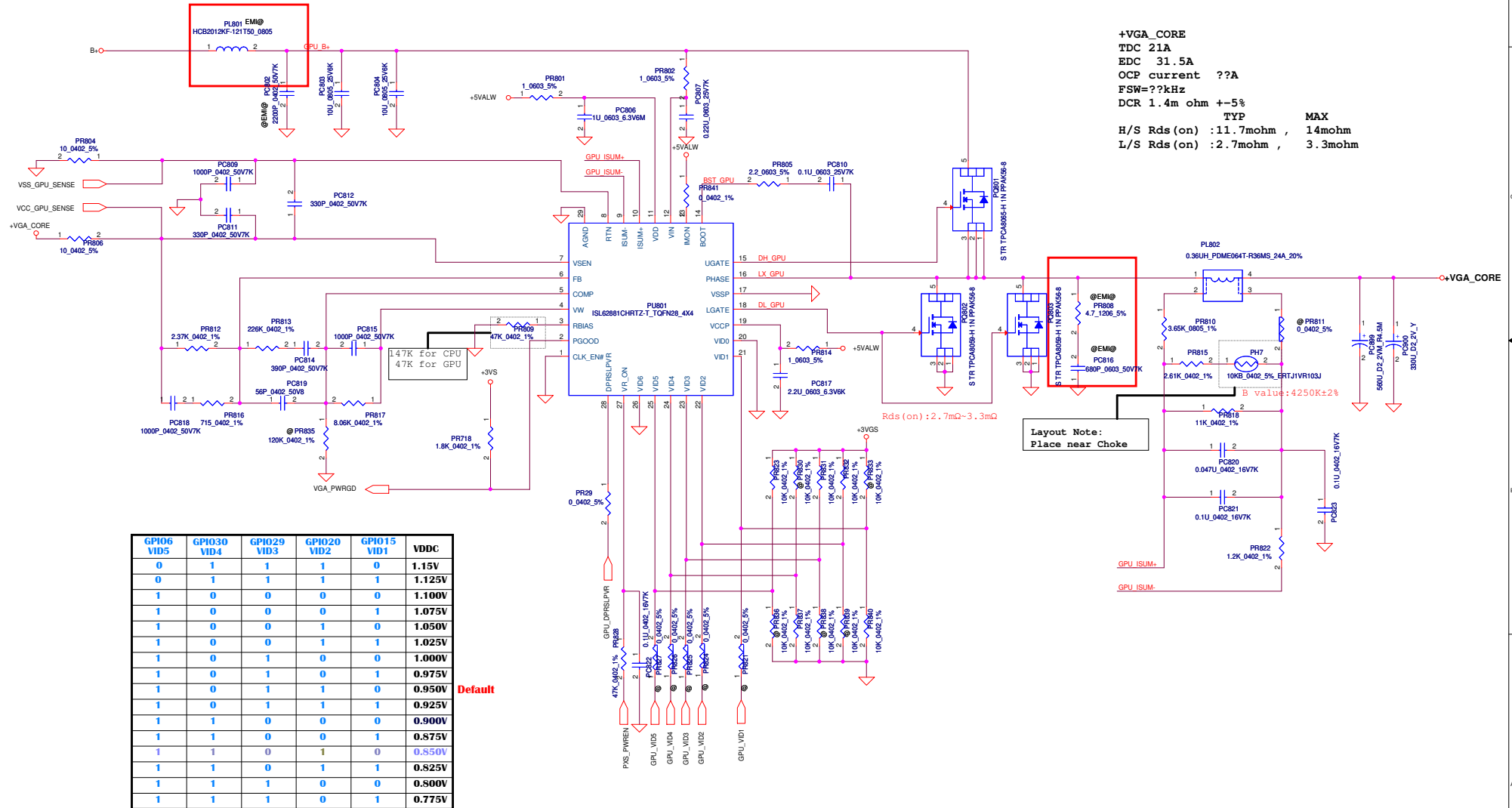
APU_core
TDC 15A(A) 13A(B)
Peak Current 21A(A) 18A(B)
OCP current > 26.58A
Load line -4mV/A
FSW=400kHz
DCR 0.98mohm +/-5%

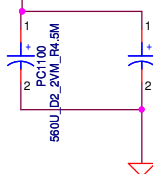
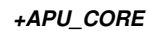
TYP MAX
H/S Rds(on) :11.2mohm , 14mohm
L/S Rds(on) :3.8mohm , 4.8mohm

Security Classification		Compal Secret Data		Title	
Issued Date	2012/09/27	Deciphered Date		Document Number	LA-A551P
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Customer	Rev 1.0
				Date: Friday, June 14, 2013	Sheet 36 of 40

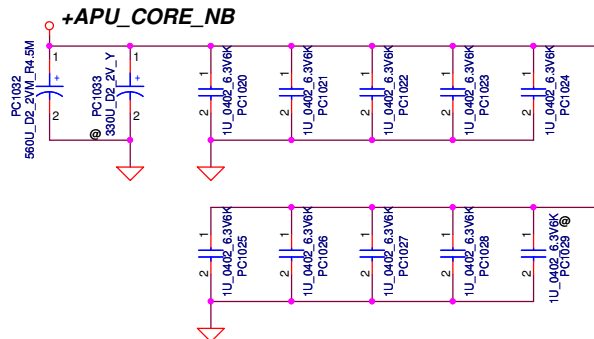
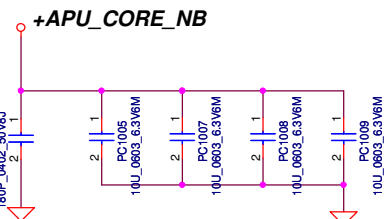
VGA controller (43.1), Driver (43.2) Support component (43.3)

EMI Part (47.1)

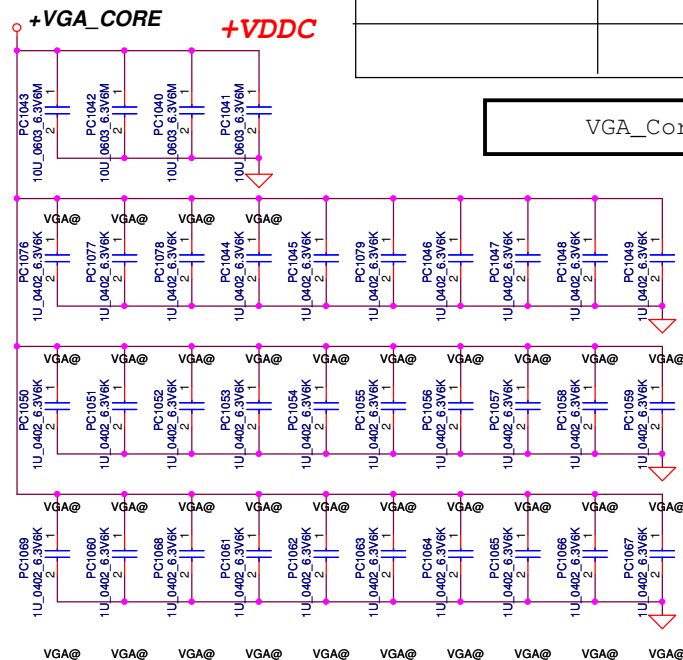




GFX output CAP (Including MLCC) 36.5



VGA_Core output CAP (Including MLCC 43.9)



kABINI	560uF*4.5m	330uF	22uF (0603)	10uF (0603)	1u (0402)	180P (0402)
VDD	1	1	2	1	12	1
VDD_NB	1			4	9	1

Item	Reason for change	PG#	Modify List	Date	Phase
------	-------------------	-----	-------------	------	-------

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2013/05/15	Deciphered Date	2015/09/27	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	ZRMAE	0.2
				Date:	Friday, June 14, 2013	Sheet 39 of 40

HW PIR (Product Improve Record)

ZEMAE LA-A551P SCHEMATIC CHANGE
LIST
REVISION CHANGE: 0.1 to 0.2

NO.	DATE	PAGE	MODIFICATION LIST	PURPOSE
1.	05/29	P24.	Delete RR2,RR3,RR7,RR6,RR12,RE7 for USB trace.	Part conut reduce
2.	05/29	P24.	Change CR13 to 0603.	HW4 Common design
3.	05/29	P24.	Delete SLP_CHG_CB0 & SLLP_CHG_CB1 from APU.	Reduce reserve
4.	05/29	P24,27,28.	EC_CHG_CB2(GPIO1A) move to GPIO12 ` ADD NUM_LED#(JKB5.1 to JUB1.36)	Design change
5.	05/30	P08.	Add QC2 and connect HDMI_HDP_N to HDMI_HDP.	For HDMI utility
6.	05/30	P08.	Delete T24,T25,T27	For RTC issue
7.	05/30	P08.	Add RC3 15K pull down and reserve RC12,RC16,RC17 pull +3VALW_APU and RC5,RC11 pull gnd.	For RTC issue
8.	06/03	P24.	Change CR3&CR2 47u 0805 to CR6&CR10 and CR4&CR5 22u 0603	For hight limit
9.	06/03	P10.	Change CD43 from 47u 0805 to CD43&CD44 22u*2 0603	For hight limit
10.	06/03	P09.	Change CC14 from 47u 0805 to CC14&CC16 22u*2 0603	For hight limit
11.	06/07	P22.	Reserve varistor DL14 for LANGND to DGND	For ESD request
12.	06/07	P22.	Add diode DL5 for LANGND to DGND	For ESD request
13.	06/07	P26.	Remove RA18 and RA24	Remove reserve 0 ohm
14.	06/07	P25.	Add C18 0.1uF 0402 on +USB_VCCC close to JSB5	For EMI request
15.	06/10	P20.	Change C17 form 1500P to 0.015uF	For LCD sequence
16.	06/10	P25.	Remove 0ohm for 14 and 15 and add LR10 and LR9	For part count reduce
17.	06/11	P26.	Change LA7 and LA6 form 0402 to 0603 size	For EMI request
18.	06/11	P25.	Swap LR7/LR8 pin1&4 and pin3&2	For layout smooth
19.	06/11	P24.	Add test point for S&C IC T10&T11&T24	For NPI debug
20.	06/13	P28.	Change H4&H5 form H_3P3 to H_3P2	For ME limite

Title		
HW PIR		
Size	Document Number	Rev
B	LA-A551P	0.2
Date:	Friday, June 14, 2013	Sheet 40 of 40