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34	AUDIO(AMP & HP & SPK)	0.1	12/04				
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Project Code & Schematics Subject: MS72 Main Board

PCB P/N: (FUBAI) 1P-006B100-60SA
(NAN YA) 1P-006B200-60SA
(HANSTAR) 1P-006B500-60SA

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FOXCONN HON HAI Precision Ind. Co., Ltd CCPBG - R&D Division			
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Size A3	Document Number MS72-1-01		Rev 0.1
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07	CALISTOG (DMI) 2/7	0.1	12/04	42	USB2.0	0.1	12/04
08	CALIST (GRAPHIC) 3/7	0.1	12/04	43	HOLE	0.1	12/04
09	CALISTOGA (DDR2) 4/7	0.1	12/04	44	Power Design Diagram	0.1	12/04
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11	CALIST (VCC CORE) 6/7	0.1	12/04	46	SYS Power (+3_3V/+5V)	0.1	12/04
12	CALIST (VSS) 7/7	0.1	12/04	47	SYS Power(+1_5V/+1_05V)	0.1	12/04
13	DDR2(SO-DIMM_0) 1/3	0.1	12/04	48	DDR2 Power(+1_8V/+0_9V)	0.1	12/04
14	DDR2(SO-DIMM_1) 2/3	0.1	12/04	49	CPU_Vcore ---MAX8771	0.1	12/04
15	DDR2(Termination) 3/3	0.1	12/04	50	Others power plan	0.1	12/04
16	LVDS	0.1	12/04	51	OVP protection	0.1	12/04
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30	Mini-PCIE Card	0.1	12/04				
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32	OIDE	0.1	12/04				
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34	AUDIO(AMP & HP & SPK)	0.1	12/04				
35	AUDIO(EXTMIC)	0.1	12/04				



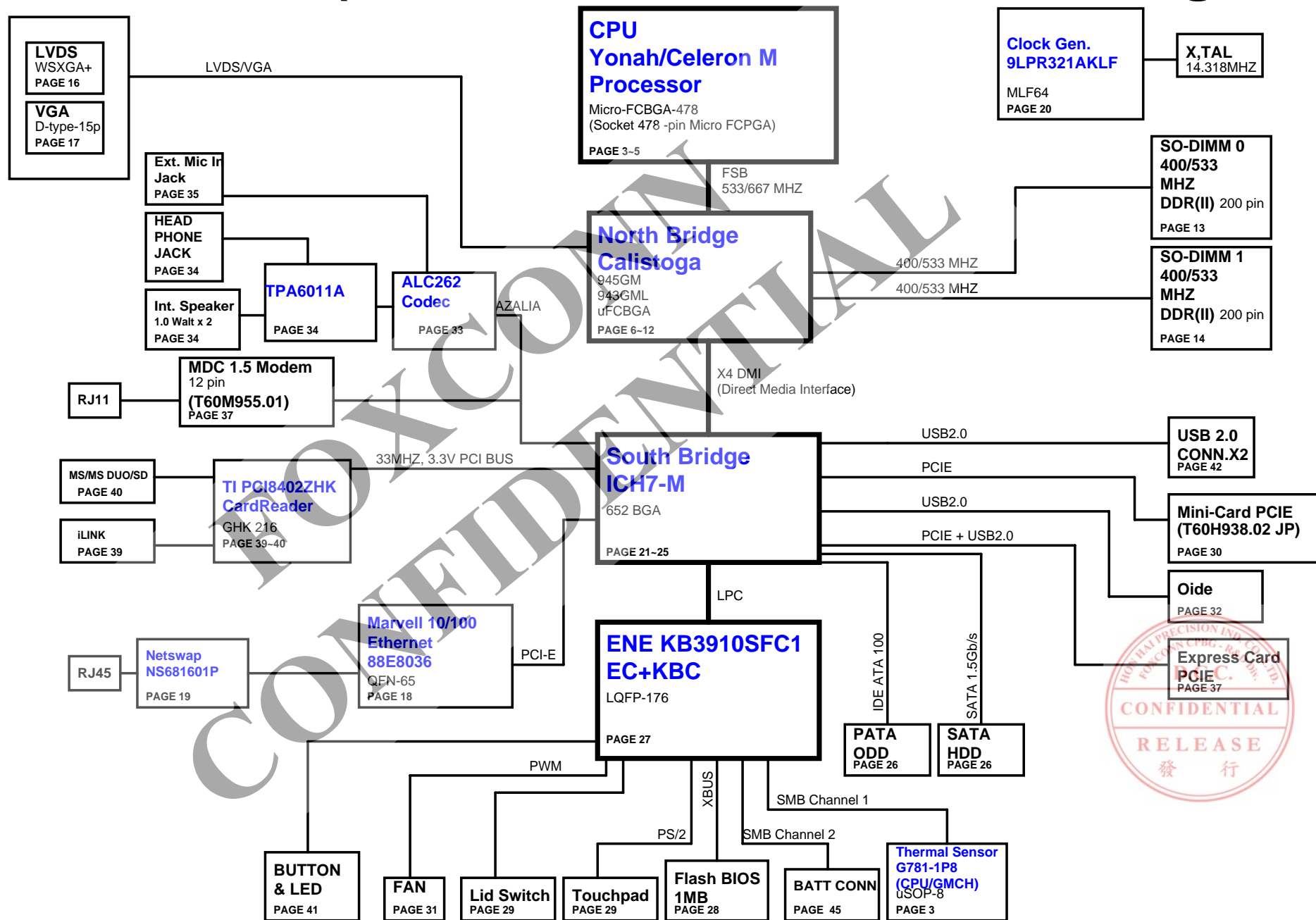
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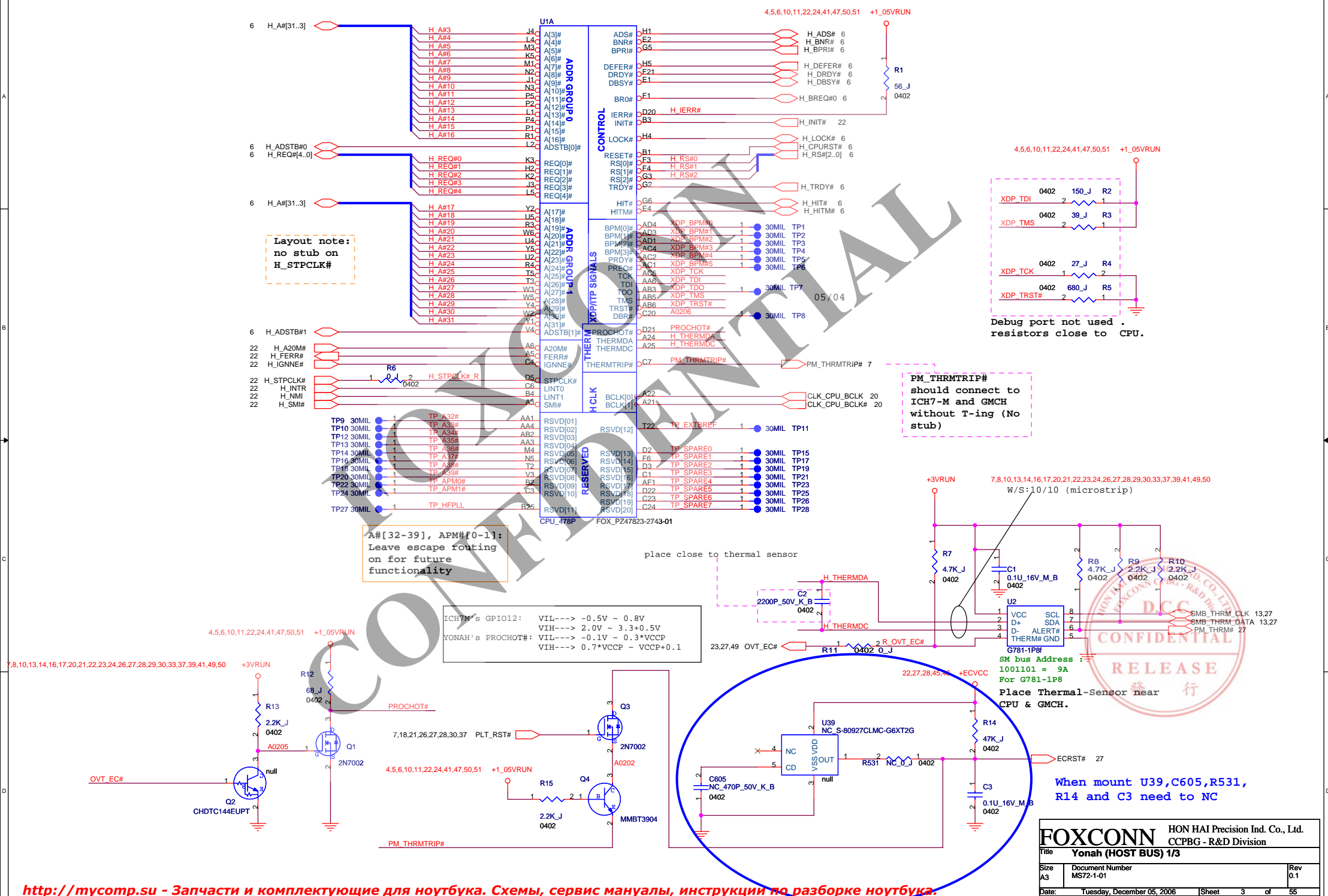
Project Code & Schematics Subject: MS72 Main Board

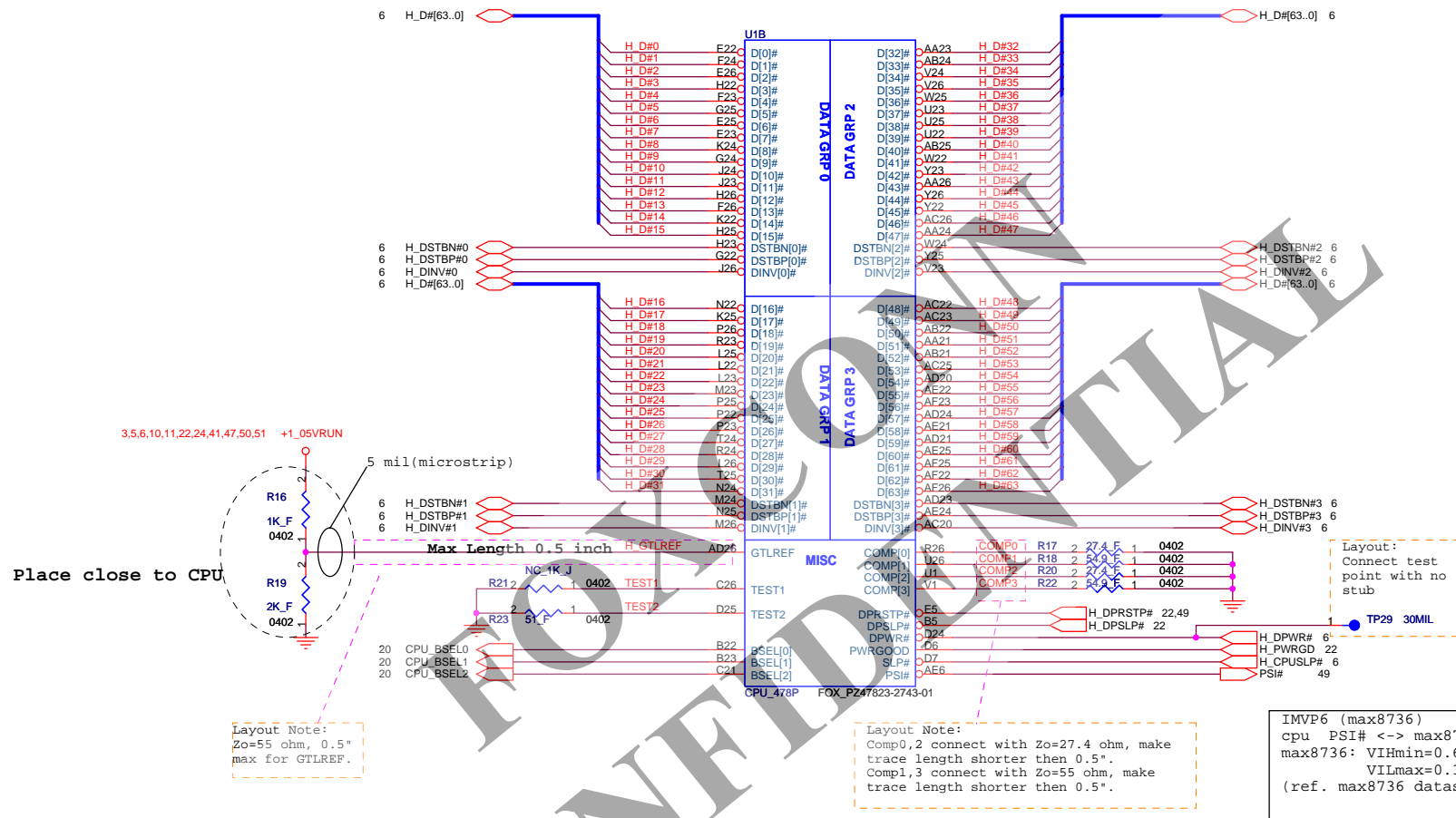
PCB P/N: (FUBAI) 1P-006B100-60SA
(NAN YA) 1P-006B200-60SA
(HANSTAR) 1P-006B500-60SA<http://mycomp.ru> - Запчасти и комплектующие для ноутбука. Схемы, сервис мануалы, инструкции по разборке ноутбука.

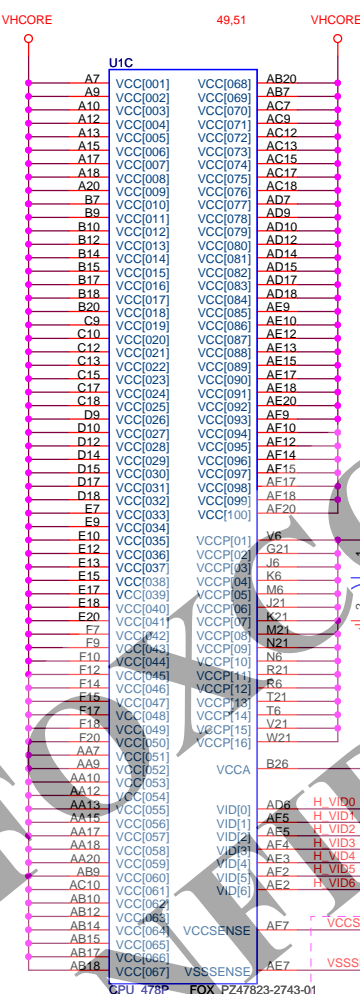
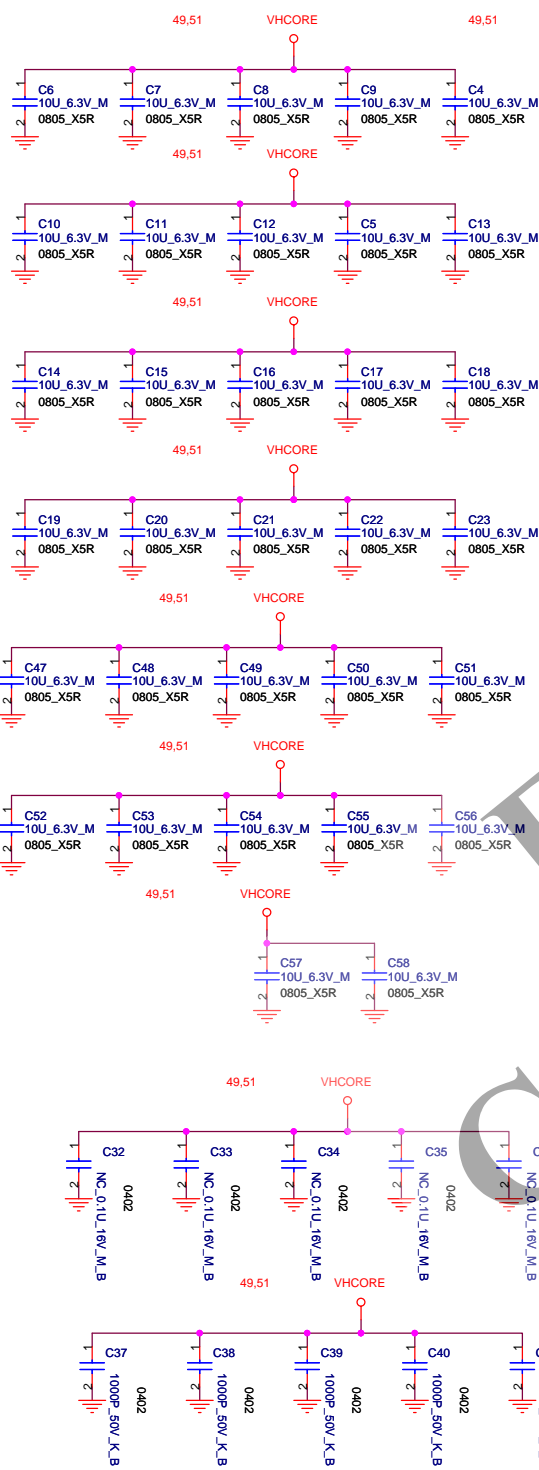
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RAPTOR3/MS72(CALISTOGA GM/GML Block Diagram)





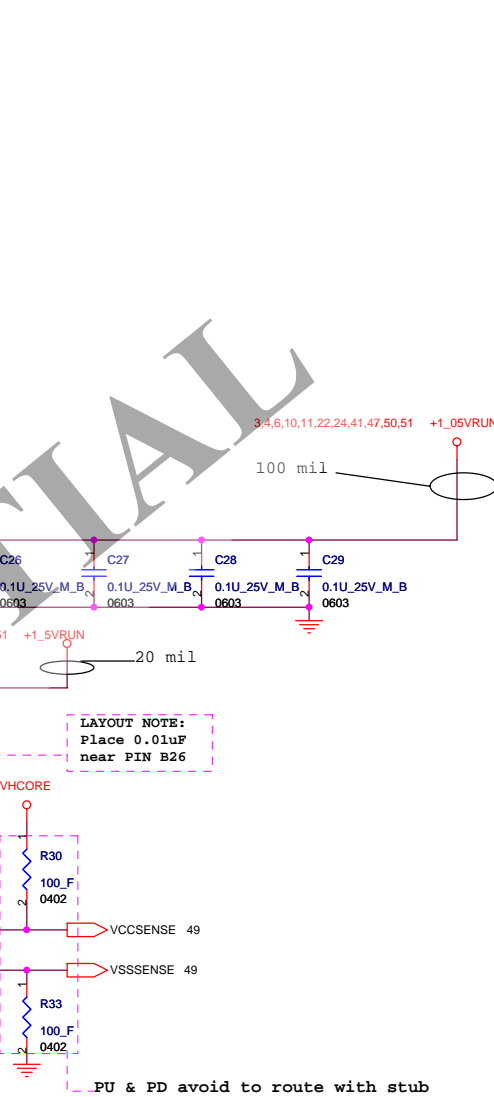
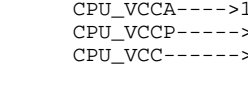
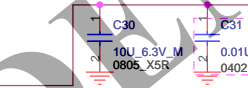
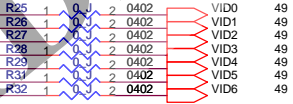




CPU_VCCA---->120mA
CPU_VCCP----->2.5A
CPU_VCC----->36A

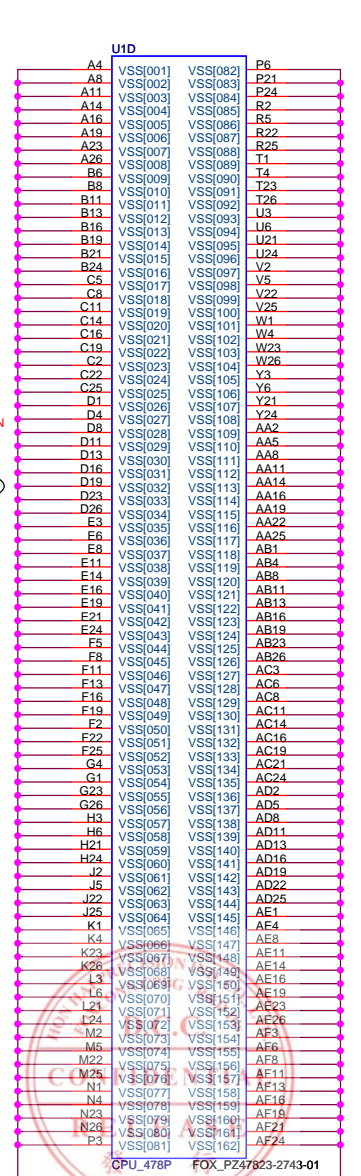
Layout Note: Route VCCSENSE traces at 27.4 Ohms with 50 mil spacing. Place PU and PD within 1 inch of cpu.
width=18 mil
spacing=7 mil

Same Length

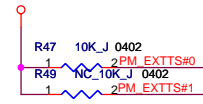


LAYOUT NOTE:
Place 0.01uF
near PIN B26

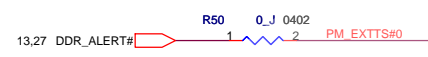
PU & PD avoid to route with stub



+3VRUN 3,8,10,13,14,16,17,20,21,22,23,24,26,27,28,29,30,33,37,39,41,49,50



23 PM_BMBUSY#
13,14 PM_EXTTS#0
3 PM_THRMTRIP#
23,27 IMVP_PWRGD
3,18,21,26,27,28,30,37 PLT_RST#



TP30	30MIL	1	MCH_RSVD_1	T32	RSVD_1
TP31	30MIL	1	MCH_RSVD_2	R32	RSVD_2
TP32	30MIL	1	MCH_RSVD_3	F3	RSVD_3
TP33	30MIL	1	MCH_RSVD_4	F7	RSVD_4
TP34	30MIL	1	MCH_RSVD_5	AG11	RSVD_5
TP35	30MIL	1	MCH_RSVD_6	AE11	RSVD_6
TP36	30MIL	1	MCH_RSVD_7	H7	RSVD_7
TP152	30MIL	1	MCH_RSVD_8	J19	RSVD_8
TP37	30MIL	1	MCH_RSVD_9	A41	RSVD_9
TP38	30MIL	1	MCH_RSVD_10	A34	RSVD_10
TP39	30MIL	1	MCH_RSVD_11	A34	RSVD_11
TP151	30MIL	1	MCH_RSVD_12	D28	RSVD_12
TP153	30MIL	1	MCH_RSVD_13	D27	RSVD_13

20 MCH_BSEL0
20 MCH_BSEL1
20 MCH_BSEL2

TP40	30MIL	1	MCH_CFG_3	F18	CFG_3
TP42	30MIL	1	MCH_CFG_4	E15	CFG_4
TP44	30MIL	1	MCH_CFG_8	E18	CFG_8
TP45	30MIL	1	MCH_CFG_14	C15	CFG_14
TP46	30MIL	1	MCH_CFG_15	H16	CFG_15
TP47	30MIL	1	MCH_CFG_17	H15	CFG_17

12 MCH_CFG_5
12 MCH_CFG_6
12 MCH_CFG_7

12 MCH_CFG_9
12 MCH_CFG_10
12 MCH_CFG_11
12 MCH_CFG_12
12 MCH_CFG_13

12 MCH_CFG_16
12 MCH_CFG_18
12 MCH_CFG_19
12 MCH_CFG_20

23 PM_BMBUSY#
13,14 PM_EXTTS#0
3 PM_THRMTRIP#
23,27 IMVP_PWRGD
3,18,21,26,27,28,30,37 PLT_RST#

21 MCH_ICH_SYNC#
20 MCH_CLK_REQ#

CFG_0	K16	CFG_1	K18	CFG_2	J18	CFG_3	F18	CFG_4	E15	CFG_5	E18	CFG_6	D19	CFG_7	D16	CFG_8	G16	CFG_9	E16	CFG_10	D15	CFG_11	G15	CFG_12	K15	CFG_13	C15	CFG_14	H16	CFG_15	H15	CFG_16	J26	CFG_17	J26	CFG_18	J26	CFG_19	J26	CFG_20	J26
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U3B

RSVD

CFG

PM

MISC

NC

DMI

CALISTOGA

SM_CK_0	AY35	M_CLK_DDR0	13
SM_CK_1	AR1	M_CLK_DDR1	13
SM_CK_2	AW7	M_CLK_DDR2	14
SM_CK_3	AW40	M_CLK_DDR3	14
SM_CK#_0	AW35	M_CLK_DDR#0	13
SM_CK#_1	AT1	M_CLK_DDR#1	13
SM_CK#_2	AY7	M_CLK_DDR#2	14
SM_CK#_3	AY40	M_CLK_DDR#3	14
SM_CKE_0	AU20	M_CKE0	13,15
SM_CKE_1	AT20	M_CKE1	13,15
SM_CKE_2	BA29	M_CKE2	14,15
SM_CKE_3	AY29	M_CKE3	14,15
SM_CS#_0	AW13	M_CS#0	13,15
SM_CS#_1	AW12	M_CS#1	13,15
SM_CS#_2	AW21	M_CS#2	14,15
SM_CS#_3	AW21	M_CS#3	14,15
SM_OCDCOMP_0	AL20	M_OCDCOMP_0	1
SM_OCDCOMP_1	AE10	M_OCDCOMP_1	1
SM_ODT_0	BA13	M_ODT0	13,15
SM_ODT_1	BA12	M_ODT1	13,15
SM_ODT_2	AY20	M_ODT2	14,15
SM_ODT_3	AU21	M_ODT3	14,15
SM_RCOMP#	AT9	M_RCOMP#	11,13,14,41,48,50,51
SM_VREF_0	AK1	M_VREF_0	1
SM_VREF_1	AK41	M_VREF_1	1
G_CLKIN#	AE33	M_CLK_MCH_3GPLL#	20
G_CLKIN	AG33	M_CLK_MCH_3GPLL	20
D_REFCLKIN#	A27	M_DREFCLK#	20
D_REFCLKIN	A26	M_DREFCLK	20
D_REFSSCLKIN#	C40	M_DREFSSCLK#	20
D_REFSSCLKIN	D41	M_DREFSSCLK	20
DMI_RXN_0	AE35	M_DMI_TXN0	3
DMI_RXN_1	AE39	M_DMI_TXN1	3
DMI_RXN_2	AG35	M_DMI_TXN2	3
DMI_RXN_3	AH39	M_DMI_TXN3	3
DMI_RXP_0	AC35	M_DMI_TXP0	3
DMI_RXP_1	AE39	M_DMI_TXP1	3
DMI_RXP_2	AG35	M_DMI_TXP2	3
DMI_RXP_3	AG39	M_DMI_TXP3	3
DMI_RXN_0	AE37	M_DMI_RXN0	3
DMI_RXN_1	AE41	M_DMI_RXN1	3
DMI_RXN_2	AG37	M_DMI_RXN2	3
DMI_RXN_3	AH41	M_DMI_RXN3	3
DMI_TXP_0	AC37	M_DMI_RXP0	3
DMI_TXP_1	AE41	M_DMI_RXP1	3
DMI_TXP_2	AG37	M_DMI_RXP2	3
DMI_TXP_3	AG41	M_DMI_RXP3	3

DDR

MUXING

CLK

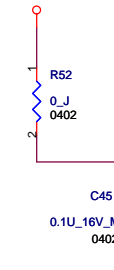
MISC

NC

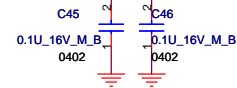
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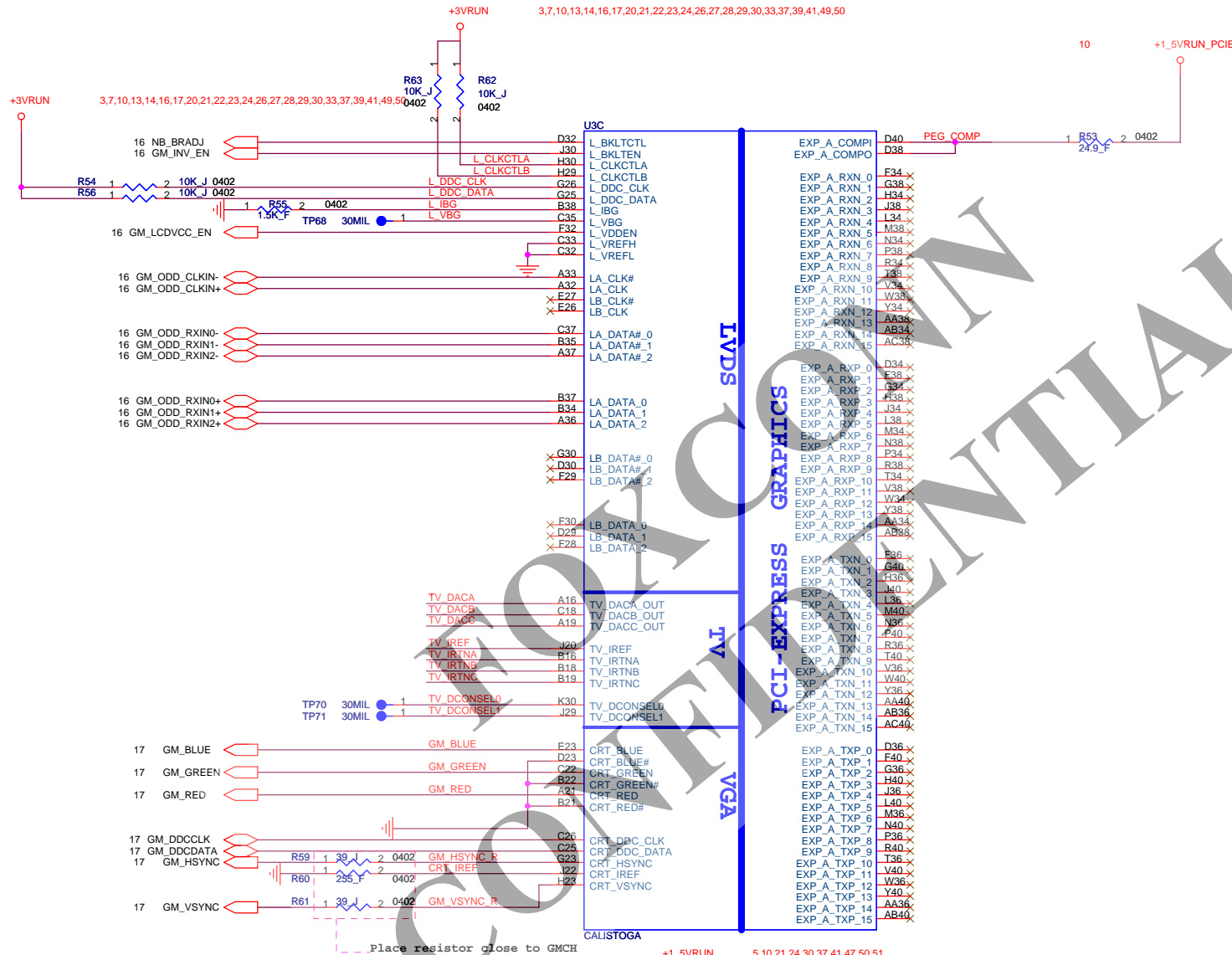
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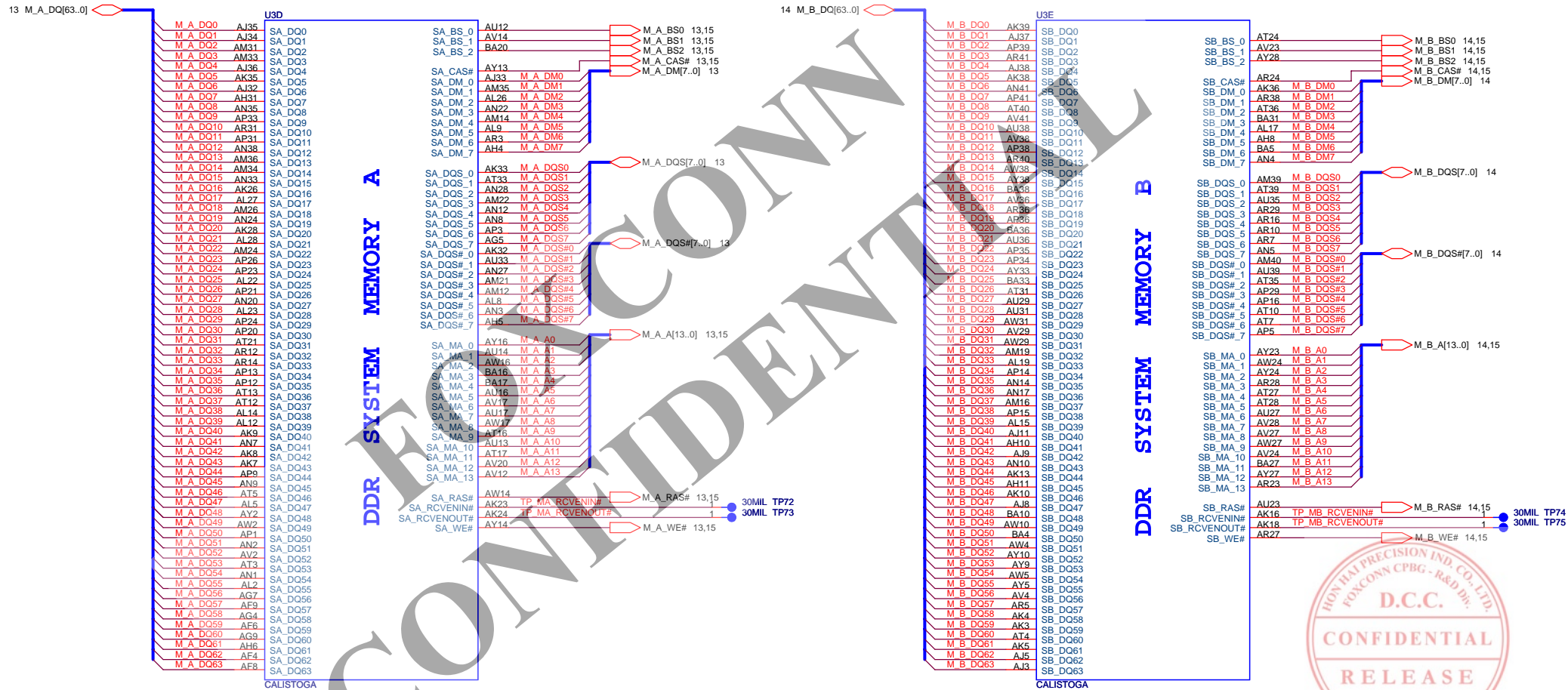
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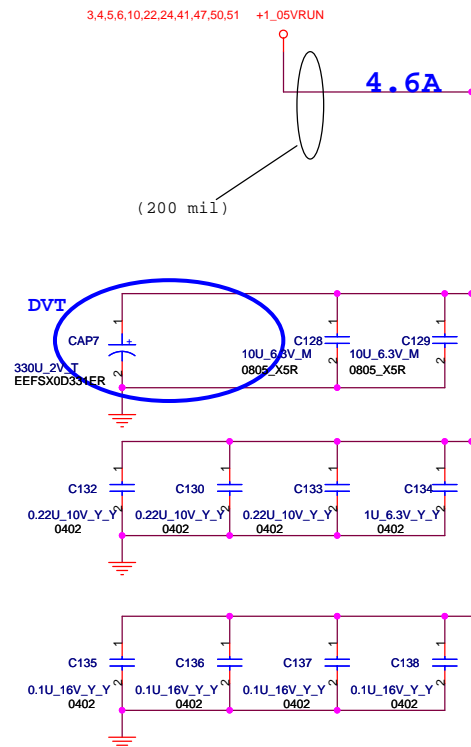


Place close to chipset









AA33 VCC_0
W33 VCC_1
P33 VCC_2
L33 VCC_3
J33 VCC_4
Y32 VCC_5
W32 VCC_6
Y32 VCC_7
N32 VCC_8
M32 VCC_9
P32 VCC_10
N32 VCC_11
M32 VCC_12
J32 VCC_13
L32 VCC_14
AA31 VCC_15
W31 VCC_16
P31 VCC_17
L31 VCC_18
Y31 VCC_19
W31 VCC_20
Y31 VCC_21
N31 VCC_22
M31 VCC_23
AA30 VCC_24
W30 VCC_25
Y30 VCC_26
W30 VCC_27
T30 VCC_28
R30 VCC_29
P30 VCC_30
N30 VCC_31
M30 VCC_32
L30 VCC_33
AA29 VCC_34
W29 VCC_35
Y29 VCC_36
W29 VCC_37
U29 VCC_38
R29 VCC_39
P29 VCC_40
M29 VCC_41
L29 VCC_42
AA28 VCC_43
W28 VCC_44
Y28 VCC_45
W28 VCC_46
U28 VCC_47
T28 VCC_48
R28 VCC_49
P28 VCC_50
M28 VCC_51
L28 VCC_52
AA27 VCC_53
W27 VCC_54
Y27 VCC_55
W27 VCC_56
U27 VCC_57
T27 VCC_58
R27 VCC_59
P27 VCC_60
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W26 VCC_66
U26 VCC_67
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P26 VCC_70
M26 VCC_71
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Y22 VCC_105
W22 VCC_106
U22 VCC_107
T22 VCC_108
R22 VCC_109
P22 VCC_110

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VCC_1
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VCC_3
VCC_4
VCC_5
VCC_6
VCC_7
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VCC_SM_105
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VCC_NCTF69
VCC_NCTF70
VCC_NCTF71
VCC_NCTF72

7 MCH_CFG_5 1 30MIL TP76

MCH_CFG_5 Low = DMIX2 High = DMIX4

MCH_CFG_18 Low = 1.05V(default) High = 1.5V (VCC_CORE Select)

7 MCH_CFG_18 1 30MIL TP79

7 MCH_CFG_6 1 30MIL TP77

MCH_CFG_6 Low = Moby Dick High = Calistoga DDR2 select (default high)

MCH_CFG_19 Low = Normal(default) High = LANES REVERSED (DMI LANE REVERSAL)

7 MCH_CFG_19 1 30MIL TP80

7 MCH_CFG_7 1 30MIL TP78

MCH_CFG_7 (CPU Strap) Low = RSVD High = Mobile Yonah processor

7 MCH_CFG_9 1 30MIL TP81

MCH_CFG_9 (PCIe Graphics Lane) Low = Reverse Lane High = Normal operation

For layout convenience

MCH_CFG_20 Low = Only SDVO or PCIe x1 is operational (defaults) High = SDVO and PCIe x1 are operating simultaneously via the PEG port (PCIe Backward Interoperability mode)

7 MCH_CFG_20 1 30MIL TP83

7 MCH_CFG_10 1 30MIL TP82

MCH_CFG_10 (HOST PLL VCC SELECT) Low = RESERVED High = MOBILITY

Layout Noe: Location of all MCH_CFG strap resistors needs to be close to trace to minimize stub

7 MCH_CFG_11 1 30MIL TP84

MCH_CFG_11 (PSB 4x CLK ENABLE) Low = Calistoga High = Reserved

R93 NC 2.2K Ω 0402

7 MCH_CFG_12 1 30MIL TP84

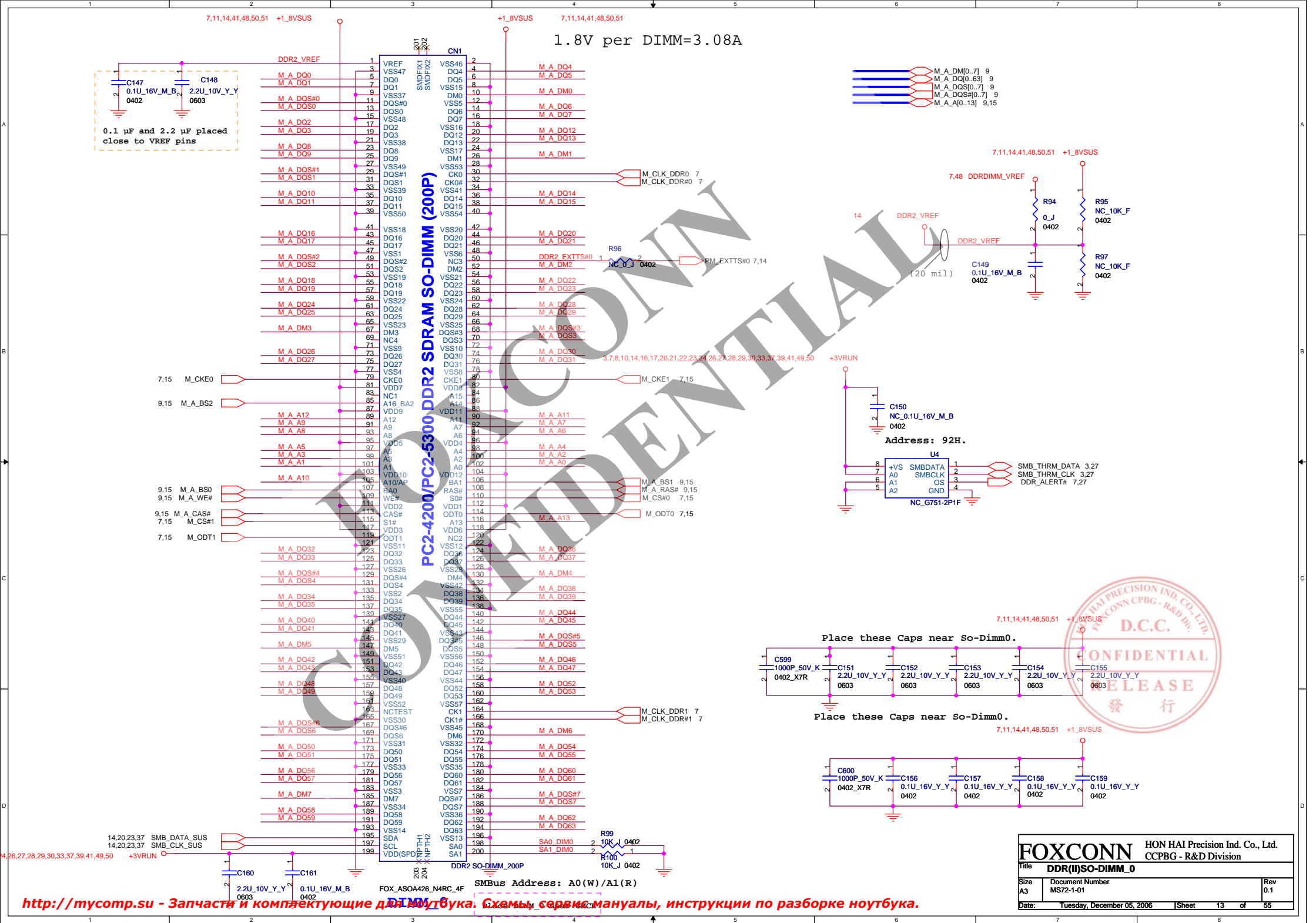
7 MCH_CFG_13 1 30MIL TP85

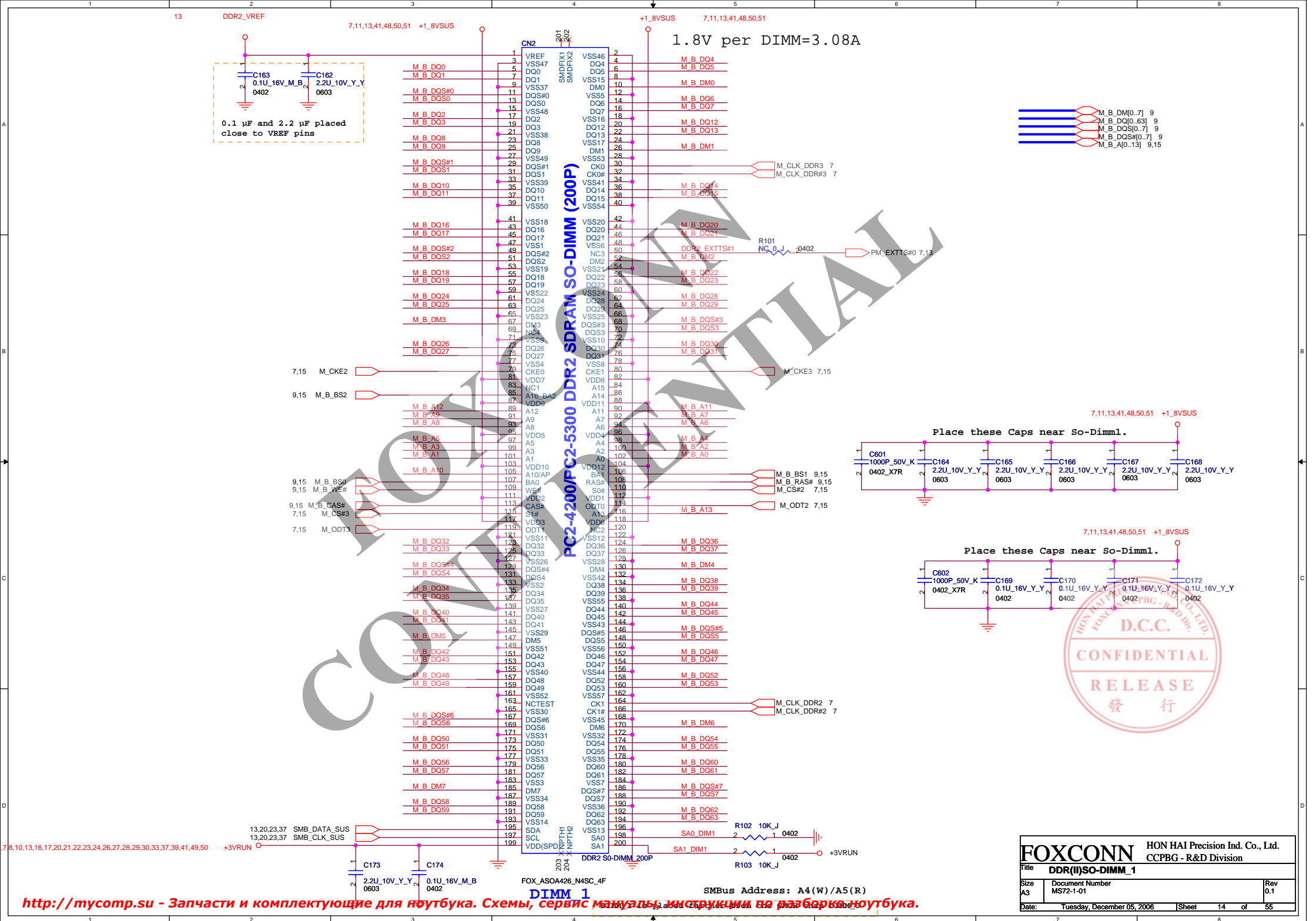
MCH_CFG [13:12] (XOR/ALLZ) 00=Partial Clock Gating Disable 01=XOR Mode Enable 10=All-Z Mode Enable 11=Normal Operation(Default)

7 MCH_CFG_16 1 30MIL TP160

MCH_CFG_16 Low = Dynamic ODT Disabled High = Dynamic ODT Enable (FSB Dynamic ODT)

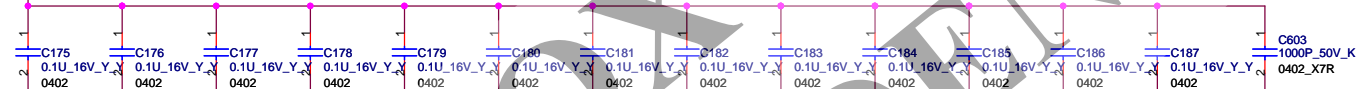
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AA41	VSS_1	AG34	VSS_98
W41	VSS_2	AF34	VSS_99
T41	VSS_3	AE34	VSS_100
P41	VSS_4	AC34	VSS_101
M41	VSS_5	C34	VSS_102
J41	VSS_6	AW33	VSS_103
F41	VSS_7	AV33	VSS_104
AV40	VSS_8	AR33	VSS_105
AP40	VSS_9	AE33	VSS_106
AN40	VSS_10	AE33	VSS_107
AK40	VSS_11	Y33	VSS_108
AG40	VSS_12	G22	VSS_109
AG40	VSS_13	T33	VSS_110
AG40	VSS_14	R33	VSS_111
AE40	VSS_15	M33	VSS_112
AE40	VSS_16	H33	VSS_113
B40	VSS_17	G33	VSS_114
AV39	VSS_18	F33	VSS_115
AW39	VSS_19	D33	VSS_116
AR39	VSS_20	B33	VSS_117
AN39	VSS_21	AH32	VSS_118
AG39	VSS_22	AG32	VSS_119
AC39	VSS_23	AF32	VSS_120
AB39	VSS_24	AE32	VSS_121
AA39	VSS_25	AC32	VSS_122
Y39	VSS_26	AB32	VSS_123
W39	VSS_27	G32	VSS_124
V39	VSS_28	B32	VSS_125
T39	VSS_29	Y31	VSS_126
R39	VSS_30	AT29	VSS_127
P39	VSS_31	AN29	VSS_128
N39	VSS_32	AM29	VSS_129
M39	VSS_33	AL29	VSS_130
L39	VSS_34	AK29	VSS_131
K39	VSS_35	AK29	VSS_132
J39	VSS_36	AK29	VSS_133
I39	VSS_37	AK29	VSS_134
H39	VSS_38	AK29	VSS_135
G39	VSS_39	AK29	VSS_136
F39	VSS_40	AK29	VSS_137
E39	VSS_41	AK29	VSS_138
D39	VSS_42	AK29	VSS_139
C39	VSS_43	AK29	VSS_140
B39	VSS_44	AK29	VSS_141
A39	VSS_45	AK29	VSS_142
Y39	VSS_46	AK29	VSS_143
W39	VSS_47	AK29	VSS_144
V39	VSS_48	AK29	VSS_145
T39	VSS_49	AK29	VSS_146
R39	VSS_50	AK29	VSS_147
P39	VSS_51	AK29	VSS_148
N39	VSS_52	AK29	VSS_149
M39	VSS_53	AK29	VSS_150
L39	VSS_54	AK29	VSS_151
K39	VSS_55	AK29	VSS_152
J39	VSS_56	AK29	VSS_153
I39	VSS_57	AK29	VSS_154
H39	VSS_58	AK29	VSS_155
G39	VSS_59	AK29	VSS_156
F39	VSS_60	AK29	VSS_157
E39	VSS_61	AK29	VSS_158
D39	VSS_62	AK29	VSS_159
C39	VSS_63	AK29	VSS_160
B39	VSS_64	AK29	VSS_161
A39	VSS_65	AK29	VSS_162
Y39	VSS_66	AK29	VSS_163
W39	VSS_67	AK29	VSS_164
V39	VSS_68	AK29	VSS_165
T39	VSS_69	AK29	VSS_166
R39	VSS_70	AK29	VSS_167
P39	VSS_71	AK29	VSS_168
N39	VSS_72	AK29	VSS_169
M39	VSS_73	AK29	VSS_170
L39	VSS_74	AK29	VSS_171
K39	VSS_75	AK29	VSS_172
J39	VSS_76	AK29	VSS_173
I39	VSS_77	AK29	VSS_174
H39	VSS_78	AK29	VSS_175
G39	VSS_79	AK29	VSS_176
F39	VSS_80	AK29	VSS_177
E39	VSS_81	AK29	VSS_178
D39	VSS_82	AK29	VSS_179
C39	VSS_83	AK29	VSS_180
B39	VSS_84	AK29	VSS_181
A39	VSS_85	AK29	VSS_182
Y39	VSS_86	AK29	VSS_183
W39	VSS_87	AK29	VSS_184
V39	VSS_88	AK29	VSS_185
T39	VSS_89	AK29	VSS_186
R39	VSS_90	AK29	VSS_187
P39	VSS_91	AK29	VSS_188
N39	VSS_92	AK29	VSS_189
M39	VSS_93	AK29	VSS_190
L39	VSS_94	AK29	VSS_191
K39	VSS_95	AK29	VSS_192
J39	VSS_96	AK29	VSS_193
I39	VSS_97	AK29	VSS_194
H39	VSS_98	AK29	VSS_195
G39	VSS_99	AK29	VSS_196
F39	VSS_100	AK29	VSS_197
E39	VSS_101	AK29	VSS_198
D39	VSS_102	AK29	VSS_199
C39	VSS_103	AK29	VSS_200
B39	VSS_104	AK29	VSS_201
A39	VSS_105	AK29	VSS_202
Y39	VSS_106	AK29	VSS_203
W39	VSS_107	AK29	VSS_204
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K39	VSS_115	AK29	VSS_212
J39	VSS_116	AK29	VSS_213
I39	VSS_117	AK29	VSS_214
H39	VSS_118	AK29	VSS_215
G39	VSS_119	AK29	VSS_216
F39	VSS_120	AK29	VSS_217
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D39	VSS_122	AK29	VSS_219
C39	VSS_123	AK29	VSS_220
B39	VSS_124	AK29	VSS_221
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Y39	VSS_126	AK29	VSS_223
W39	VSS_127	AK29	VSS_224
V39	VSS_128	AK29	VSS_225
T39	VSS_129	AK29	VSS_226
R39	VSS_130	AK29	VSS_227
P39	VSS_131	AK29	VSS_228
N39	VSS_132	AK29	VSS_229
M39	VSS_133	AK29	VSS_230
L39	VSS_134	AK29	VSS_231
K39	VSS_135	AK29	VSS_232
J39	VSS_136	AK29	VSS_233
I39	VSS_137	AK29	VSS_234
H39	VSS_138	AK29	VSS_235
G39	VSS_139	AK29	VSS_236
F39	VSS_140	AK29	VSS_237
E39	VSS_141	AK29	VSS_238
D39	VSS_142	AK29	VSS_239
C39	VSS_143	AK29	VSS_240
B39	VSS_144	AK29	VSS_241
A39	VSS_145	AK29	VSS_242
Y39	VSS_146	AK29	VSS_243
W39	VSS_147	AK29	VSS_244
V39	VSS_148	AK29	VSS_245
T39	VSS_149	AK29	VSS_246
R39	VSS_150	AK29	VSS_247
P39	VSS_151	AK29	VSS_248
N39	VSS_152	AK29	VSS_249
M39	VSS_153	AK29	VSS_250
L39	VSS_154	AK29	VSS_251
K39	VSS_155	AK29	VSS_252
J39	VSS_156	AK29	VSS_253
I39	VSS_157	AK29	VSS_254
H39	VSS_158	AK29	VSS_255
G39	VSS_159	AK29	VSS_256
F39	VSS_160	AK29	VSS_257
E39	VSS_161	AK29	VSS_258
D39	VSS_162	AK29	VSS_259
C39	VSS_163	AK29	VSS_260
B39	VSS_164	AK29	VSS_261
A39	VSS_165	AK29	VSS_262
Y39	VSS_166	AK29	VSS_263
W39	VSS_167	AK29	VSS_264
V39	VSS_168	AK29	VSS_265
T39	VSS_169	AK29	VSS_266
R39	VSS_170	AK29	VSS_267
P39	VSS_171	AK29	VSS_268
N39	VSS_172	AK29	VSS_269
M39	VSS_173	AK29	VSS_270
L39	VSS_174	AK29	VSS_271
K39	VSS_175	AK29	VSS_272
J39	VSS_176	AK29	VSS_273
I39	VSS_177	AK29	VSS_274
H39	VSS_178	AK29	VSS_275
G39	VSS_179	AK29	VSS_276
F39	VSS_180	AK29	VSS_277
E39	VSS_181	AK29	VSS_278
D39	VSS_182	AK29	VSS_279
C39	VSS_183	AK29	VSS_280
B39	VSS_184	AK29	VSS_281
A39	VSS_185	AK29	VSS_282
Y39	VSS_186	AK29	VSS_283
W39	VSS_187	AK29	VSS_284
V39	VSS_188	AK29	VSS_285
T39	VSS_189	AK29	VSS_286
R39	VSS_190	AK29	VSS_287
P39	VSS_191	AK29	VSS_288
N39	VSS_192	AK29	VSS_289
M39	VSS_193	AK29	VSS_290
L39	VSS_194	AK29	VSS_291
K39	VSS_195	AK29	VSS_292
J39	VSS_196	AK29	VSS_293
I39	VSS_197	AK29	VSS_294
H39	VSS_198	AK29	VSS_295
G39	VSS_199	AK29	VSS_296
F39	VSS_200	AK29	VSS_297
E39	VSS_201	AK29	VSS_298
D39	VSS_202	AK29	VSS_299
C39	VSS_203	AK29	VSS_300
B39	VSS_204	AK29	VSS_301
A39	VSS_205	AK29	VSS_302
Y39	VSS_206	AK29	VSS_303
W39	VSS_207	AK29	VSS_304
V39	VSS_208	AK29	VSS_305
T39	VSS_209	AK29	VSS_306
R39	VSS_210	AK29	VSS_307
P39	VSS_211	AK29	VSS_308
N39	VSS_212	AK29	VSS_309
M39	VSS_213	AK29	VSS_310
L39	VSS_214	AK29	VSS_311
K39	VSS_215	AK29	VSS_312
J39	VSS_216	AK29	VSS_313
I39	VSS_217	AK29	VSS_314
H39	VSS_218	AK29	VSS_315
G39	VSS_219	AK29	VSS_316
F39	VSS_220	AK29	VSS_317
E39	VSS_221	AK29	VSS_318
D39	VSS_222	AK29	VSS_319
C39	VSS_223	AK29	VSS_320
B39	VSS_224	AK29	VSS_321
A39	VSS_225	AK29	VSS_322
Y39	VSS_226	AK29	VSS_323
W39	VSS_227	AK29	VSS_324
V39	VSS_228	AK29	VSS_325
T39	VSS_229	AK29	VSS_326
R39	VSS_230	AK29	VSS_327
P39	VSS_231	AK29	VSS_328
N39	VSS_232	AK29	VSS_329
M39	VSS_233	AK29	VSS_330
L39	VSS_234	AK29	VSS_331
K39	VSS_235	AK29	VSS_332
J39	VSS_236	AK29	VSS_333
I39	VSS_237	AK29	VSS_334
H39	VSS_238	AK29	VSS_335
G39	VSS_239	AK29	VSS_336
F39	VSS_240	AK29	VSS_337
E39	VSS_241	AK29	VSS_338
D39	VSS_242	AK29	VSS_339
C39	VSS_243	AK29	VSS_340
B39	VSS_244	AK29	VSS_341
A39	VSS_245	AK29	VSS_342
Y39	VSS_246	AK29	VSS_343
W39	VSS_247	AK29	VSS_344
V39	VSS_248	AK29	VSS_345
T39	VSS_249	AK29	VSS_346
R39	VSS_250	AK29	VSS_347
P39	VSS_251	AK29	VSS_348
N39	VSS_252	AK29	VSS_349
M39	VSS_253	AK29	VSS_350
L39	VSS_254	AK29	VSS_351
K39	VSS_255	AK29	VSS_352
J39	VSS_256	AK29	VSS_353
I39	VSS_257	AK29	VSS_354
H39	VSS_258	AK29	VSS_355
G39	VSS_259	AK29	VSS_356
F39	VSS_260	AK29	VSS_357
E39	VSS_261	AK29	VSS_358
D39	VSS_262	AK29	VSS_359
C39	VSS_263	AK29	VSS_360
B39	VSS_264	AK29	VSS_361
A39	VSS_265	AK29	VSS_362
Y39	VSS_266	AK29	VSS_363
W39	VSS_267	AK29	VSS_364
V39	VSS_268	AK29	VSS_365
T39	VSS_269	AK29	VSS_366
R39	VSS_270	AK29	VSS_367
P39	VSS_271	AK29	VSS_368
N39	VSS_272	AK29	VSS_369
M39	VSS_273	AK29	VSS_370
L39	VSS_274	AK29	VSS_371
K39	VSS_275	AK29	VSS_372
J39	VSS_276	AK29	VSS_373
I39	VSS_277	AK29	VSS_374
H39	VSS_278	AK29	VSS_375
G39	VSS_279	AK29	VSS_376
F39	VSS_280	AK29	VSS_377
E39	VSS_281	AK29	VSS_378
D39	VSS_282	AK29	VSS_379
C39	VSS_283	AK29	VSS_380





48,50,51

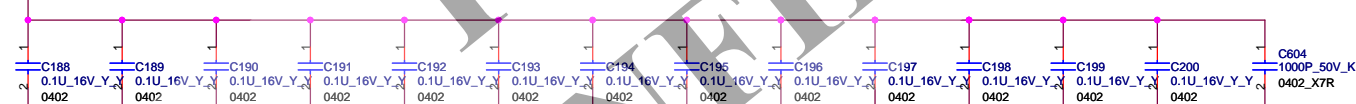
+0_9VSUS



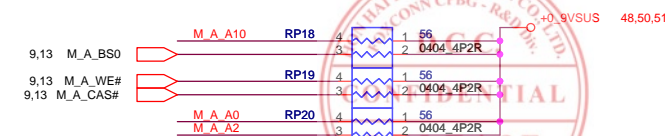
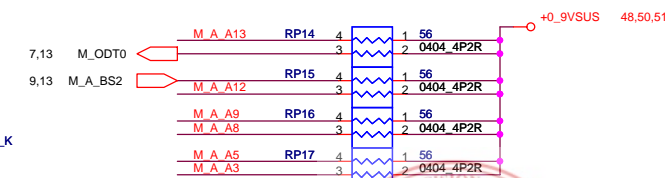
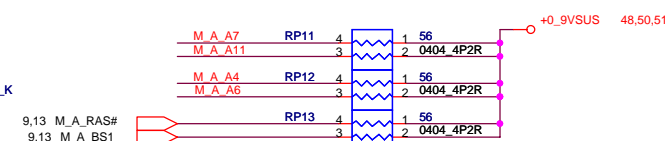
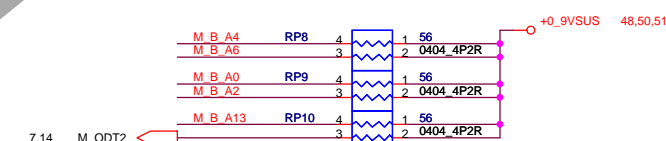
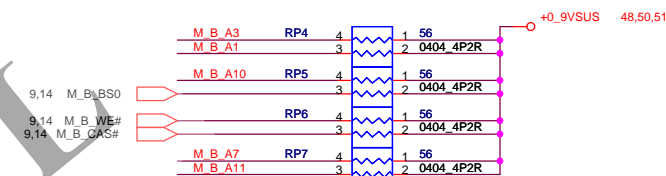
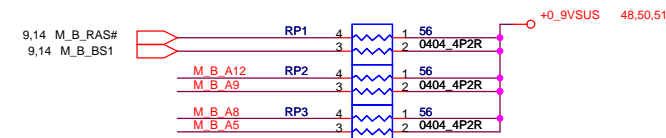
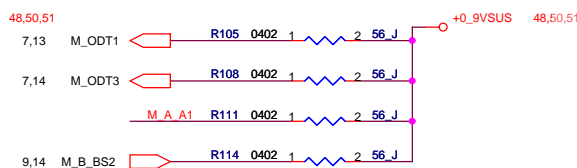
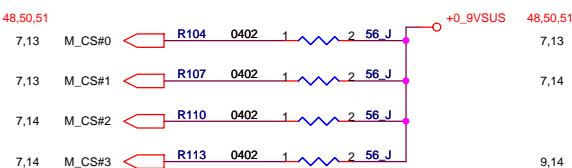
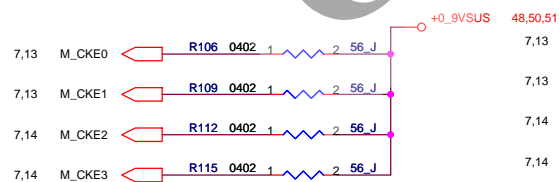
Layout note: Place 1 cap close to every 1 R-pack terminated to +0_9VSUS

48,50,51

+0_9VSUS



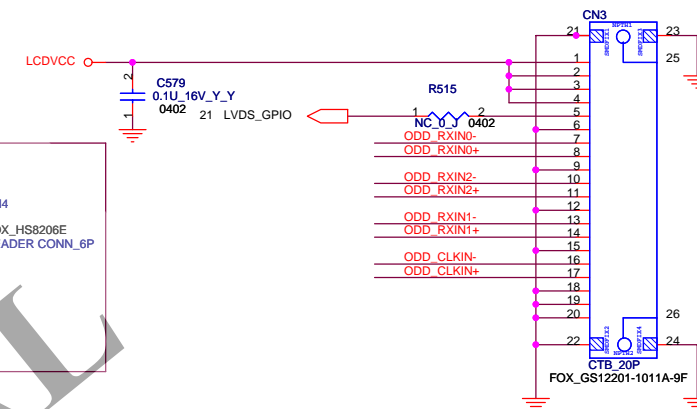
Layout note: Place 1 cap close to every 1 R-pack terminated to +0_9VSUS



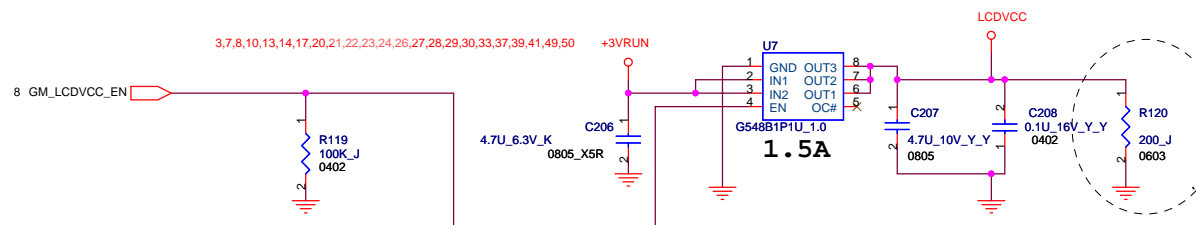
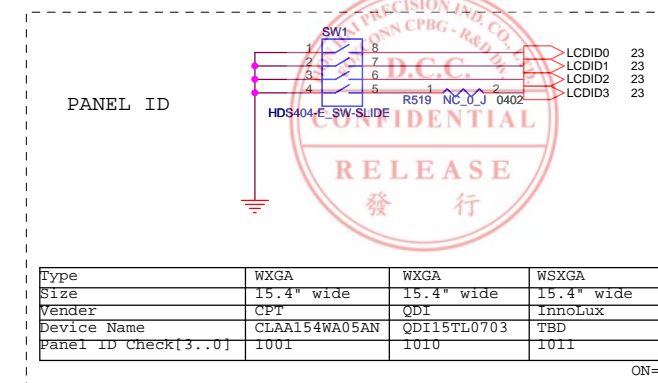
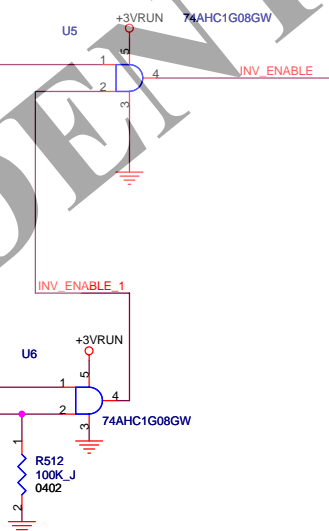
LVDS



LVDS CONNECTOR



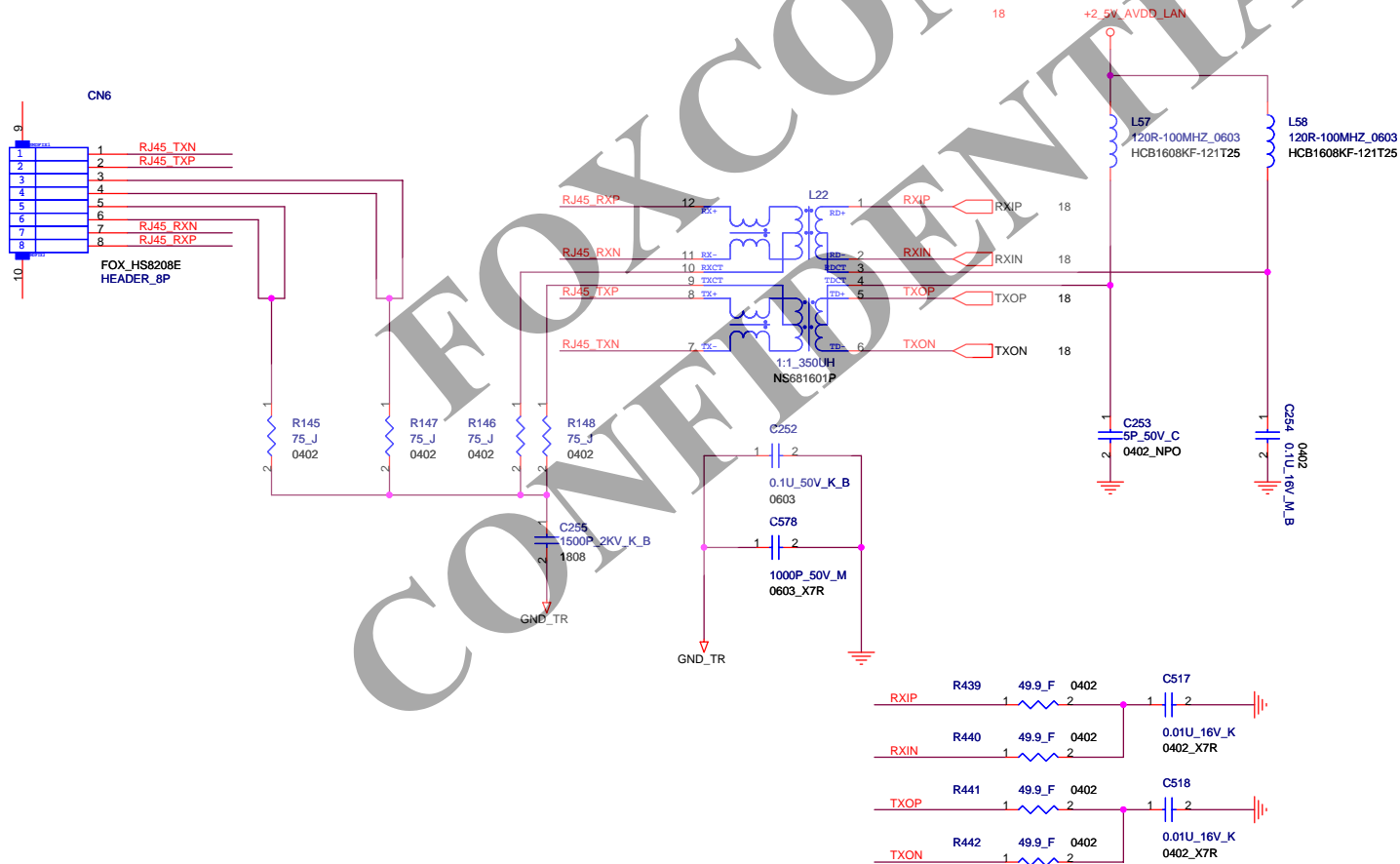
INVERTER CONNECTOR



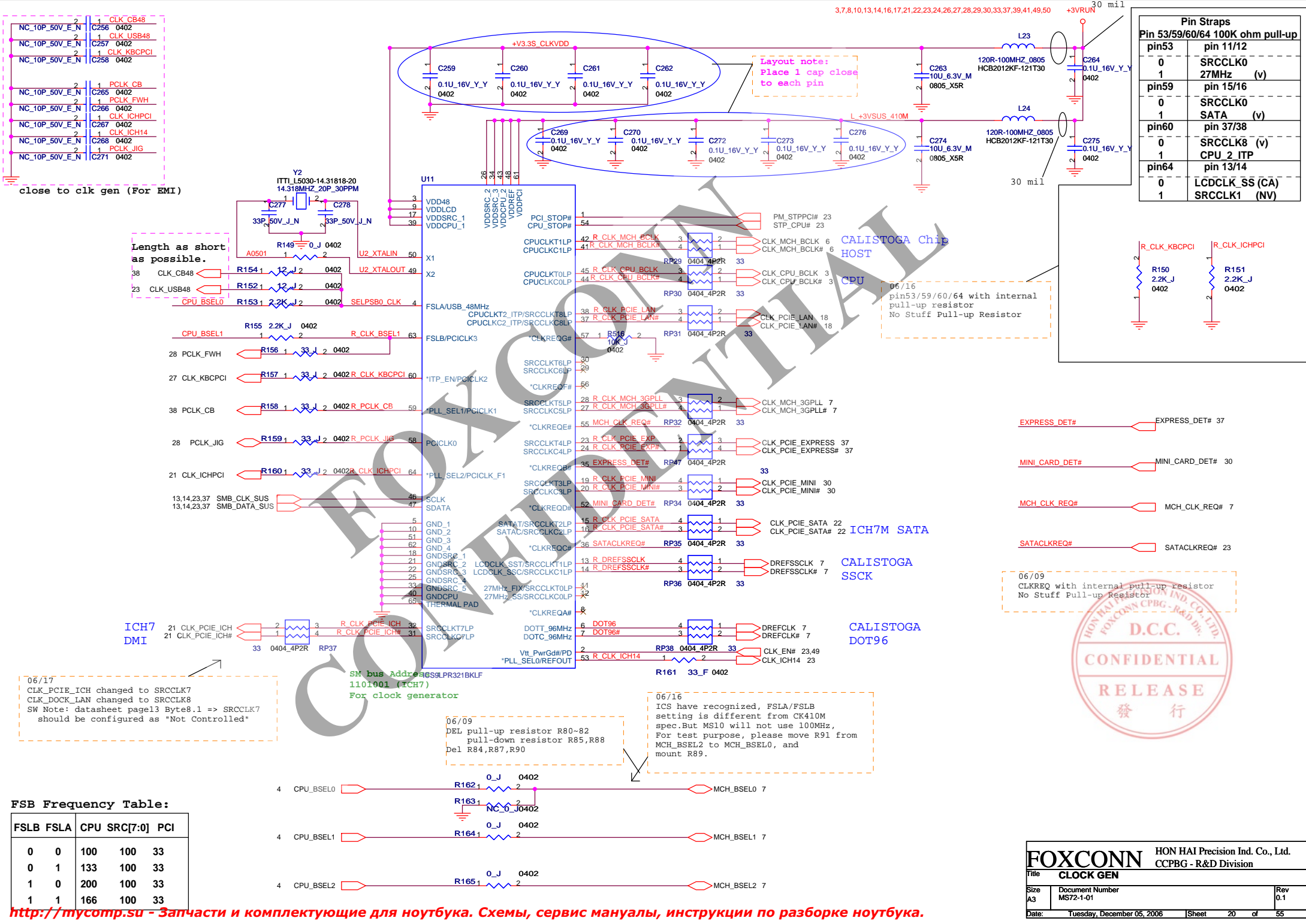
DISCHARGE

The R461 will consume about 0.054 Watt (3.3x3.3/200 = 0.054W). We changed resistor to 0603 size (1/8 Watt)

FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division			
Title LVDS			
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NC_10P_50V_E_N	2	1	CLK_CB48
NC_10P_50V_E_N	2	1	CLK_USB48
NC_10P_50V_E_N	2	1	CLK_KBCPCI
NC_10P_50V_E_N	2	1	CLK_KBCPCI
NC_10P_50V_E_N	2	1	PCLK_CB
NC_10P_50V_E_N	2	1	PCLK_FWH
NC_10P_50V_E_N	2	1	CLK_ICHPCI
NC_10P_50V_E_N	2	1	CLK_ICH14
NC_10P_50V_E_N	2	1	PCLK_JIG
NC_10P_50V_E_N	2	1	PCLK_JIG

Pin Straps	
Pin	Signal
pin53	pin 11/12
0	SRCCLK0
1	27MHz (v)
pin59	pin 15/16
0	SRCCLK0
1	SATA (v)
pin60	pin 37/38
0	SRCCLK8 (v)
1	CPU 2 ITP
pin64	pin 13/14
0	LCDCLK_SS (CA)
1	SRCCLK1 (NV)

close to clk gen (For EMI)

Length as short as possible.

ICH7 DMI

FSB Frequency Table:

FSLB	FSLA	CPU	SRC[7:0]	PCI
0	0	100	100	33
0	1	133	100	33
1	0	200	100	33
1	1	166	100	33





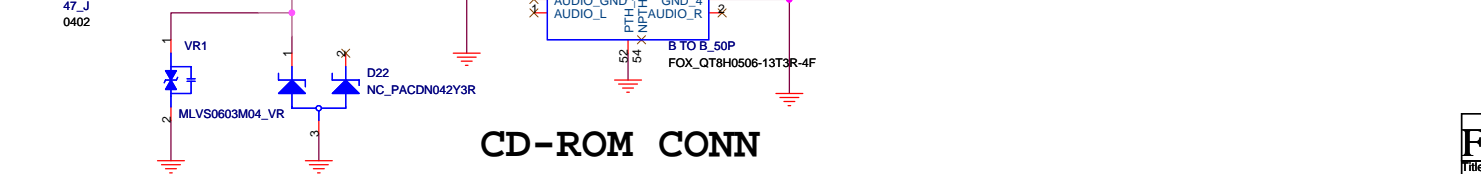
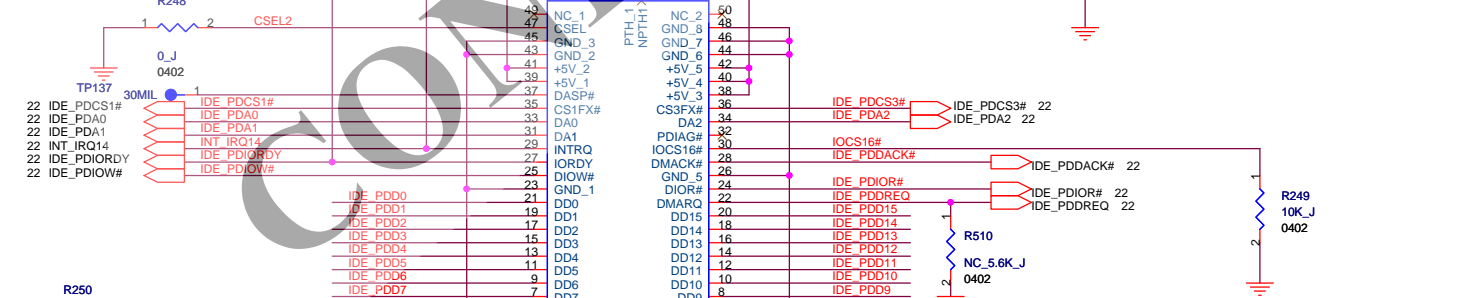
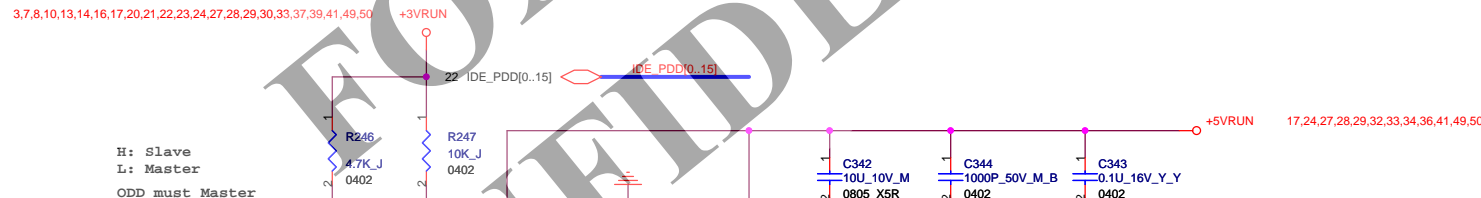
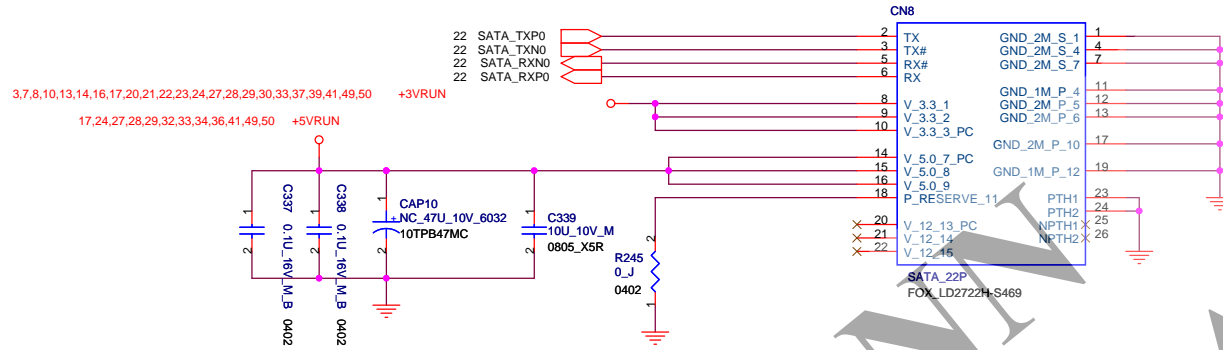
U12E		
A4	VSS[1]	VSS[98]
A23	VSS[2]	VSS[99]
B1	VSS[3]	VSS[100]
B8	VSS[4]	VSS[101]
B11	VSS[5]	VSS[102]
B14	VSS[6]	VSS[103]
B17	VSS[7]	VSS[104]
B20	VSS[8]	VSS[105]
B26	VSS[9]	VSS[106]
B28	VSS[10]	VSS[107]
C2	VSS[11]	VSS[108]
C6	VSS[12]	VSS[109]
C27	VSS[13]	VSS[110]
D10	VSS[14]	VSS[111]
D13	VSS[15]	VSS[112]
D18	VSS[16]	VSS[113]
D21	VSS[17]	VSS[114]
D24	VSS[18]	VSS[115]
E1	VSS[19]	VSS[116]
E2	VSS[20]	VSS[117]
E4	VSS[21]	VSS[118]
E8	VSS[22]	VSS[119]
F16	VSS[23]	VSS[120]
F3	VSS[24]	VSS[121]
F4	VSS[25]	VSS[122]
F5	VSS[26]	VSS[123]
F12	VSS[27]	VSS[124]
F27	VSS[28]	VSS[125]
F28	VSS[29]	VSS[126]
G1	VSS[30]	VSS[127]
G2	VSS[31]	VSS[128]
G5	VSS[32]	VSS[129]
G6	VSS[33]	VSS[130]
G9	VSS[34]	VSS[131]
G14	VSS[35]	VSS[132]
G18	VSS[36]	VSS[133]
G21	VSS[37]	VSS[134]
G24	VSS[38]	VSS[135]
G25	VSS[39]	VSS[136]
G26	VSS[40]	VSS[137]
H3	VSS[41]	VSS[138]
H4	VSS[42]	VSS[139]
H5	VSS[43]	VSS[140]
H24	VSS[44]	VSS[141]
H27	VSS[45]	VSS[142]
H28	VSS[46]	VSS[143]
J1	VSS[47]	VSS[144]
J2	VSS[48]	VSS[145]
J5	VSS[49]	VSS[146]
J24	VSS[50]	VSS[147]
J25	VSS[51]	VSS[148]
J26	VSS[52]	VSS[149]
K24	VSS[53]	VSS[150]
K27	VSS[54]	VSS[151]
K28	VSS[55]	VSS[152]
L13	VSS[56]	VSS[153]
L15	VSS[57]	VSS[154]
L24	VSS[58]	VSS[155]
L25	VSS[59]	VSS[156]
L26	VSS[60]	VSS[157]
M3	VSS[61]	VSS[158]
M4	VSS[62]	VSS[159]
M5	VSS[63]	VSS[160]
M12	VSS[64]	VSS[161]
M13	VSS[65]	VSS[162]
M14	VSS[66]	VSS[163]
M15	VSS[67]	VSS[164]
M16	VSS[68]	VSS[165]
M17	VSS[69]	VSS[166]
M24	VSS[70]	VSS[167]
M27	VSS[71]	VSS[168]
M28	VSS[72]	VSS[169]
N1	VSS[73]	VSS[170]
N2	VSS[74]	VSS[171]
N5	VSS[75]	VSS[172]
N6	VSS[76]	VSS[173]
N11	VSS[77]	VSS[174]
N12	VSS[78]	VSS[175]
N13	VSS[79]	VSS[176]
N14	VSS[80]	VSS[177]
N15	VSS[81]	VSS[178]
N16	VSS[82]	VSS[179]
N17	VSS[83]	VSS[180]
N18	VSS[84]	VSS[181]
N24	VSS[85]	VSS[182]
N25	VSS[86]	VSS[183]
N26	VSS[87]	VSS[184]
P3	VSS[88]	VSS[185]
P4	VSS[89]	VSS[186]
P12	VSS[90]	VSS[187]
P13	VSS[91]	VSS[188]
P14	VSS[92]	VSS[189]
P15	VSS[93]	VSS[190]
P16	VSS[94]	VSS[191]
P17	VSS[95]	VSS[192]
P24	VSS[96]	VSS[193]
P27	VSS[97]	VSS[194]

ICH7-M



FOXCONN HON HAI Precision Ind. Co., Ltd.		
CCPBG - R&D Division		
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SATA HDD CONN



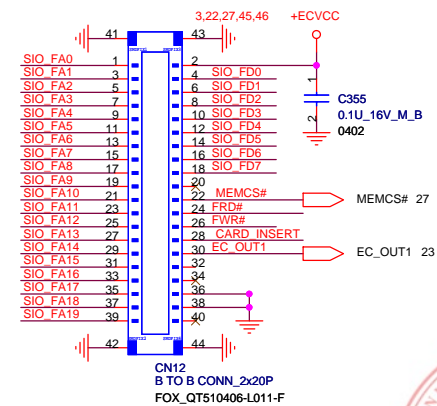
CD-ROM CONN

<http://mycomp.ru> - Запчасти и комплектующие для ноутбука. Следуйте Adoi san suggest ODD: Master/HDD:Slave. Схемы, сервис мануалы, инструкции по разборке ноутбука.



FOXCONN HON HAI Precision Ind. Co., Ltd.		
CCPBG - R&D Division		
Title	SATA HDD/CD-ROM	
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<http://mycomp.su> - Запчасти и комплектующие для ноутбука. Схемы, сервис мануалы, инструкции по разборке ноутбука.

17,24,26,27,28,32,33,34,36,41,49,50 +5VRUN

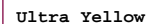


SCROLL_LOCK_LED#

CAR LED#

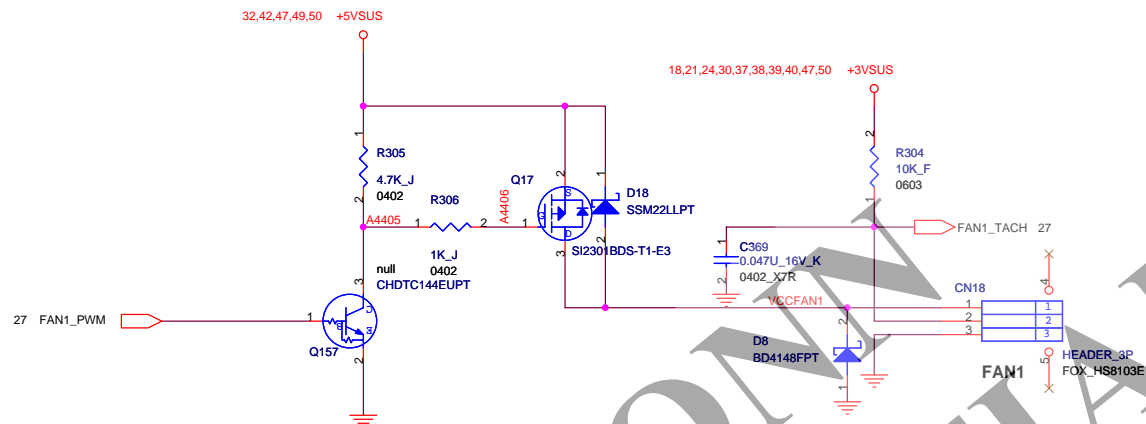


21,23,24,27,41,46,48,50,51 +3VALW



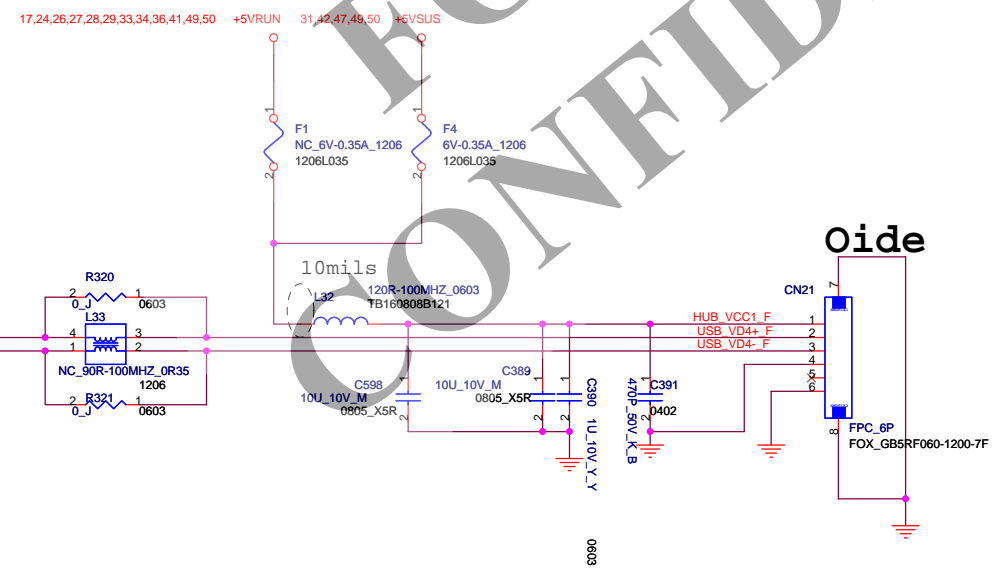
FOXCONN		HON HAI Precision Ind. Co., Ltd.	
Title LED/Touch PAD		CCPBG - R&D Division	
Size A3	Document Number MS72-1-01	Rev 0.1	
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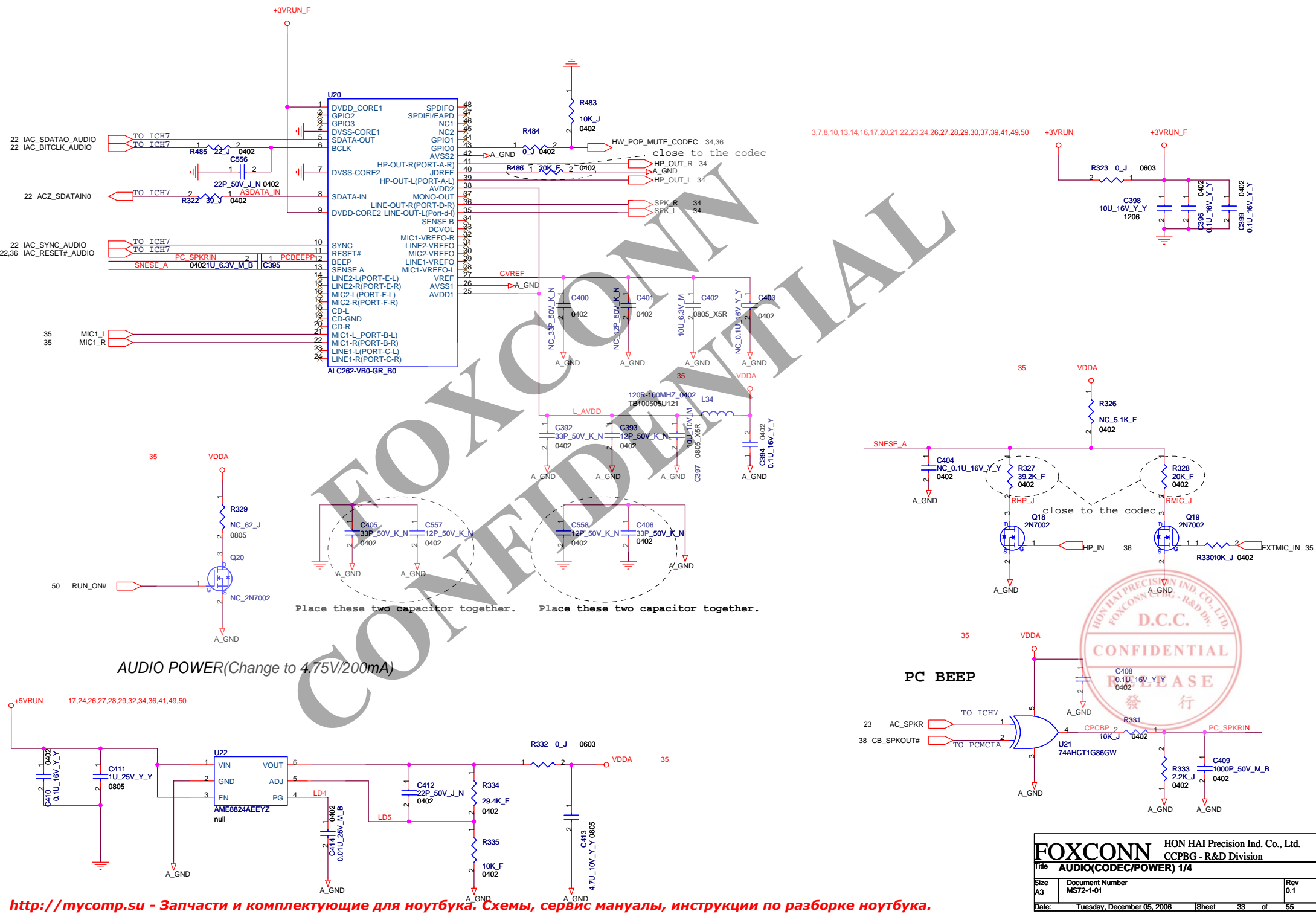
FAN1

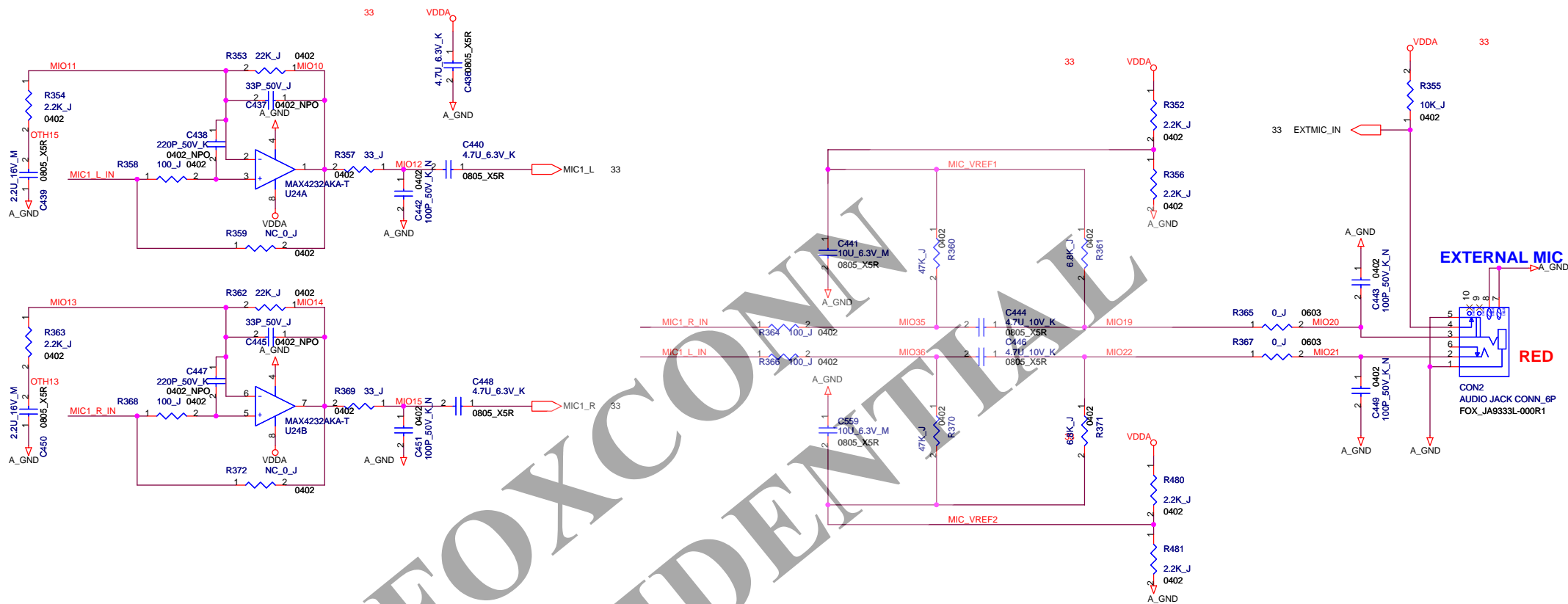


FOXCONN
CONFIDENTIAL

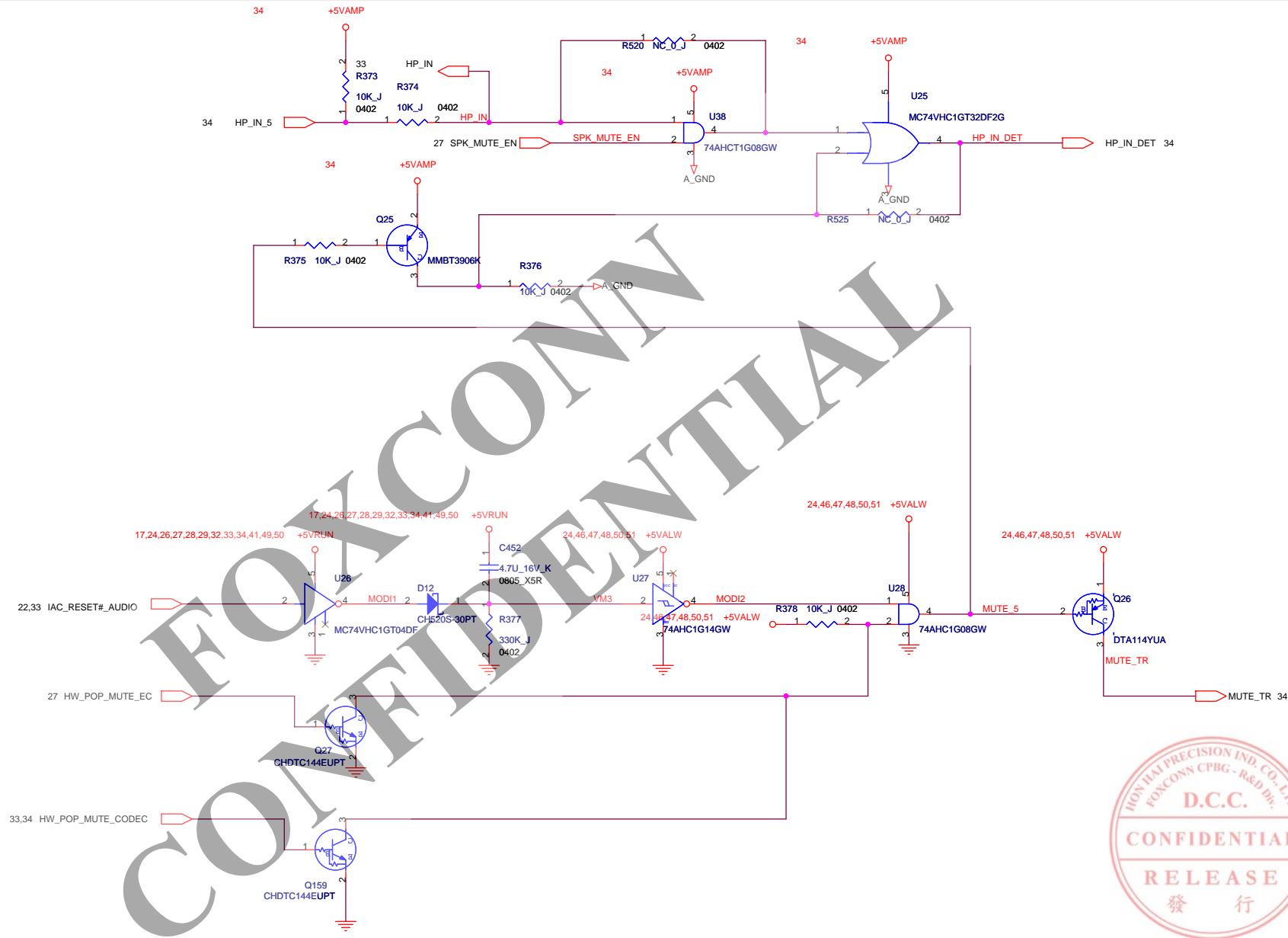




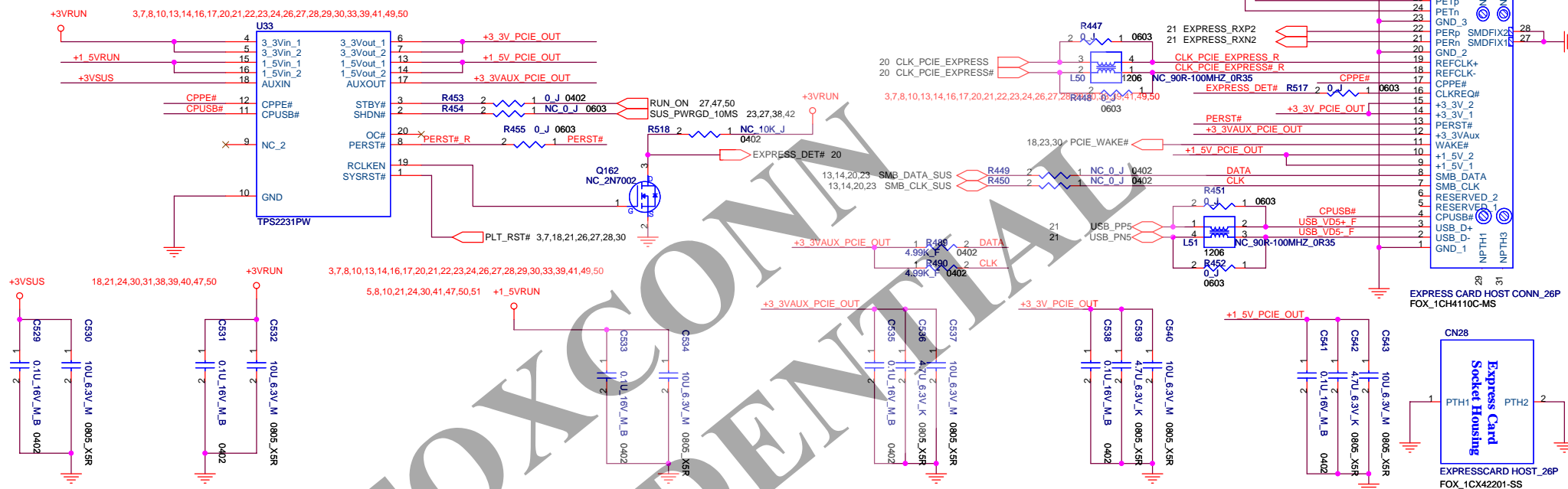




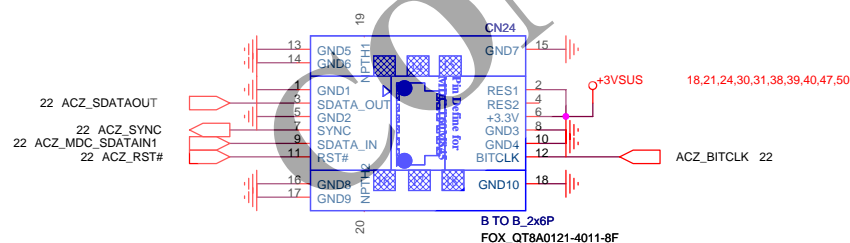
FOXCONN		HON HAI Precision Ind. Co., Ltd.
Title AUDIO(EXT MIC) 3/4		CCPBG - R&D Division
Size A3	Document Number MS72-1-01	Rev 0.1
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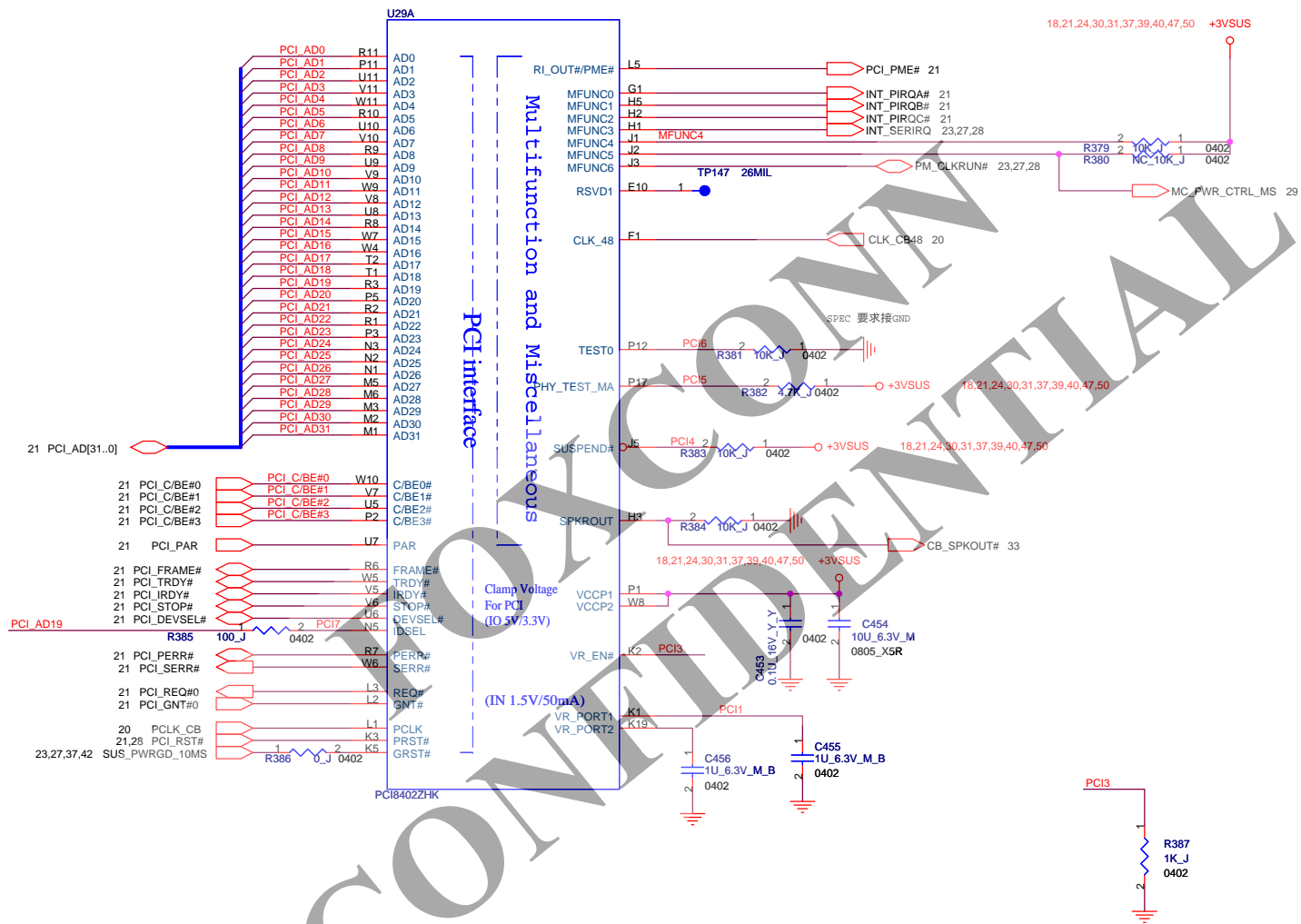
EXPRESS CONN



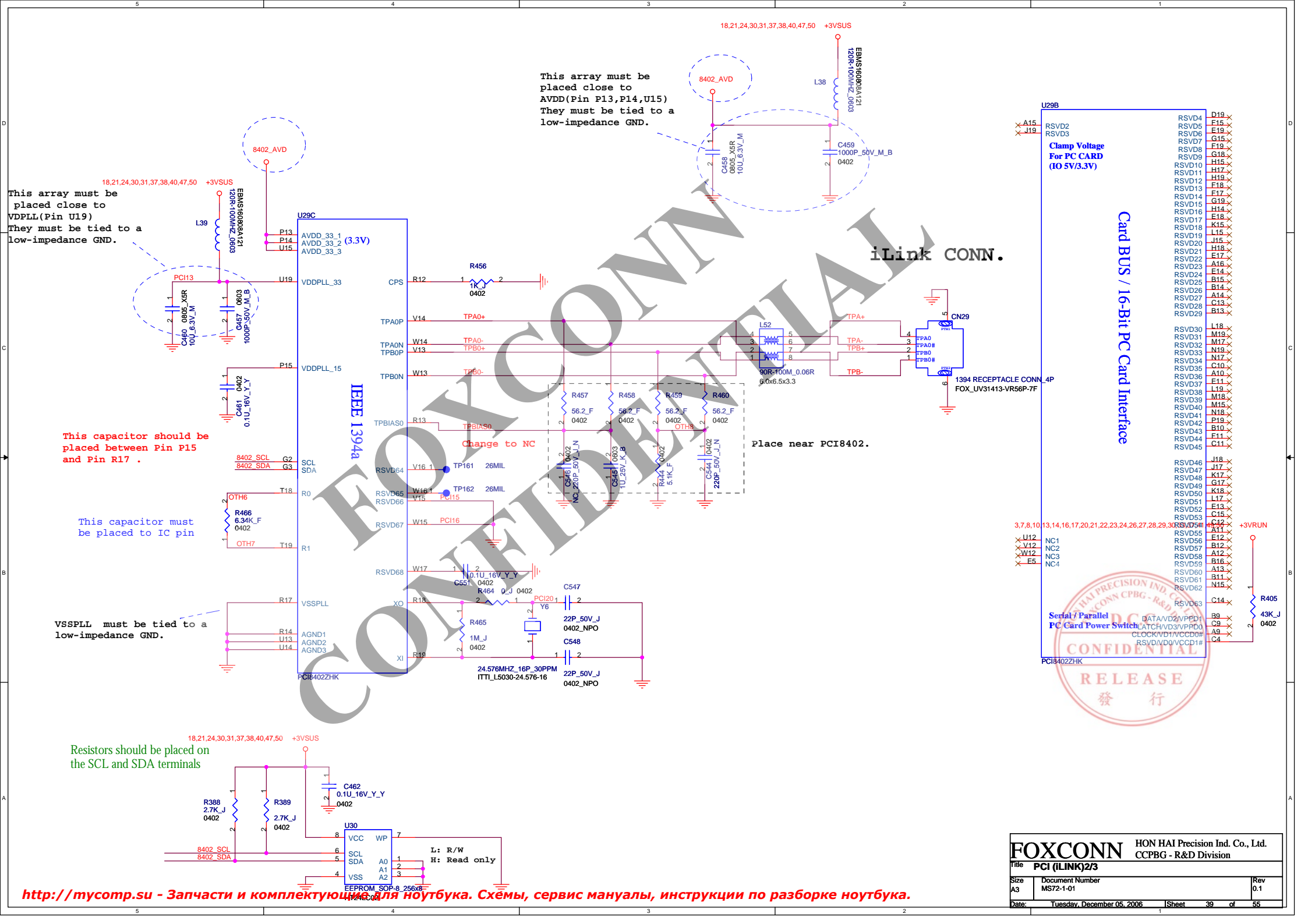
MDC CONN.

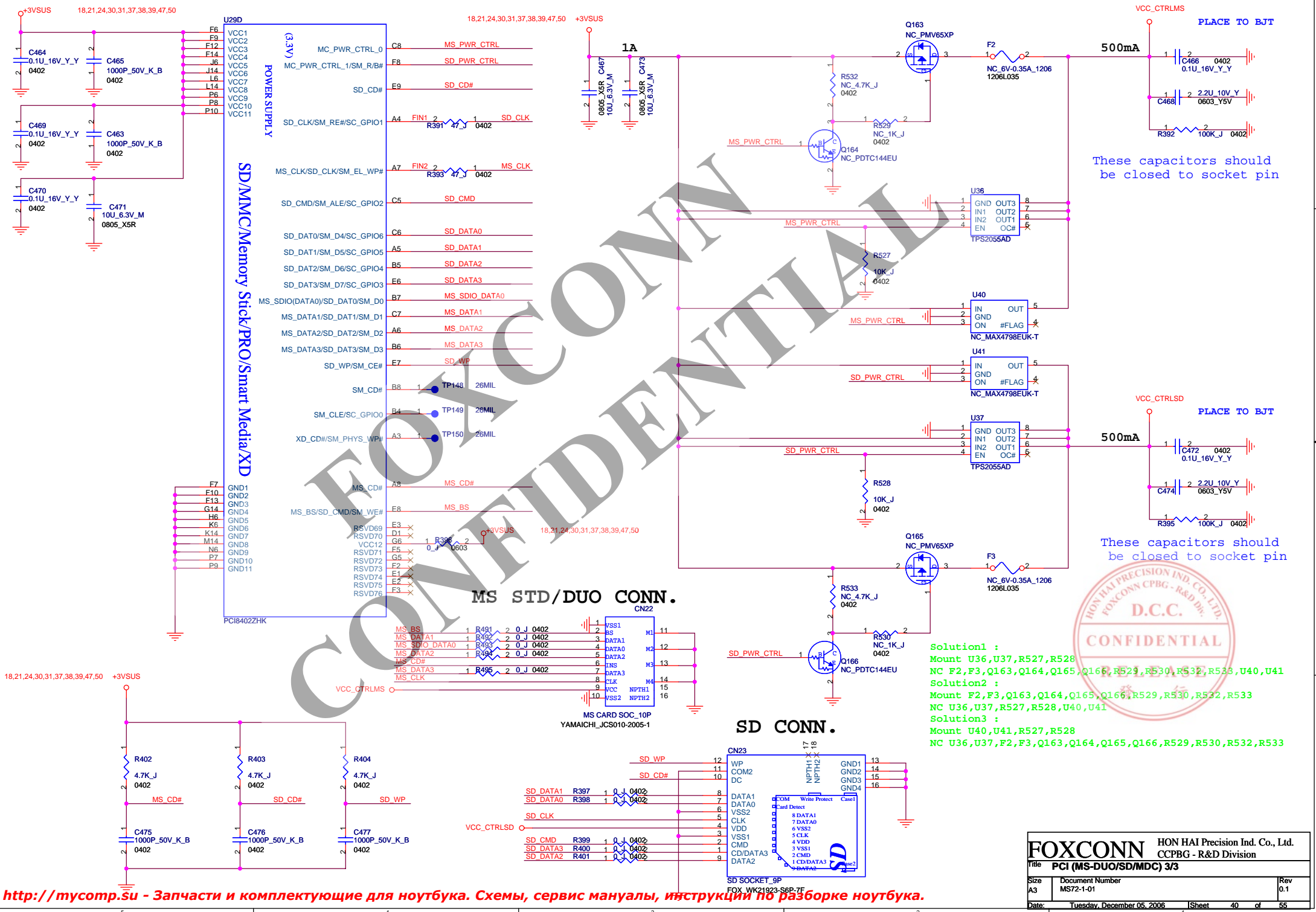


FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
File EXPRESS CARD		
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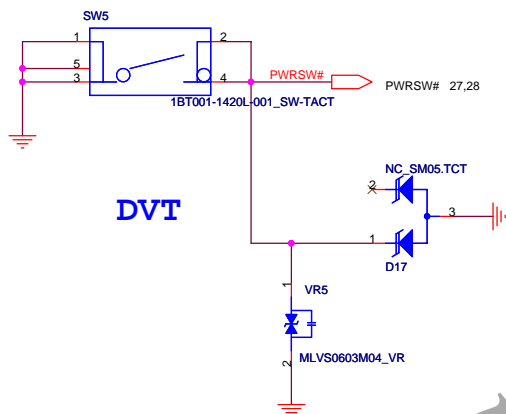


FOXCONN		HON HAI Precision Ind. Co., Ltd.
Title PCI (PCI BUS) 1/3		CCPBG - R&D Division
Size A3	Document Number MS72-1-01	Rev 0.1
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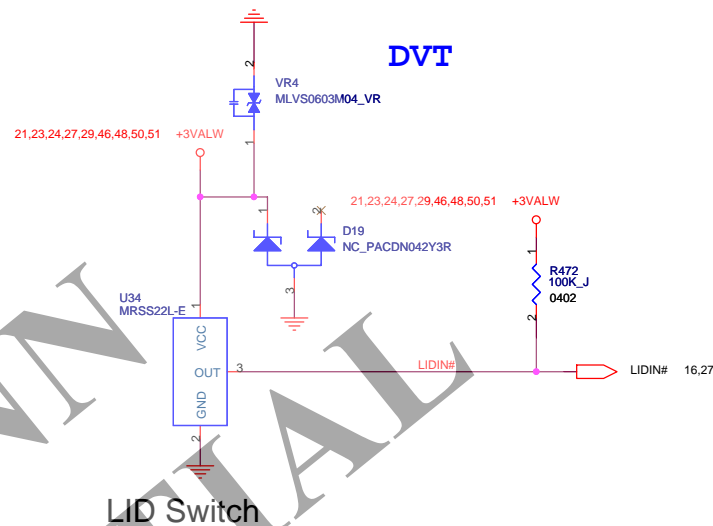




POWER BUTTON

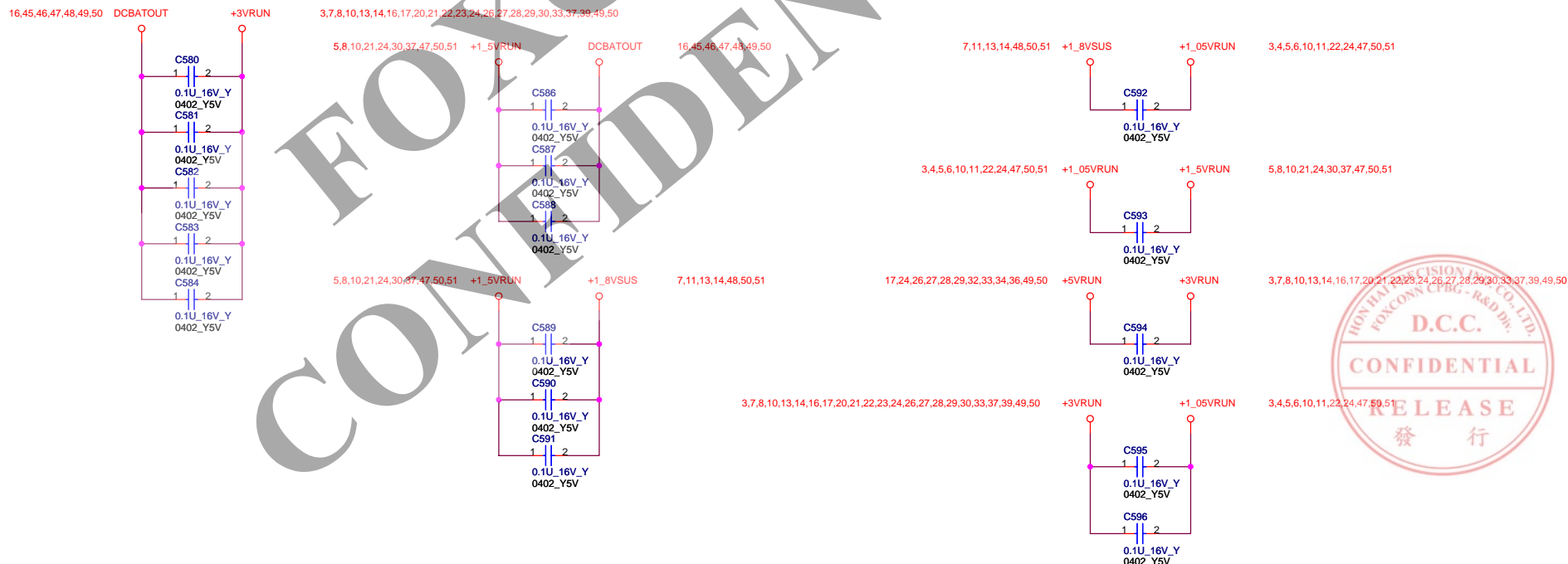


DVT



LID Switch

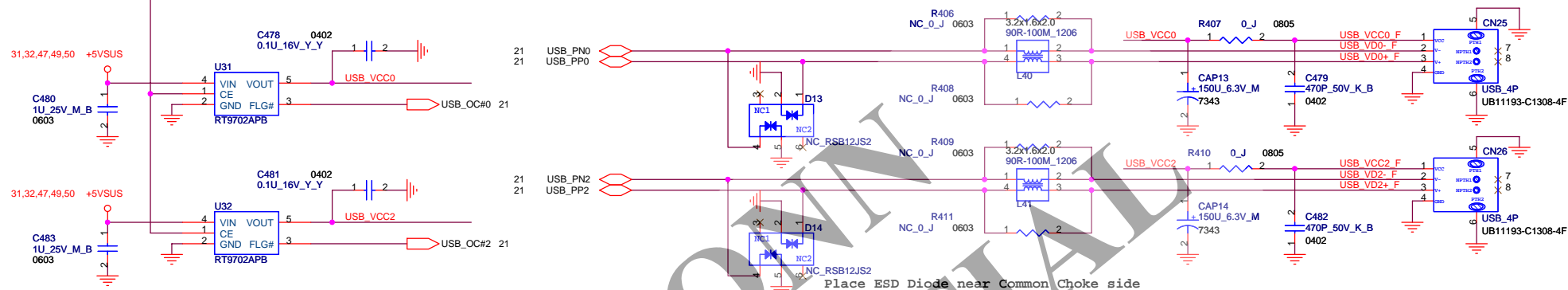
EMI CAP



FOXCONN HON HAI Precision Ind. Co., Ltd. CCPBG - R&D Division		
Title Button/LID Switch/EMI CAP		
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USB CONN X 2

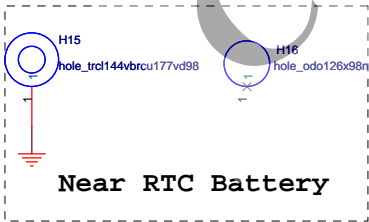
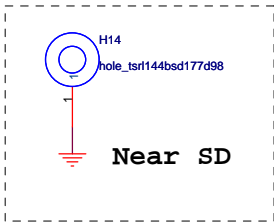
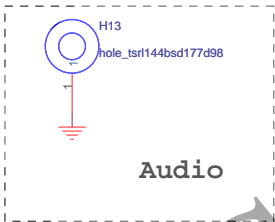
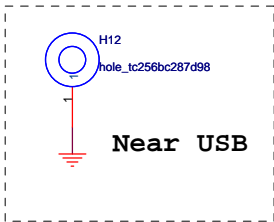
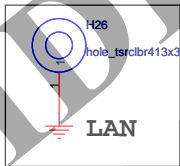
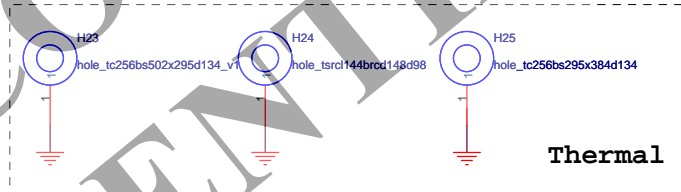
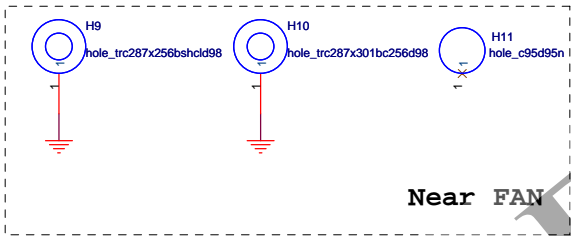
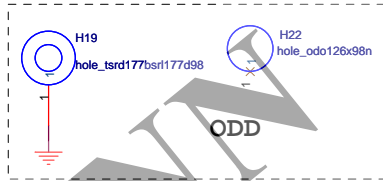
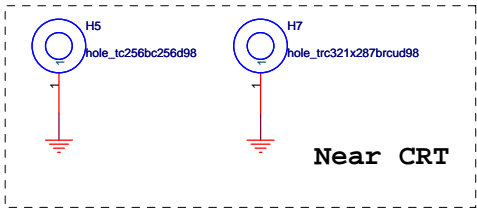
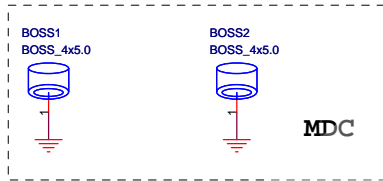
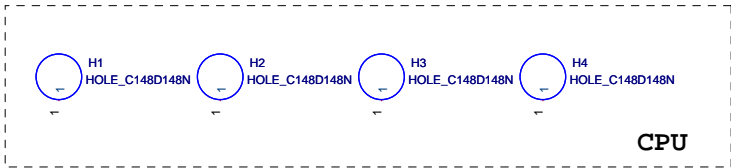
23,27,37,38 SUS_PWRGD_10MS

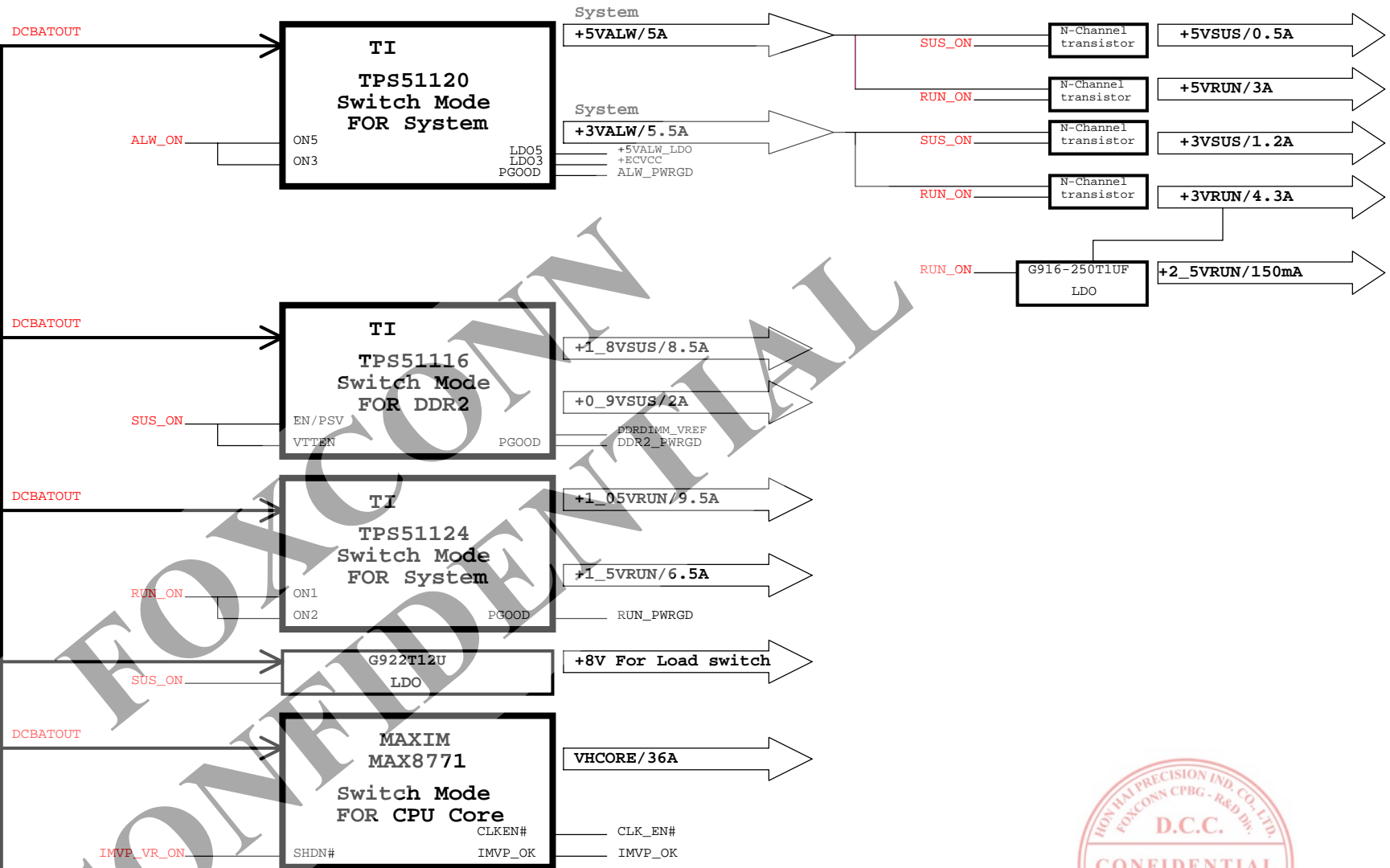
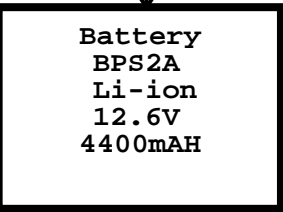
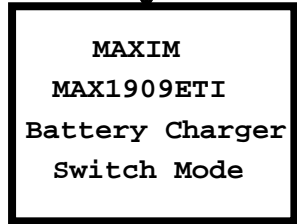
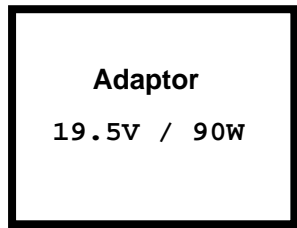


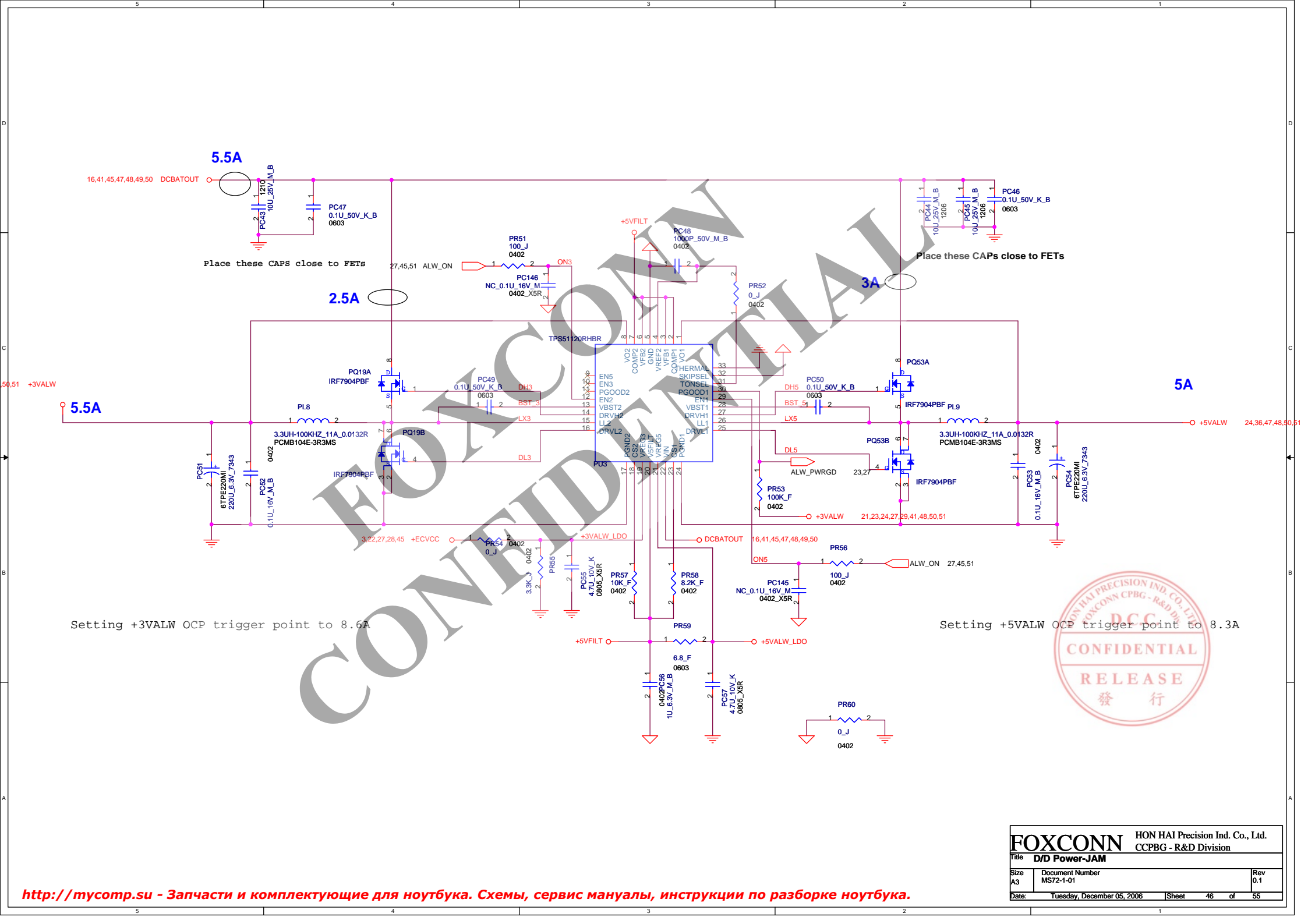
Place ESD Diode near Common Choke side

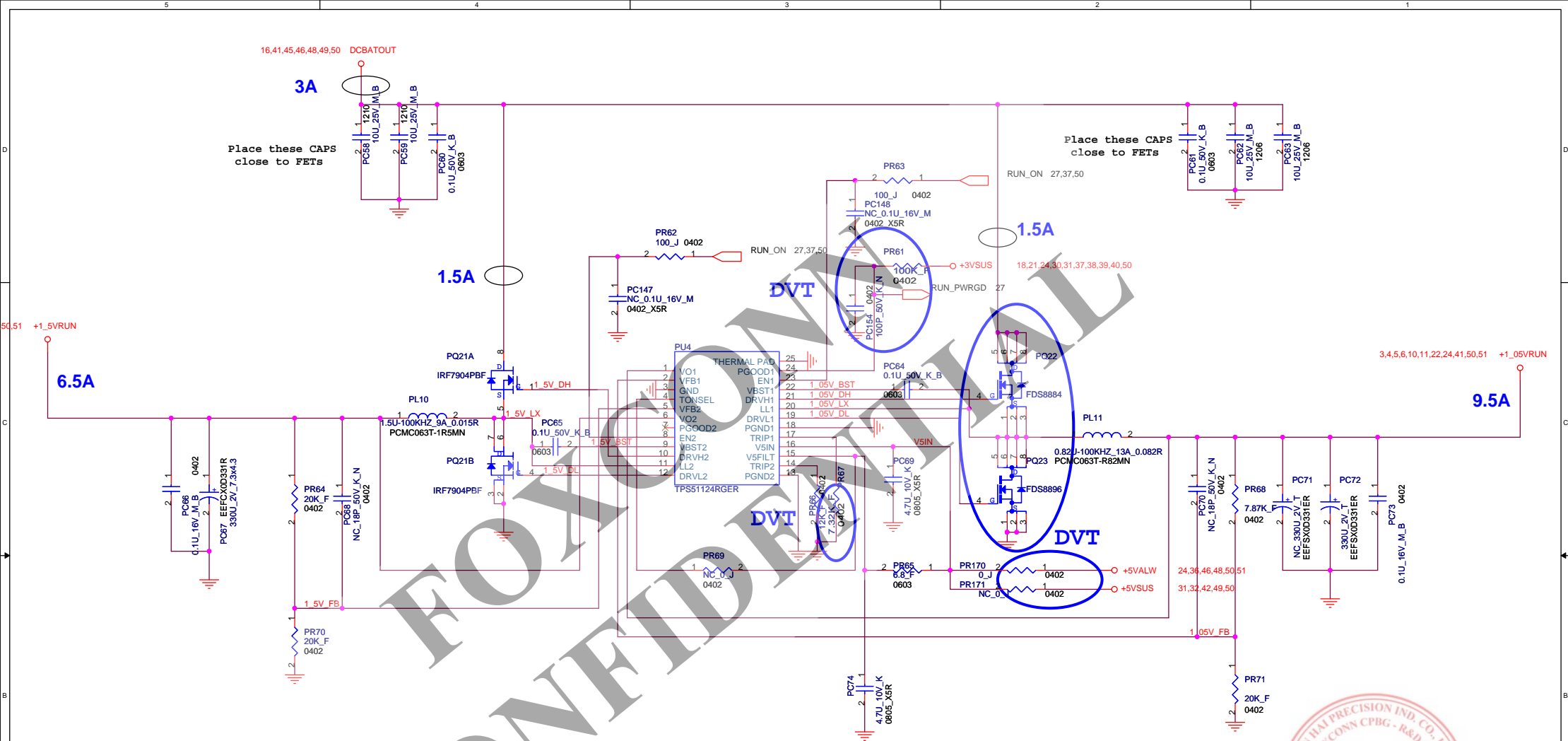
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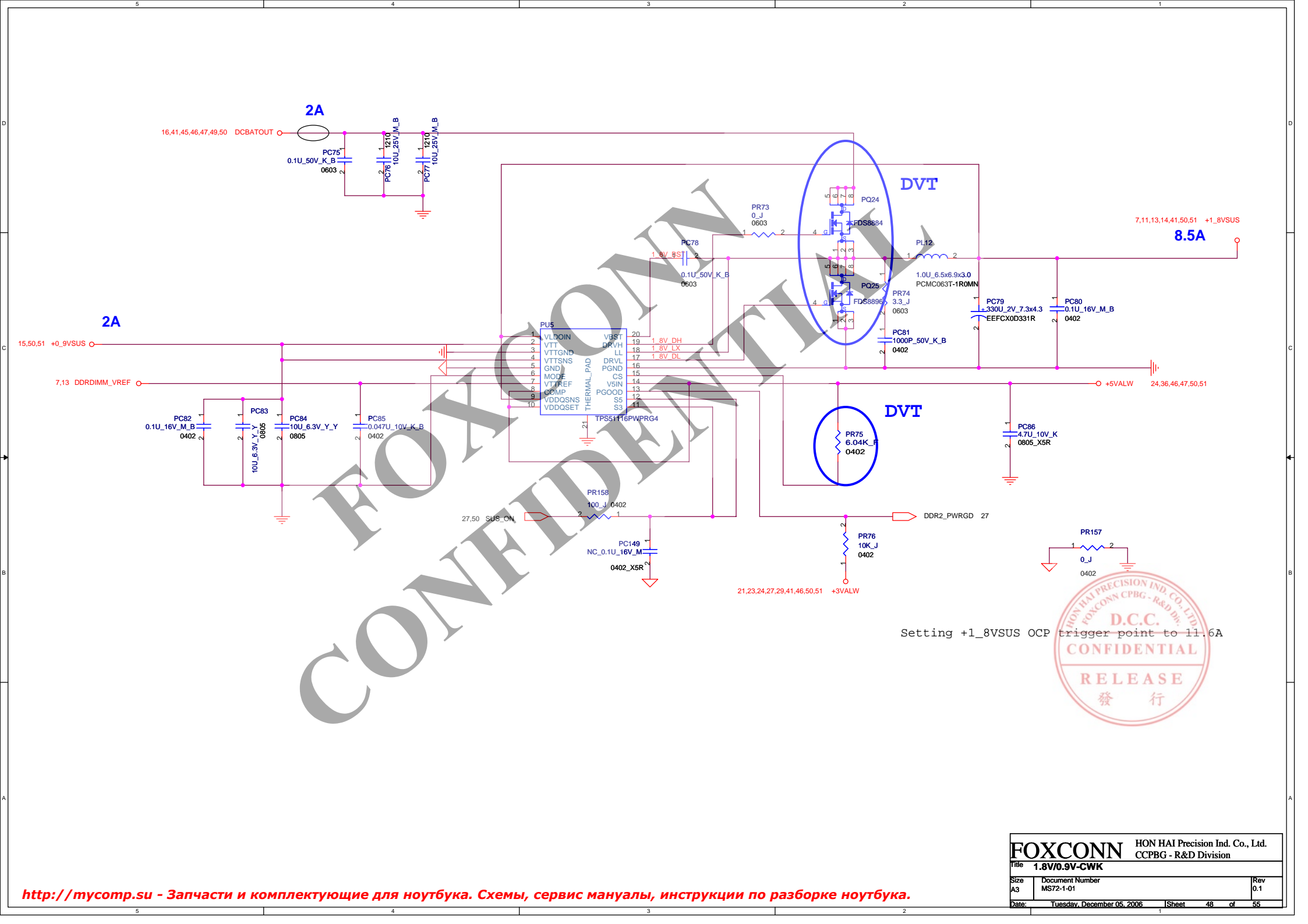




Setting +1_5VRUN OCP trigger point to 10.5A

Setting +1_05VRUN OCP trigger point to 12.8A





Setting +1_8VSUS OCP trigger point to 11.6A





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		CCPBG - R&D Division	
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HISTORY (1)

DVT

(2006/11/14)
P.11 Add C608 and NC CAP7 for purchase convenient.
P.17 Add R544 and NC R121for CRB requirement(refer to Capell_Valley_CRB_Schematics_Rev1_502 page18)
P.33 NC R483 for Page36 had pull low 10k resistor to GND
P.34 Change CAP11,CAP12 from 1C-31T0337-M101 to 1C-42T0107-M100 for application modification
P.36 Add Q167, Q168, R536 and NC U26, U27,U28, D12, C452, R377,R378 for application modification
Change Q27, Q159, from 17-CHDTC14-4E01 to 17-PMBT390-4200 for application modification
Change Q26 from 17-DTA114Y-UA00 to 17-MMBT390-6K00 for application modification
Add R540,R541,R542,R543,R537,R538,R539,C609 for application modification

(2006/11/15)
P.47 Change PQ22 from 17-1RF7807-2000 to 17-FDS8884-0000 for application modification
Change PQ23 from 17-1RF8113-0000 to 17-FDS8896-0000 for application modification
P.48 Change PQ24 from 17-1RF7807-2000 to 17-FDS8884-0000 for application modification
Change PQ25 from 17-1RF8113-0000 to 17-FDS8896-0000 for application modification

(2006/11/20)
P.11 Del C608 and mount CAP7 for MOR requirement
P.17 Del R544 and mount R121 for MOR requirement
P.47 Change PR67 from 1R-0000472-F200 to 1R-0007321-F200 for power PWM OCP application modification
P.48 Change PR75 from 1R-0000562-F200 to 1R-0006041-F200 for power PWM OCP application modification

(2006/11/21)
P.33 Add R483 for MOR requirement
P.36 Del Q167, Q168, R536 and add U26, U27,U28,D12, C452, R377,R378 for MOR requirement
Change Q27, Q159, from 17-PMBT390-42001 to 17-CHDTC14-4E01for MOR requirement
Change Q26 from 17-MMBT390-6K00 to 17-DTA114Y-UA00 for MOR requirement
Del R540,R541,R542,R543,R537,R538,R539,C609 for MOR requirement

(2006/11/22)
P.28 Add R535 0om for X-BUS CONN del on MP
P.47 Add PC154 and change PR61 from 10k to 100k for RUN_PWRGD glitch issue
Change the power supply of PU4 pin16 from +5VSUS to +5VALW for application modification

(2006/11/23)
P.47 Add PR170,PR171 for MOR requirement

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