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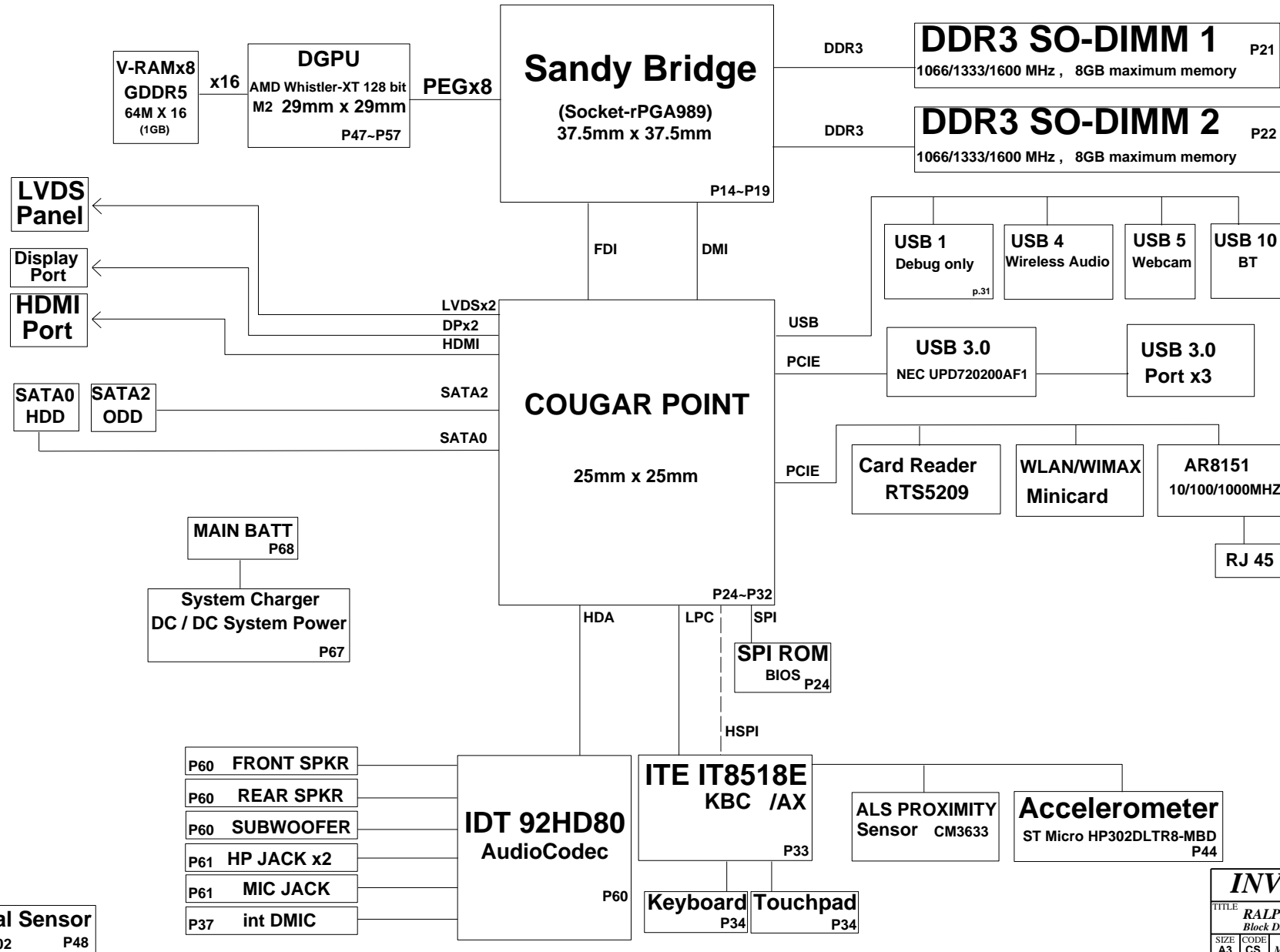
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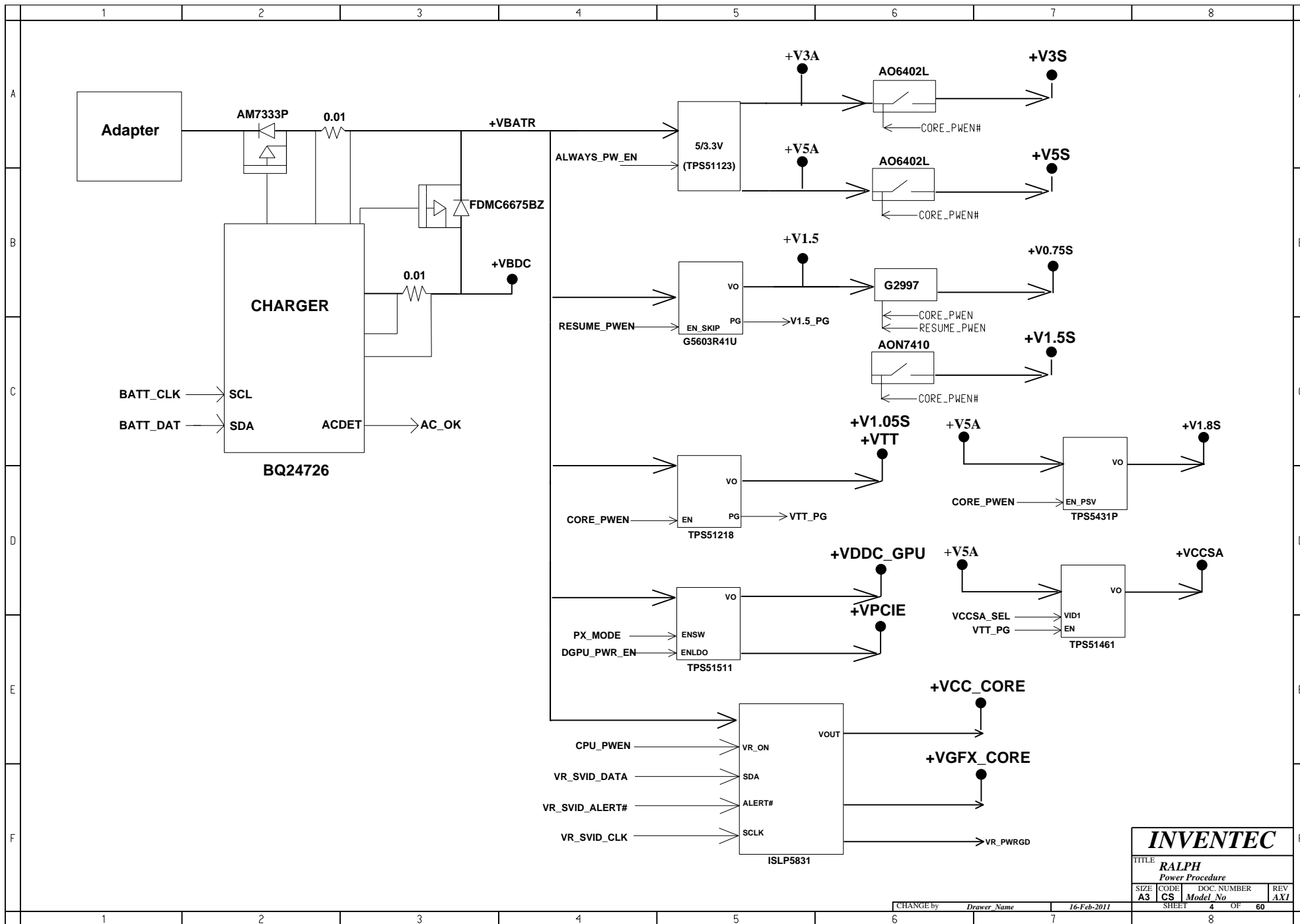
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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
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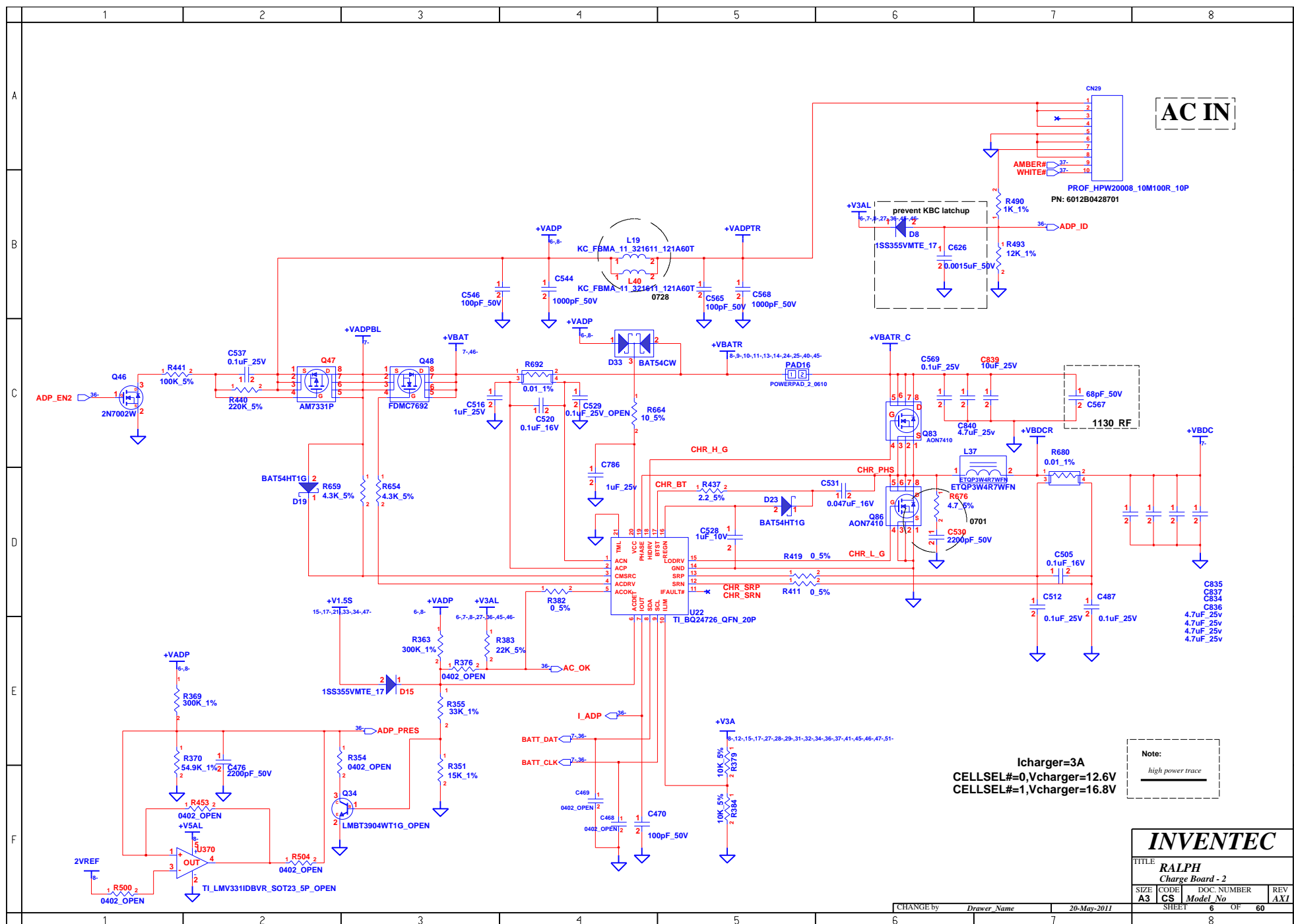
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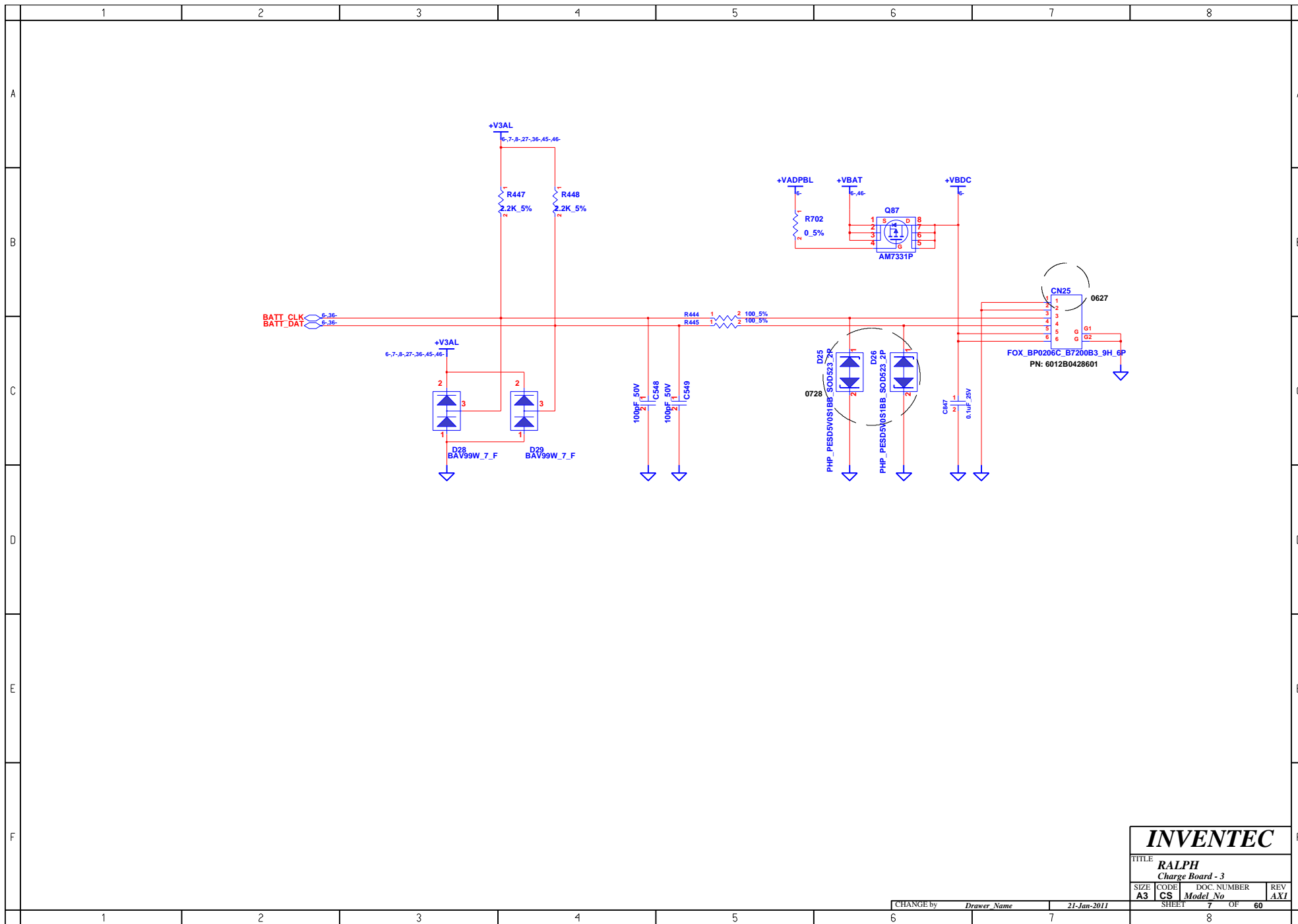


	1	2	3	4	5	6	7	8	
A	<div>BLANK</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

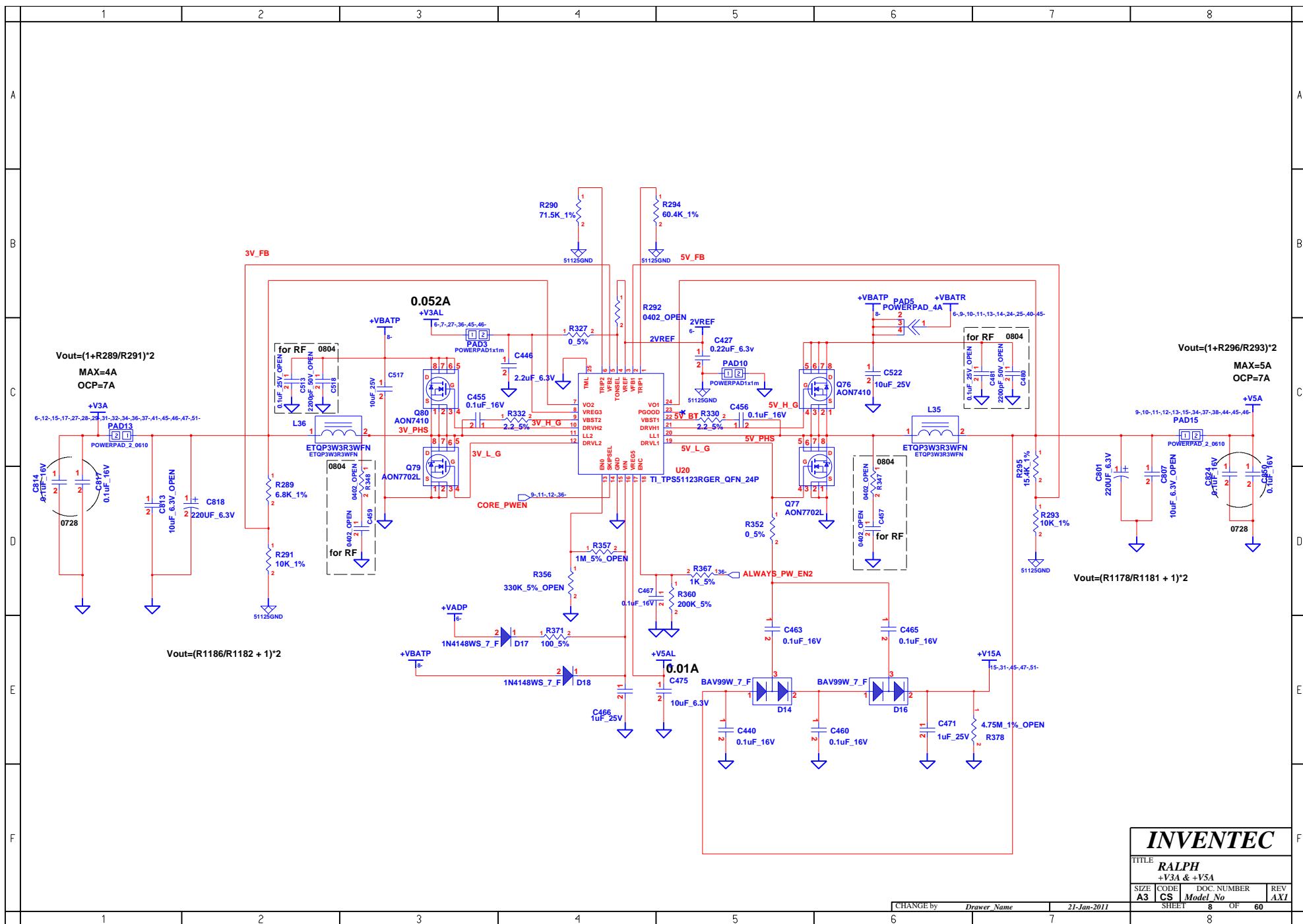
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SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AXI
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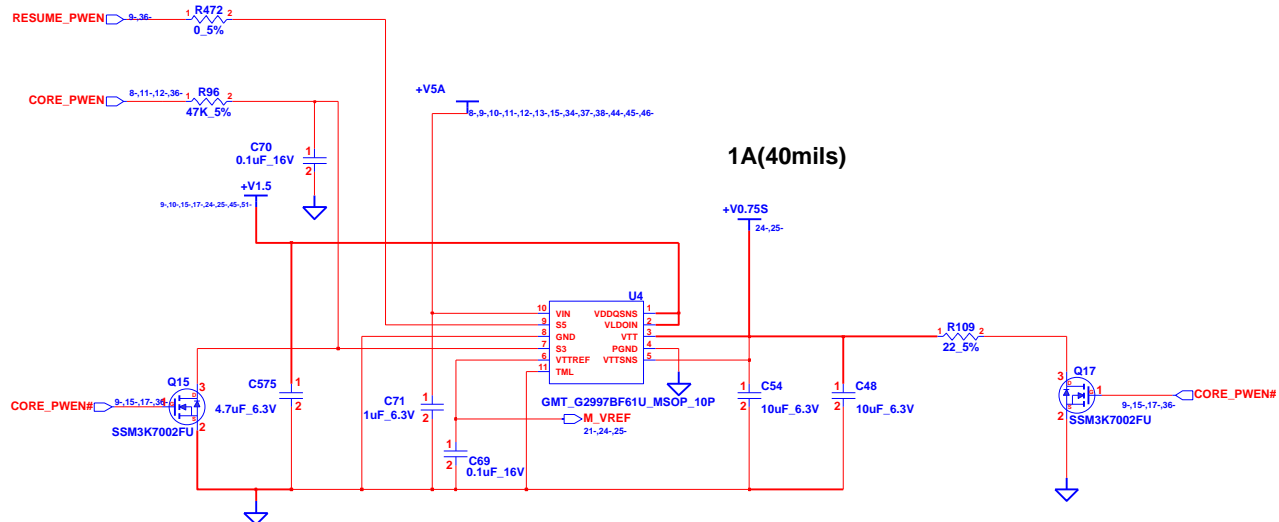
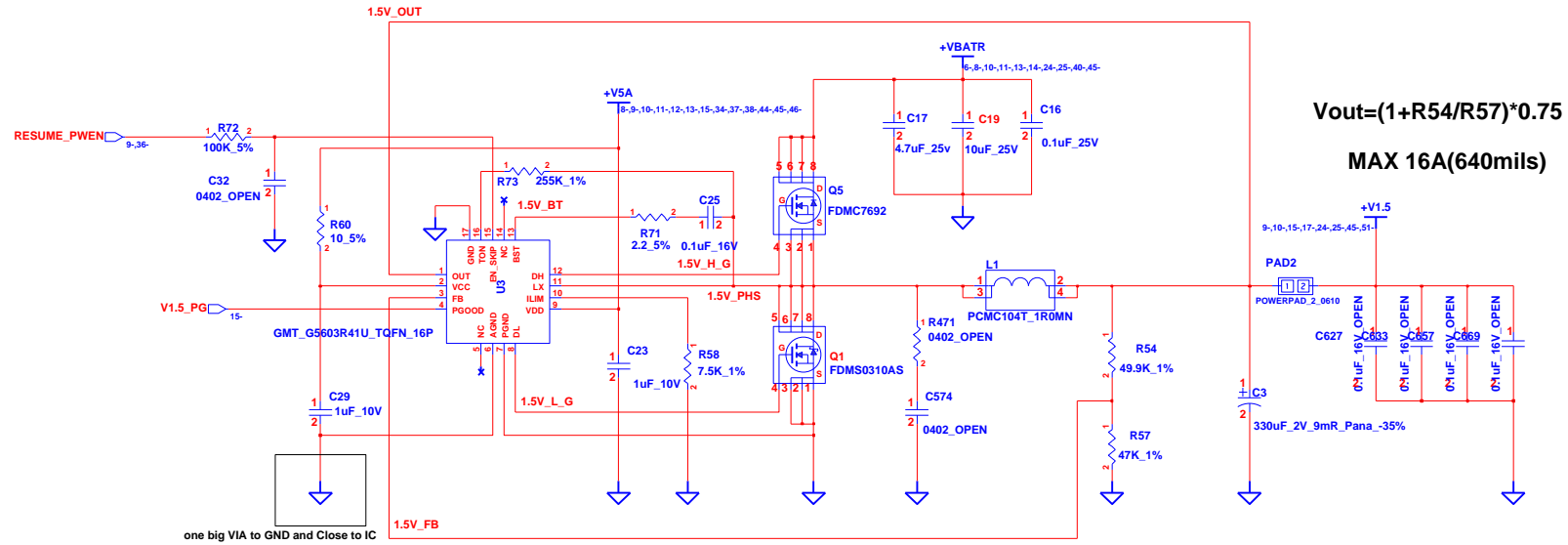
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INVENTEC			
TITLE			
RALPH			
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
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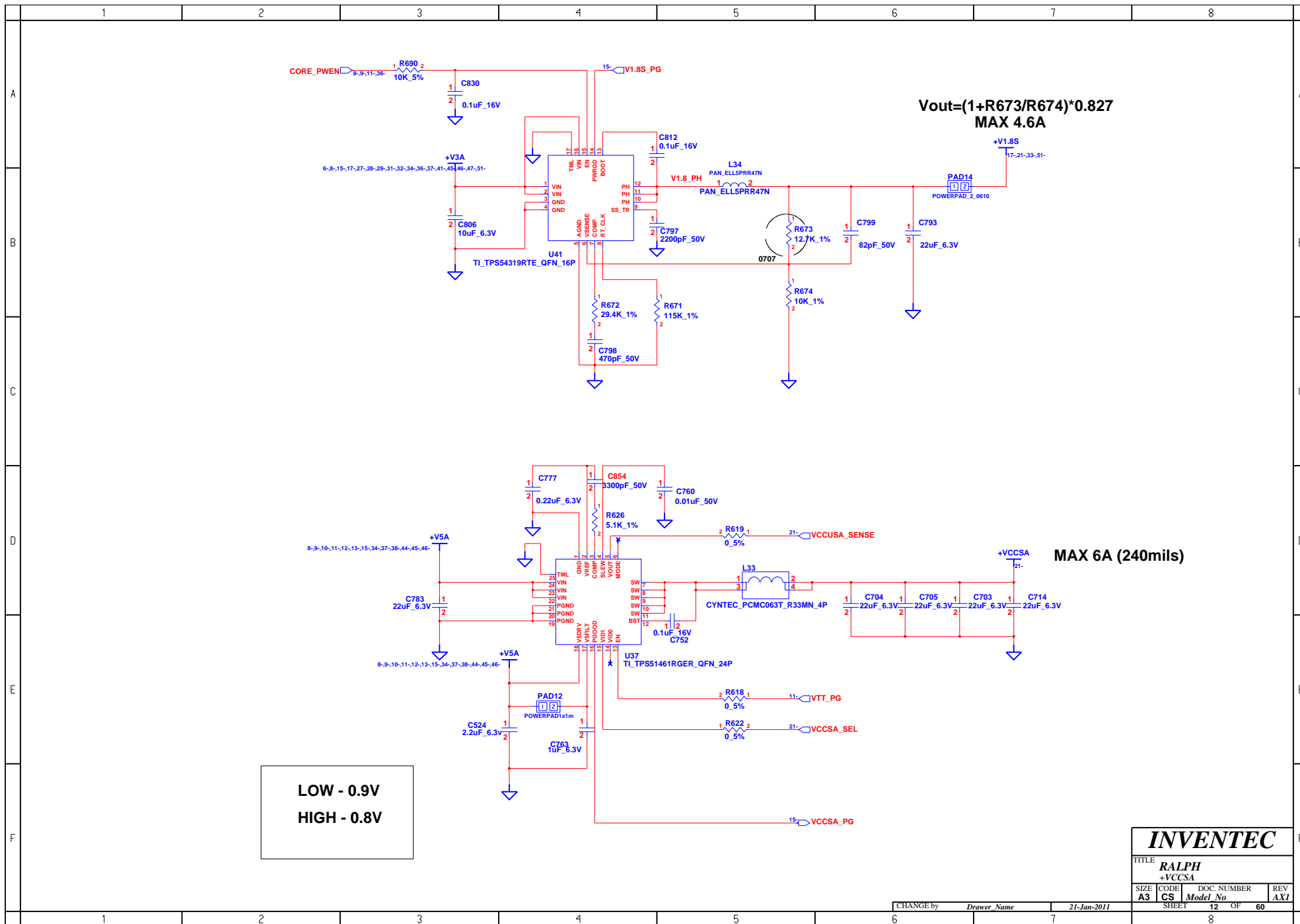


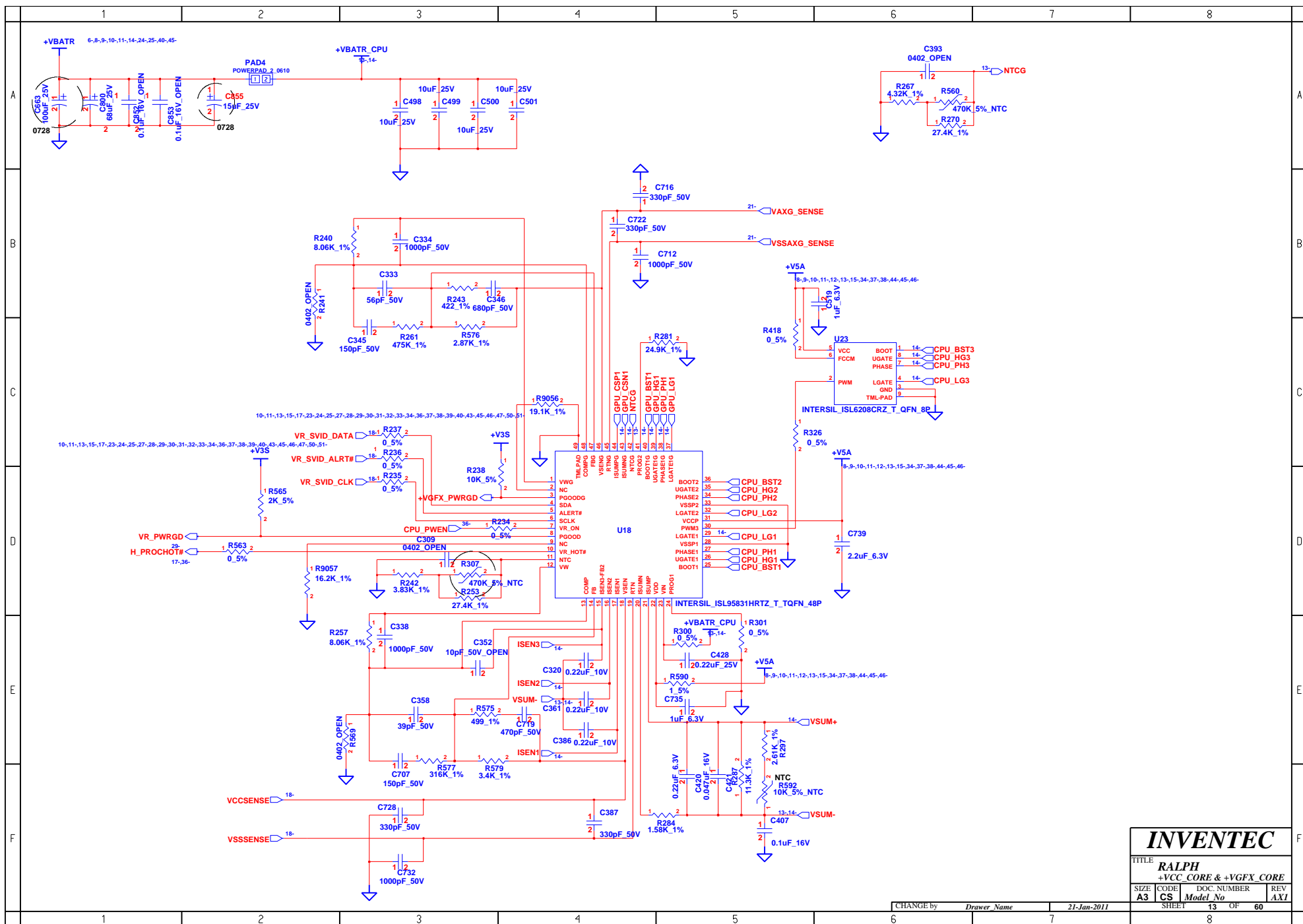


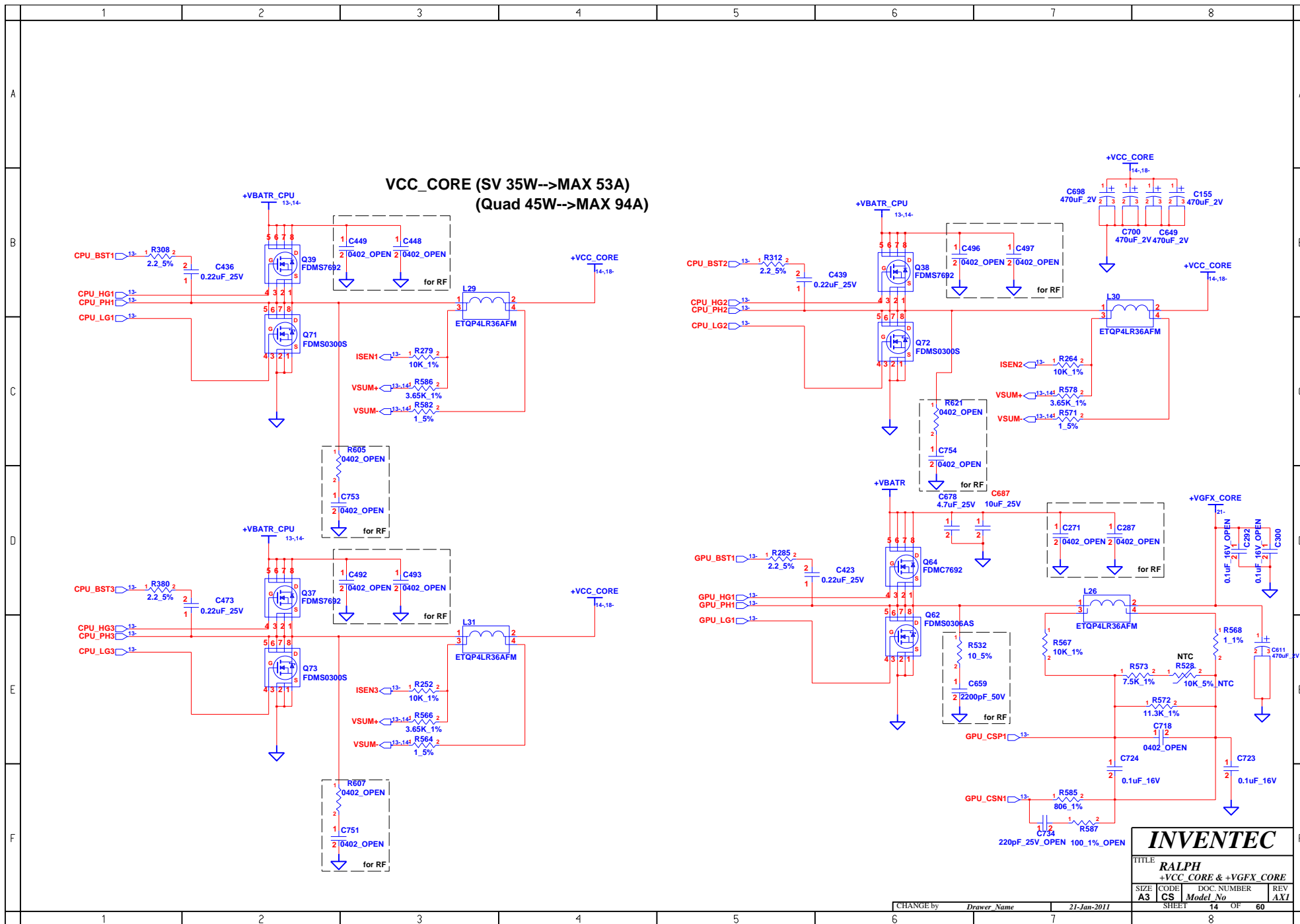
NOTE: DDR3 REGULATOR

INVENTEC

TITLE			
RALPH			
+V1.5 & +V0.75			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
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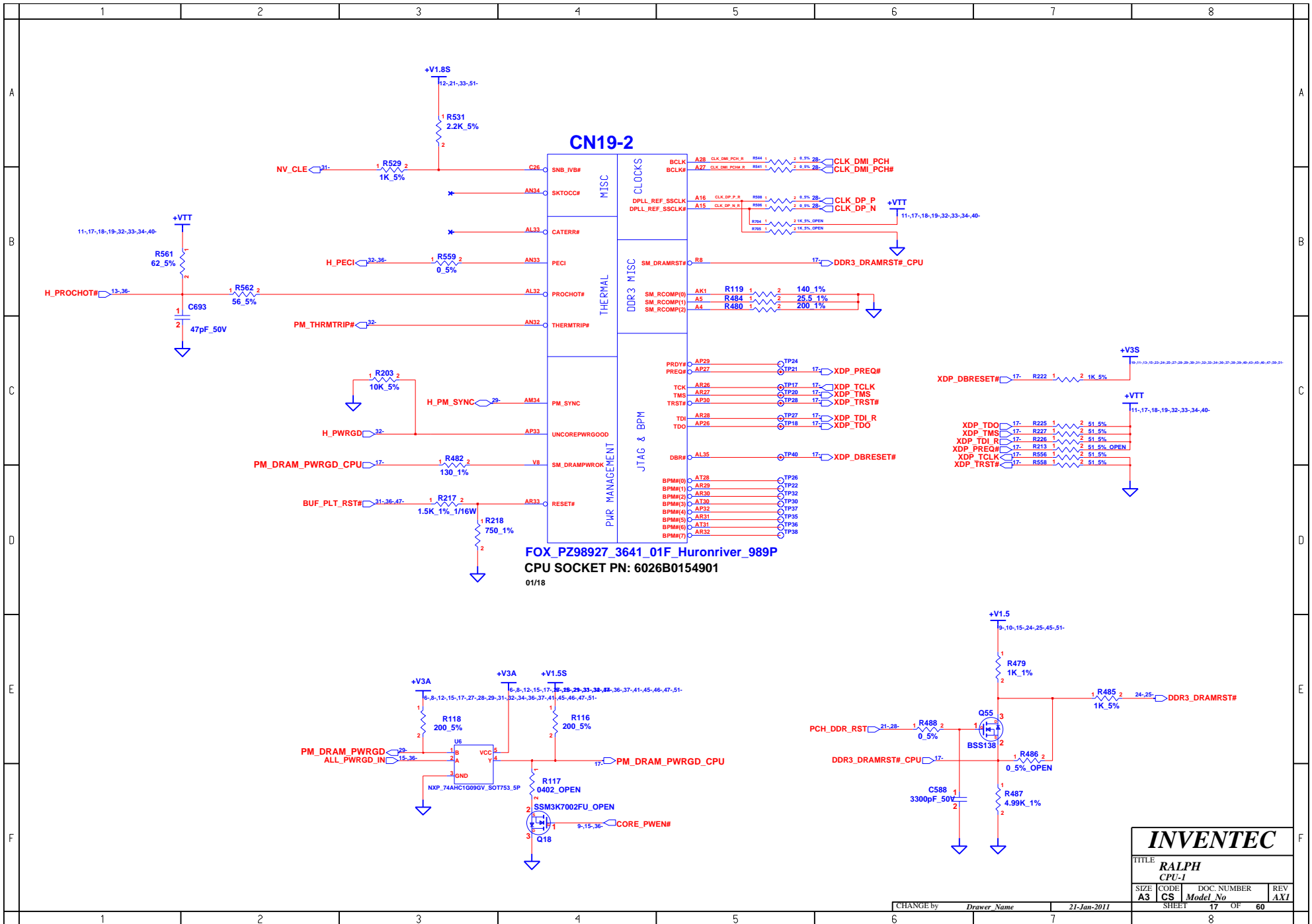


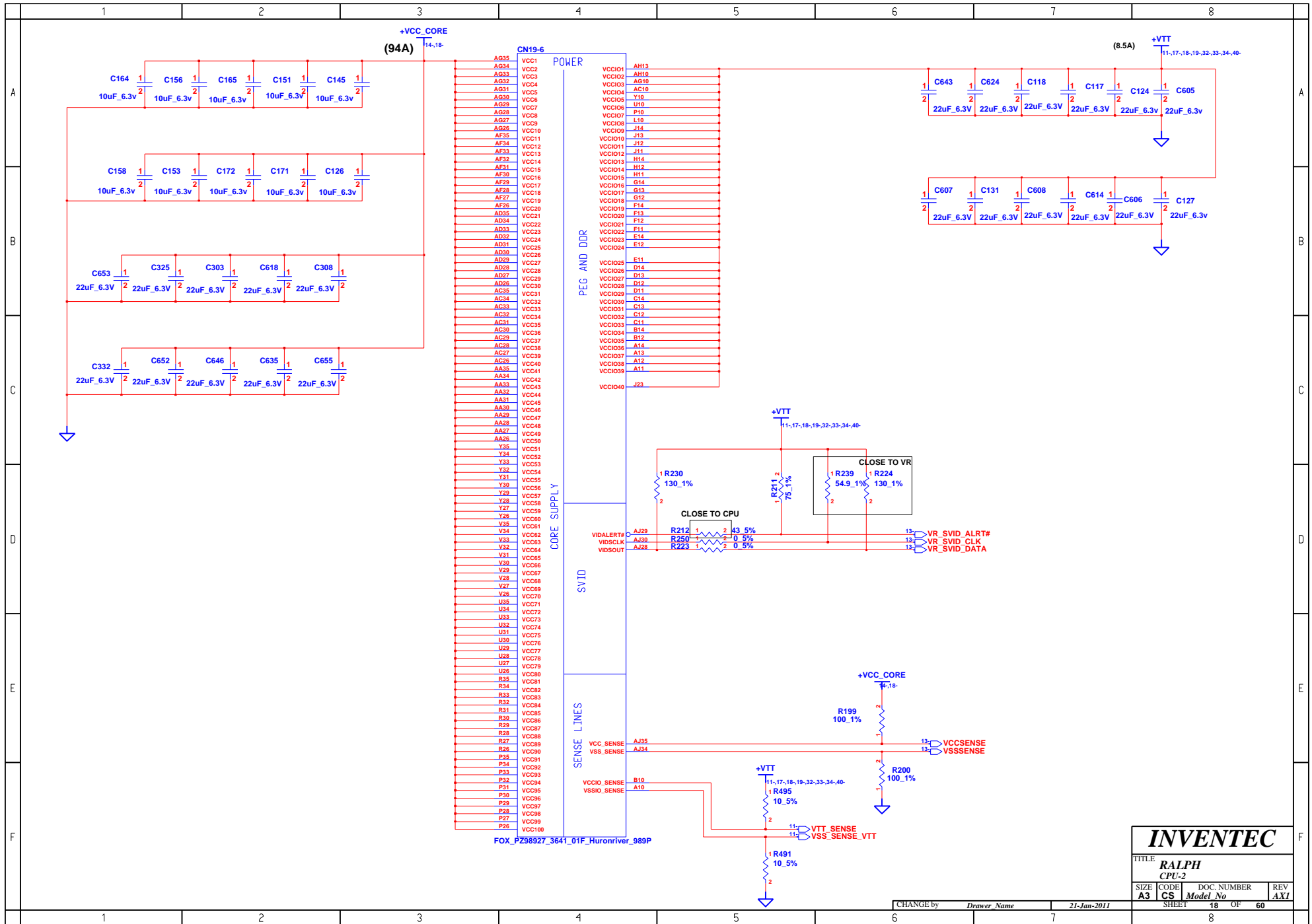


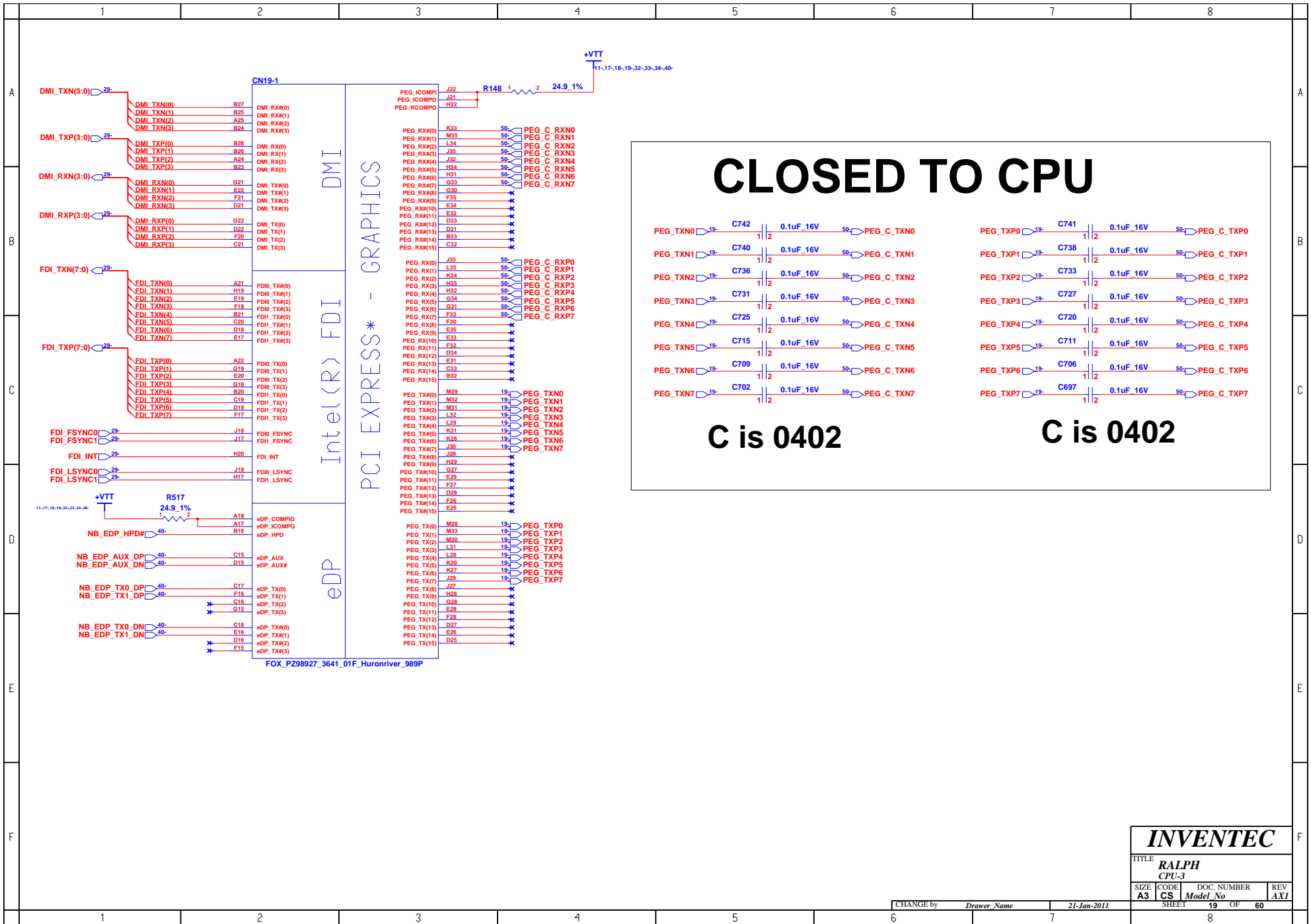
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A	<div>CPU</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

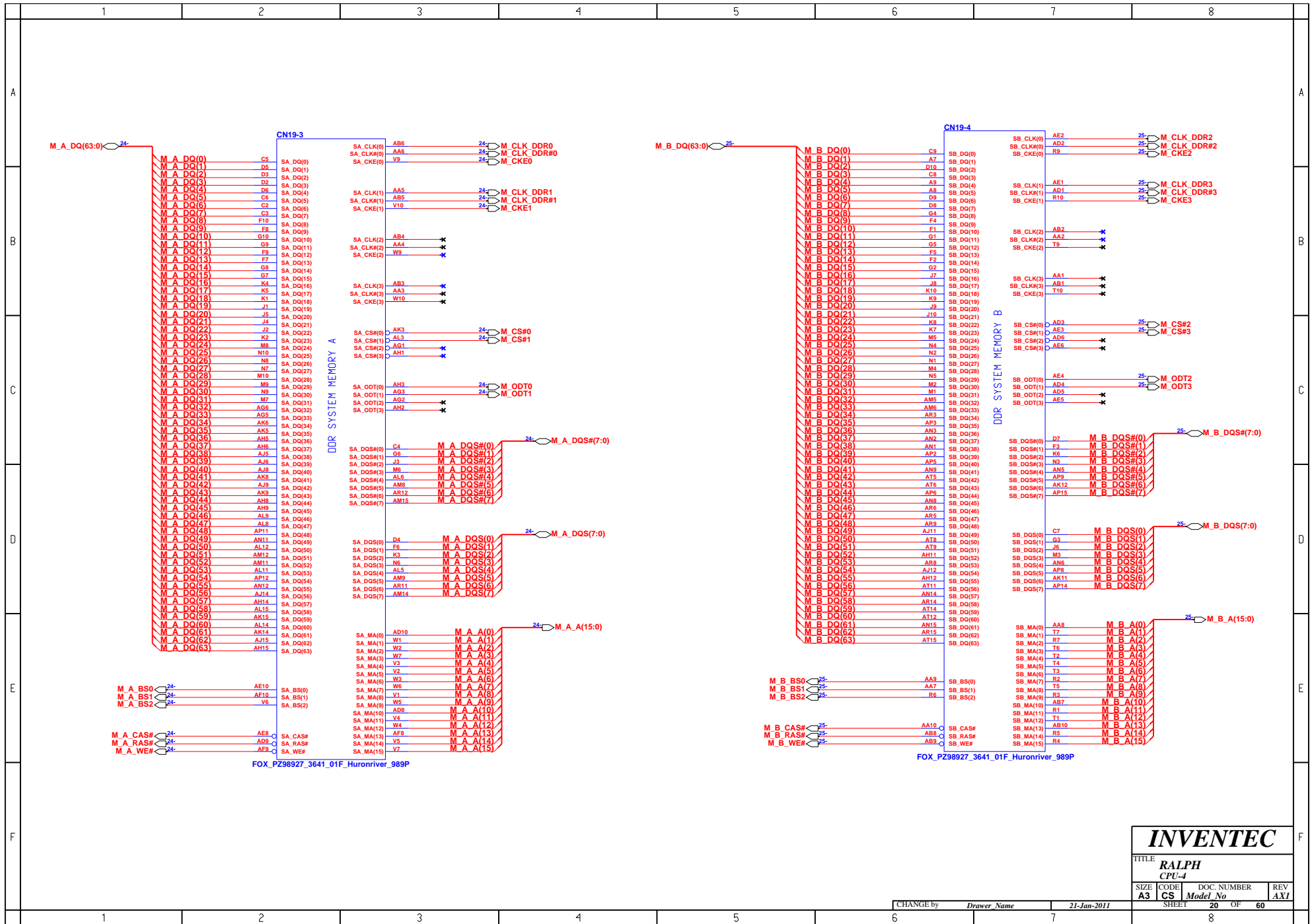
INVENTEC			
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SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
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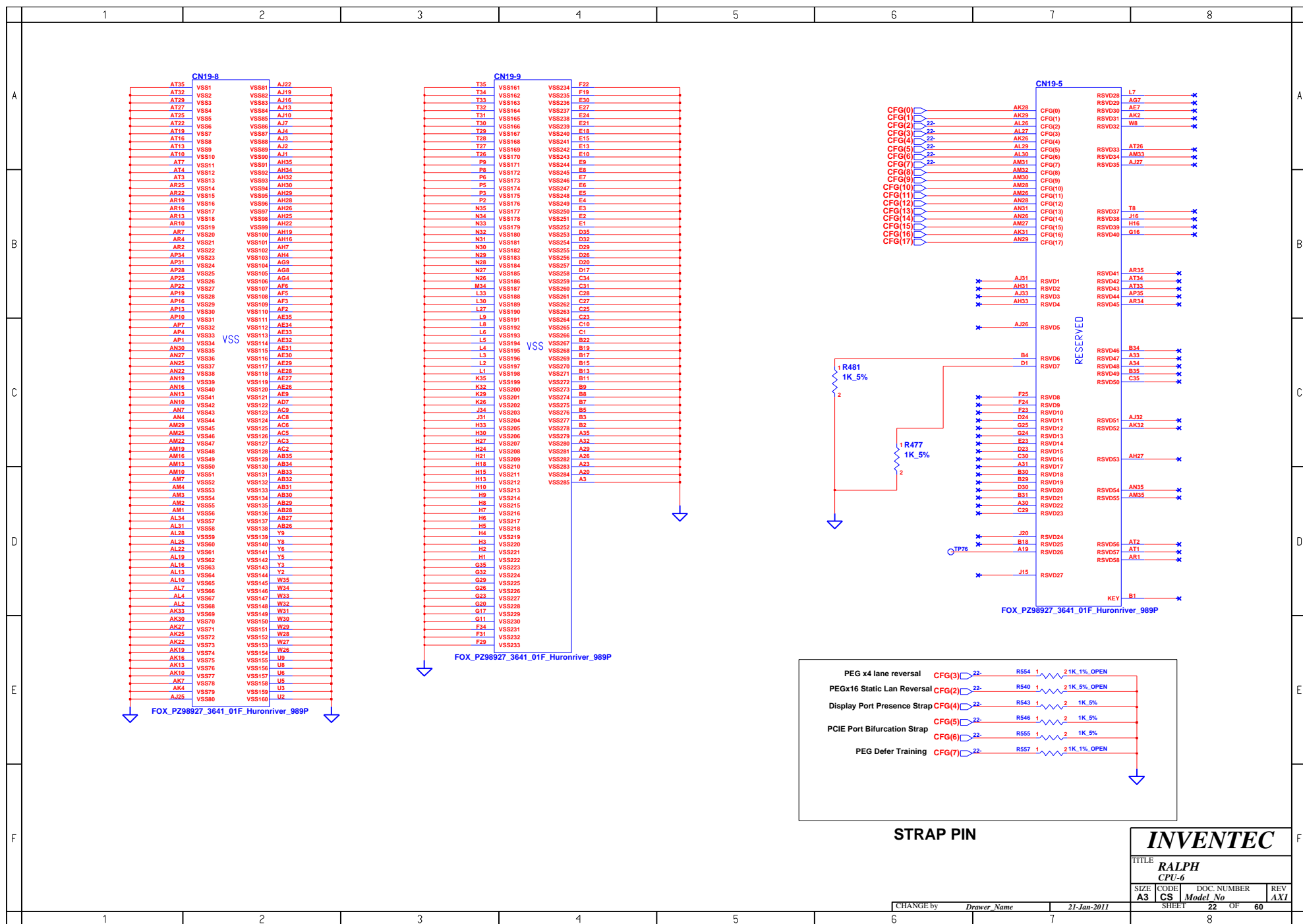
INVENTEC

TITLE
RALPH
CPU-4

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by Drawer Name 21-Jan-2011

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A



A

22

C

1

1

7

1

3

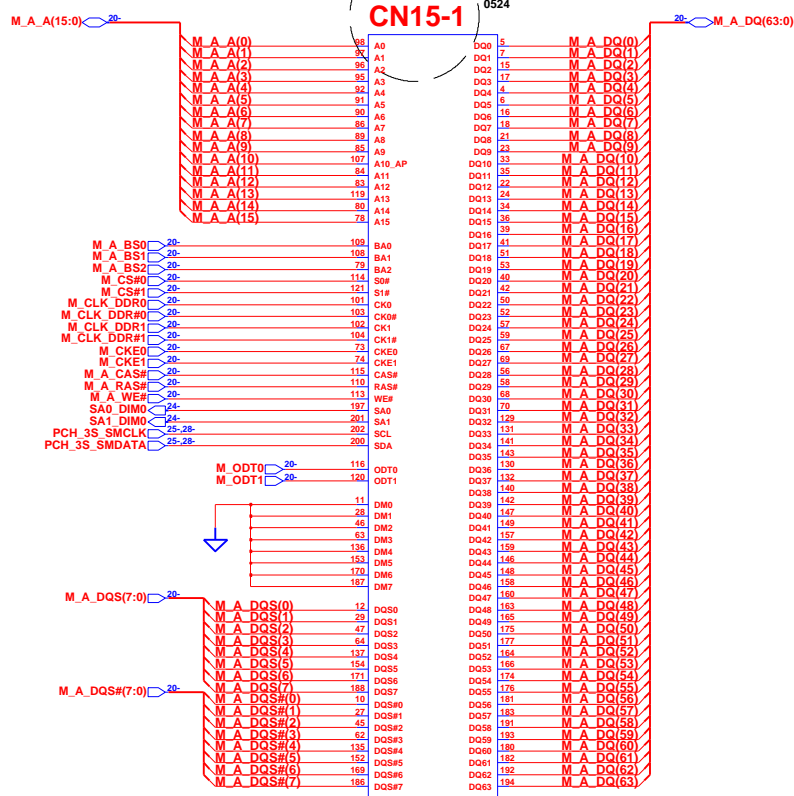
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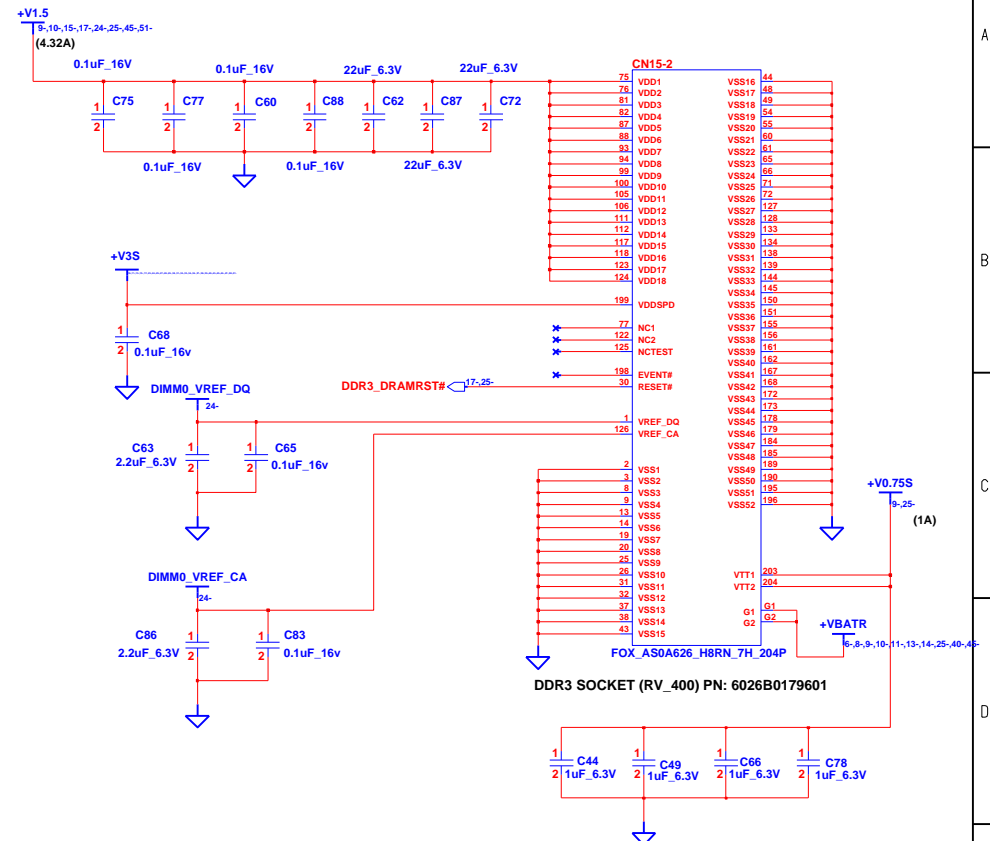
7

9

DDR3 SO-DIMM 0



FOX_AS0A626_H8RN_7H_204P
DDR3 SOCKET (RV_800) PN: 6026B0216601



[illegible]

TITLE			
<i>RALPH</i> <i>SO-DIMM2</i>			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	<i>AXI</i>
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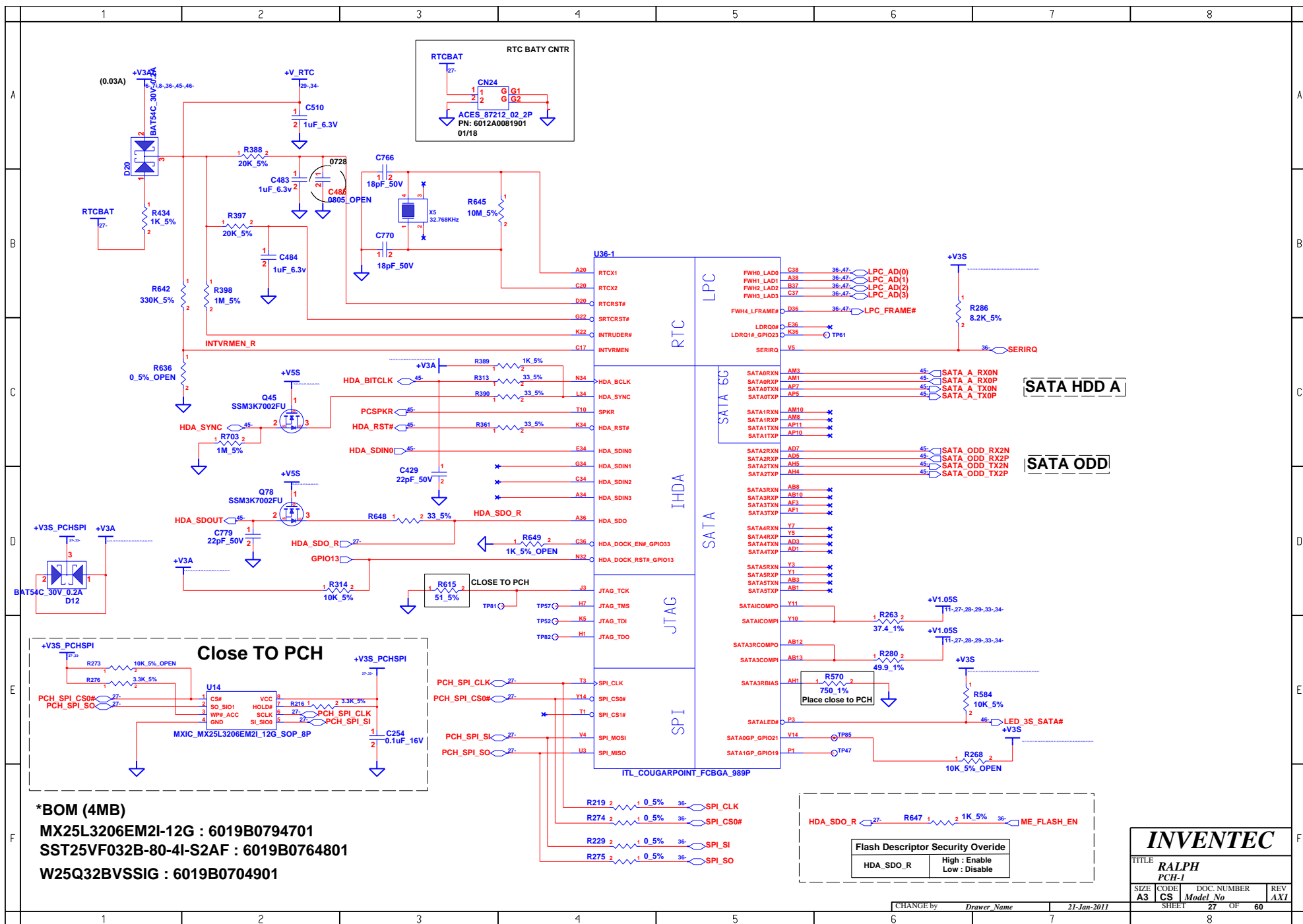
CHANGE by	<i>Drawer Name</i>	<i>21-Jan-2011</i>
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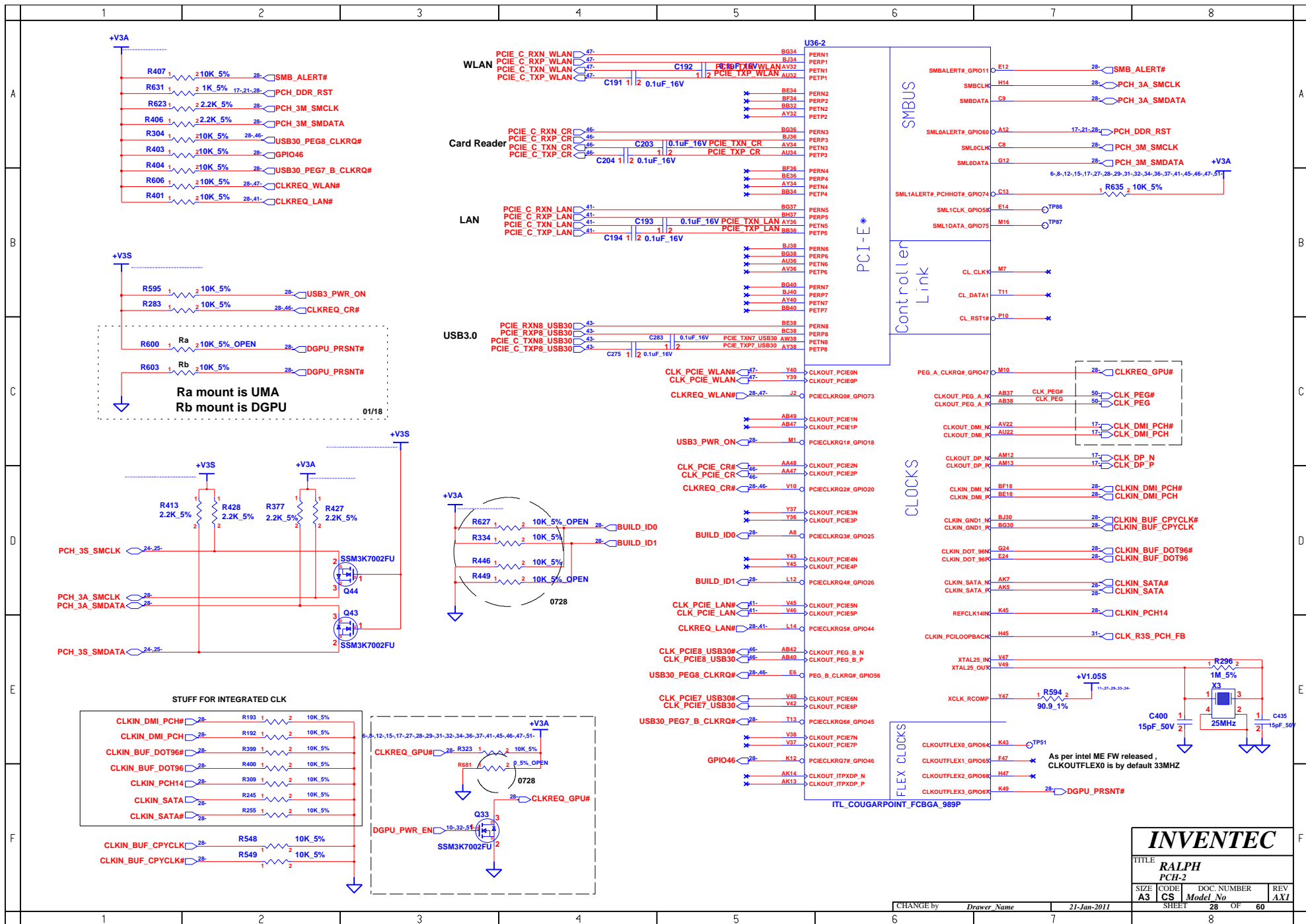
A3	CS	Model No	AA1
SHEET		25	OF 60
8			

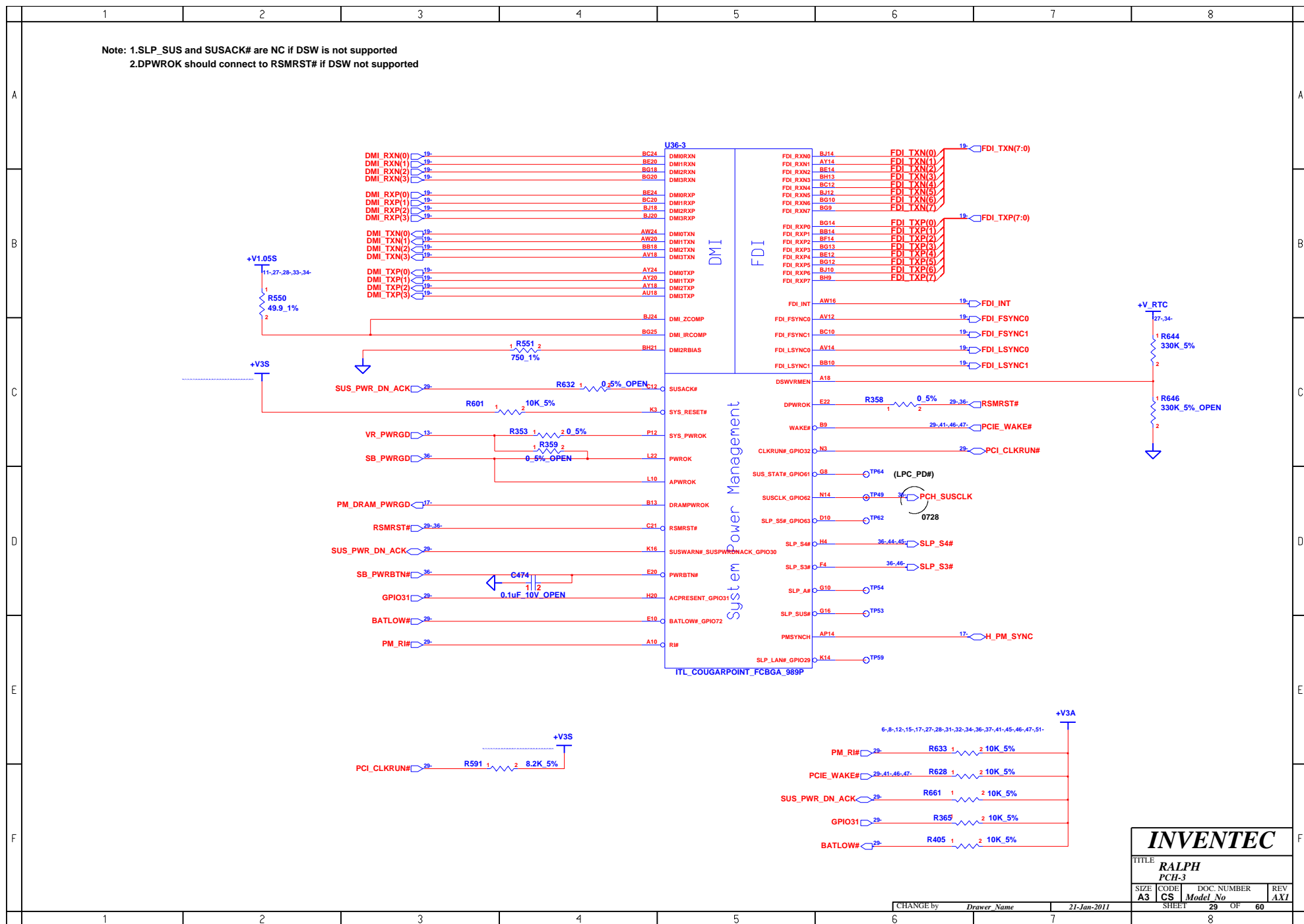
	1	2	3	4	5	6	7	8	
A	<div>PCH</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

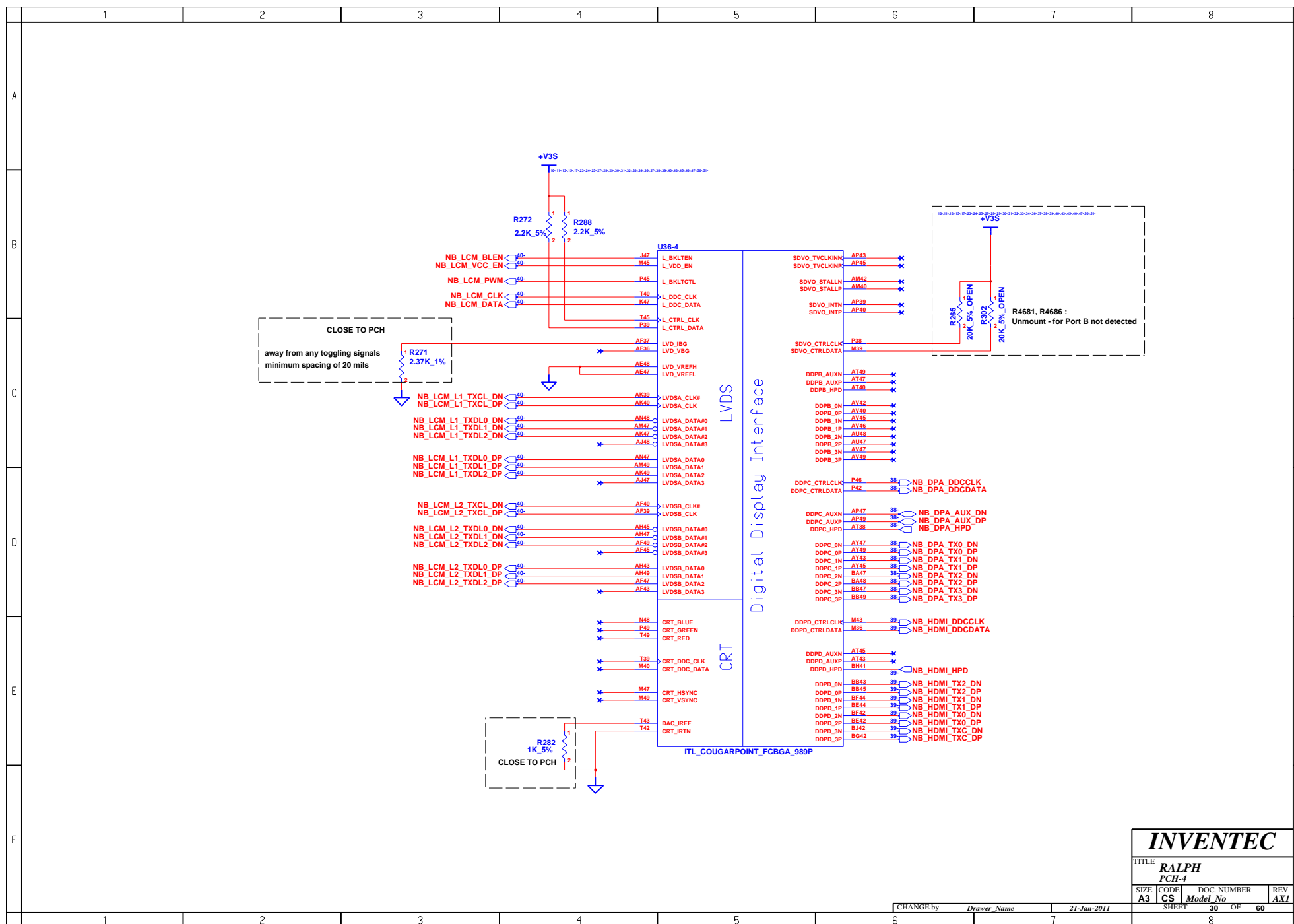
INVENTEC			
TITLE <i>RALPH BLANK</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>AXI</i>
SHEET 26		OF 60	

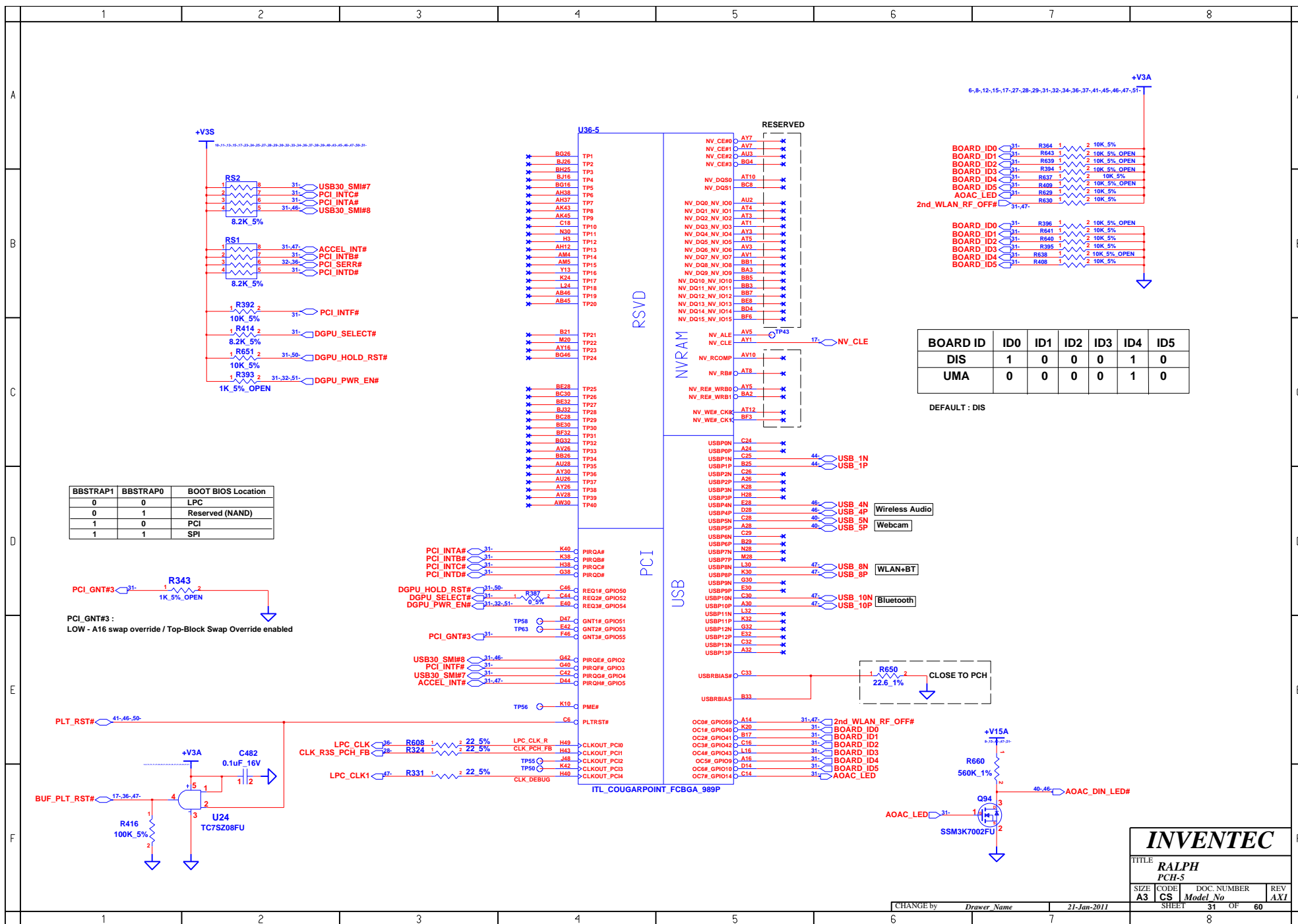
CHANGE by *Drawer Name* 24-Feb-2011











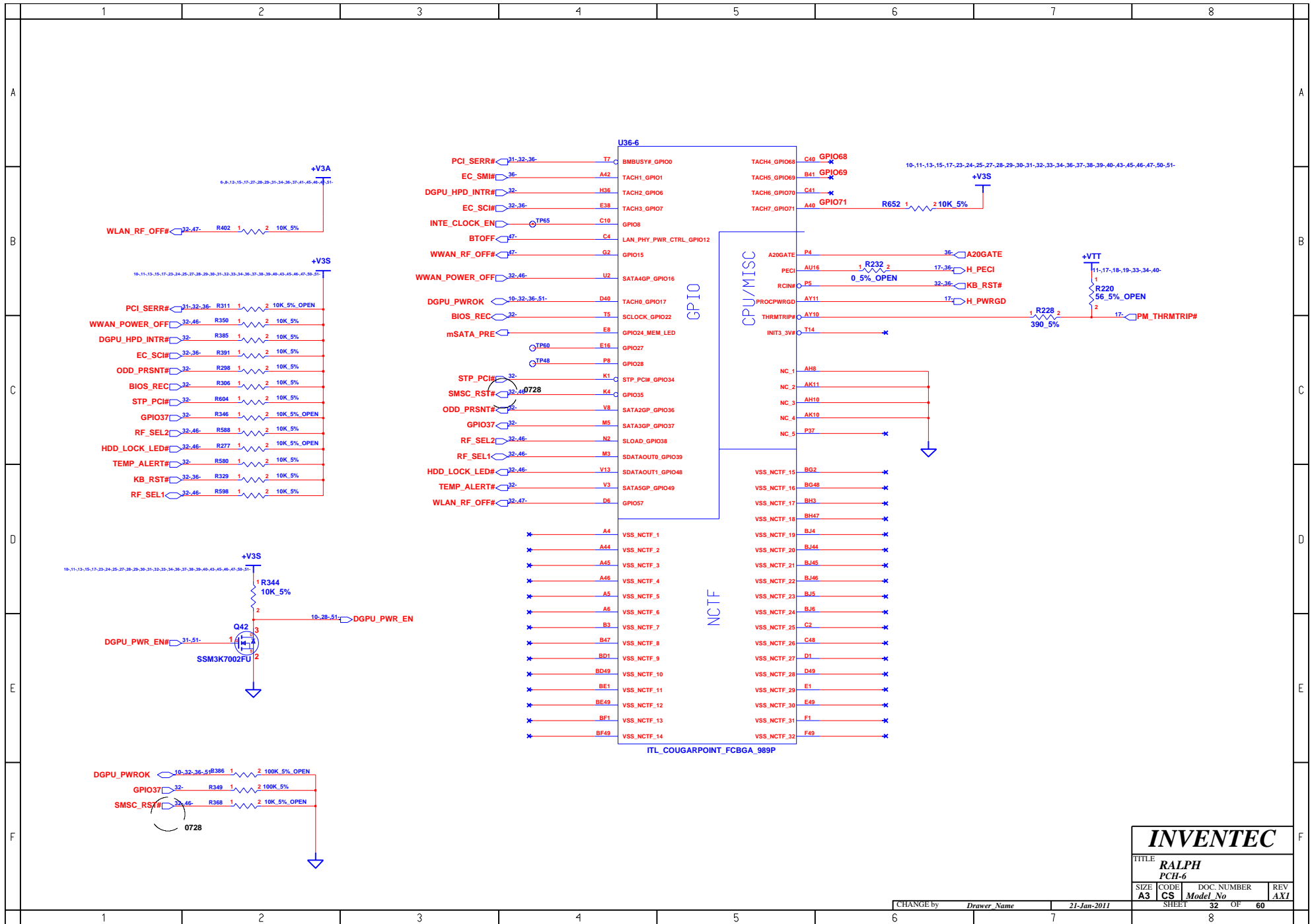
INVENTEC

TITLE
RALPH
PCH-5

SIZE CODE DOC. NUMBER REV
A3 CS Model No **31** OF **60** **AXI**

CHANGE by *Drawer Name* 21-Jan-2011

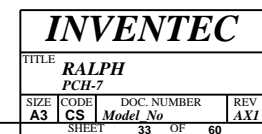
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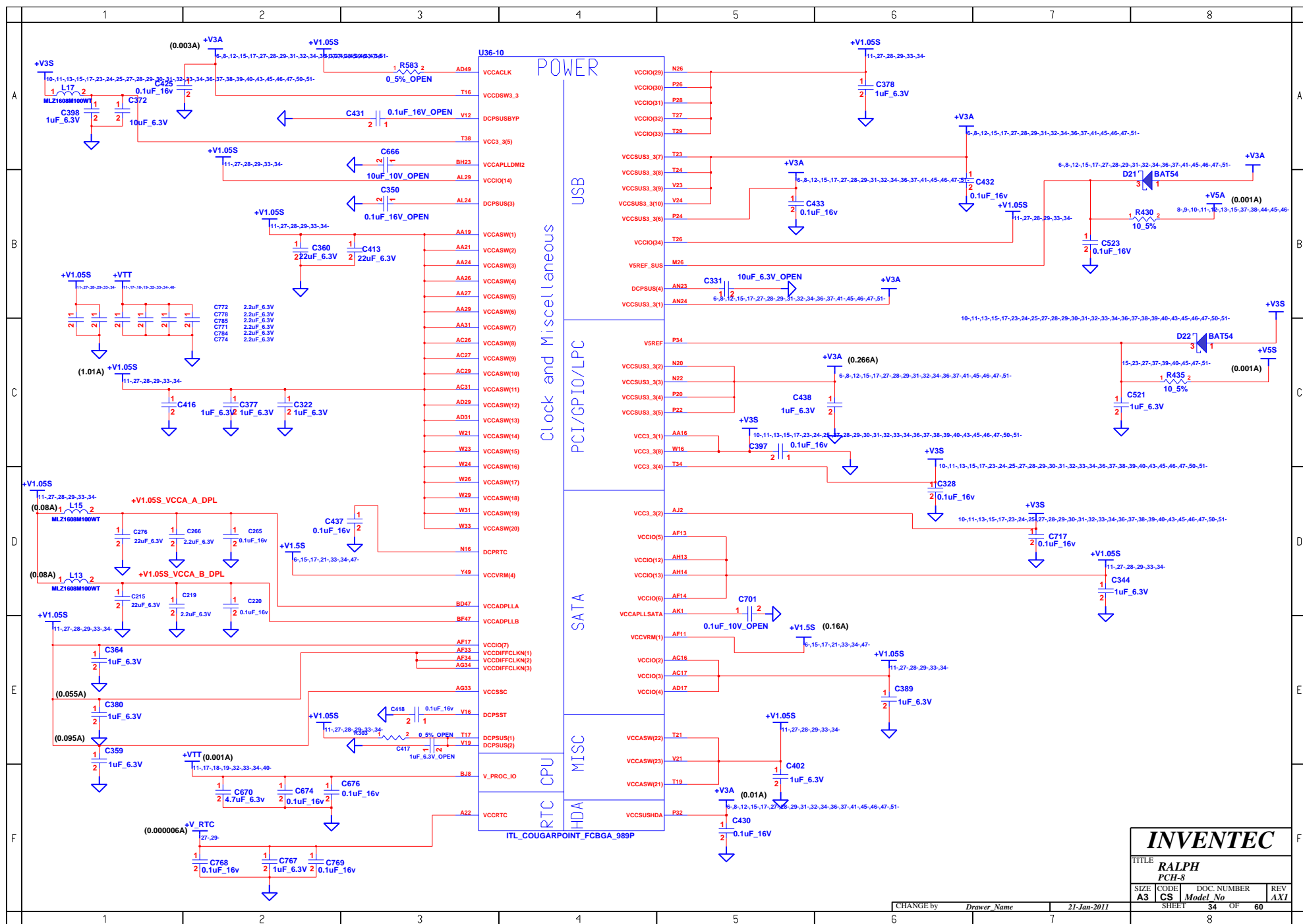


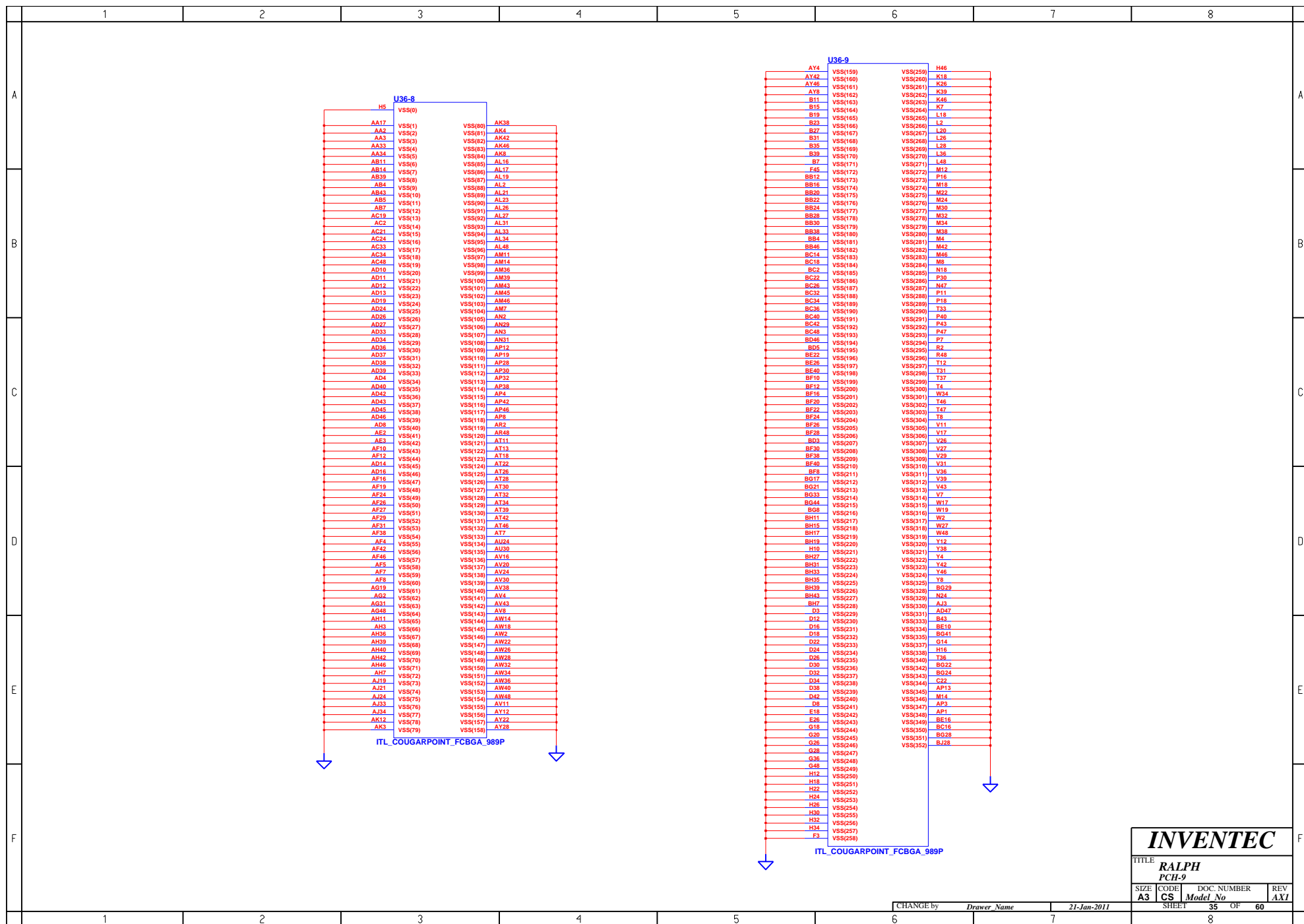
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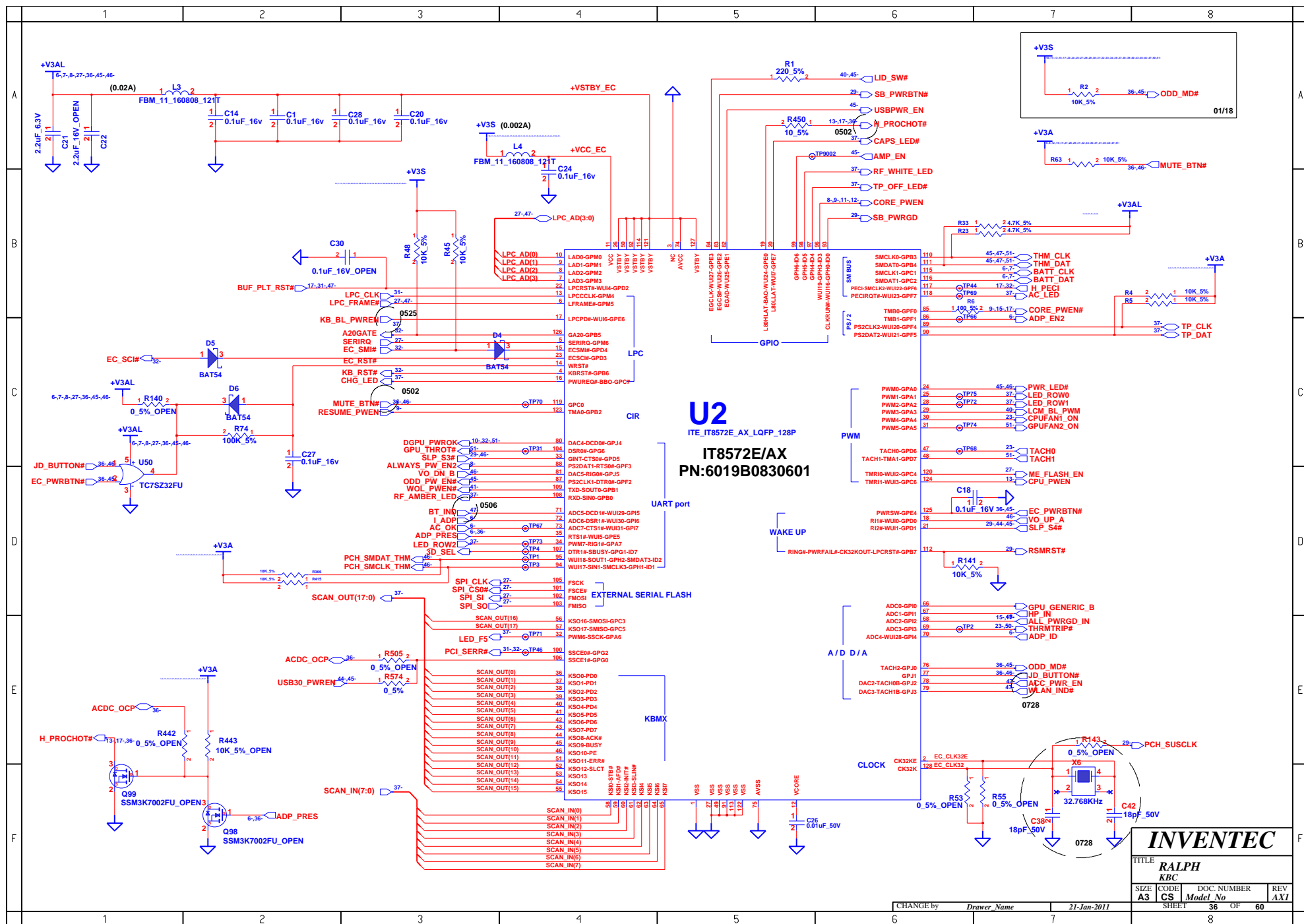
TITLE RALPH PCH-6			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AXI
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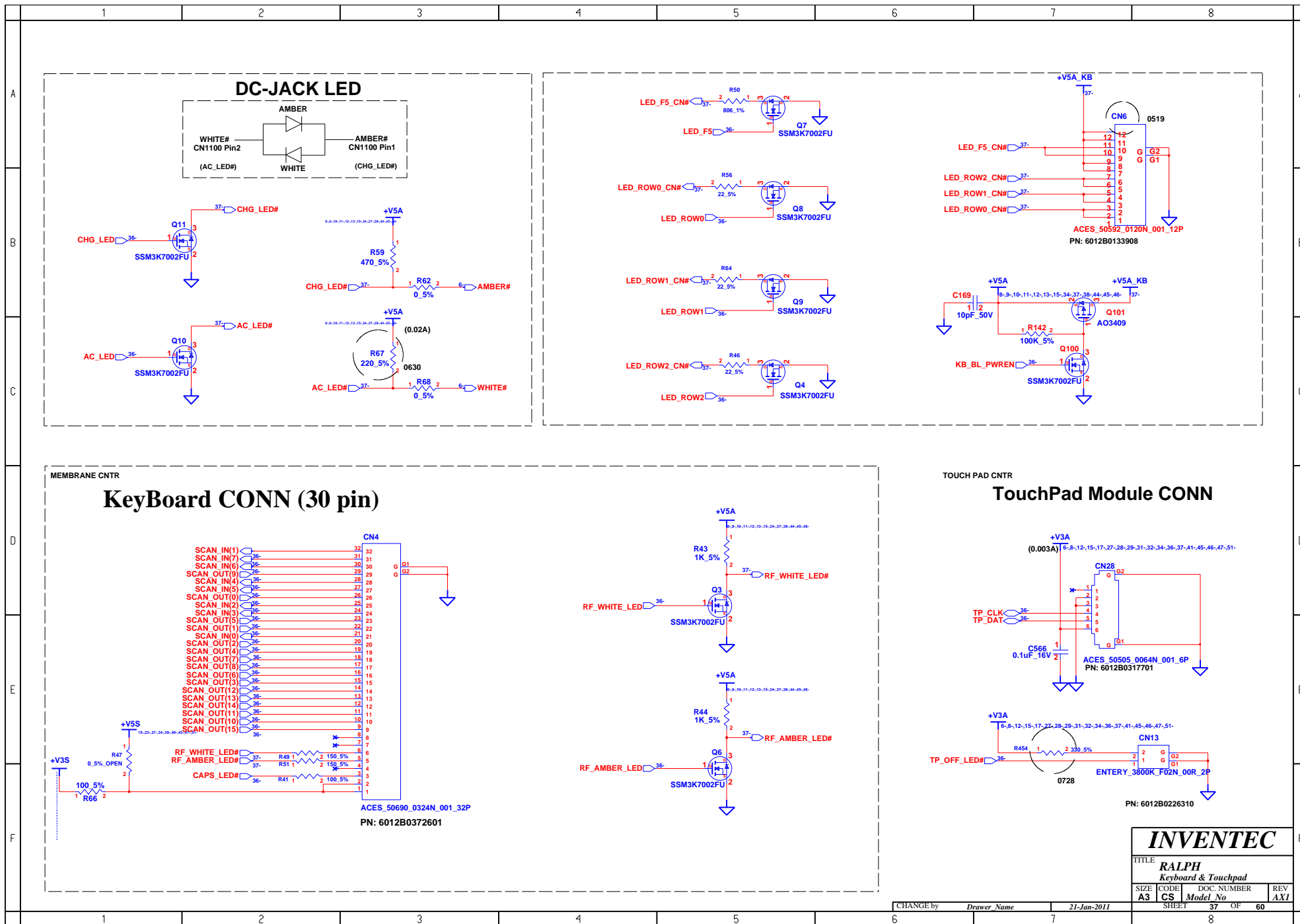
CHANGE by *Drawer Name* 21-Jan-2011

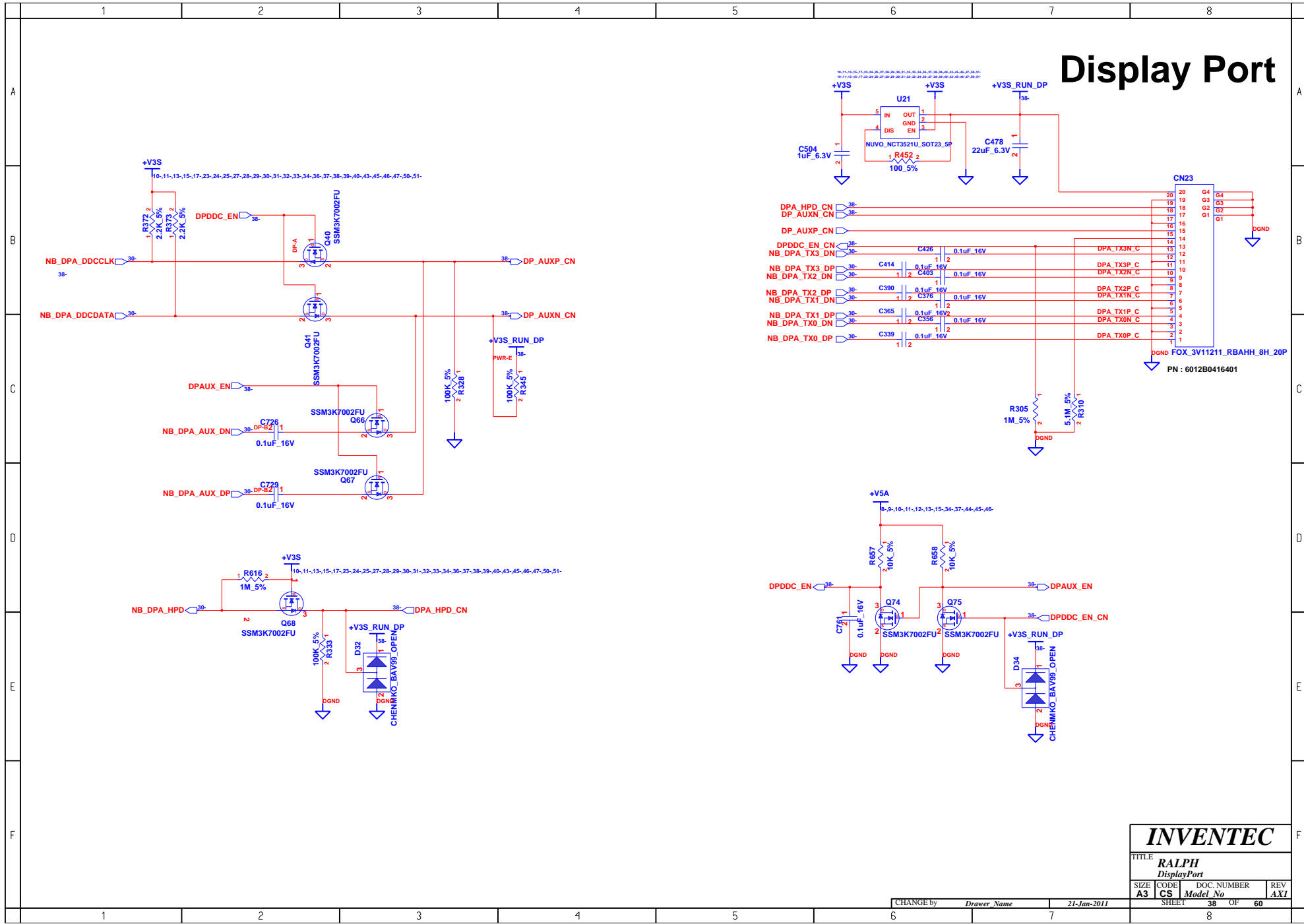












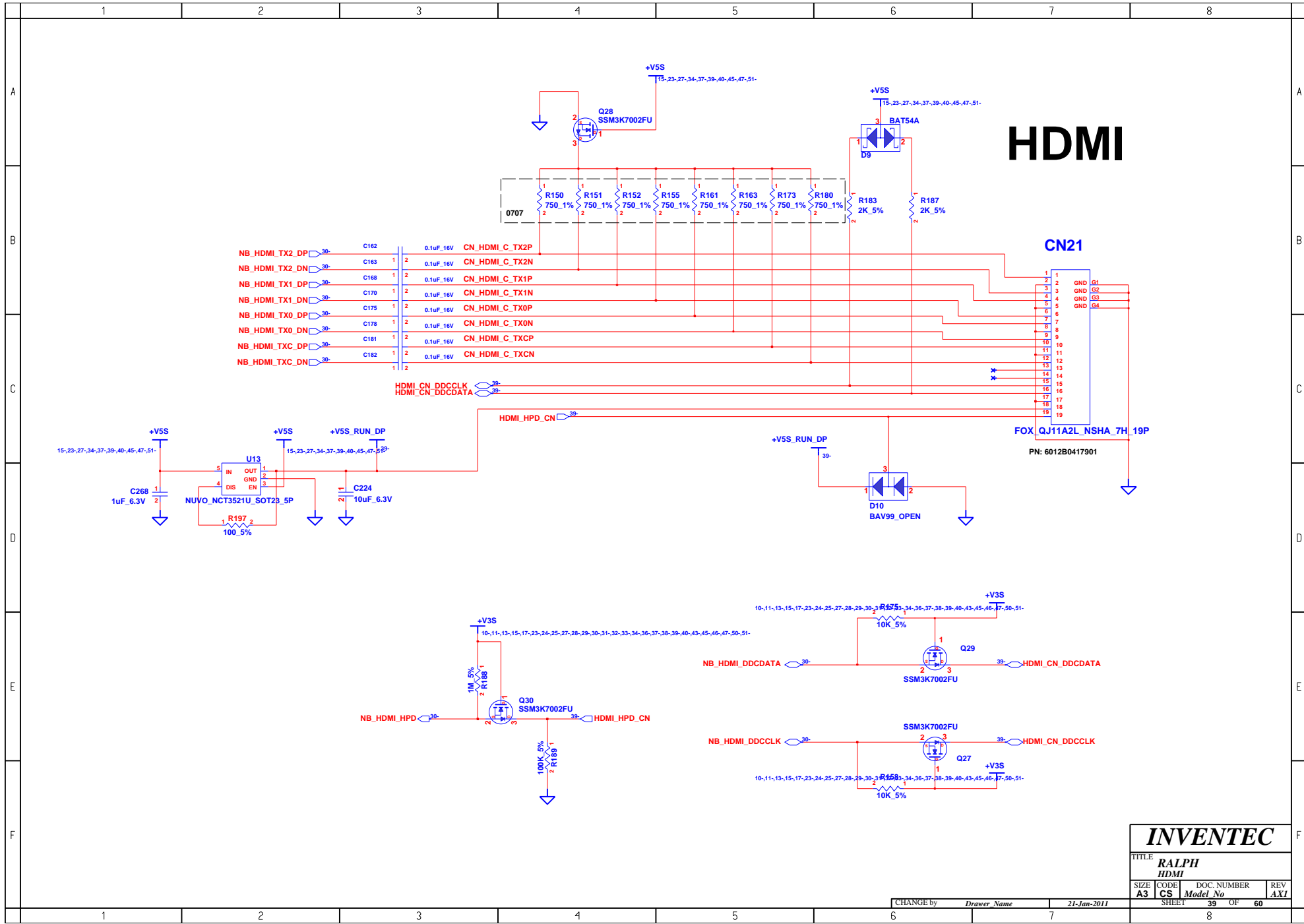
Display Port

INVENTEC

TITLE			
RALPH DisplayPort			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by *Drawer Name* 21-Jan-2011

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HDMI

INVENTEC

TITLE			
RALPH HDMI			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
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LCM CONN

A

A

B

B

C

C

D

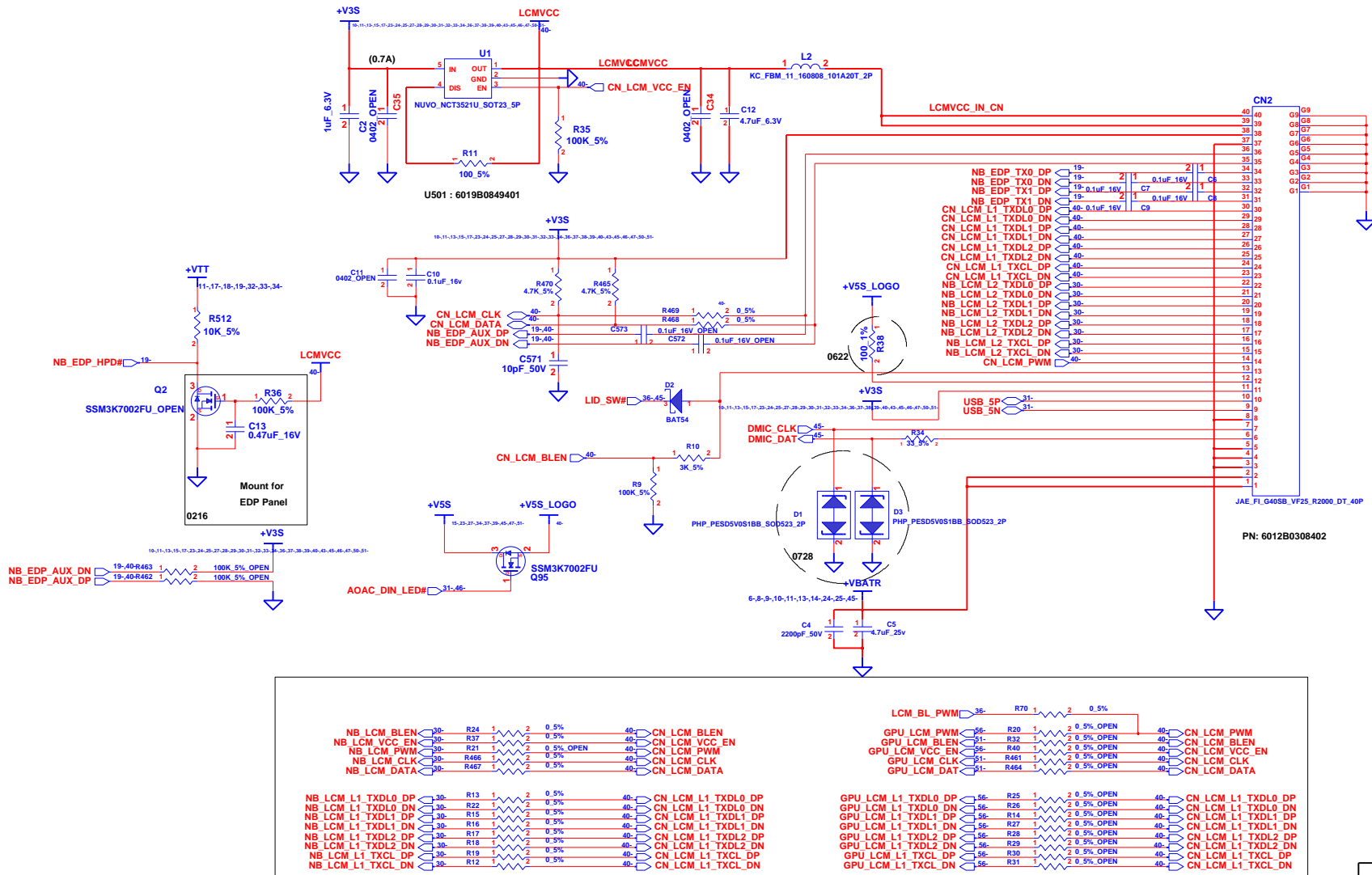
D

E

E

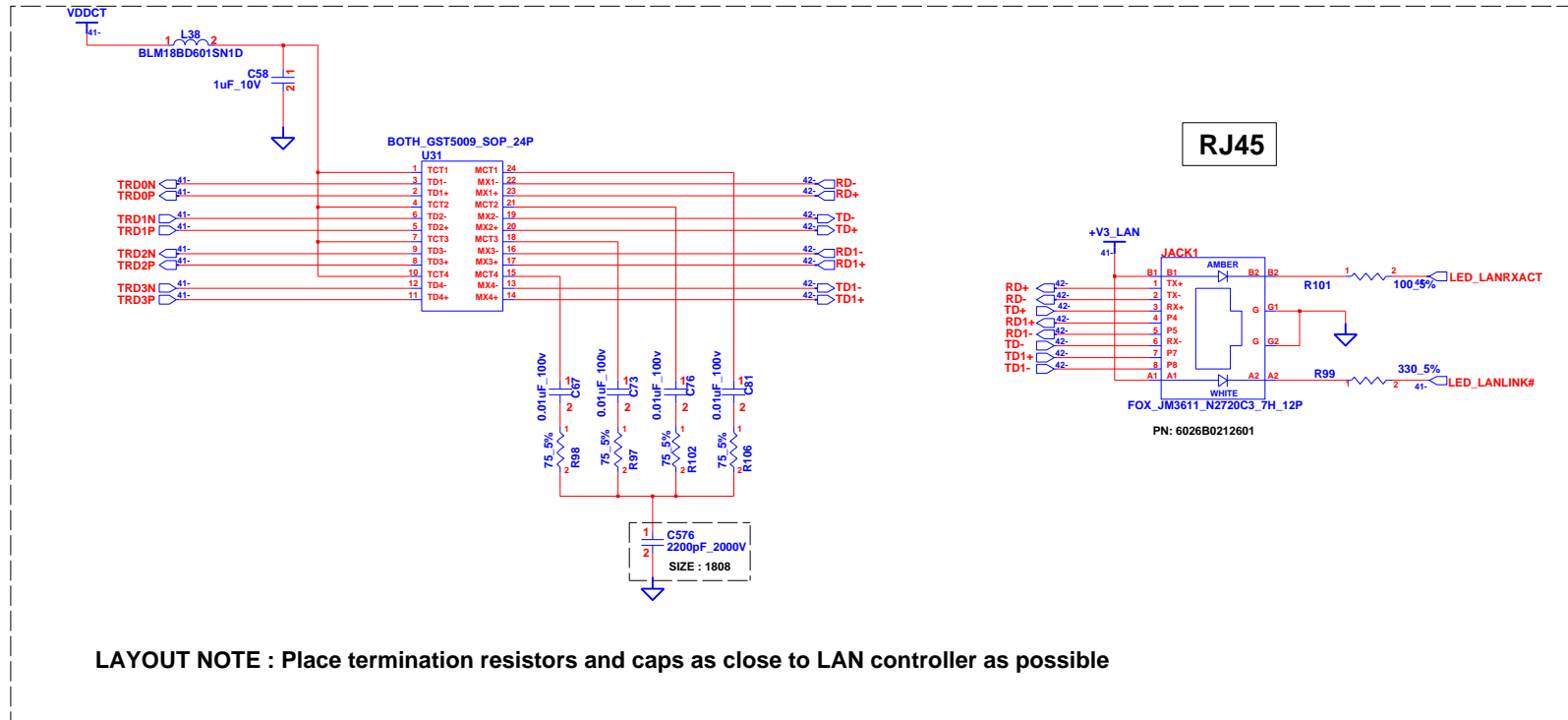
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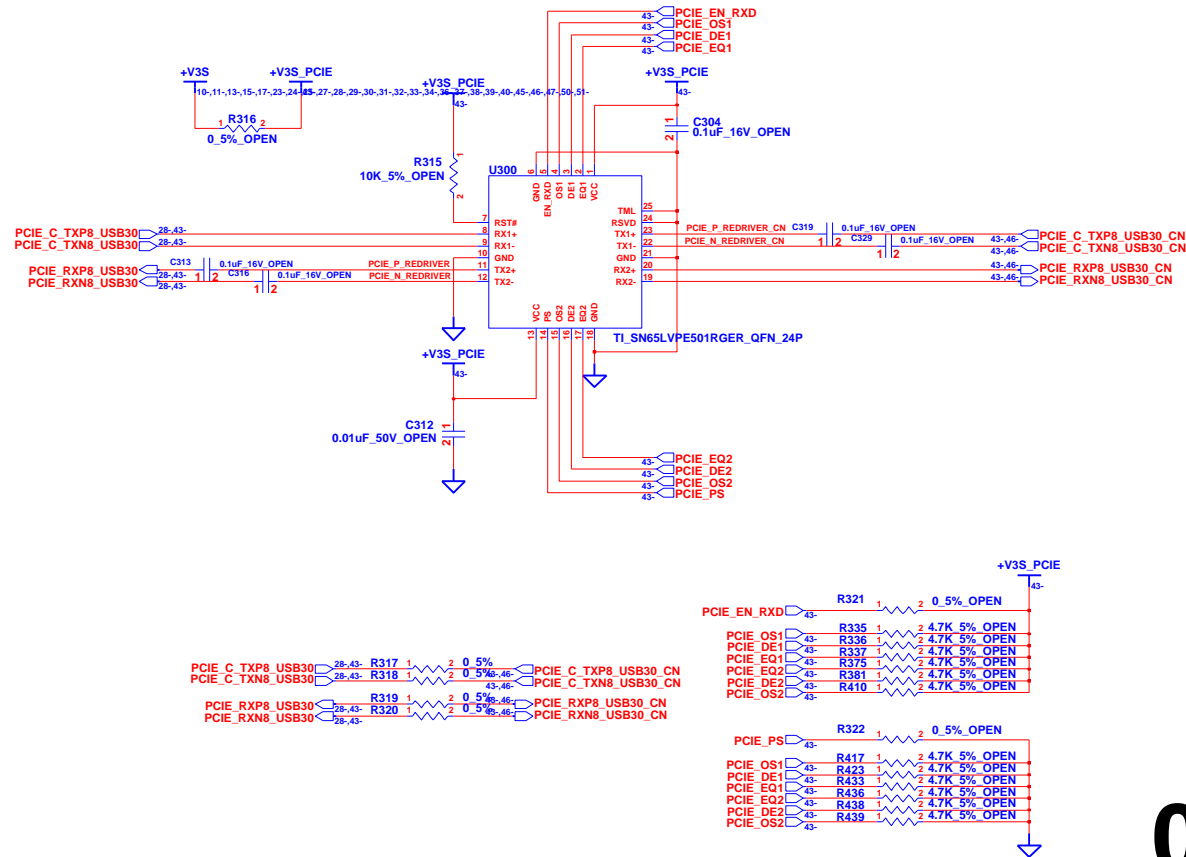


INVENTEC			
TITLE: RALPH LCM			
SIZE: A3	CODE: CS	DOC. NUMBER: Model No	REV: AXI
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CHANGE by: Drawer Name 21-Jan-2011



RE-DRIVER



0418

INVENTEC

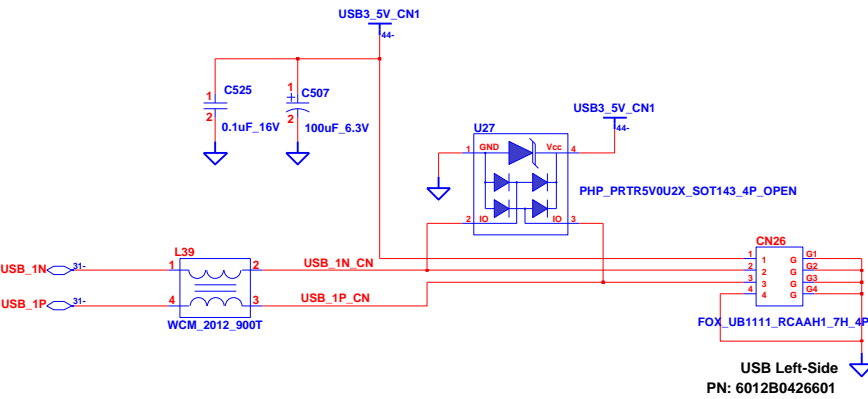
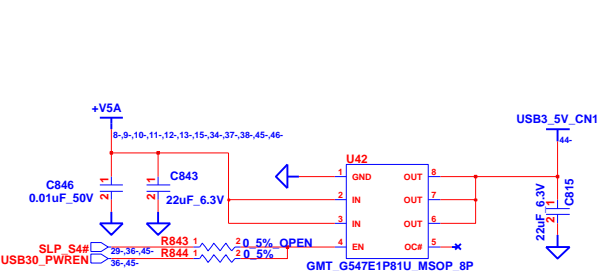
TITLE
RALPH

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AXI
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CHANGE by *Drawer Name* 12-Apr-2011

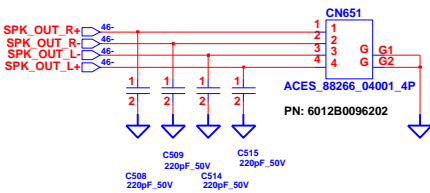
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USB 2.0



USB CONN01

REAR SPEAKER

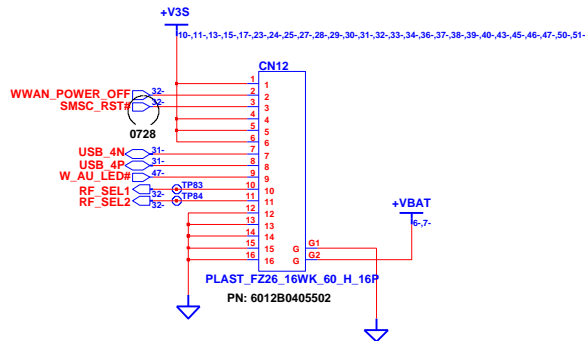


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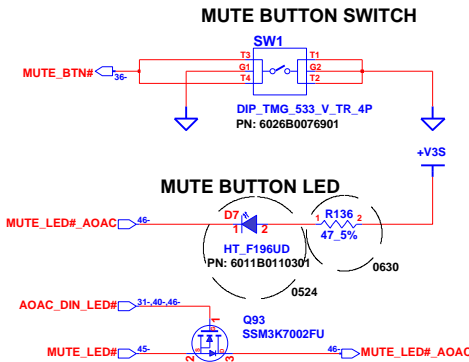
TITLE			
RALPH			
USB 3.0			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
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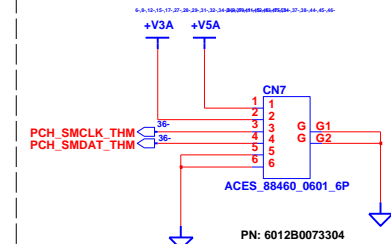
Wureless Audio Board CN on MB



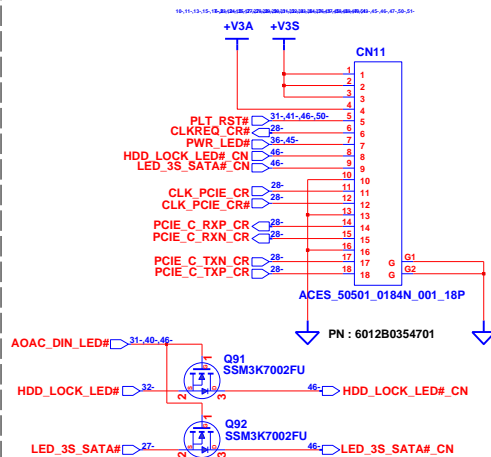
MUTE Button



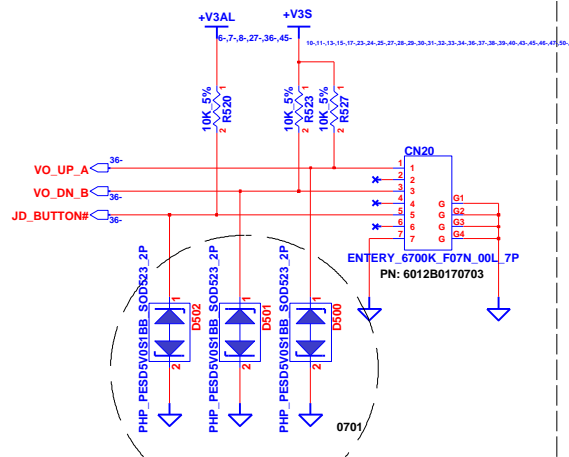
LDPS Board CN on MB



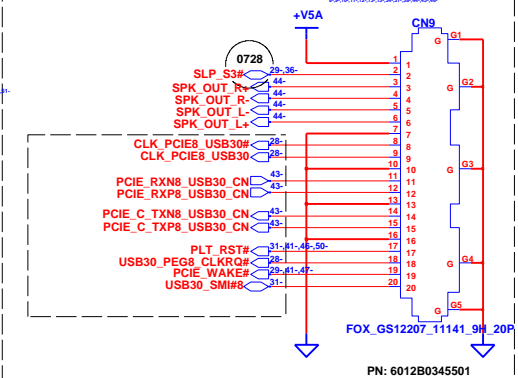
CR Board CN on MB



JOG DIAL CN ON MB



AU-USB30 CN ON MB



INVENTEC

TITLE RALPH Connector-2			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AXI
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CHANGE by Drawer Name 21-Jan-2011

8

F

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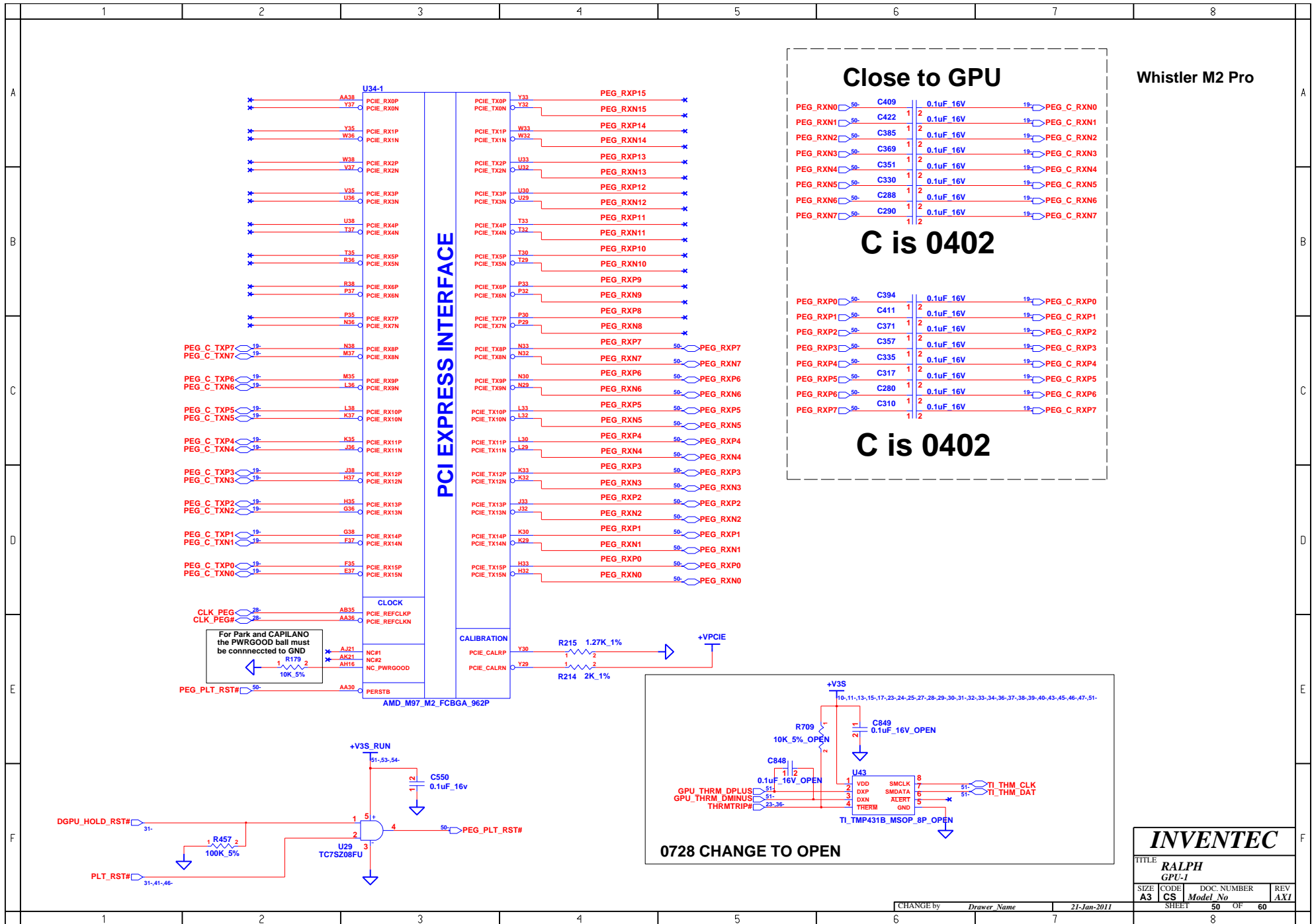
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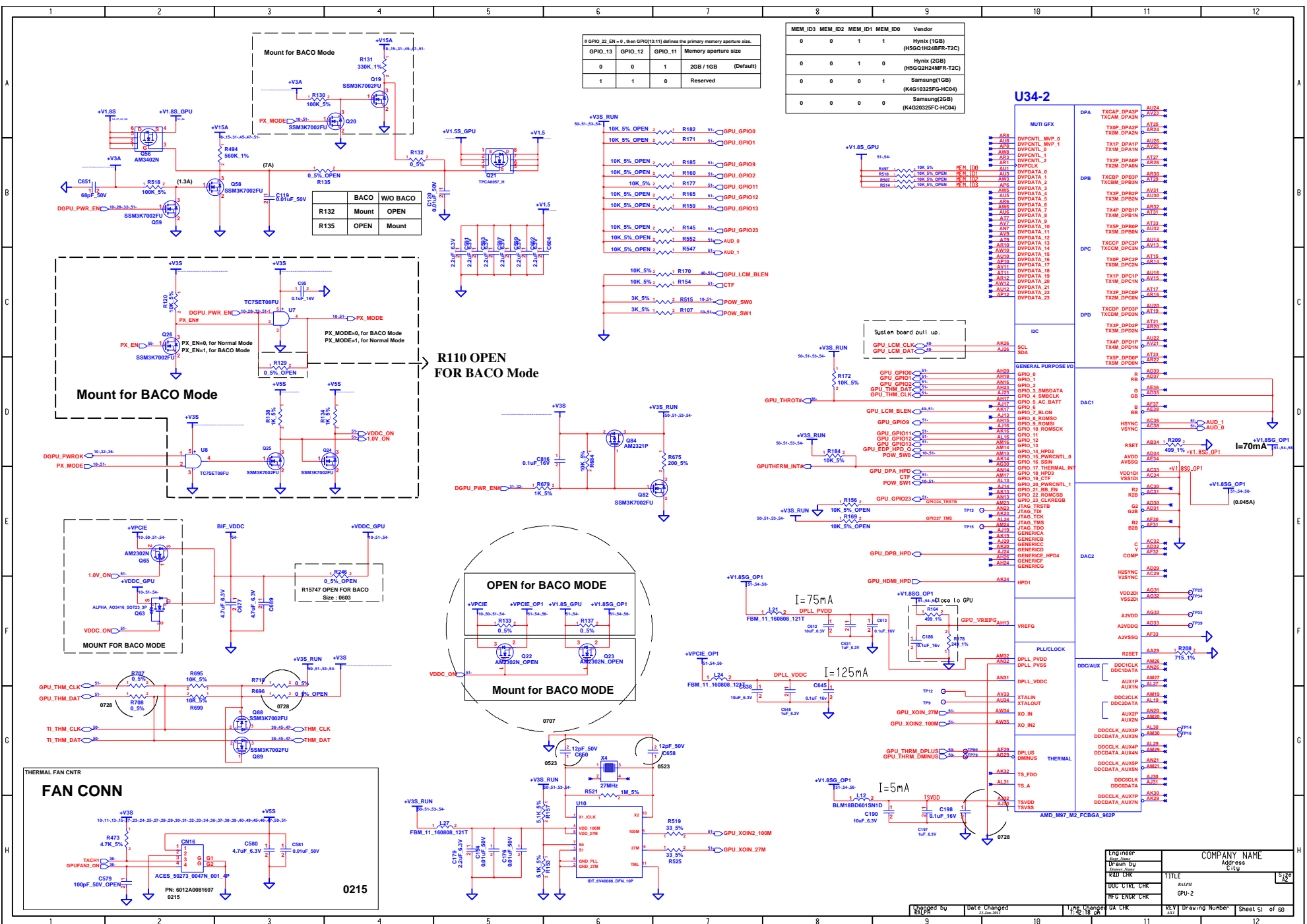
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A	<div>CPU screws</div>								A
B	<div>VGA screws</div>								B
C	<div>FAN screws</div>								C
D	<div>WWAN screws</div>								D
E	<div>Fixing screws</div>								E
F	<div>INVENTEC</div> <div> <div>TITLE</div> <div>RALPH SCREW</div> <div> <div>SIZE A3</div> <div>CODE CS</div> <div>DOC. NUMBER Model No</div> <div>REV AXI</div> </div> </div>								F
	1	2	3	4	5	6	7	8	

	1	2	3	4	5	6	7	8	
A	<div>GPU</div>								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE <i>RALPH FAN</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV <i>AXI</i>
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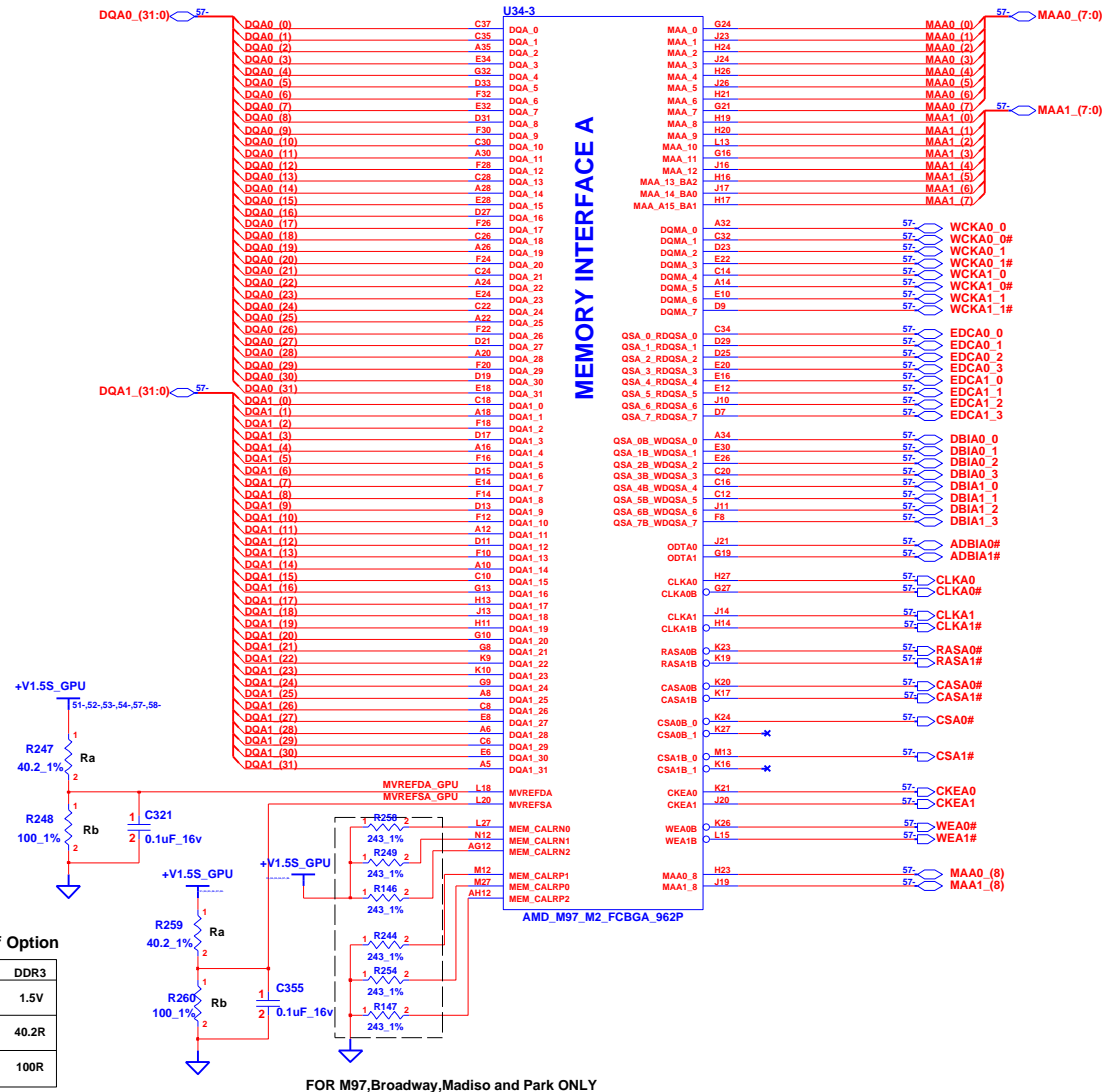




Channel A

DDR3/GDDR3Memory Stuff Option

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R



INVENTEC

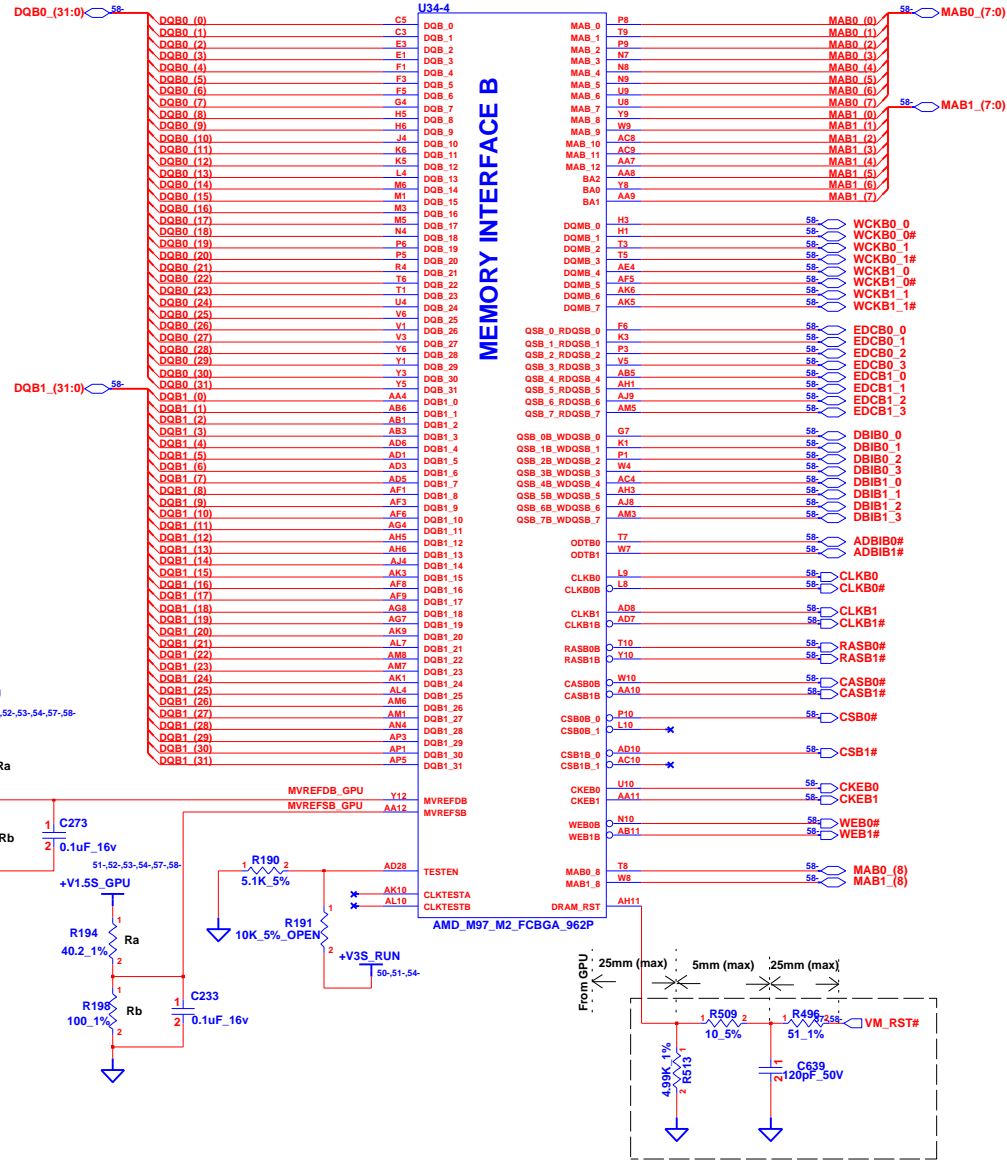
TITLE RALPH GPU-3			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
SHEET 52		OF 60	

CHANGE by *Drawer Name* 21-Jan-2011

Channel B

DDR3/GDDR3Memory Stuff Option

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R



Place all these components vry close to GPU (Within 25mm)
and keep all component close to each other (within 5mm) except Rser2
** This basic topology should be used for DRAM_RST for DDR3/GDDR3/GDDR5
These Capacitors and Resistor values are an example only.
The Series R and || Cap values will depend on the DRAM load and will have to be
calculated for different Memory , DRAM Load and board to pass Reset Signal Spec.

INVENTEC

TITLE RALPH GPU-4			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AXI
SHEET 53		OF 60	

CHANGE by *Drawer Name* 21-Jan-2011

