

SONY PROPRIETARY INFORMATION

SONY CONFIDENTIAL

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.
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IRX-3400 SCHEMATICS

MBX-147

FOR MP

REV. 1.154

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○ 9 COMBO	24 (COMBO-1)	1800 -	I.LINK & MS-DUO & PCCARD(1/2)
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○ 10 LAN	26 (LAN-1)	2000 - 2199	GIGABIT/FAST ETHERNET
○ 11 LPC	27 (LPC-1)	2500 - 2599	EC/KBC
○ 12 CN	28 (CN-1)	3000 - 3099	KEYBOARD & TP,ETC & LID
	29 (CN-2)	3100 - 3199	IDE CN (SATA,AEGIS / PATA)
	30 (CN-3)	3200 - 3259	LCD CTRL & CN
	31 (CN-4)	3260 - 3299	VGA & I.LINK
	32 (CN-5)	3300 - 3399	PC CARD & EXPRESS CARD
	33 (CN-6)	3400 - 3499	MINI CARD (WIRELESS-LAN)
	34 (CN-7)	3600 - 3699	DOCK CN
	35 (CN-8)	3800 - 3899	USB,CAMERA,MODEM & WAN & TPM
	36 (CN-9)	3900 - 3999	SW & LED & AUDIO
○ 13 POWER	37 (POWER-1)	4000 - 4099	+VCPUCORE
	38 (POWER-2)	5100 - 5199	+VGFXCORE / +VGFXMEM
	39 (POWER-3)	4100 - 4199	+1.5VSUS / +1.05VSUS
	40 (POWER-4)	4200 - 4299	+1.8VSUS / +VDDRVTT
	41 (POWER-5)	4300 - 4399	+1.2VSUS / +2.5VSUS
	42 (POWER-6)	4400 - 4499	+3VALW / +5VALW
	43 (POWER-7)	4500 - 4599	LOAD SW
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○ 15 VARIANCE	50 (VARIANCE-1)		VARIANCE

REVISIONS

STUDY	:	0.010	2005/	6/09
		0.020	2005/	6/10
		0.030	2005/	6/14
		0.040	2005/	6/20
		0.050	2005/	6/27
EVT	:	0.100	2005/	7/01
		0.110	2005/	7/11
		0.120	2005/	7/14
		0.130	2005/	7/25
		0.140	2005/	8/01
		0.150	2005/	8/03
		0.160	2005/	8/05
		0.170	2005/	8/08
		0.180	2005/	8/09
		0.190	2005/	8/10
		0.1A0	2005/	8/11
		0.1B0	2005/	8/16
		0.1B1	2005/	8/19
		0.1B2	2005/	8/22
		0.1B3	2005/	8/25
		0.1B4	2005/	8/30
DVT	:	0.200	2005/	9/17
		0.210	2005/	9/26
		0.220	2005/	9/29
		0.230	2005/	9/30
		0.240	2005/	10/2
		0.241	2005/	10/2
		0.242	2005/	10/11
		0.243	2005/	10/18
		0.244	2005/	10/18.2
		0.245	2005/	10/20
		0.245	2005/	10/20
DVT2	:	0.300	2005/	10/24
		0.310	2005/	10/26
PVT	:	1.100	2005/	11/08
		1.110	2005/	11/10
		1.111	2005/	11/15
		1.120	2005/	11/17
		1.130	2005/	11/19
		1.140	2005/	11/19.2
		1.150	2005/	11/21
		1.151	2005/	11/28
		1.152	2005/	11/30
		1.153	2005/	12/12
MP	:	1.154	2006/	01/16

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DRAWING: MBX-147
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LAST MODIFIED=Mon Jan 16 20:39:24 2006

172719611 ZS-260	QTY 1/5	REV. 1.154	PART NO. 186977311
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SIZE C DESIGNER - SHEET 1 OF 50

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REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

(EVT)

(REV. 0.100 -> 0.110)

P.07 CHG PARTS : TH110
P.11 DEL REF : C467
P.16 ADD REF : R710,734,762,763
ADD REF : RB760
P.21 CHG REF : RB1463 -> RB1457
PIN SWAP FOR A/W : RB1450 - 1457
DEL REF : R1440
P.22 DEL REF : R1513,RB1560,R1558,1559
CHG REF : R1402 -> R1502
CHG PARTS : Q1580,1581 -> Q1580(2IN1)
CHG PARTS : R1520 1005 -> 0603
CHG NOTES : R1550 MT -> DNI
P.23 CHG NET : C1645.2 +1.05VSUS -> +VCCP
P.25 ADD REF : R1907
P.27 CHG NET : RB2540.5 PM_SLP_S5# -> PM_RSMRST#
P.28 ADD REF : F3050
P.32 CHG NET : CN3350 35,37P USB_P7 -> USB_P3
P.35 PIN SWAP : CN3800 32P <-> 33P
PIN SWAP : IC3800 2P <-> 3P, 5P <-> 8P
CHG PARTS : CN3830
ADD REF : F3811,3830,3831
P.36 CHG NET : CN3900 48,49P USB_P3 -> USB_P7
CHG NOTES : JR3900 DNI -> MT
P.37 DEL REF : SL4050,4051,4070,4071
CHG PARTS : TH4050,4070

(REV. 0.110 -> 0.120)

P.08 CHG NET : IC200.18 D_MECH_DET# -> DOCKED#
P.09 PIN SWAP FOR A/W : IC300.DMI-PORT
CHG NOTES : R309 DNI <-> MT
P.14 PIN SWAP FOR A/W : PEG_TX,RX+,-(POLARITY INVERSION)
P.18 PIN SWAP FOR A/W : FBAD,FBADQM,FBADQS
P.23 ADD REF : JR1611
P.26 DEL REF : RB3931
ADD REF : R3940,3941
P.27 DEL REF : R2500,2502,2515 C2511
P.31 CHG NET : CN3260.17,16,14,13 TPA(+<->-) <-> TPB
P.34 REF : Q3640,R3640
P.36 DEL REF : R2003
ADD REF : JR2001,2002
P.43 ADD REF : R4591-4594,Q4593,4594
P.44 CHG NET : IC4660.2,C4660.2 +3VSUS -> +3VALW

P.ALL CHG PARTS : CHANGE PARTS SIZE 0603 -> 1005

0-262-375-01 -> 1-107-819-81
0-262-376-01 -> 1-119-923-81
0-262-377-01 -> 1-127-772-81
0-262-378-01 -> 1-164-936-81
0-262-379-01 -> 1-164-937-81
0-262-380-01 -> 1-164-943-81

(REV. 0.120 -> 0.130)

P.06 CHG NOTES : JR0 DNI -> MT
P.14 PIN SWAP FOR A/W : IC631,611,FL651,652
DEL PARTS : RB631,633,641
P.15 CHG PARTS : R670,671 0603 -> 1005
P.18 MOV PARTS : TP930,931,960,961
PIN SWAP FOR A/W : IC920 : DQ<8> - DQ<15>
CHG PARTS : R990,991 0603 -> 1005
ADD NET : IC2500.50 ANL66.ID
P.29 CHG PARTS : CN3100 30P ->20P
ADD PARTS : Q3100
P.30 PIN SWAP FOR A/W : FL3220,3221
CHG PARTS : R3211,3212 0.5# -> 5#
P.33 CHG PARTS : CN3400,3401
CHG NET : CN3400.18 OFFPAGE -> DGND
P.34 CHG NET : RB3630.4,5 OFFPAGE -> DGND
PIN SWAP FOR A/W : CN3600 TMDs+,-
P.35 ADD PARTS : R3802,3803
PIN SWAP : CN3830
P.36 DEL PARTS : R3900,D3901,FB3970,3972,3976
CHG PARTS : Q3904,R3902
ADD PARTS : F3970,3972,3976
P.39 PIN SWAP : Q4199
ADD PARTS : R4199,C4199,Q4198
P.41 CHG CIRCUIT ALL : +1.2VSUS(AROUND IC4350)
P.44 ADD NET : IC4610.2 VR_ON#

POWER CHG PARTS : CHG SIZE R(0.5%) 0603 -> 1005

(REV. 0.130 -> 0.140)

P.22 ADD PARTS : SL1508,RB1540,1544
DEL PARTS : R1540 R1547
CHG NET : IC2500.40,RB2530.1 MODEL_ID_EC<1> -> CAM_OFF
CHG NET : IC2500.47 CAM_OFF -> VAUD_OFF#
P.28 CHG PARTS : CN3000
P.29 PIN SWAP : CN3100.1-12
P.34 DEL PARTS : R3650 - 3652,Q3650,3651
P.36 ADD PARTS : R3990
ADD NET : CN3900.25 VAUD_OFF#
CHG NET : CN3900.36 PWRGDSUS -> ANL66.ID

PIN SWAP FOR A/W(FROM A/WKER)
P.8 RB208
P.20 RB1050 - 1056,RB1070 - 1076
P.21 RB1450 - 1455,RB1460 - 1461
P.22 RB1520,1550
P.27 RB2521,2540,2550
P.30 FL3220,3221

(REV. 0.140 -> 0.150)

P.22 CHG PARTS : IC1580
P.31 PIN SWAP FOR A/W : FL3260
PIN SWAP FOR A/W : CN3260 TPA0 <-> TPB0
P.34 PIN SWAP FOR A/W : CN3600 PCIE_DOCK1_TX <-> CLK_PCIE_DOCK
CHG NET : CN3600 DOCKEDP,N -> GND

(REV. 0.150 -> 0.160)

P.8 ADD PARTS : R250-255
DEL PARTS : RB206
P.10 ADD PARTS : TP380-385
P.14 DEL PARTS : C608,609
P.16 CHG PARTS : R752-754,R776-779
CHG NOTES : R754,778,779 DNI -> MT
P.18 CHG PARTS : TP SIZE 0.3 -> D0.6
P.28 PIN SWAP : CN3000
P.33 CHG PARTS : CN3400,3401
P.36 PARTS CHG : Q3906
P.37 CHG NET : IC4000.17
P.46 CHG PARTS : Q4850,D4860,FB4860-4863

(REV. 0.160 -> 0.170)

P.14 DEL PARTS : RB651

PIN SWAP FOR A/W(FROM A/WKER)
P.21 RB1454
P.28 D3000 - 3004

(REV. 0.170 -> 0.180)

P.6 ADD PARTS : R50,Q51
CHG PARTS : Q50
P.16 ADD PARTS : R780
P.27 CHG NET : RB2550.4,5
P.34 CHG PARTS : Q3610
P.35 PIN SWAP : CN3850

(REV. 0.180 -> 0.190)

P.16 MOV PARTS : R780

(REV. 0.190 -> 0.1A0)

P.29 CHG PARTS : Q3100
P.41 CHG NET : AROUND IC4300

(REV. 0.1A0 -> 0.1B0)

P.8 CHG PARTS : R250
CHG NOTES : R252 MT -> DNI
P.16 ADD PARTS : R755
CHG NOTES : R730,R720 MT -> DNI
CHG NOTES : R719,R731 DNI -> MT
P.34 PIN SWAP : CN3600_D_VGA_RED,GRN,BLU
P.40 CHG PARTS : R4202
P.41 CHG PARTS : R4354

(REV. 0.1B0 -> 0.1B1)

P.19 CHG PARTS : CN1000
P.37 CHG PARTS : L4050,4070,R4011,4050,4070,C4090-4093
P.38 CHG PARTS : R5110,5123,5124,5125,5151,5152,5153,C5130,C5131

(REV. 0.1B1 -> 0.1B2)

P.18 CHG PARTS : IC900,920
P.38 CHG PARTS : L5110

(REV. 0.1B2 -> 0.1B3)

P.26 CHG NOTES : C2000-C2002 MT -> DNI
P.28 CHG NOTES : C3050,3051 MT -> DNI
P.32 CHG NOTES : C3301,3303,3360,3362,3363 MT -> DNI
P.33 CHG NOTES : C3410-3412,3430,3431,3450 MT -> DNI
P.35 CHG NOTES : C3830-3832,3850 MT -> DNI
P.46 CHG NOTES : C4850 MT -> DNI
P.47 CHG NOTES : C4900 MT -> DNI

(REV. 0.1B3 -> 0.1B4)

P.35 CHG NOTES : C3830 DNI -> MT

(DVT)

(REV. 0.1B4 -> 0.200)

P.6 DEL PARTS : JR0 1
ADD PARTS : SL50,51
CHG PARTS : C1 2
P.8 DEL PARTS : JR202
ADD PARTS : SL202
P.9 DEL PARTS : JR302
ADD PARTS : SL302
CHG PARTS : C340,341,344,345
P.14 PIN SWAP : AROUND IC611(VGA),631(LVDS)
P.16 DEL PARTS : TP731 AND ADD NET : HOTPLUG
P.18 CHG PARTS : C905,906,925,926
P.19 CHG PARTS : CN1020
P.21 CHG PARTS : C1400,1401,1425
P.22 DEL PARTS : JR1560
ADD PARTS : SL1560
CHG NOTES : R1509 MT -> DNI
CHG PARTS : C1518,1519,1540
PIN SWAP : IC1800.C8 <-> F8
CHG PARTS : C1823
P.27 CHG PARTS : C2510
P.32 CHG PARTS : C3304,3305
CHG NET : CN3600.26 DGND -> HOTPLUG
P.35 CHG NOTES : JR3810 MT->DNI,JR3811 DNI->MT
CHG PARTS : C3830
CHG PARTS : C4007,4009
P.39 CHG PARTS : C4121,C4151
P.38 CHG PARTS : C5101,C5151
P.41 CHG PARTS : IC4300 AND AROUND
P.42 CHG PARTS : IC4300 AND AROUND
P.45 ADD PARTS : D4730
DEL PARTS : Q4760
CHG NET : AROUND IC4760
CHG PARTS : C4722,4722,R4740,4741
P.46 CHG PARTS : C4823,4813
PIN SWAP : CN4800

(REV. 0.200 -> 0.210)

P.6 CHG PARTS : C1 2
P.7 CHG PARTS : R100,101,C150
P.11 CHG PARTS : C420,451,453,468
CHG NOTES : C410 DNI -> MT
P.12 CHG PARTS : C495
P.22 ADD SGRP : IC1580 SGRP=03
P.23 DEL PARTS : JR1611
CHG PARTS : C1620,1642
P.24 ADD PARTS : C1860-1864
P.27 CHG NOTES : R2580 DNI -> MT
P.28 PIN SWAP : CN3050.1P-12P
P.29 CHG PARTS : Q3151
DEL PARTS : JR3151,3152
P.32 CHG PARTS : C3354
P.36 CHG PARTS : C3976
P.37 ADD PARTS : Q4054,4074,R4060,4077
CHG PARTS & ADD SGRP : L4050,4070 SGRP=01
ADD SGRP : C4090-4093 SGRP=02
P.38 DEL PARTS & CHG PARTS : Q5171 & Q5170(2IN1)
P.45 CHG PARTS : Q4740
P.46 PIN SWAP : D4860

(REV. 0.210 -> 0.220)

P.9 ADD PARTS : C346-348,R343,344,JR340
P.10 DEL PARTS : TP383,384
P.11 ADD PARTS : C414
CHG PARTS : C410
ADD PARTS : C496,497,498
P.13 CHG PARTS : R513
P.14 CHG PARTS : FL651,652,R609
P.19 ADD SGRP : CN1000,1020 SGRP=04,05
P.22 CHG REF : JR1500 -> R1501
P.24 CHG PARTS : IC1800
P.25 CHG PARTS : IC1800
P.31 CHG PARTS : D3260
P.35 DEL PARTS : JR3810,3811
CHG PARTS : F3810
DEL PARTS : JR3900
P.37 CHG PARTS : Q4010
P.39 CHG PARTS : L4140,Q4190,4198
PIN SWAP : L4110
DEL PARTS : R4156,4157,4170,4171,SL4170
ADD PARTS : SL4151,4152,4170,4171,JR4170
CHG PARTS : Q4500,4592
P.43 CHG PARTS : Q4721
P.45 CHG PARTS : Q4860
P.46 CHG PARTS : Q4914
P.48 CHG PARTS : Q5003,5050

(REV. 0.220 -> 0.230)

P.12 ADD PARTS : C475-479
P.14 PIN SWAP : FL651,652
P.15 ADD PARTS : C667,668
P.22 CHG NET : RB1550 1.8P->NC , IC1400 F19P->PCIE_WAKE_F#
P.25 CHG PARTS : C1915,C1916
P.31 CHG PARTS : R3260,3261
P.37 CHG PARTS : R4052,4072
P.38 CHG PARTS : C5130,5131
CHG NET : R5177.2 +3VALW->+3VSUS
CHG NET : IC5100.29 R5173.2->DGND
DEL PARTS : C5150,R5140,5154-5156,5173
ADD PARTS : R5160,5161,C5163
P.40 CHG PARTS : R4207,C4209
ADD PARTS : JR4255
P.42 CHG PARTS : R4411,4412,4413,4421,4422,4423
P.43 CHG PARTS : R4510,4520,4550,4552-4554,4591-4593
P.44 CHG PARTS & CHG REF : JR4620 -> SL4620
P.45 CHG PARTS : D4710,4730,R4710,4723,4740,4741,C4740
CHG NET : R4714.1 IC4700.4->IC4700.5
IC4700.3 C4711.2->DGND
DEL PARTS : D4711,4713,R4711,Q4711
P.46 DEL PARTS : R4812,4813
CHG PARTS : D4830
P.46 CHG PARTS : Q4860
P.47 CHG PARTS : Q4914
ADD PARTS : C4901
P.48 CHG PARTS : Q5003,5050

(REV. 0.230 -> 0.240)

P.17 ADD PARTS : C838
P.22 CHG PARTS : RB1540,1544
P.38 ADD PARTS : R5190,5191,5184
CHG NET : IC5100.34,35P

(REV. 0.240 -> 0.241)

P.29 CHG P/N : CN3100(SAME PARTS)
P.30 CHG P/N : CN3201(SAME PARTS)
P.32 CHG P/N : CN3300(SAME PARTS)
P.34 CHG P/N : CN3600(SAME PARTS)
P.49 CHG P/N : CN5520(SAME PARTS)

(REV. 0.241 -> 0.242)

P.7 CHG PARTS : C100-125 22UF ->10UF
CHG NOTES : MT->DNI C102,204,107,109,110-113
P.11 CHG PARTS : L450,453,JR401
P.15 CHG PARTS : R682
P.31 CHG PARTS : Q3280
P.43 CHG PARTS : Q4570

(REV. 0.242 -> 0.243)

P.39 CHG PARTS : L4140,R4151
P.41 CHG PARTS : R4354,R4357

(REV. 0.243 -> 0.244)

P.30 CHG PARTS : FL3220,3221

(REV. 0.244 -> 0.245)

P.16 CHG NOTES : MT->DNI R703,706
DNI->MT R704,705
P.30 CHG PARTS : FL3220,3221,CN3200
P.39 CHG PARTS : IC4100
CHG NOTES : DNI->MT R4190
P.42 CHG PARTS : R4430,4450
P.46 CHG PARTS : CN4800

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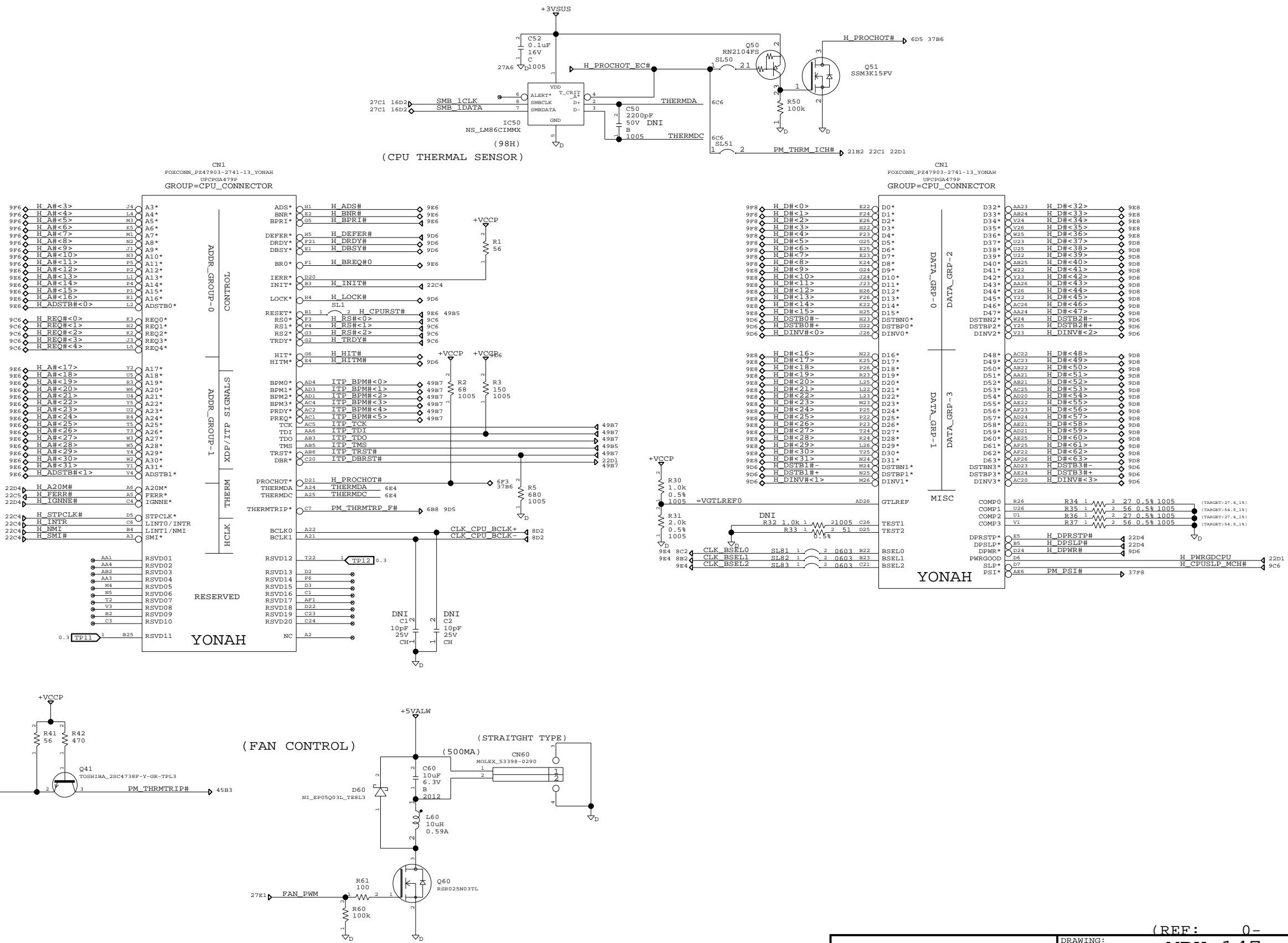
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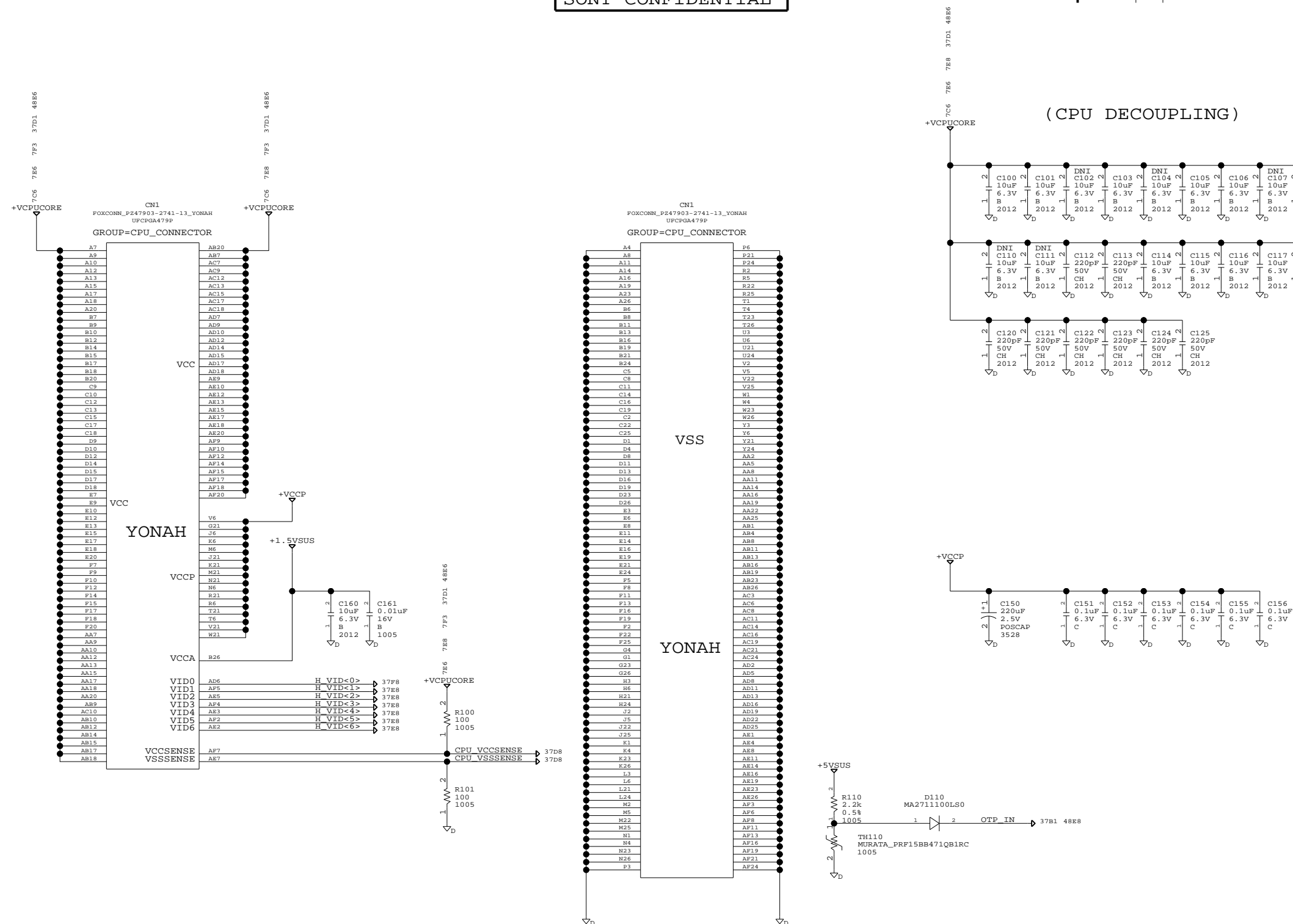
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-	QTY 1	REV. 0.245	PART NO. -
SIZE C	DESIGNER -	(REVISION-1) SHEET OF	



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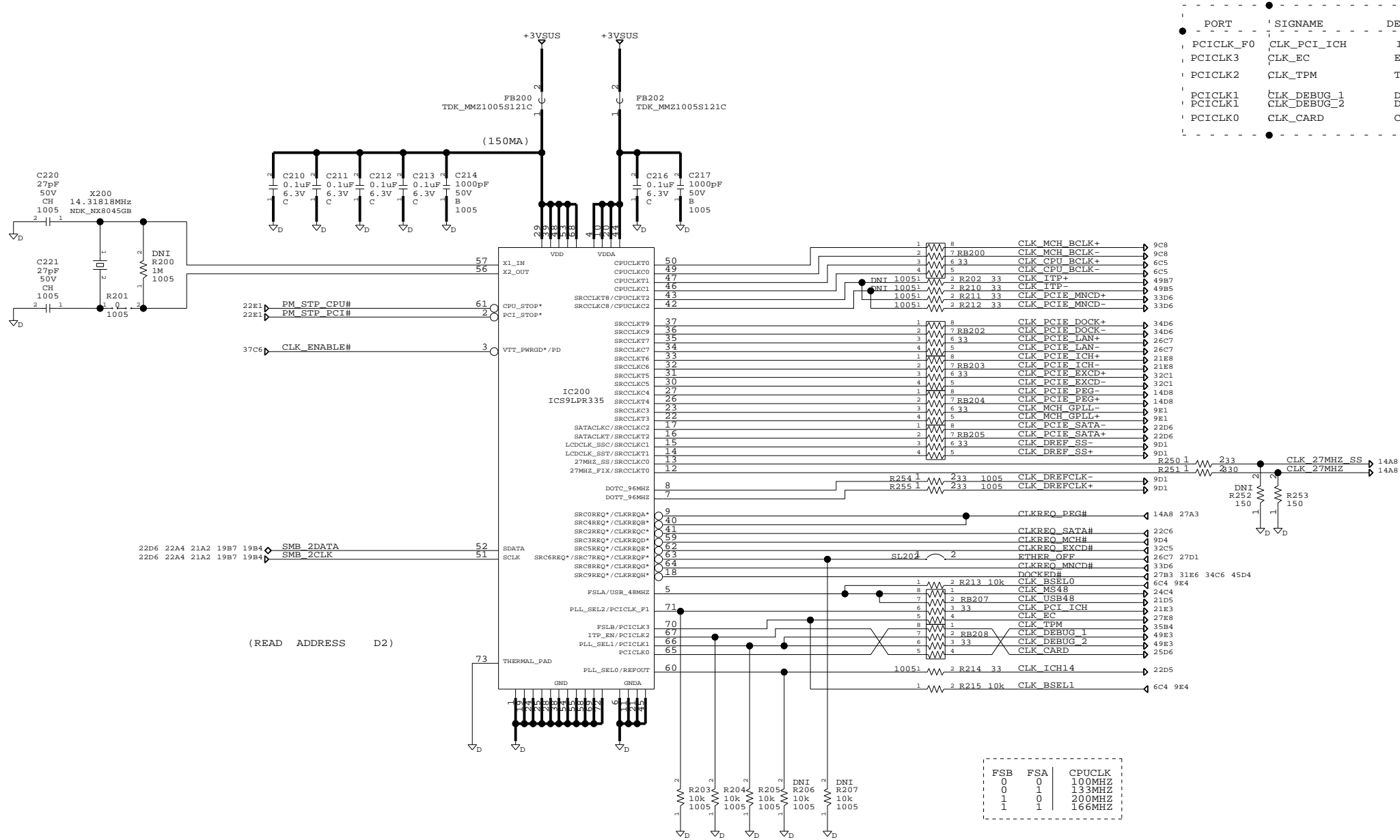
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MBX-147			
CPU (2/2)			
LAST_MODIFIED=Mon Jan 16 20:46:49 2006			
-	QTY 1	REV. 1.154	PART NO. -
SIZE C	DESIGNER -	SHEET 7 OF 50 (CPU-2)	

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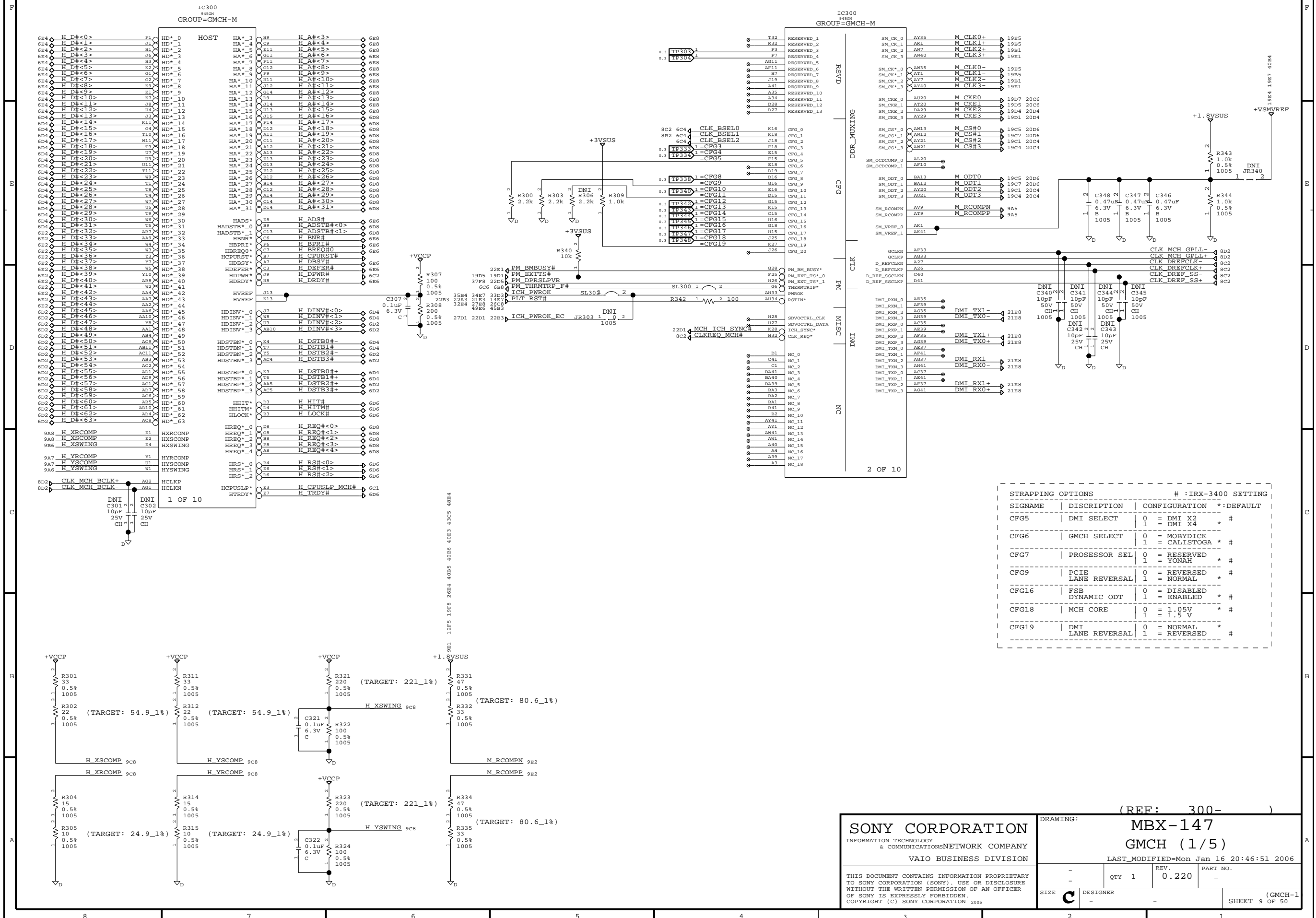
DRAWING: MBX-147 CLOCK GENERATOR			
LAST MODIFIED=Mon Jan 16 20:46:50 2006			
-	QTY 1	REV. 1.151	PART NO. -
SIZE C	DESIGNER -	(CLKGEN-1) SHEET 8 OF 50	

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ZONE	LTR	DESCRIPTION	DATE	APPR.



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ZONE	LTR	DESCRIPTION	DATE	APPR.

IC300
945GM
GROUP=GMCH-M

1987	M A DQ<0>	AJ35	SA_DQ_0	SA_BS_0	AU12	M A BS<0>	19C7	20D6	
1987	M A DQ<1>	AJ34	SA_DQ_1	SA_BS_1	AV14	M A BS<1>	19C5	20C6	
1987	M A DQ<2>	AM31	SA_DQ_2	SA_BS_2	BA20	M A BS<2>	19D7	20C6	
1987	M A DQ<3>	AM33	SA_DQ_3						
1985	M A DQ<4>	AJ36	SA_DQ_4	SA_DM_0	AY13	M A CAS#	19C7	20D6	
1985	M A DQ<5>	AK35	SA_DQ_5	SA_DM_1	AJ33	M A DM<0>	19E5		
1985	M A DQ<6>	AJ32	SA_DQ_6	SA_DM_2	AM35	M A DM<1>	19E5		
1985	M A DQ<7>	AM33	SA_DQ_7	SA_DM_3	AL26	M A DM<2>	19D5		
1987	M A DQ<8>	AM35	SA_DQ_8	SA_DM_4	AM22	M A DM<3>	19D7		
1987	M A DQ<9>	AP33	SA_DQ_9	SA_DM_5	AM14	M A DM<4>	19C5		
1987	M A DQ<10>	AR31	SA_DQ_10	SA_DM_6	AL9	M A DM<5>	19C7		
1987	M A DQ<11>	AP31	SA_DQ_11	SA_DM_7	AR3	M A DM<6>	19B5		
1985	M A DQ<12>	AM38	SA_DQ_12		AM4	M A DM<7>	19B7		
1985	M A DQ<13>	AM36	SA_DQ_13						
1985	M A DQ<14>	AM34	SA_DQ_14	SA_DQS_0	AK33	M A DQS0+	19E7		
1985	M A DQ<15>	AM33	SA_DQ_15	SA_DQS_1	AT33	M A DQS1+	19E7		
19D7	M A DQ<16>	AM28	SA_DQ_16	SA_DQS_2	AM28	M A DQS2+	19D7		
19D7	M A DQ<17>	AL27	SA_DQ_17	SA_DQS_3	AM22	M A DQS3+	19D5		
19D7	M A DQ<18>	AM26	SA_DQ_18	SA_DQS_4	AM12	M A DQS4+	19C7		
19D7	M A DQ<19>	AM24	SA_DQ_19	SA_DQS_5	AM8	M A DQS5+	19C5		
19D5	M A DQ<20>	AM28	SA_DQ_20	SA_DQS_6	AP3	M A DQS6+	19B7		
19D5	M A DQ<21>	AL28	SA_DQ_21	SA_DQS_7	AG5	M A DQS7+	19B7	19B5	
19D5	M A DQ<22>	AM24	SA_DQ_22	SA_DQS*0	AK32	M A DQS0-	19E7		
19D5	M A DQ<23>	AP26	SA_DQ_23	SA_DQS*1	AU33	M A DQS1-	19E7		
19D7	M A DQ<24>	AP23	SA_DQ_24	SA_DQS*2	AM27	M A DQS2-	19D7		
19D7	M A DQ<25>	AL22	SA_DQ_25	SA_DQS*3	AM21	M A DQS3-	19D5		
19D7	M A DQ<26>	AP21	SA_DQ_26	SA_DQS*4	AM12	M A DQS4-	19C7		
19D7	M A DQ<27>	AM20	SA_DQ_27	SA_DQS*5	AL8	M A DQS5-	19C5		
19D5	M A DQ<28>	AL23	SA_DQ_28	SA_DQS*6	AR3	M A DQS6-	19B7		
19D5	M A DQ<29>	AP24	SA_DQ_29	SA_DQS*7	AM5	M A DQS7-	19B7	19B5	
19D5	M A DQ<30>	AP20	SA_DQ_30						
19D5	M A DQ<31>	AT21	SA_DQ_31	SA_MA_0	AV16	M A A<0>	19C5	20C6	
19C7	M A DQ<32>	AM12	SA_DQ_32	SA_MA_1	AU14	M A A<1>	19C7	20D6	
19C7	M A DQ<33>	AR14	SA_DQ_33	SA_MA_2	AM16	M A A<2>	19C5	20D6	
19C7	M A DQ<34>	AP13	SA_DQ_34	SA_MA_3	BA16	M A A<3>	19C7	20D6	
19C7	M A DQ<35>	AP12	SA_DQ_35	SA_MA_4	BA17	M A A<4>	19C5	20D6	
19C5	M A DQ<36>	AU13	SA_DQ_36	SA_MA_5	AU16	M A A<5>	19C7	20C6	
19C5	M A DQ<37>	AT12	SA_DQ_37	SA_MA_6	AV17	M A A<6>	19D5	20D6	
19C5	M A DQ<38>	AL14	SA_DQ_38	SA_MA_7	AU17	M A A<7>	19D5	20D6	
19C5	M A DQ<39>	AL12	SA_DQ_39	SA_MA_8	AM17	M A A<8>	19D7	20C6	
19C7	M A DQ<40>	AK5	SA_DQ_40	SA_MA_9	AT16	M A A<9>	19D7	20C6	
19C7	M A DQ<41>	AM7	SA_DQ_41	SA_MA_10	AU13	M A A<10>	19C7	20D6	
19B7	M A DQ<42>	AK8	SA_DQ_42	SA_MA_11	AT17	M A A<11>	19D5	20C6	
19B7	M A DQ<43>	AK7	SA_DQ_43	SA_MA_12	AV20	M A A<12>	19D7	20C6	
19C5	M A DQ<44>	AP9	SA_DQ_44	SA_MA_13	AV12	M A A<13>	19D5	20D6	
19C5	M A DQ<45>	AM9	SA_DQ_45						
19B5	M A DQ<46>	AT5	SA_DQ_46	SA_RAS*	AM14	M A RAS#	19C5	20D6	
19B5	M A DQ<47>	AL5	SA_DQ_47	SA_RCVENIN*	AK23	=A RCVENIN#			
19B7	M A DQ<48>	AY2	SA_DQ_48	SA_RCVENOUT*	AK24	=A RCVENOUT#			
19B7	M A DQ<49>	AM2	SA_DQ_49	SA_WE*	AY14	M A WE#	19C7	20D6	
19B7	M A DQ<50>	AP1	SA_DQ_50						
19B7	M A DQ<51>	AM2	SA_DQ_51						
19B5	M A DQ<52>	AV2	SA_DQ_52						
19B5	M A DQ<53>	AT3	SA_DQ_53						
19B5	M A DQ<54>	AM1	SA_DQ_54						
19B5	M A DQ<55>	AL2	SA_DQ_55						
19B7	M A DQ<56>	AM7	SA_DQ_56						
19B7	M A DQ<57>	AP9	SA_DQ_57						
19B7	M A DQ<58>	AG4	SA_DQ_58						
19B5	M A DQ<59>	AP6	SA_DQ_59						
19B5	M A DQ<60>	AG9	SA_DQ_60						
19B5	M A DQ<61>	AM6	SA_DQ_61						
19B5	M A DQ<62>	AP4	SA_DQ_62						
19B5	M A DQ<63>	AP8	SA_DQ_63						

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19B5	10D5	M A DQS7+	1	TP380	0.3
19B5	10C5	M A DQS7-	1	TP381	0.3
19B5	10B7	M A DQ<63>	1	TP382	0.3

19E4	10E4	M B DQ<0>	1	TP385	0.3
------	------	-----------	---	-------	-----

IC300
945GM
GROUP=GMCH-M

19E4	10B7	M B DQ<0>	AK39	SB_DQ_0	AT24	M B BS<0>	19C4	20C4	
19E4	M B DQ<1>	AJ37	SB_DQ_1	SB_BS_1	AV23	M B BS<1>	19C1	20C4	
19E4	M B DQ<2>	AP39	SB_DQ_2	SB_BS_2	AY28	M B BS<2>	19D4	20D4	
19E4	M B DQ<3>	AM41	SB_DQ_3						
19E1	M B DQ<4>	AJ38	SB_DQ_4	SB_CAS*	AR24	M B CAS#	19C4	20C4	
19E1	M B DQ<5>	AK38	SB_DQ_5	SB_DM_0	AK36	M B DM<0>	19E1		
19E1	M B DQ<6>	AM41	SB_DQ_6	SB_DM_1	AR38	M B DM<1>	19E1		
19E1	M B DQ<7>	AM41	SB_DQ_7	SB_DM_2	AT36	M B DM<2>	19D1		
19E4	M B DQ<8>	AT40	SB_DQ_8	SB_DM_3	BA11	M B DM<3>	19D4		
19E4	M B DQ<9>	AV41	SB_DQ_9	SB_DM_4	AL17	M B DM<4>	19C1		
19E4	M B DQ<10>	AU38	SB_DQ_10	SB_DM_5	AM8	M B DM<5>	19C4		
19E4	M B DQ<11>	AV38	SB_DQ_11	SB_DM_6	BA5	M B DM<6>	19B1		
19E1	M B DQ<12>	AP28	SB_DQ_12	SB_DM_7	AM4	M B DM<7>	19B4		
19E1	M B DQ<13>	AR40	SB_DQ_13						
19E1	M B DQ<14>	AM38	SB_DQ_14	SB_DQS_0	AM39	M B DQS0+	19E4		
19E1	M B DQ<15>	AJ38	SB_DQ_15	SB_DQS_1	AT39	M B DQS1+	19E4		
19D4	M B DQ<16>	BA38	SB_DQ_16	SB_DQS_2	AU35	M B DQS2+	19D4		
19D4	M B DQ<17>	AV36	SB_DQ_17	SB_DQS_3	AR29	M B DQS3+	19D1		
19D4	M B DQ<18>	AR36	SB_DQ_18	SB_DQS_4	AR16	M B DQS4+	19C4		
19D4	M B DQ<19>	AP36	SB_DQ_19	SB_DQS_5	AR10	M B DQS5+	19C1		
19D1	M B DQ<20>	BA36	SB_DQ_20	SB_DQS_6	AR7	M B DQS6+	19B4		
19D1	M B DQ<21>	AU36	SB_DQ_21	SB_DQS_7	AM5	M B DQS7+	19B1		
19D1	M B DQ<22>	AP35	SB_DQ_22	SB_DQS*0	AM40	M B DQS0-	19E4		
19D1	M B DQ<23>	AP34	SB_DQ_23	SB_DQS*1	AU39	M B DQS1-	19E4		
19D4	M B DQ<24>	AT33	SB_DQ_24	SB_DQS*2	AT35	M B DQS2-	19D4		
19D4	M B DQ<25>	BA33	SB_DQ_25	SB_DQS*3	AP29	M B DQS3-	19D1		
19D4	M B DQ<26>	AT31	SB_DQ_26	SB_DQS*4	AP16	M B DQS4-	19C4		
19D4	M B DQ<27>	AU29	SB_DQ_27	SB_DQS*5	AT10	M B DQS5-	19C1		
19D1	M B DQ<28>	AU31	SB_DQ_28	SB_DQS*6	AT7	M B DQS6-	19B4		
19D1	M B DQ<29>	AM31	SB_DQ_29	SB_DQS*7	AP5	M B DQS7-	19B1		
19D1	M B DQ<30>	AV29	SB_DQ_30						
19D1	M B DQ<31>	AM29	SB_DQ_31	SB_MA_0	AV23	M B A<0>	19C1	20D4	
19C4	M B DQ<32>	AM18	SB_DQ_32	SB_MA_1	AM24	M B A<1>	19C4	20D4	
19C4	M B DQ<33>	AP19	SB_DQ_33	SB_MA_2	AT24	M B A<2>	19C1	20D4	
19C4	M B DQ<34>	AP14	SB_DQ_34	SB_MA_3	AR28	M B A<3>	19C4	20D4	
19C4	M B DQ<35>	AM14	SB_DQ_35	SB_MA_4	AT27	M B A<4>	19C1	20D4	
19C1	M B DQ<36>	AM17	SB_DQ_36	SB_MA_5	AT28	M B A<5>	19C4	20D4	
19C1	M B DQ<37>	AM16	SB_DQ_37	SB_MA_6	AU27	M B A<6>	19D1	20D4	
19C1	M B DQ<38>	AP15	SB_DQ_38	SB_MA_7	AV28	M B A<7>	19D1	20D4	
19C1	M B DQ<39>	AL15	SB_DQ_39	SB_MA_8	AV27	M B A<8>	19D4	20D4	
19C4	M B DQ<40>	AU11	SB_DQ_40	SB_MA_9	AM27	M B A<9>	19D4	20D4	
19C4	M B DQ<41>	AM10	SB_DQ_41	SB_MA_10	AV24	M B A<10>	19C4	20C4	
19B4	M B DQ<42>	AU9	SB_DQ_42	SB_MA_11	BA27	M B A<11>	19D1	20D4	
19B4	M B DQ<43>	AM10	SB_DQ_43	SB_MA_12	AT27	M B A<12>	19D4	20D4	
19C1	M B DQ<44>	AM13	SB_DQ_44	SB_MA_13	AR23	M B A<13>	19C1	20C4	
19C1	M B DQ<45>	AM11	SB_DQ_45						
19B1	M B DQ<46>	AK10	SB_DQ_46	SB_RAS*	AU23	M B RAS#	19C1	20C4	
19B1	M B DQ<47>	AT8	SB_DQ_47	SB_RCVENIN*	AK16	=B RCVENIN#			
19B4	M B DQ<48>	BA10	SB_DQ_48	SB_RCVENOUT*	AK18	=B RCVENOUT#			
19B4	M B DQ<49>	AM10	SB_DQ_49	SB_WE*	AR27	M B WE#	19C4	20C4	
19B4	M B DQ<50>	BA4	SB_DQ_50						
19B4	M B DQ<51>	AM4	SB_DQ_51						
19B1	M B DQ<52>	AY10	SB_DQ_52						
19B1	M B DQ<53>	AY9	SB_DQ_53						
19B1	M B DQ<54>	AM5	SB_DQ_54						
19B1	M B DQ<55>	AY5	SB_DQ_55						
19B4	M B DQ<56>	AV4	SB_DQ_56						
19B4	M B DQ<57>	AR5	SB_DQ_57						
19B4	M B DQ<58>	AK4	SB_DQ_58						
19B4	M B DQ<59>	AK3	SB_DQ_59						
19B1	M B DQ<60>	AT4	SB_DQ_60						
19B1	M B DQ<61>	AK5	SB_DQ_61						
19B1	M B DQ<62>	AJ5	SB_DQ_62						
19B1	M B DQ<63>	AJ3	SB_DQ_63						

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DRAWING:

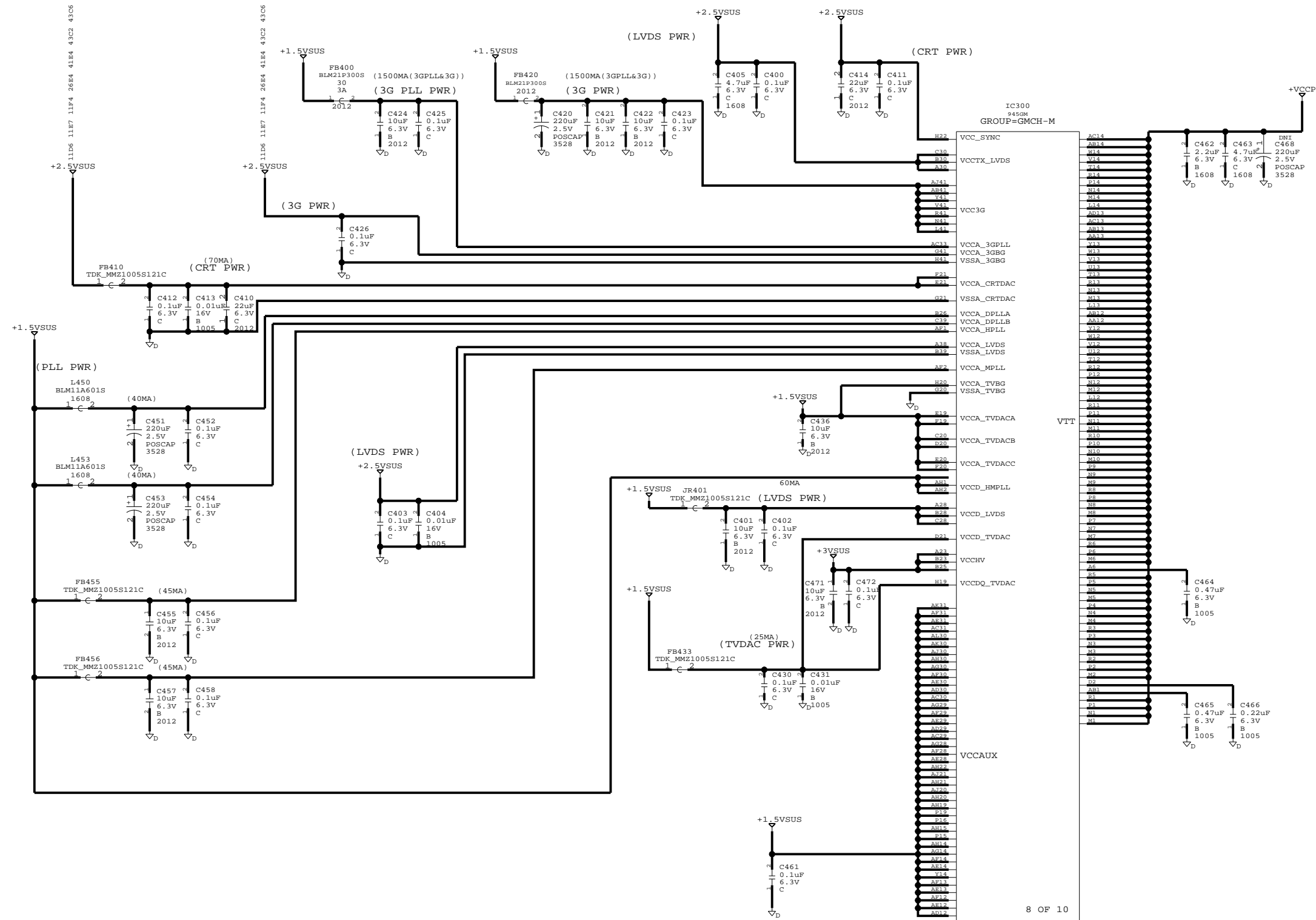
MBX-147
GMCH (2/5)

LAST_MODIFIED=Mon Jan 16 20:46:53 2006

-	QTY	1	REV.	0.160	PART NO.	-
SIZE	C	DESIGNER	-			(GMCH-2) SHEET 10 OF 50

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MBX-147
GMCH (3/5

LAST_MODIFIED=Mon Jan 16 20:46:06 2006

	-	QTY 1	REV. 0.242	PART NO. -
--	---	-------	------------	------------

SIZE	DESIGNER		
C	-	-	SI

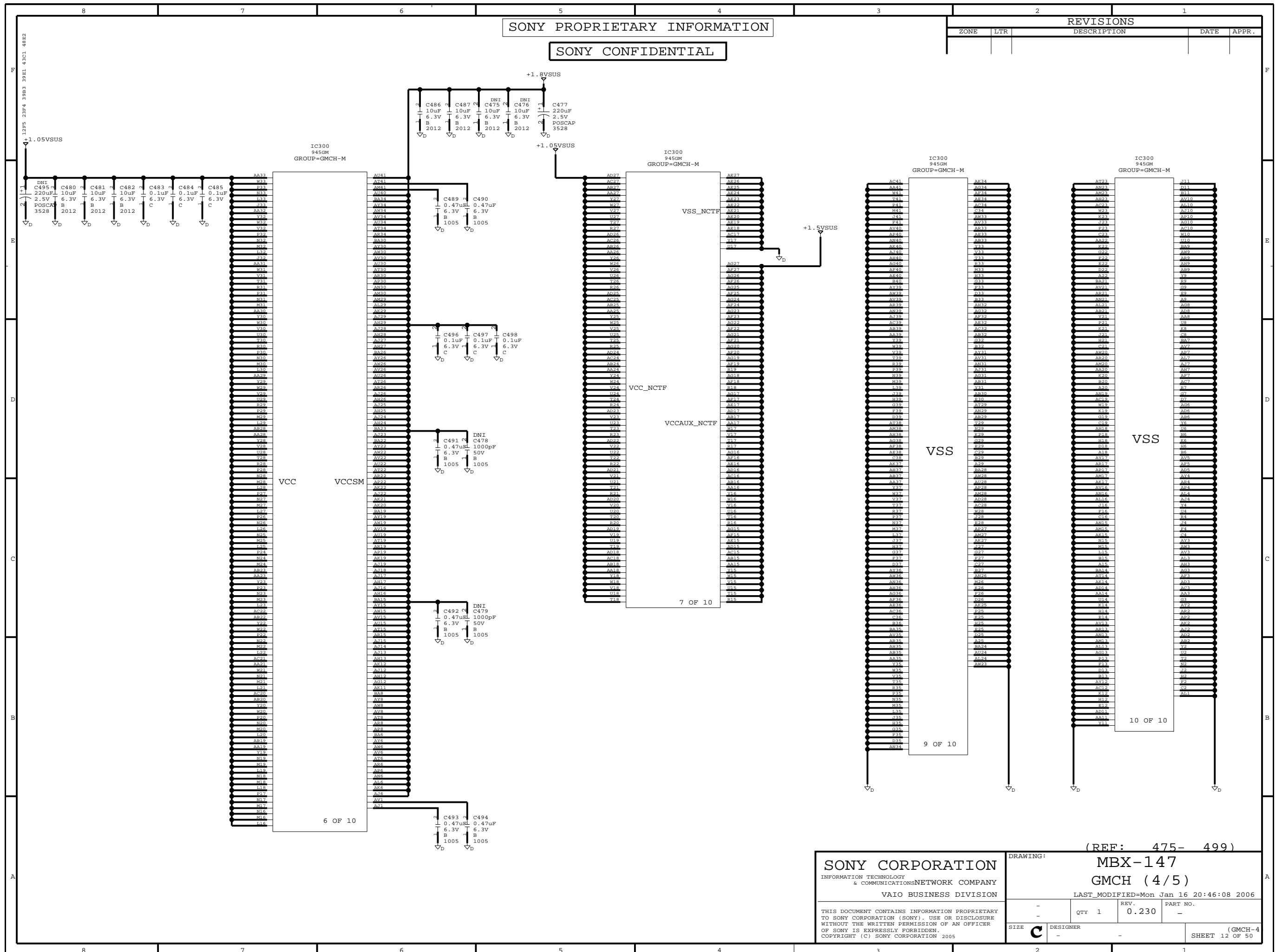
(GMCH-3
SHEET 11 OF 50

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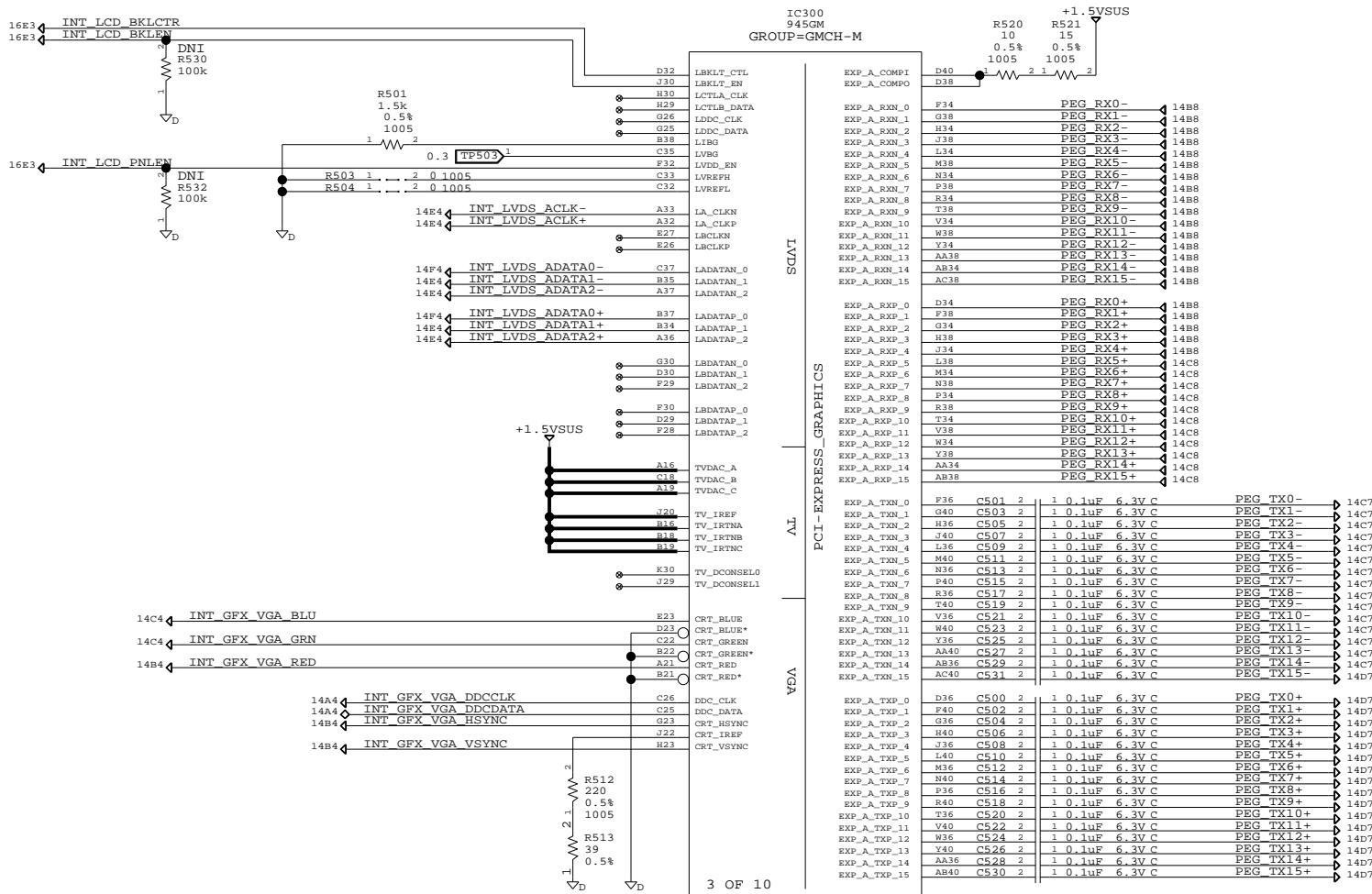
DRAWING:		(REF: 475- 499)	
MBX-147		GMCH (4/5)	
LAST MODIFIED=Mon Jan 16 20:46:08 2006			
SIZE	QTY 1	REV. 0.230	PART NO. -
DESIGNER	-	(GMCH-4 SHEET 12 OF 50)	

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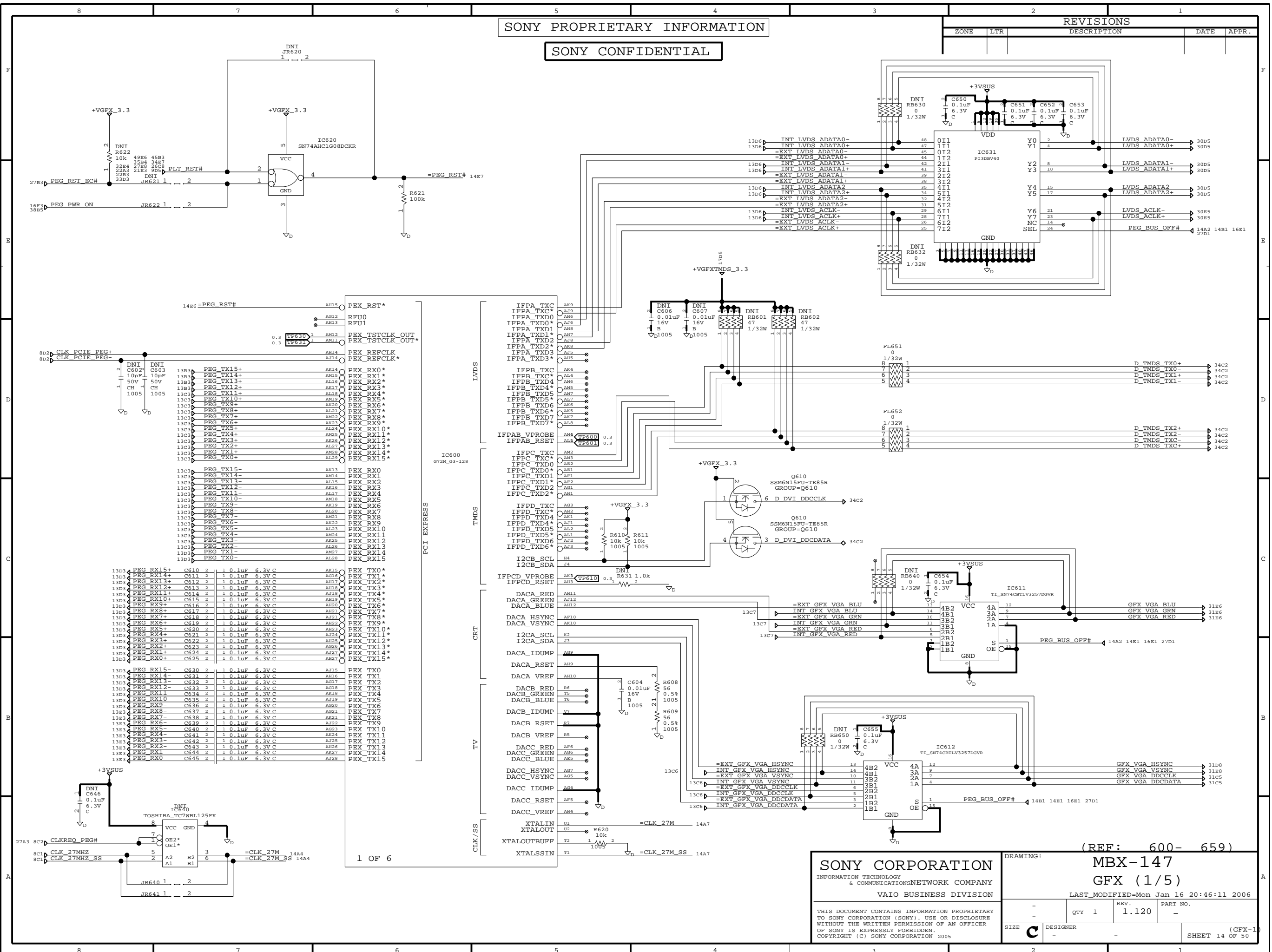
DRAWING:		MBX-147	
		GMCH (5/5)	
		LAST MODIFIED=Mon Jan 16 20:46:09 2006	
SIZE	C	QTY	1
DESIGNER	-	REV.	0.220
		PART NO.	-
		(GMCH-5 SHEET 13 OF 50)	

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DRAWING:

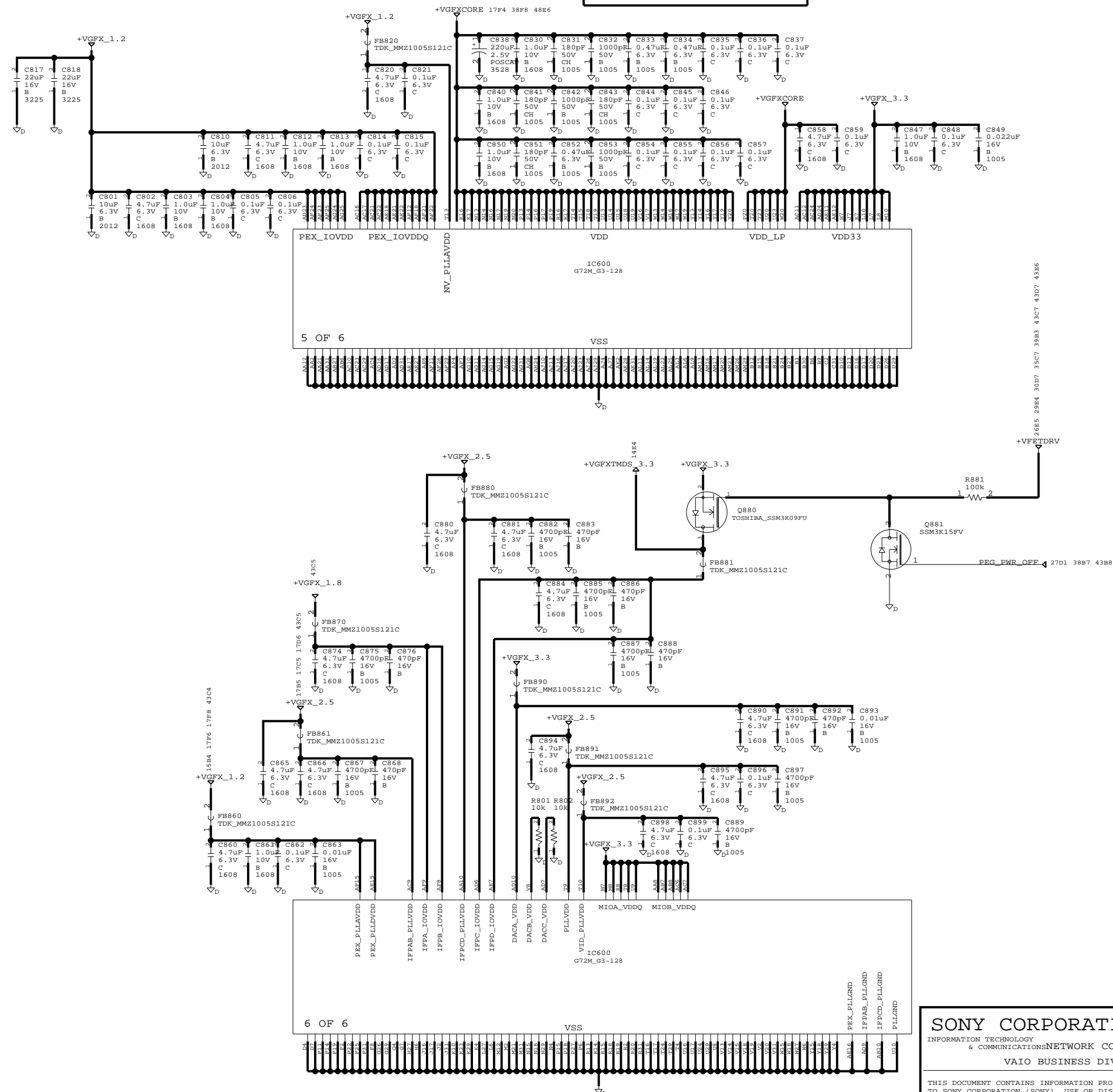
(REF: 600- 659)

MBX-147

GFX (1/5)

LAST MODIFIED=Mon Jan 16 20:46:11 2006

SIZE	QTY	REV.	PART NO.
C	1	1.120	-
DESIGNER	-	-	-
SHEET	14	OF 50	(GFX-1)



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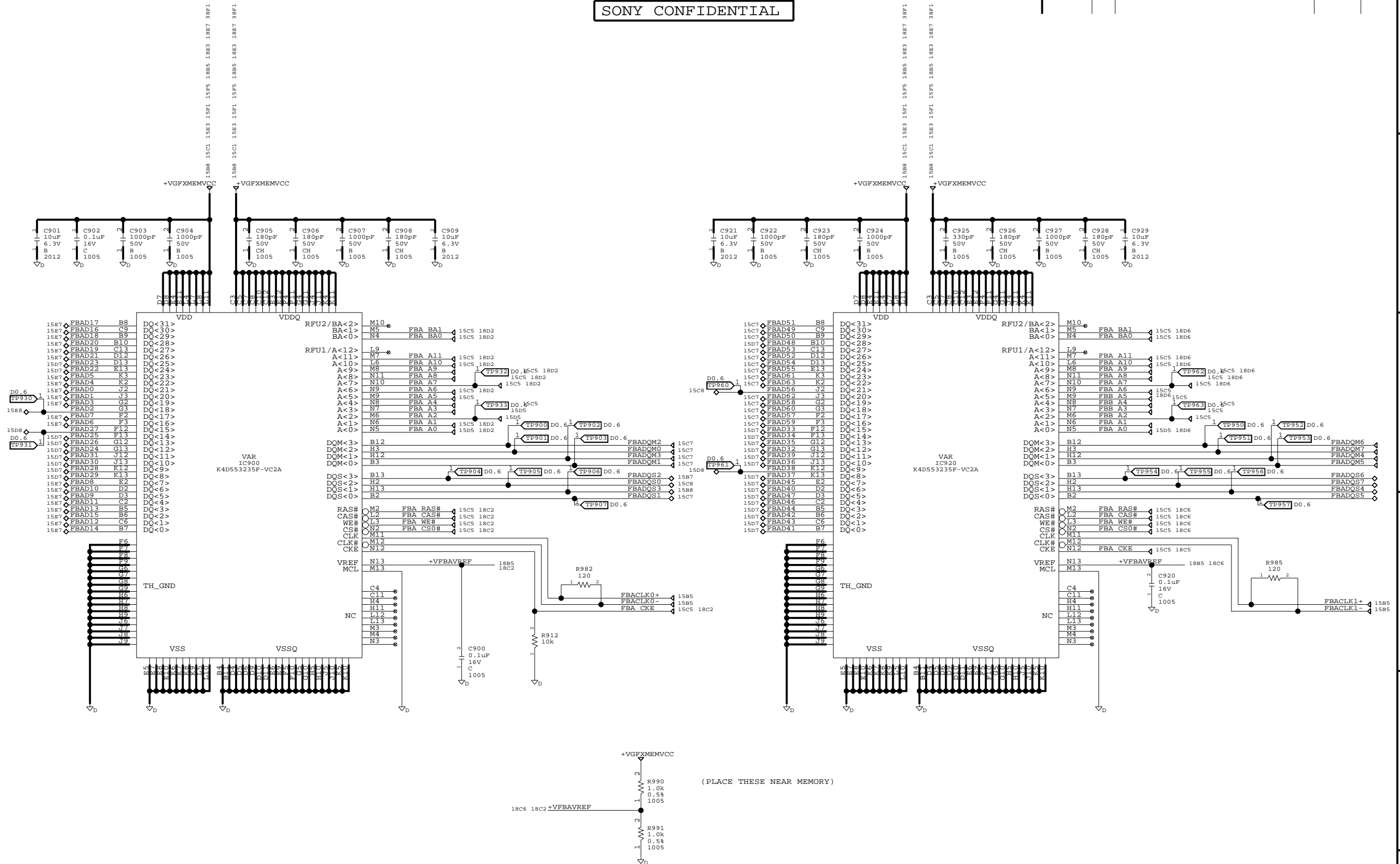
DRAWING:				(REV. 800-899) MBX-147 GFX (4/5)	
LAST_MODIFIED=Mon Jan 16 20:46:15 2006					
-		REV.	PART NO.		
-	QTY 1	1.154	-		
SIZE	DESIGNER			(GFX-4)	
C	-	-	SHEET 17 OF 50		

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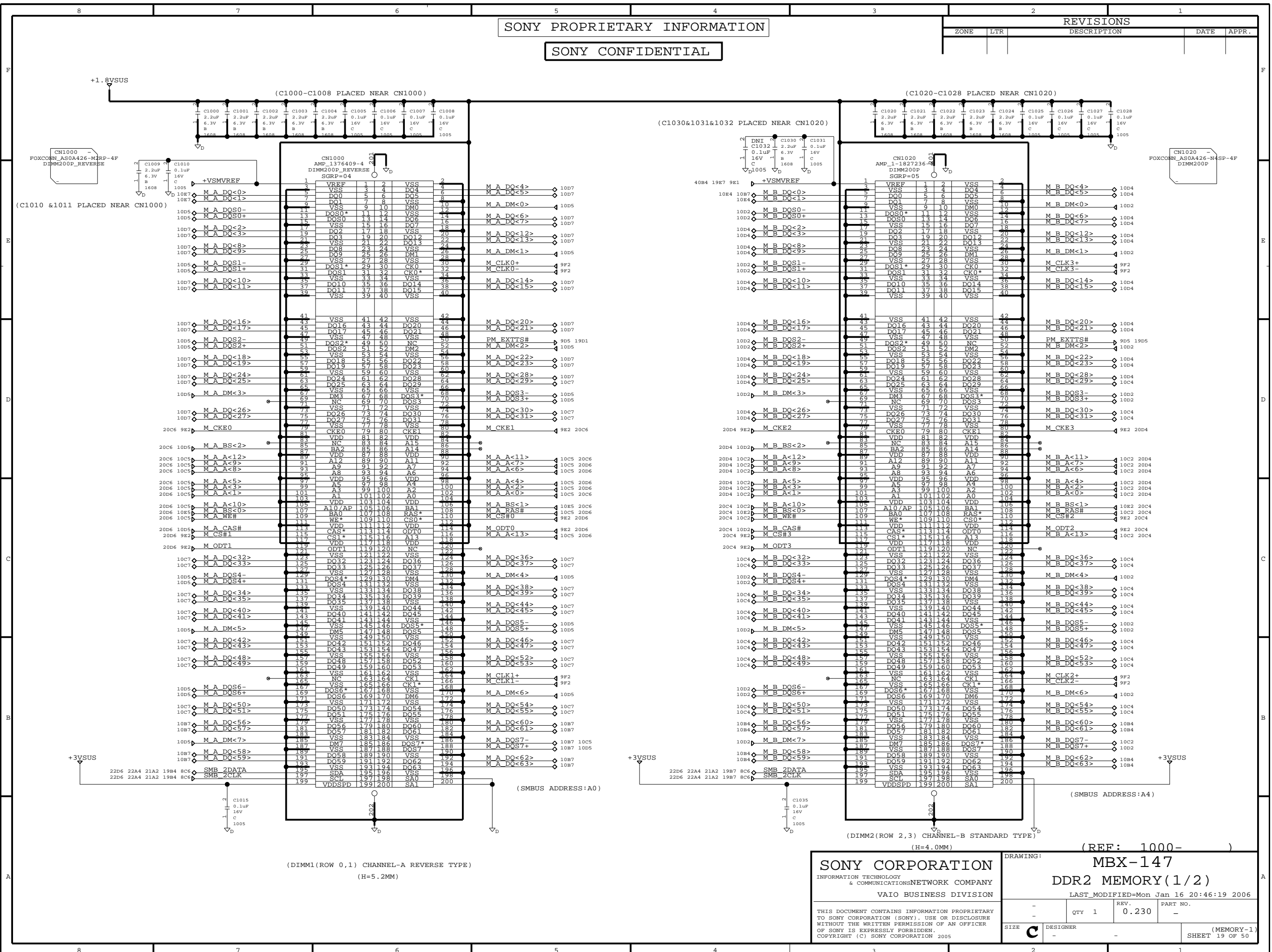
DRAWING:		REV. 1.154		PART NO. -	
QTY 1		LAST_MODIFIED=Mon Jan 16 20:46:16 2006		DESIGNER -	
SIZE C		SHEET 18 OF 50		(GFX-5)	

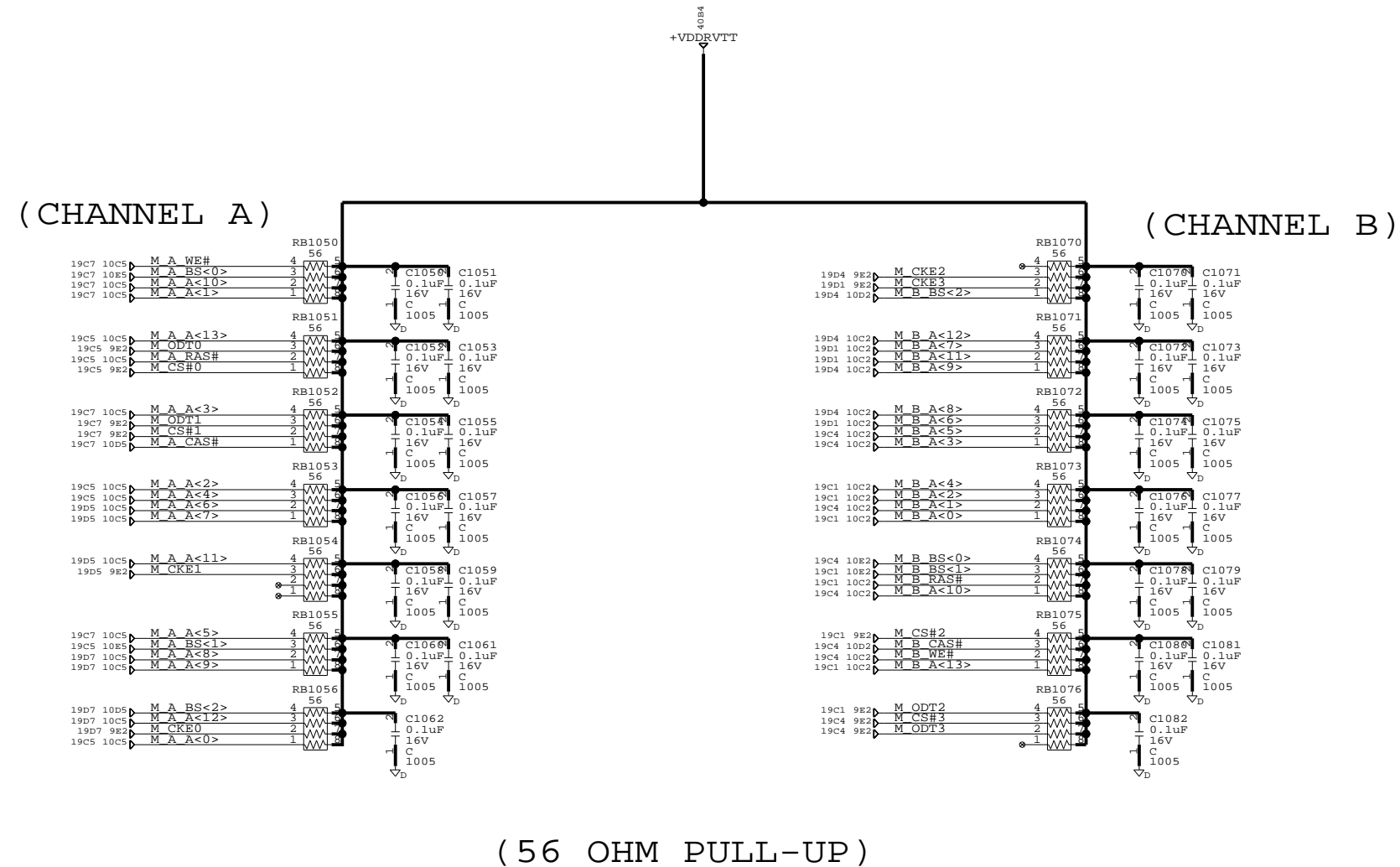
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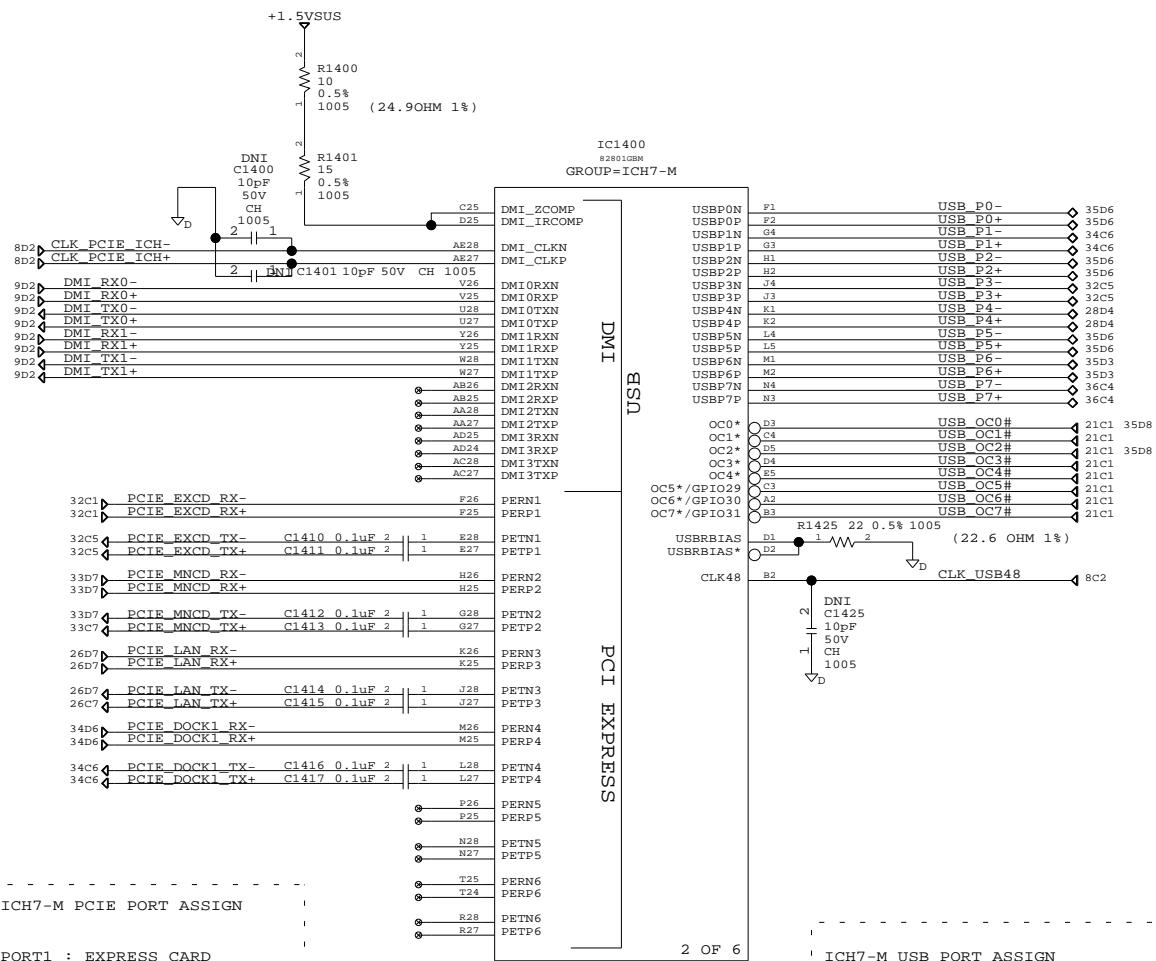
DRAWING:		MBX-147	
		DDR2 MEMORY(2/2)	
LAST_MODIFIED=Mon Jan 16 20:46:20 2006			
-	QTY 1	REV. 0.100	PART NO. -
SIZE C	DESIGNER -		(MEMORY-2) SHEET 20 OF 50

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ICH7-M PCIE PORT ASSIGN

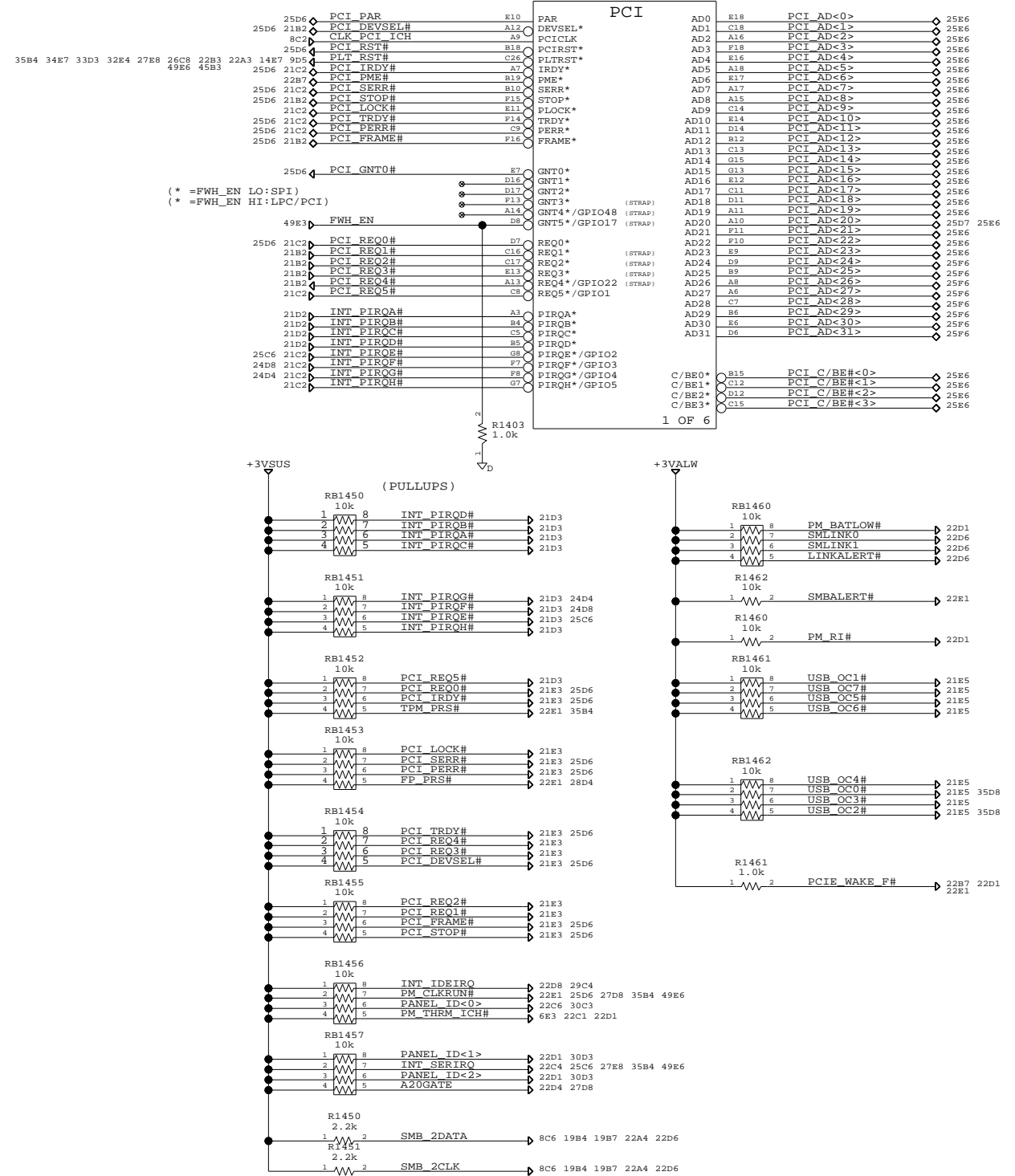
PORT1 : EXPRESS CARD
PORT2 : MINI CARD
PORT3 : LAN
PORT4 : DOCK(LAN)

ICH7-M USB PORT ASSIGN

PORT0 : MAIN UNIT CN
PORT1 : DOCK CN
PORT2 : MAIN UNIT CN
PORT3 : EXPRESS CARD
PORT4 : FINGER PRINT SENSOR
PORT5 : CAMERA
PORT6 : FELICA / WAN
PORT7 : BLUE TOOTH

REQ/GNT BUS NO DEV.NO AD<XX> PIRQ

CARD A	0	8	4	20	E
I.LINK	-	8	4	20	G
MEMORY STICK	-	8	4	20	F
INT LAN	-	8	8	-	E
DRAM CONT	-	0	0	-	
PEG BRIDGE	-	0	1	-	
INT GFX	-	0	2	-	
AZALIA	-	0	27	-	D
USB UHCI1	-	0	29	-	B
USB UHCI2	-	0	29	-	D
USB UHCI3	-	0	29	-	F
USB UHCI4	-	0	29	-	B
USB EHCI	-	0	29	-	H



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ICH7-M(1/3)

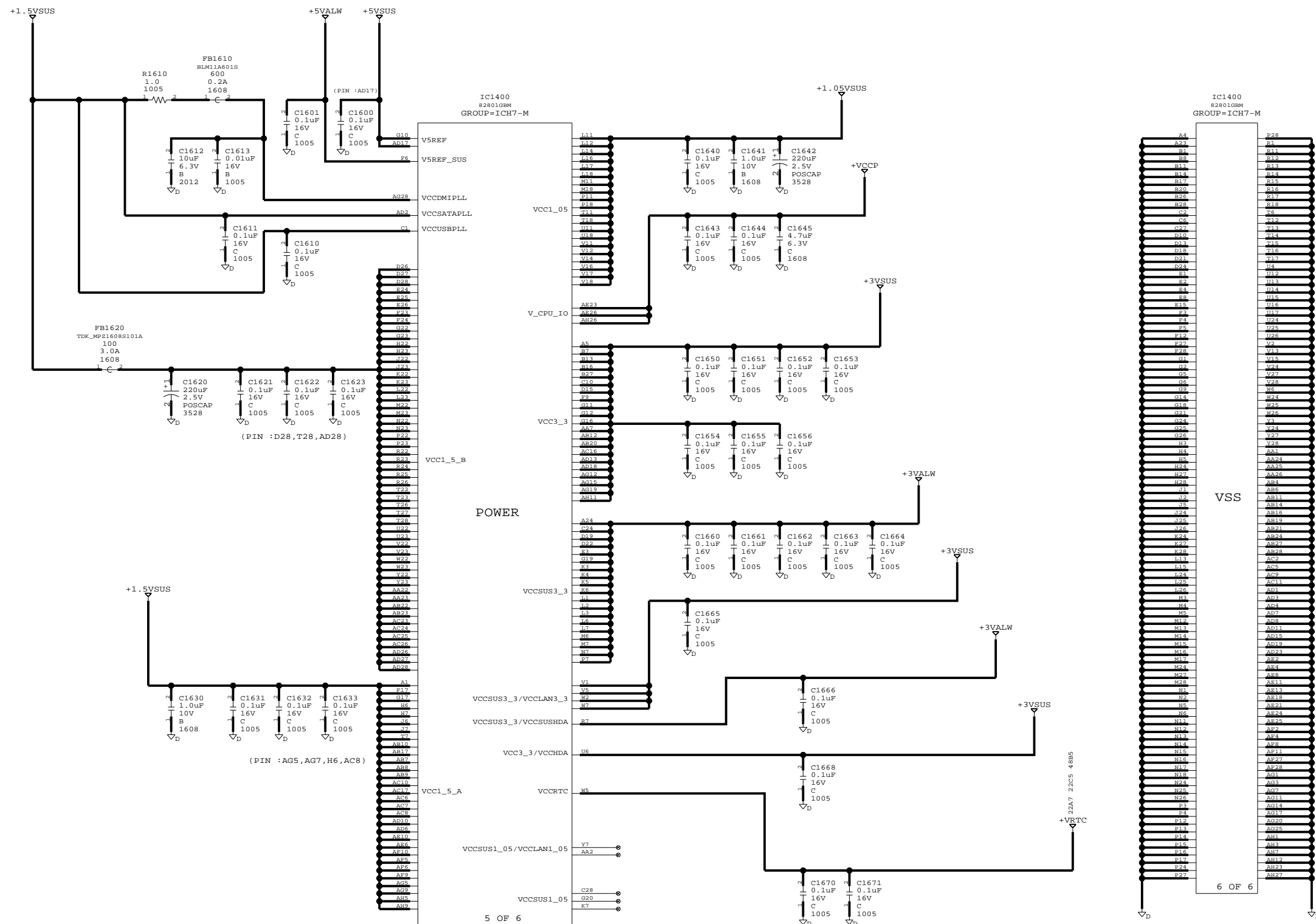
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SIZE	C	DESIGNER	-	QTY	1	REV.	0.200	PART NO.	-
								SHEET 21 OF 50	

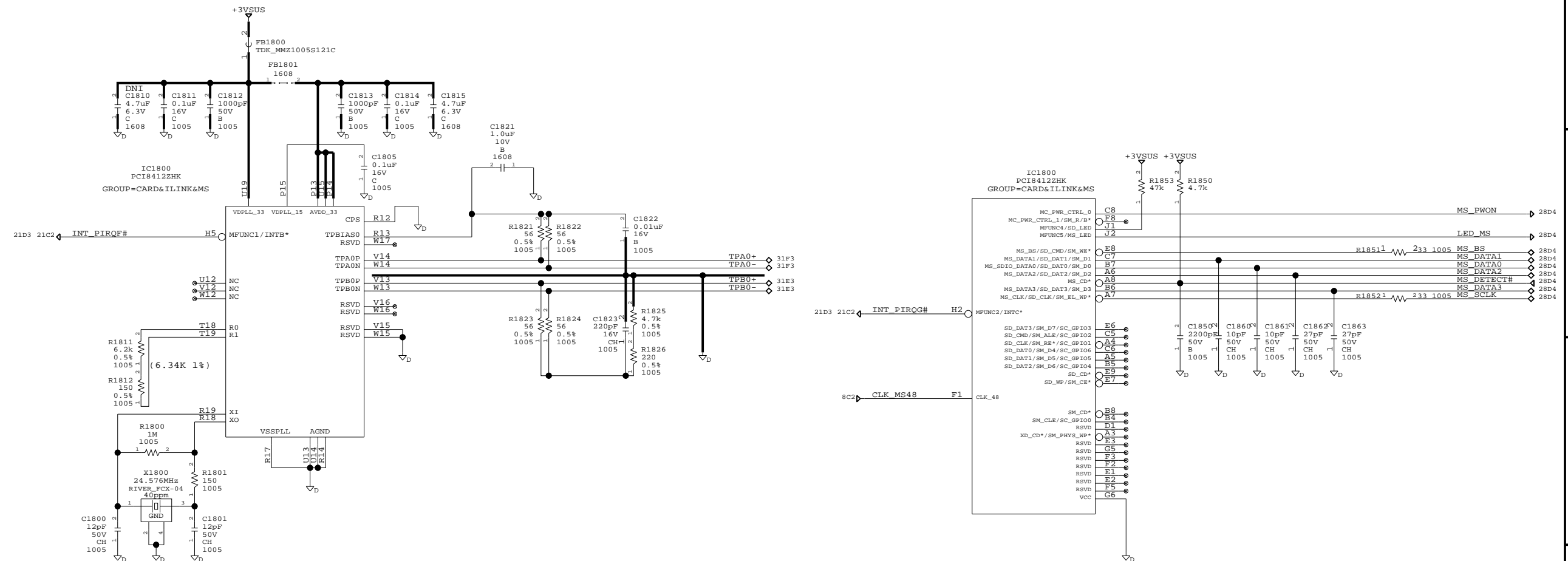
DRAWING:		(REF: 1300-1399)	
MBX-147			
ICH7-M(2/3)			
LAST_MODIFIED=Mon Jan 16 20:46:22 2006			
-	REV.	PART NO.	
-	QTY 1	0.240	-
SIZE	DESIGNER		
C	-		
		SHEET 22 OF 50 (ICH-2)	

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(REF: 1600- 1699)			
DRAWING: MBX-147			
ICH7-M(3/3)			
LAST_MODIFIED=Mon Jan 16 20:46:24 2006			
-	QTY 1	REV. 0.210	PART NO. -
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(REF: 1800-)			
DRAWING: MBX-147			
I.LINK & MS-DUO & PCCARD(1/2)			
LAST_MODIFIED=Mon Jan 16 20:46:25 2006			
-	QTY 1	REV. 1.111	PART NO. -
SIZE C	DESIGNER -	SHEET (COMBO-1 24 OF 50)	

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I.LINK & MS-DUO & PCCARD(2/2)			
LAST_MODIFIED=Mon Jan 16 20:46:26 2006			
-	QTY 1	REV. 0.230	PART NO. -
SIZE C	DESIGNER -	-	SHEET (COMBO-2 25 OF 50)

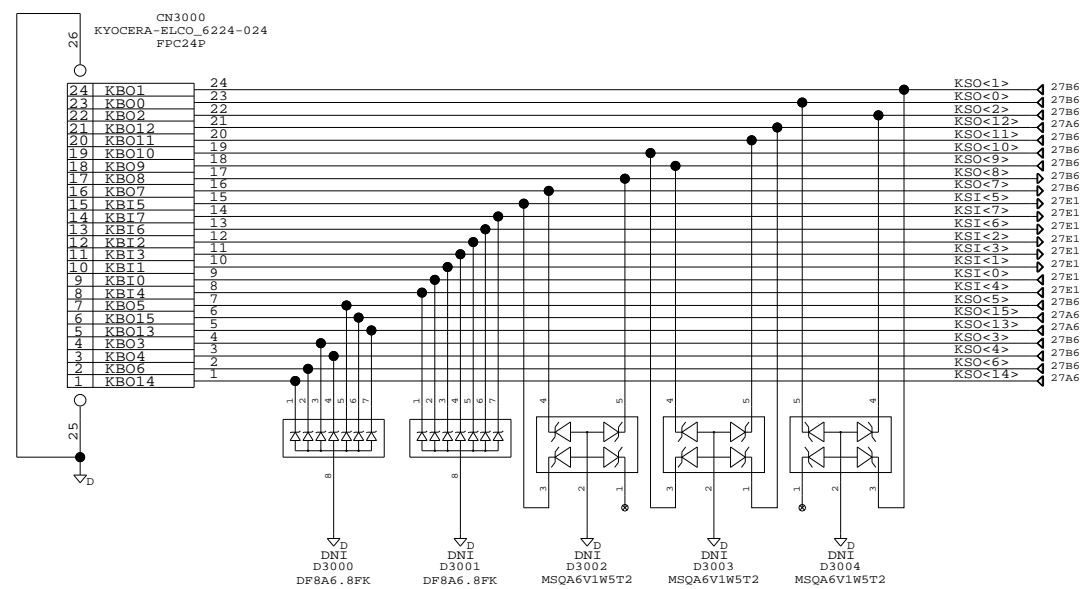
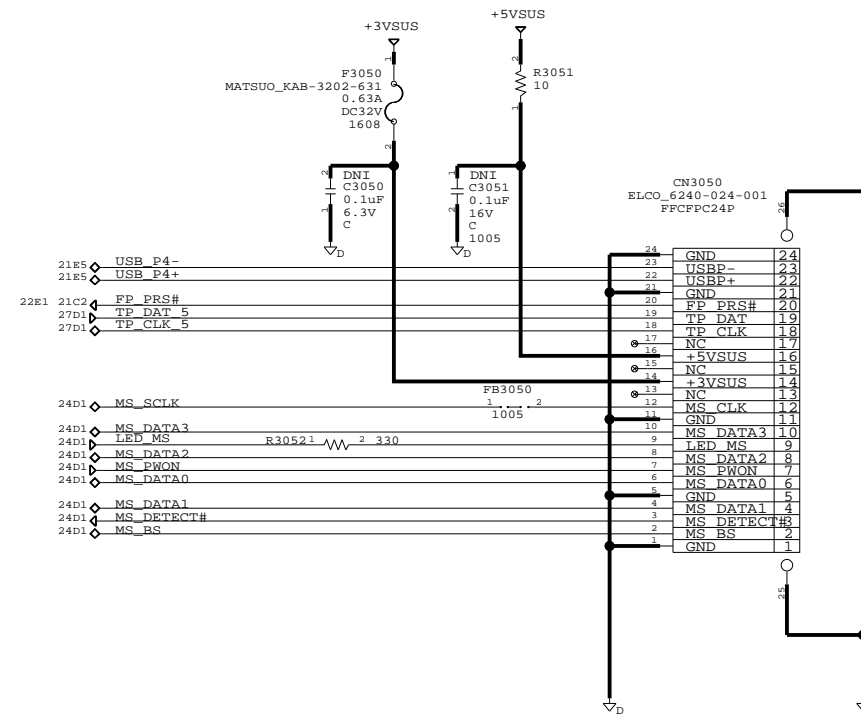
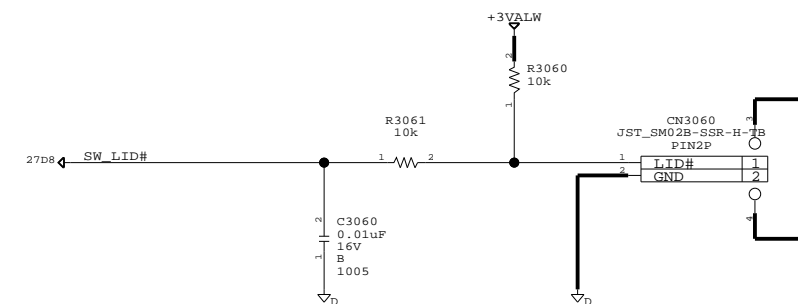
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(KEYBOARD)

(TP, FP, MS-DUO)
(TO SWX-213)(LID)
(TO SWX-214)

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DRAWING:		(REF: 3000- 3099)	
MBX-147		KEYBOARD & TP,ETC & LID	
LAST_MODIFIED=Mon Jan 16 20:46:28 2006		PART NO.	
-	QTY 1	REV. 0.210	-
SIZE C	DESIGNER	(CN-1)	
-		SHEET 28 OF 50	

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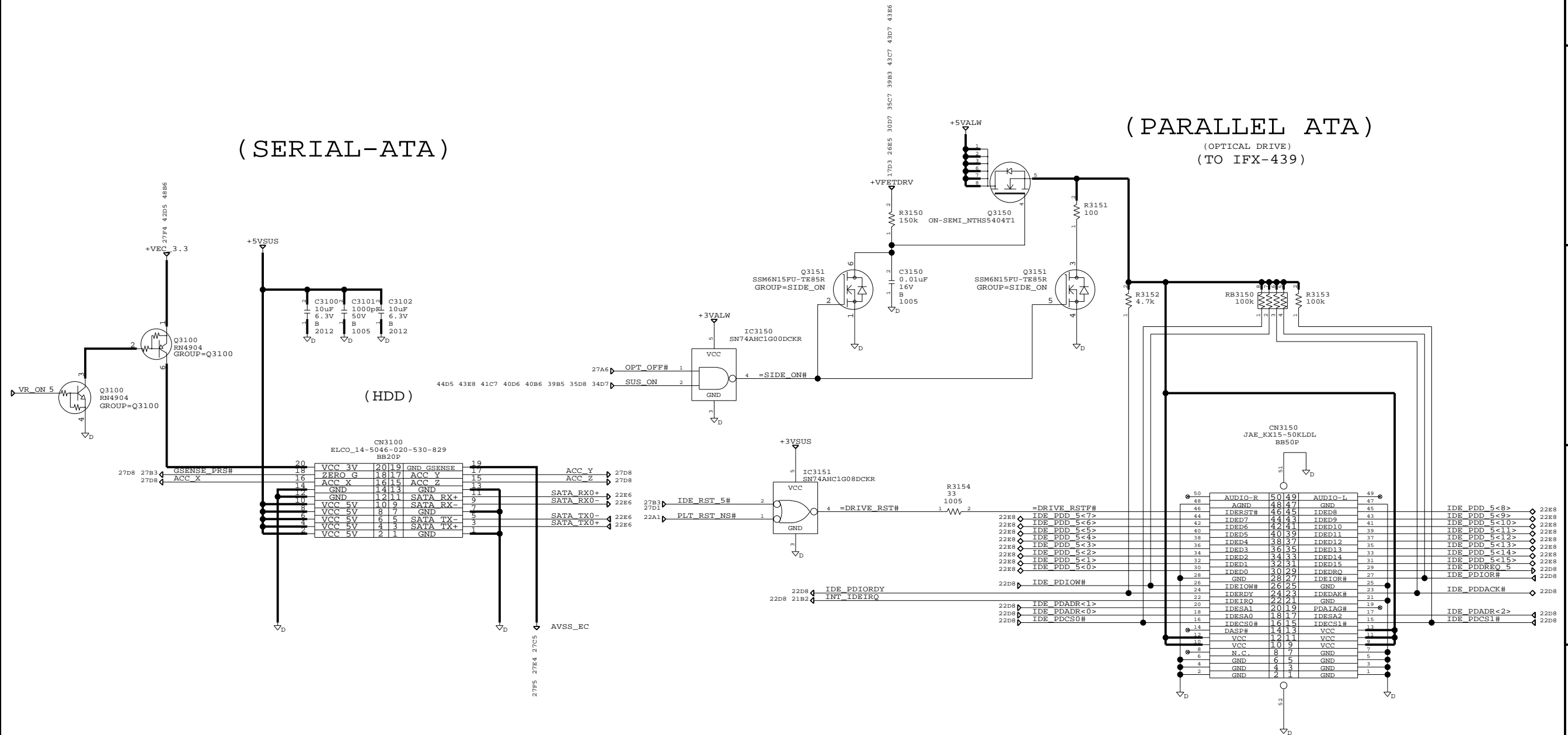
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ZONE	LTR	DESCRIPTION	DATE	APPR.

(SERIAL-ATA)

(PARALLEL ATA)

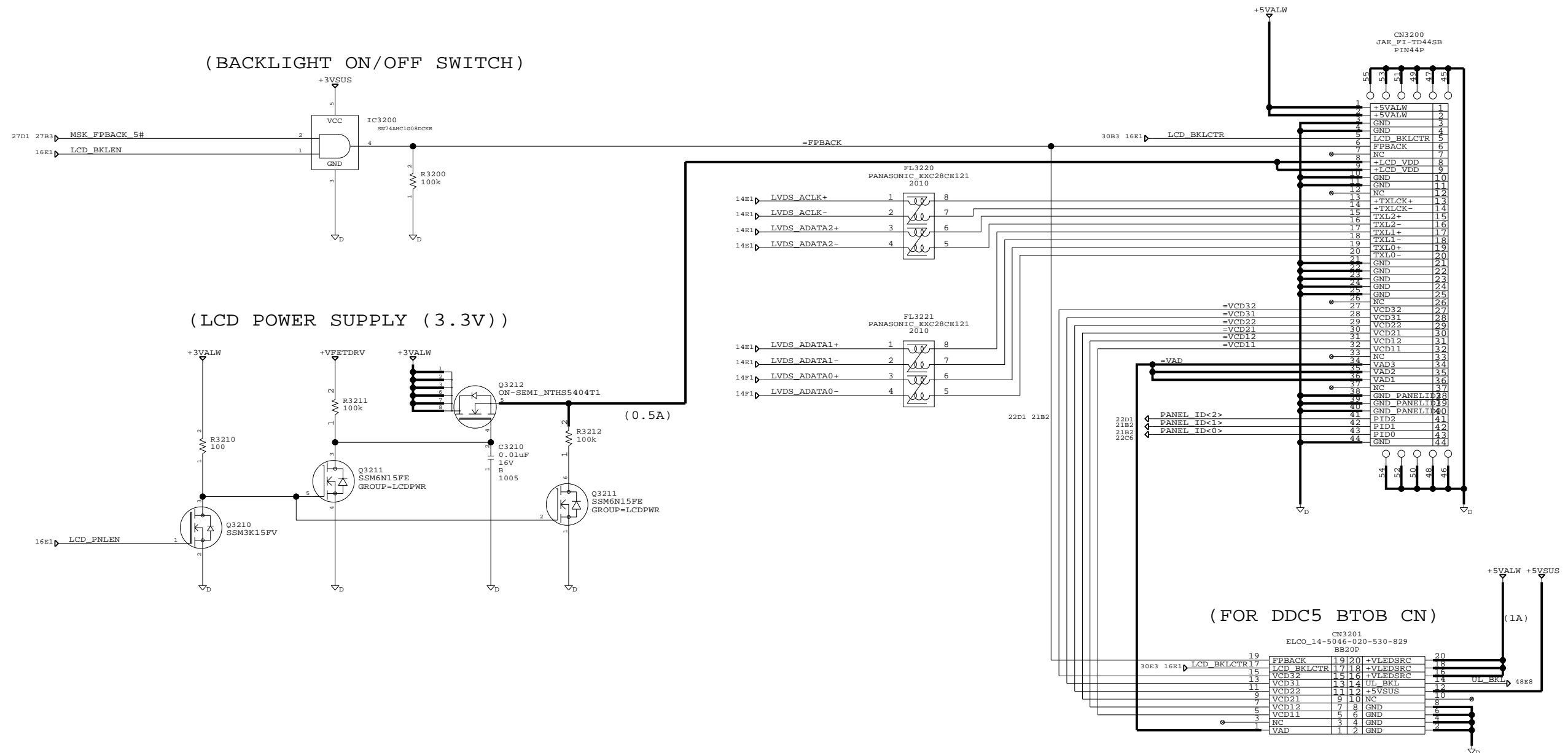
(OPTICAL DRIVE)
(TO IFX-439)

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DRAWING: (REF: 3100- 3199)			
MBX-147			
IDE CN(SATA, AEGIS/PATA)			
LAST_MODIFIED=Mon Jan 16 20:46:28 2006			
QTY 1	REV. 0.210	PART NO. -	
SIZE C	DESIGNER -	(CN-2) SHEET 29 OF 50	

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DRAWING:		(REF. 3200-)	
MBX-147			
LCD CTRL			
LAST_MODIFIED=Mon Jan 16 20:46:29 2006			
-	REV.	PART NO.	
-	0.245	-	
QTY 1			
SIZE	DESIGNER	(CN-3)	
C	-	-	SHEET 30 OF 50

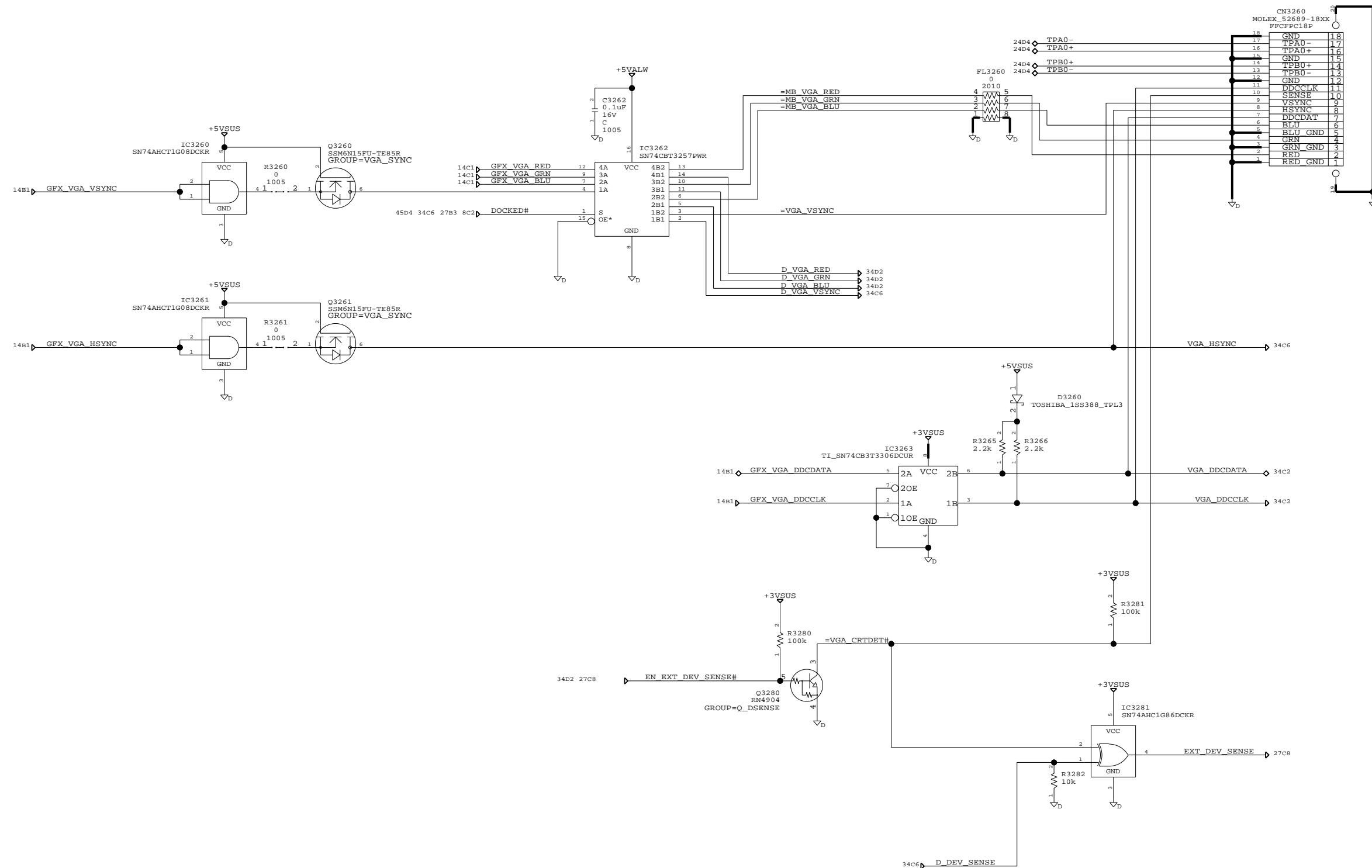
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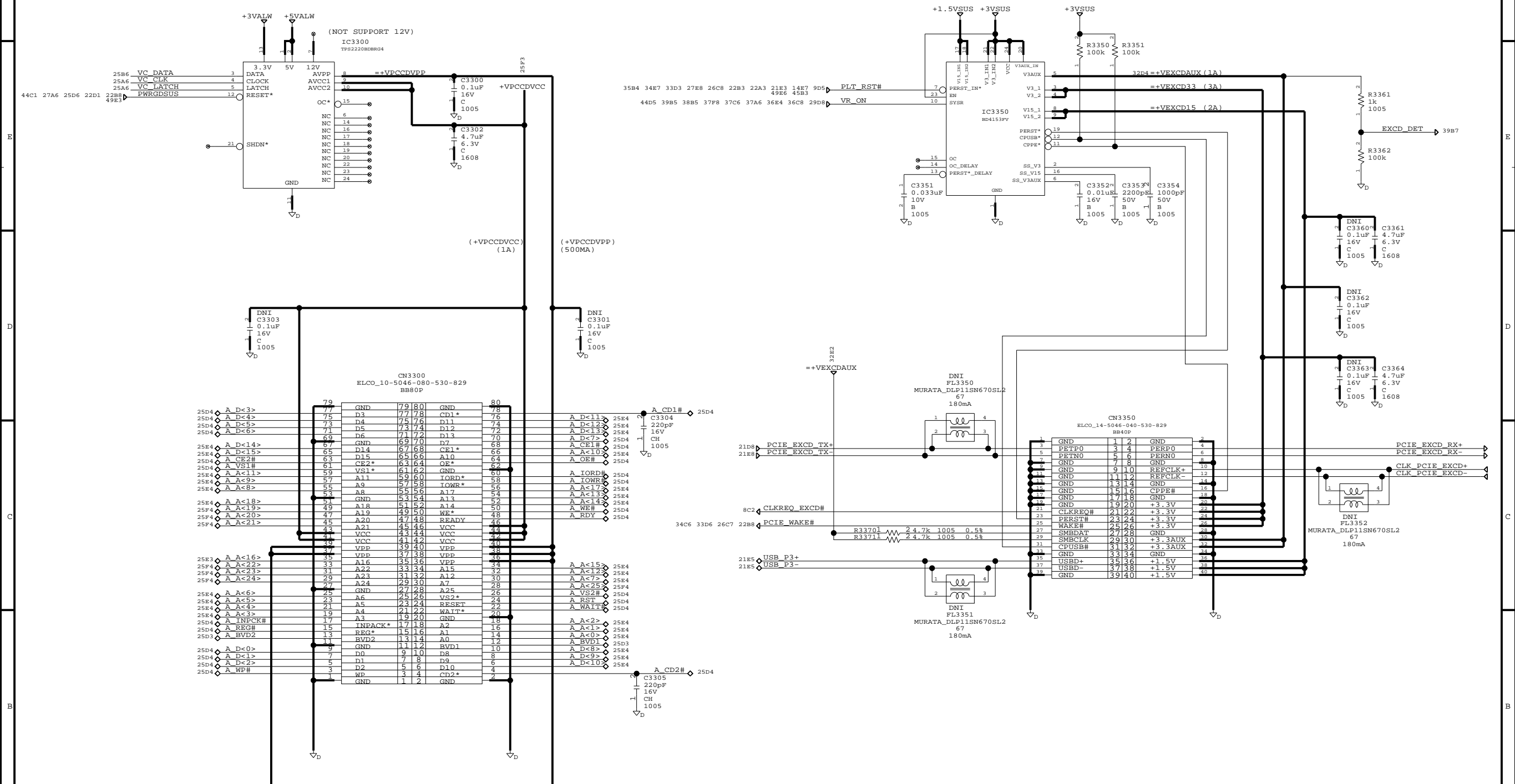
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(VGA, I.LINK)
(TO CNX-348)SONY CORPORATION
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MBX-147		VGA & I.LINK	
LAST MODIFIED=Mon Jan 16 20:46:30 2006			
QTY 1	REV. 0.230	PART NO. -	
SIZE C	DESIGNER -	(CN-4) SHEET 31 OF 50	



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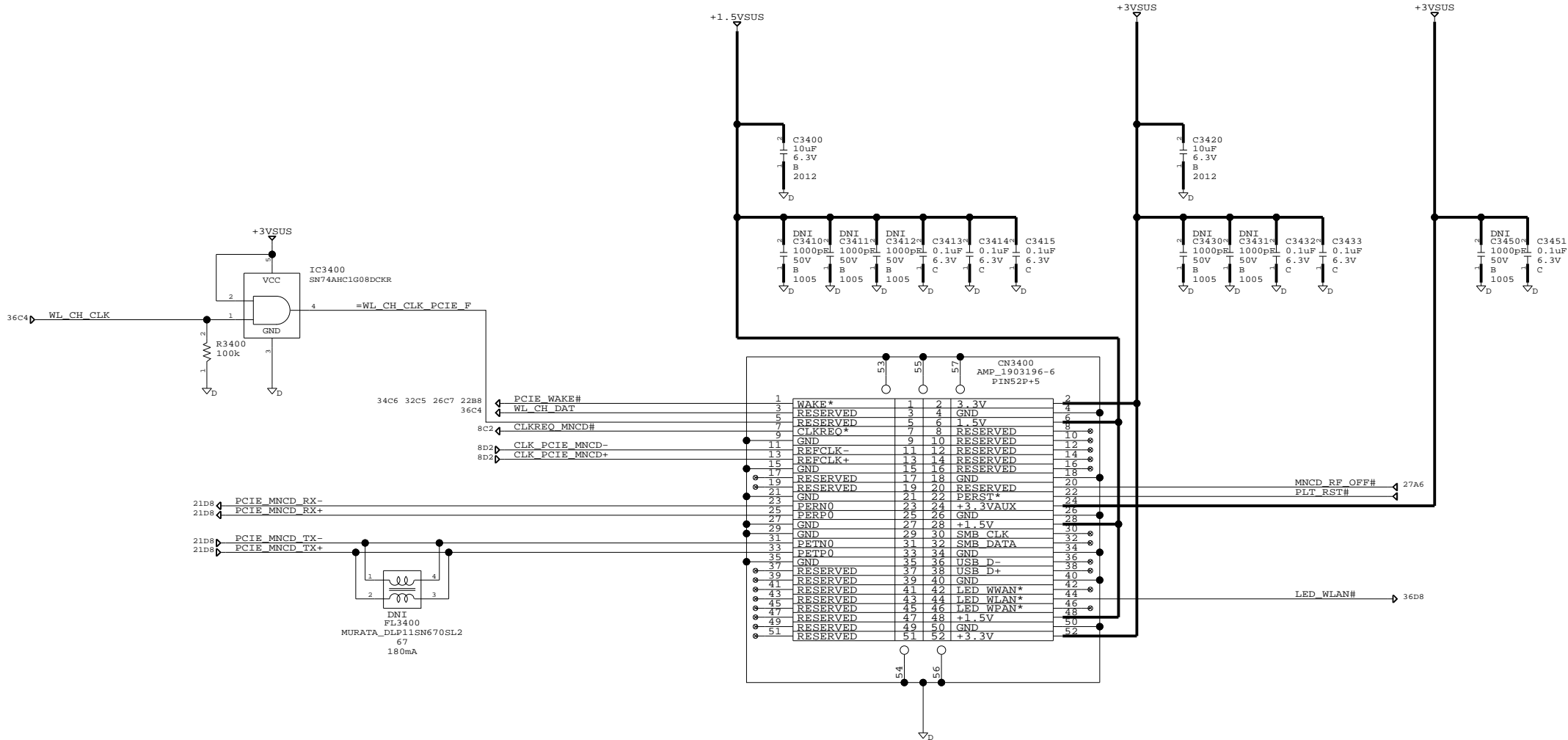
DRAWING:		(REF. 3300-3397)	
MBX-147			
PCCARD & EXPRESSCARD			
LAST_MODIFIED=Mon Jan 16 20:46:31 2006			
-	QTY 1	REV. 1.150	PART NO. -
SIZE C	DESIGNER -	SHEET 32 OF 50 (CN-5)	

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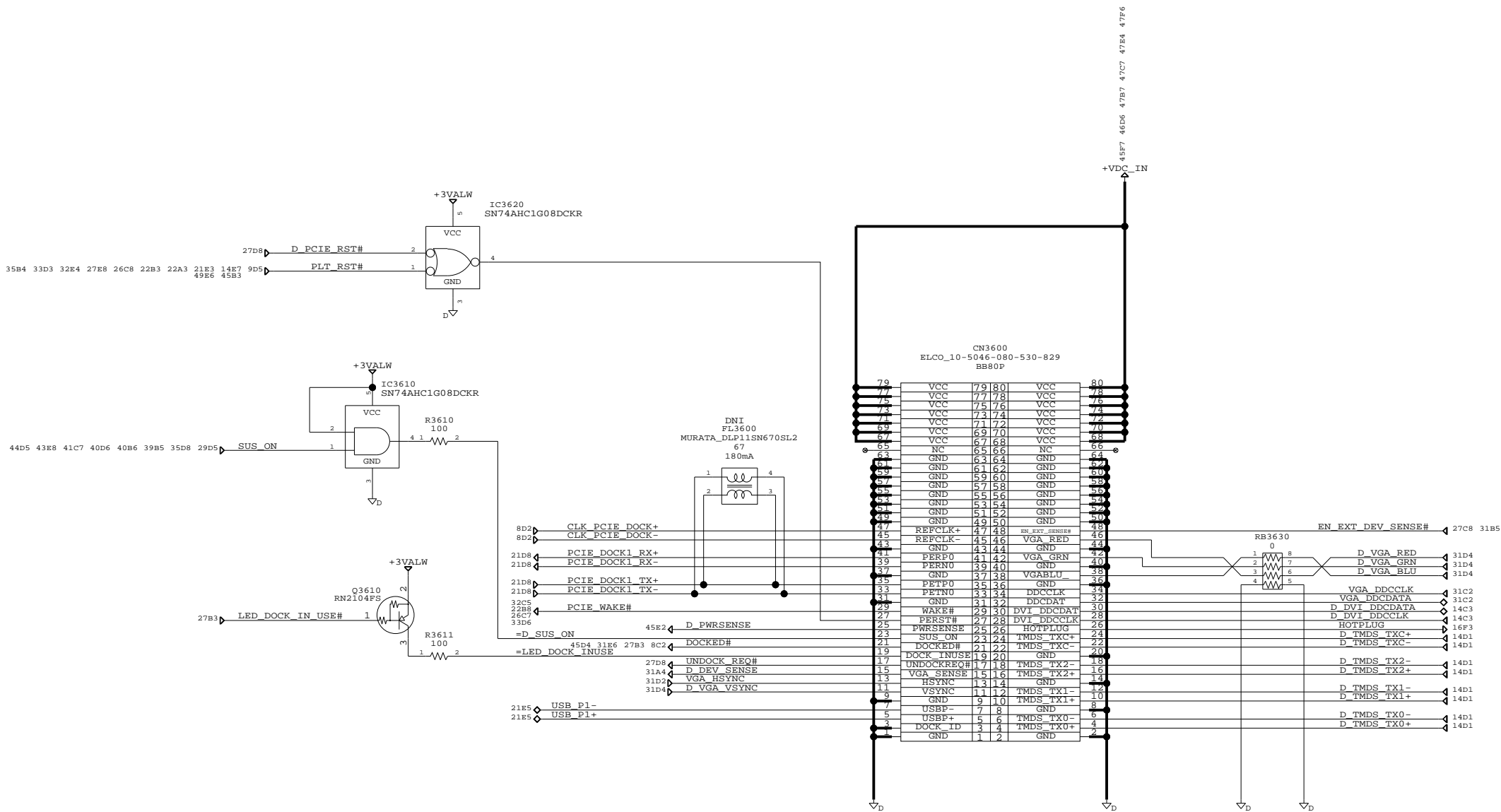
DRAWING: (REF: 3400- 3499) MBX-147 MINI CARD (WIRELESS-LAN)			
LAST_MODIFIED=Mon Jan 16 20:46:31 2006			
-	QTY 1	REV. 1.120	PART NO. -
SIZE C	DESIGNER -	(CN-6) SHEET 33 OF 50	

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DRAWING:		(REF: 3600- 3699)	
MBX-147		DOCK CN	
LAST_MODIFIED=Mon Jan 16 20:46:32 2006			
SIZE	DESIGNER	REV.	PART NO.
C	-	0.241	-
SHEET 34 OF 50		(CN-7)	

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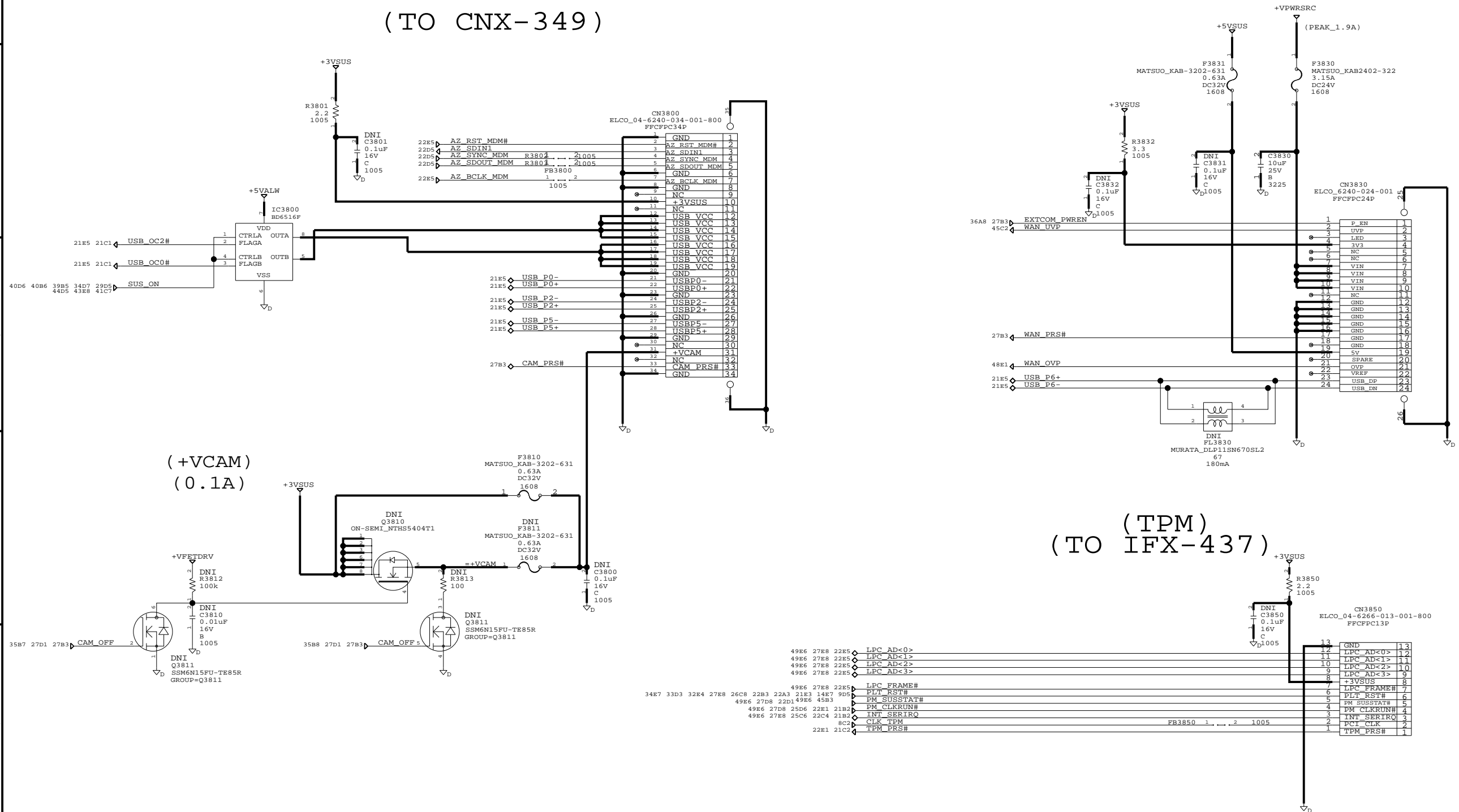
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(WAN, FELICA)

(USB, CAMERA, MODEM)
(TO CNX-349)



(TPM)
(TO IFX-437)

(REF: 3800- 3899)

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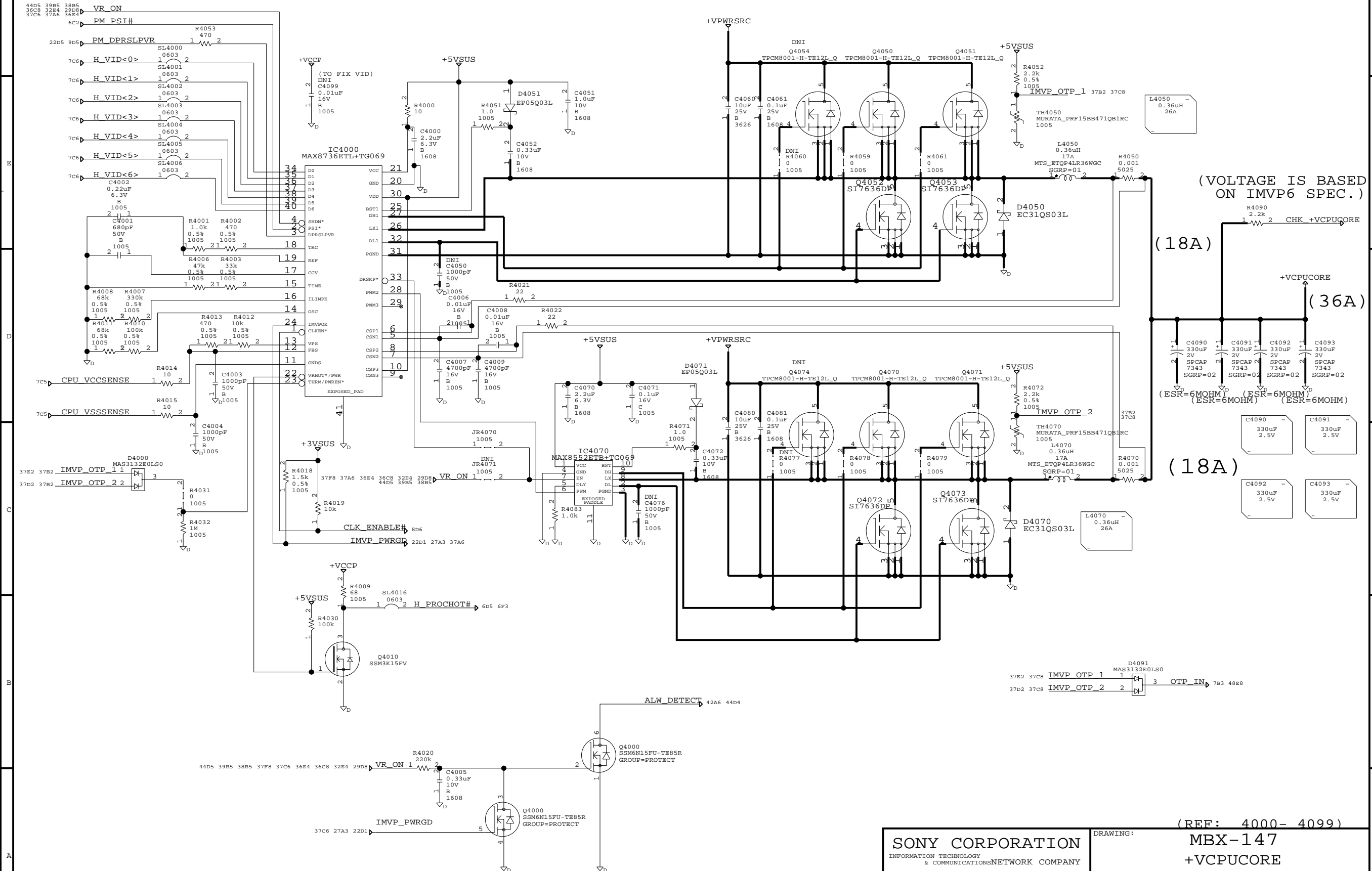
DRAWING: MBX-147			
USB, CAMERA, MODEM & WAN & TPM			
LAST_MODIFIED=Mon Jan 16 20:46:33 2006			
SIZE	QTY	REV.	PART NO.
C	1	1.100	-
DESIGNER	-		(CN-8)
SHEET 35 OF 50			

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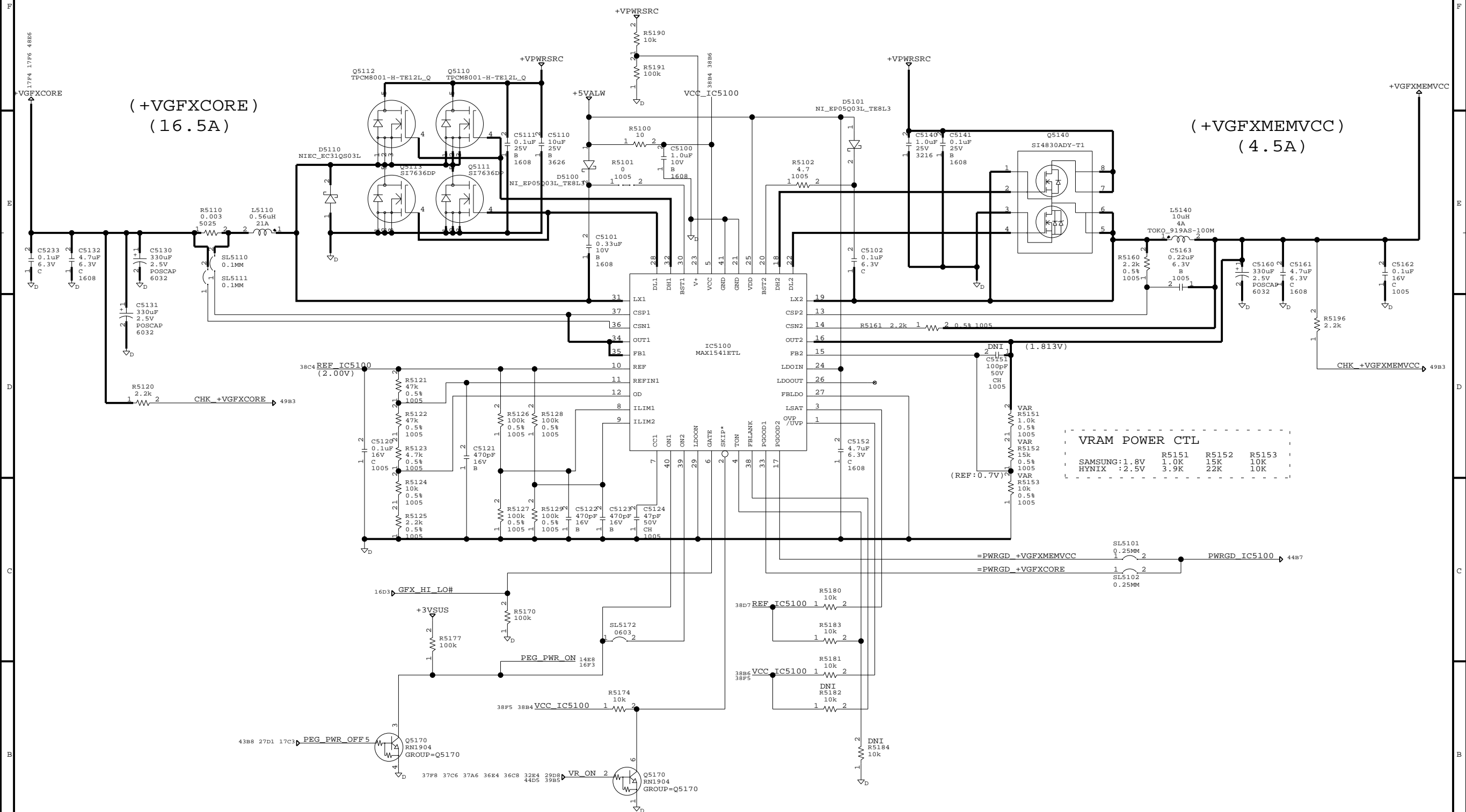
DRAWING:		MBX-147	
		+VCPUCORE	
		LAST_MODIFIED=Mon Jan 16 20:46:35 2006	
SIZE	QTY 1	REV. 1.100	PART NO.
C			
DESIGNER		(POWER-1)	
		SHEET 37 OF 50	

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ZONE	LTR	DESCRIPTION	DATE	APPR.

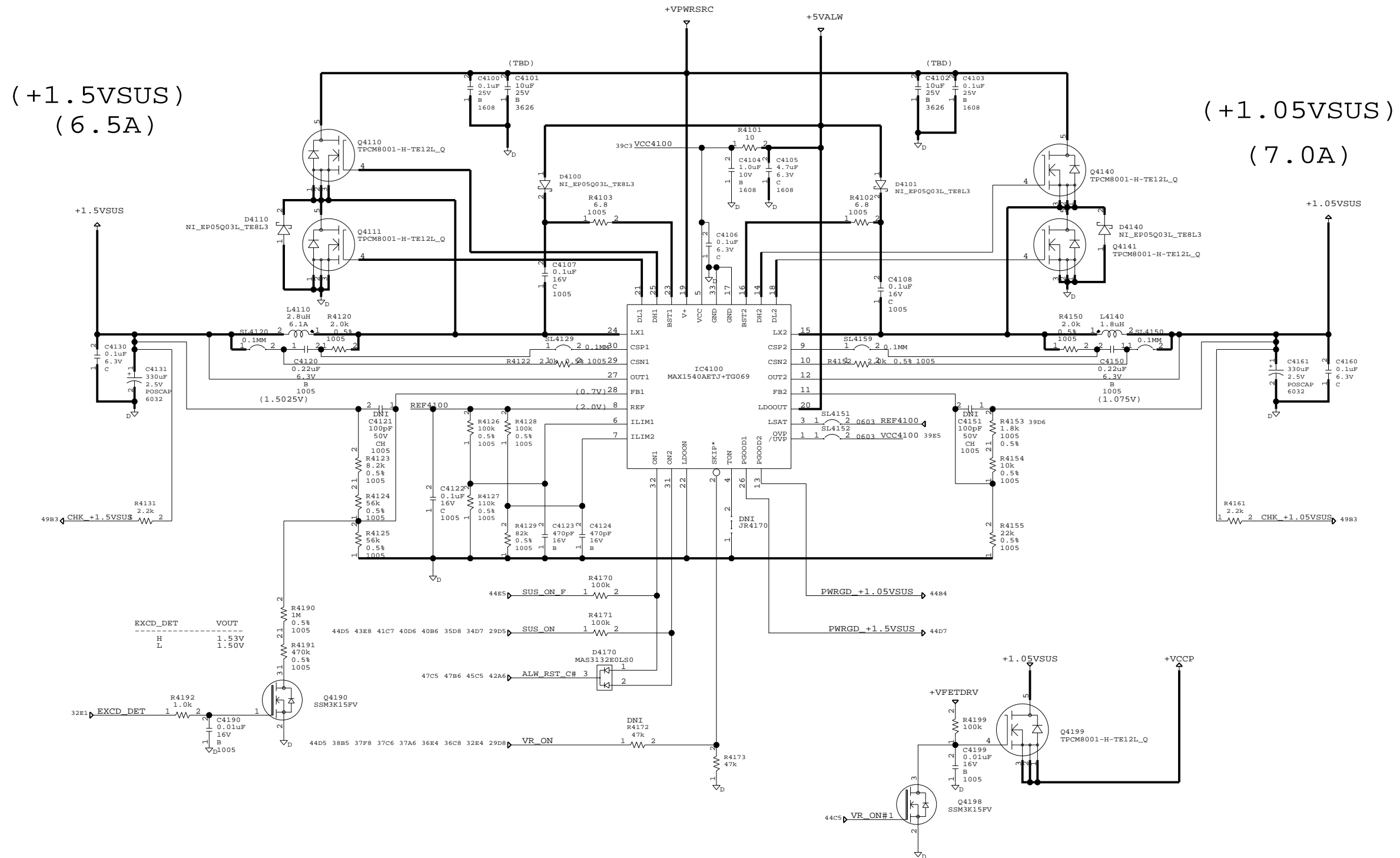


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DRAWING:		(REF: 5100- 5199)	
MBX-147		+VGFXCORE / +VGFXMEM	
LAST_MODIFIED=Mon Jan 16 20:46:36 2006			
QTY	1	REV.	1.100
PART NO.	-	DESIGNER	-
SIZE	C	DESIGNER	-
		(POWER-2)	
		SHEET 38 OF 50	

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DRAWING:			
<div style="text-align: center;"> (REF: 4100- 4199) MBX-147 +1.5VSUS / +1.05VSUS </div>			
LAST_MODIFIED=Mon Jan 16 20:46:37 2006			
-	REV.	PART NO.	
-	QTY 1	1.120	-
SIZE	DESIGNER	(POWER-3)	
C	-	-	SHEET 39 OF 50

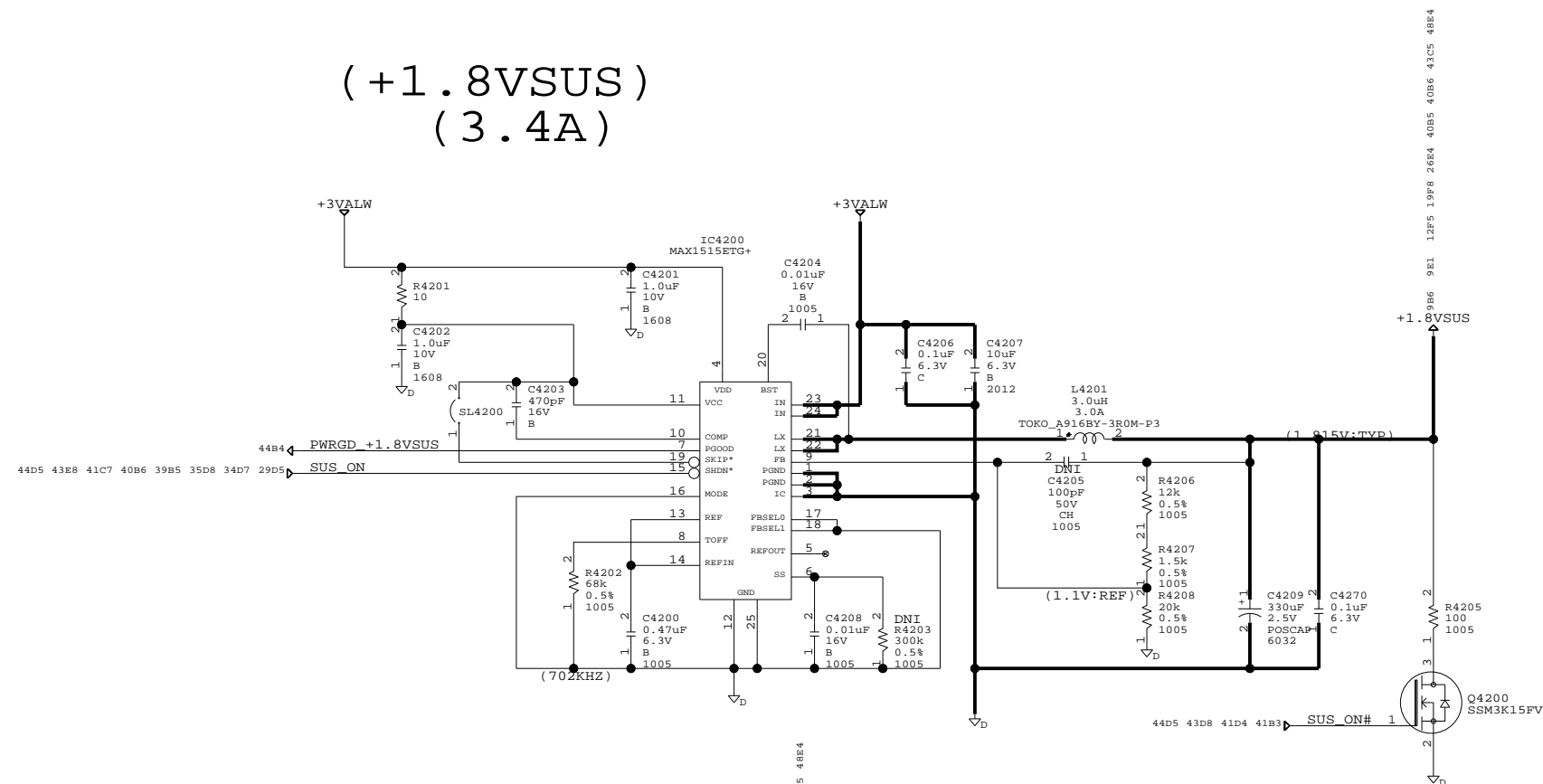
SONY PROPRIETARY INFORMATION

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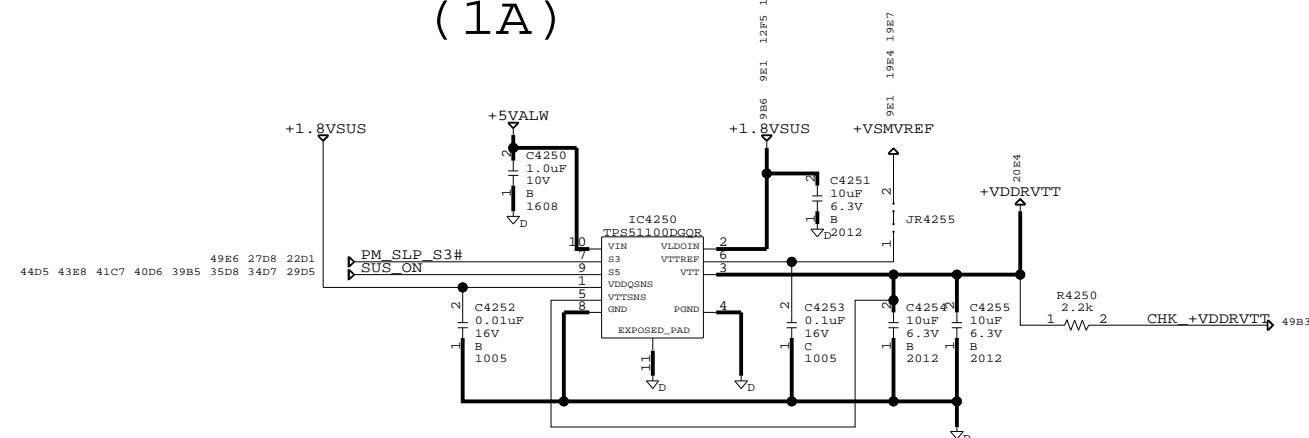
REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

(+1.8VSUS)
(3.4A)



(+VDDRVTT)
(1A)



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DRAWING: (REF: 4200- 4299) MBX-147 +1.8VSUS / +VDDRVTT			
LAST_MODIFIED=Mon Jan 16 20:46:38 2006			
SIZE	DESIGNER	REV.	PART NO.
C	-	0.230	-
SHEET 40 OF 50			(POWER-4)

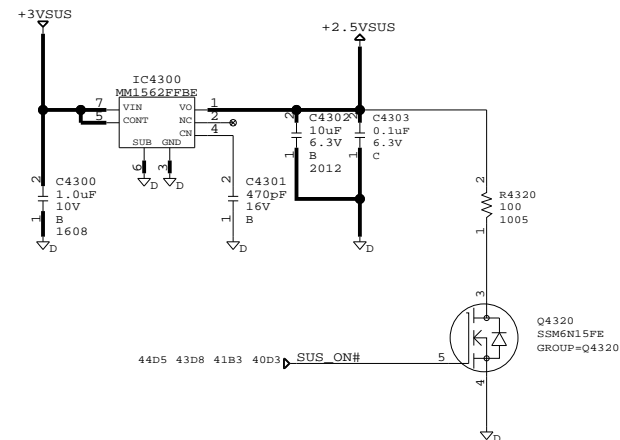
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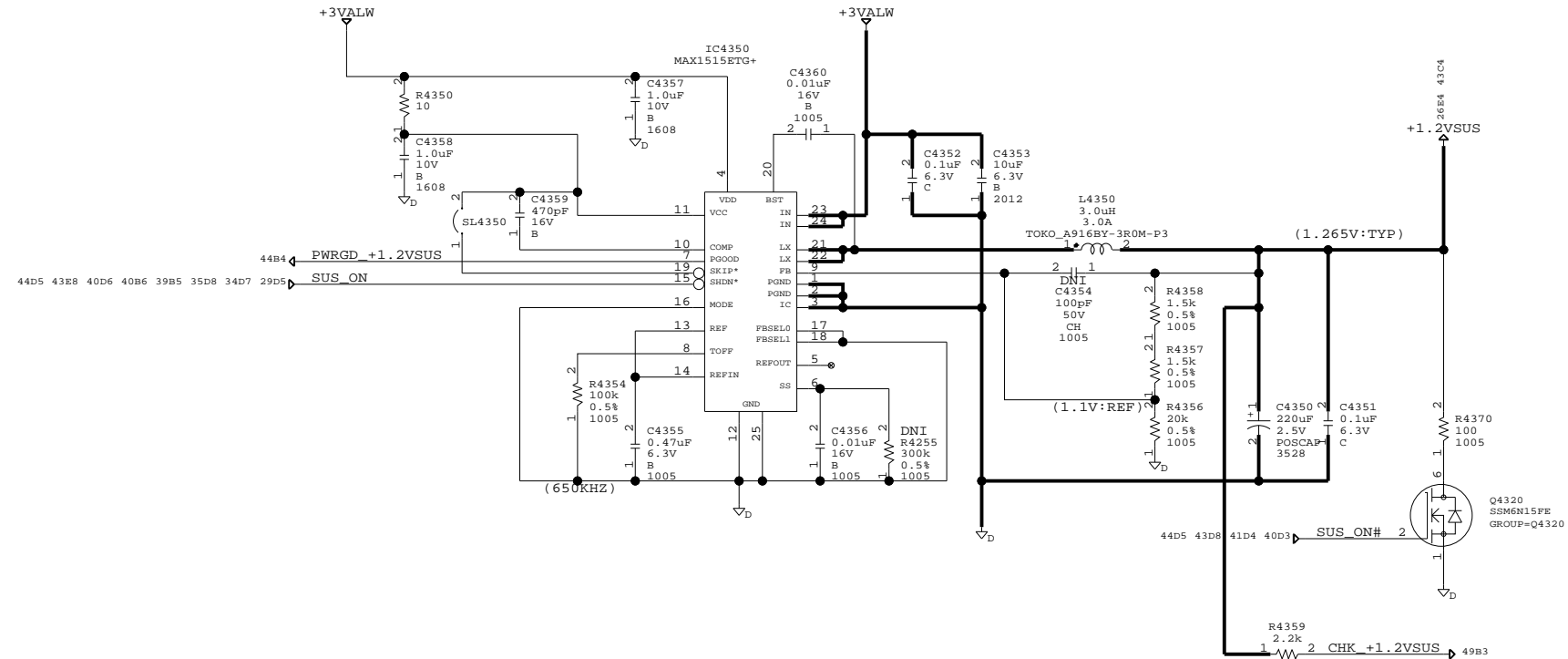
REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

(+2.5VSUS)
(0.4A)



(+1.2VSUS)
(1.8A)



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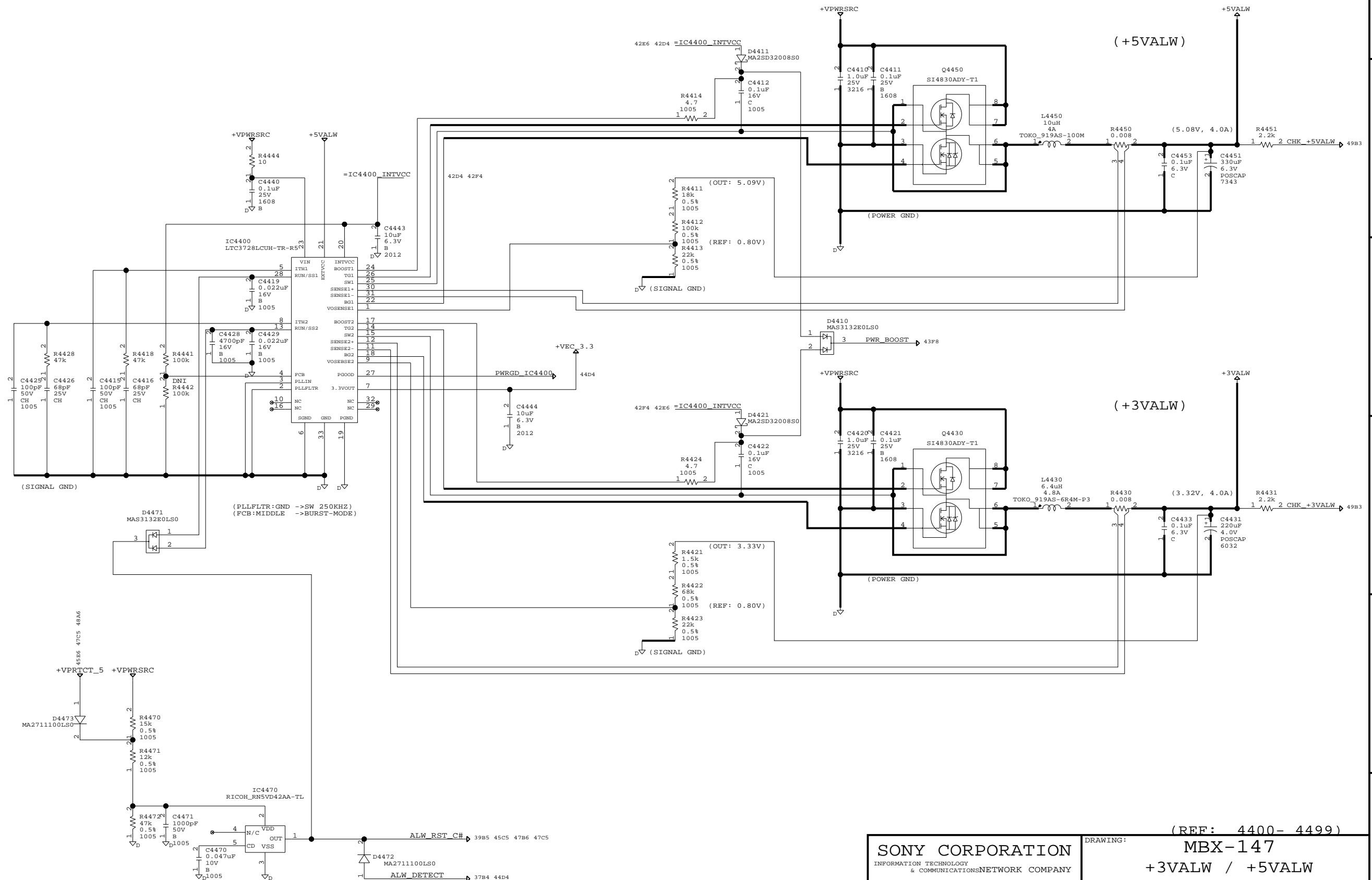
DRAWING:		(REF: 4300- 4399)		
MBX-147		+1.2VSUS/+2.5VSUS		
LAST_MODIFIED=Mon Jan 16 20:46:38 2006				
REV.	QTY	1	0.243	PART NO.
SIZE	C	DESIGNER		(POWER-5)
				SHEET 41 OF 50

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DRAWING:		(REF: 4400- 4499)		
		MBX-147		
		+3VALW / +5VALW		
		LAST_MODIFIED=Mon Jan 16 20:46:39 2006		
SIZE	C	DESIGNER	-	(POWER-6) SHEET 42 OF 50
QTY	1	REV.	1.154	PART NO.

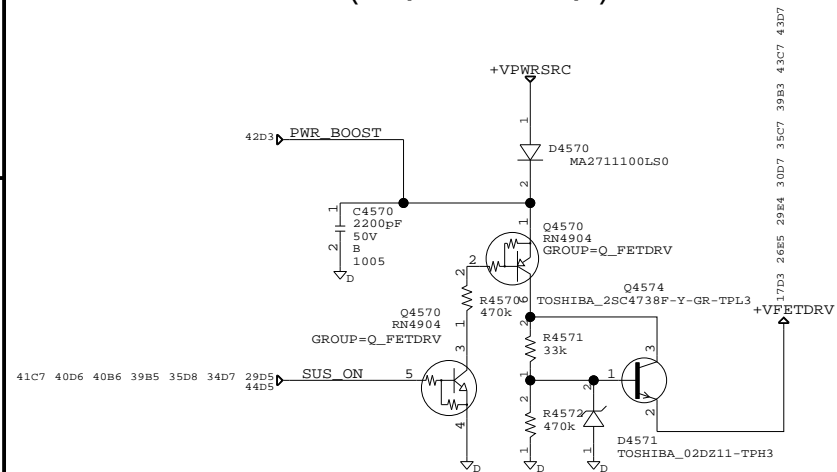
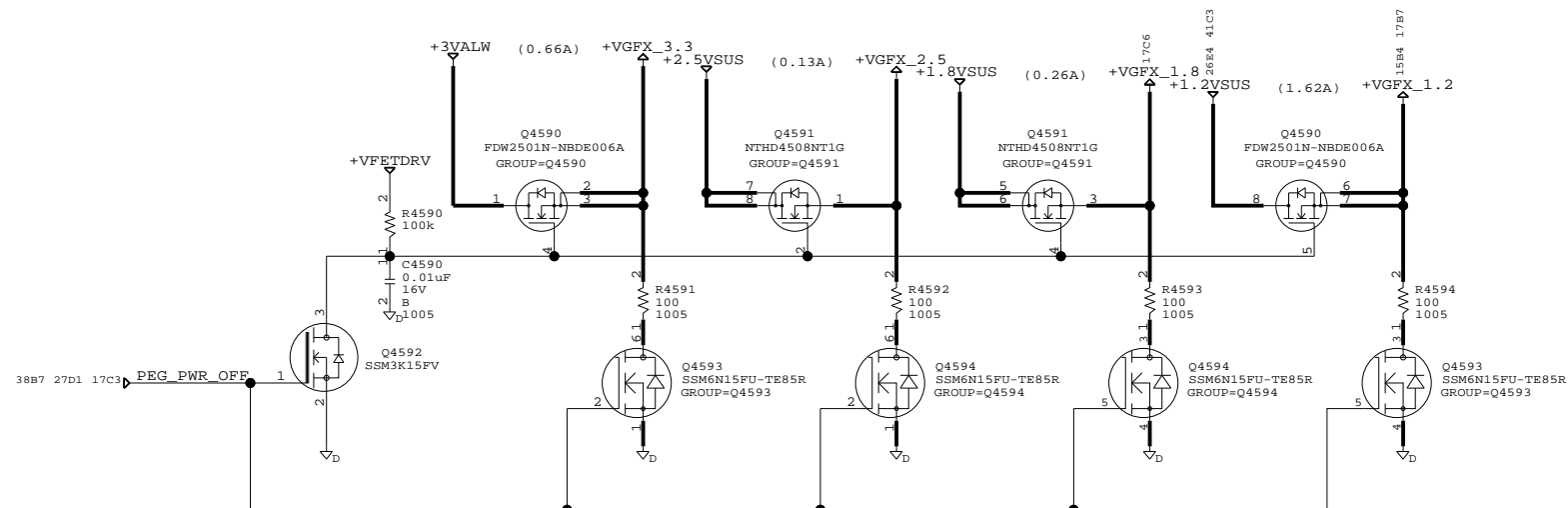
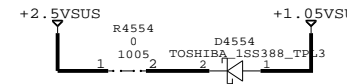
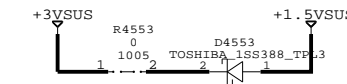
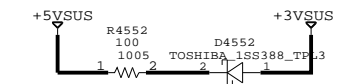
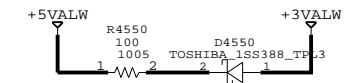
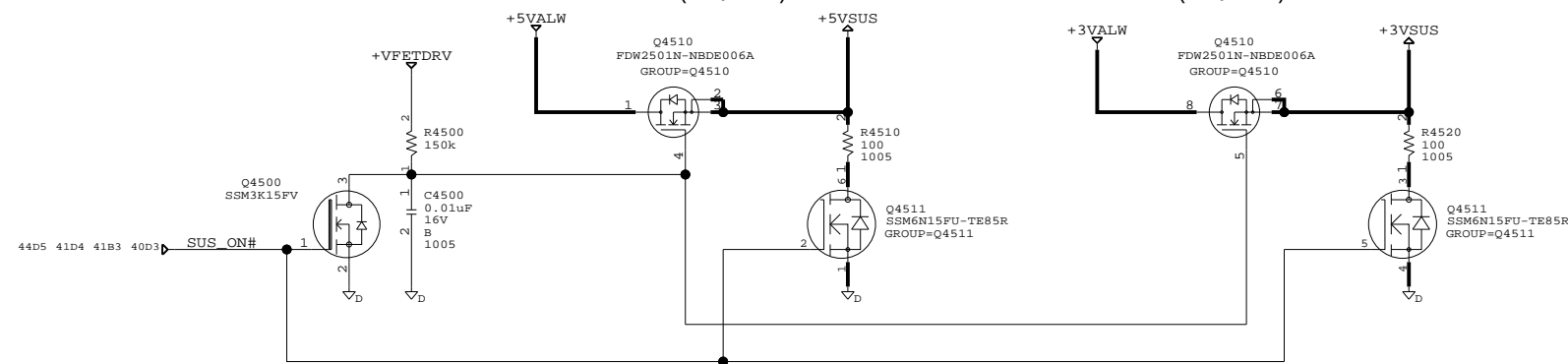
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(+VFETDRV)

(+5VSUS)
(1.1A)(+3VSUS)
(1.1A)

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DRAWING: (REF: 4500- 4599) MBX-147 LOAD SW			
LAST_MODIFIED=Mon Jan 16 20:46:40 2006			
-	QTY 1	REV. 1.154	PART NO. -
SIZE C	DESIGNER -	(POWER-7) SHEET 43 OF 50	

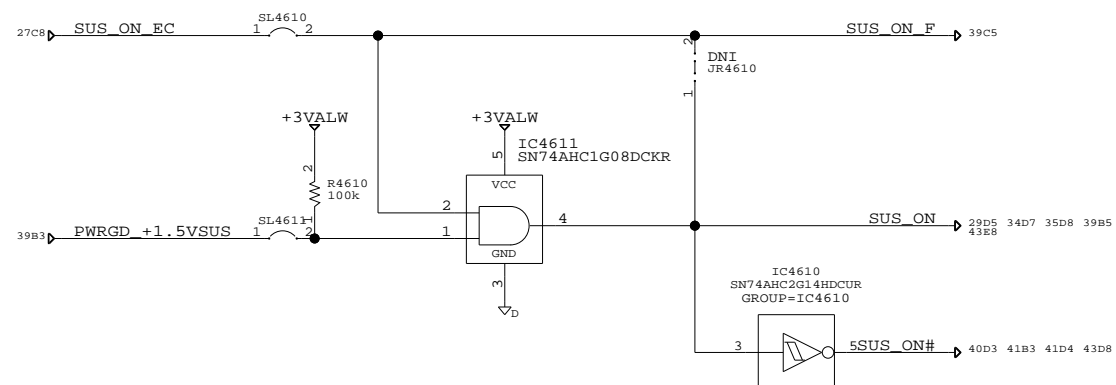
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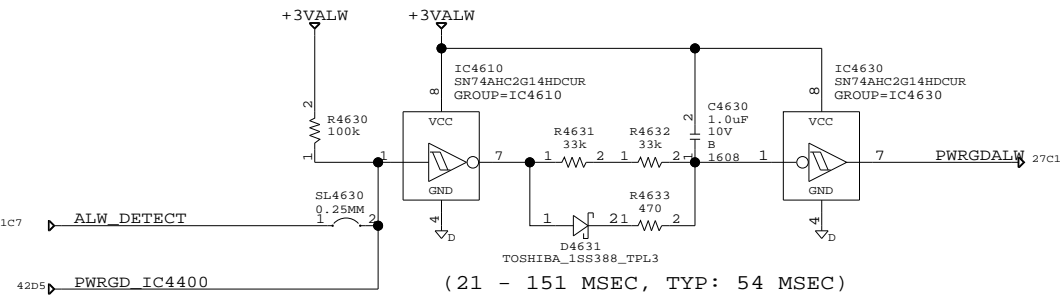
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ZONE	LTR	DESCRIPTION	DATE	APPR.

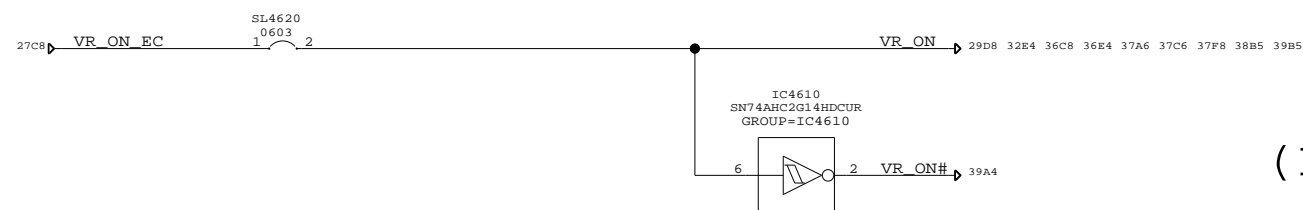
(SUS_ON)



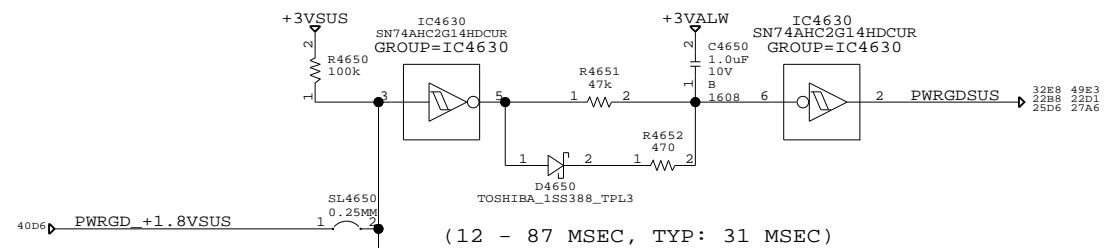
(PWRGDALW)



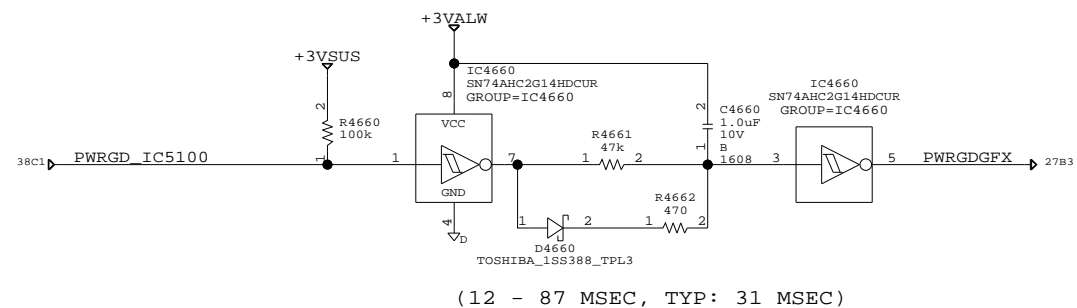
(VR_ON)



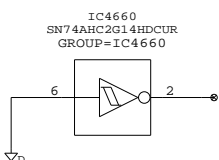
(PWRGDSUS)



(PWRGDGFX)



(12 - 87 MSEC, TYP: 31 MSEC)



(REF: 4600- 4699)

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MBX-147
TIMING SEQUENCER

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SIZE	DESIGNER	REV.	QTY	PART NO.
C	-	1.120	1	-

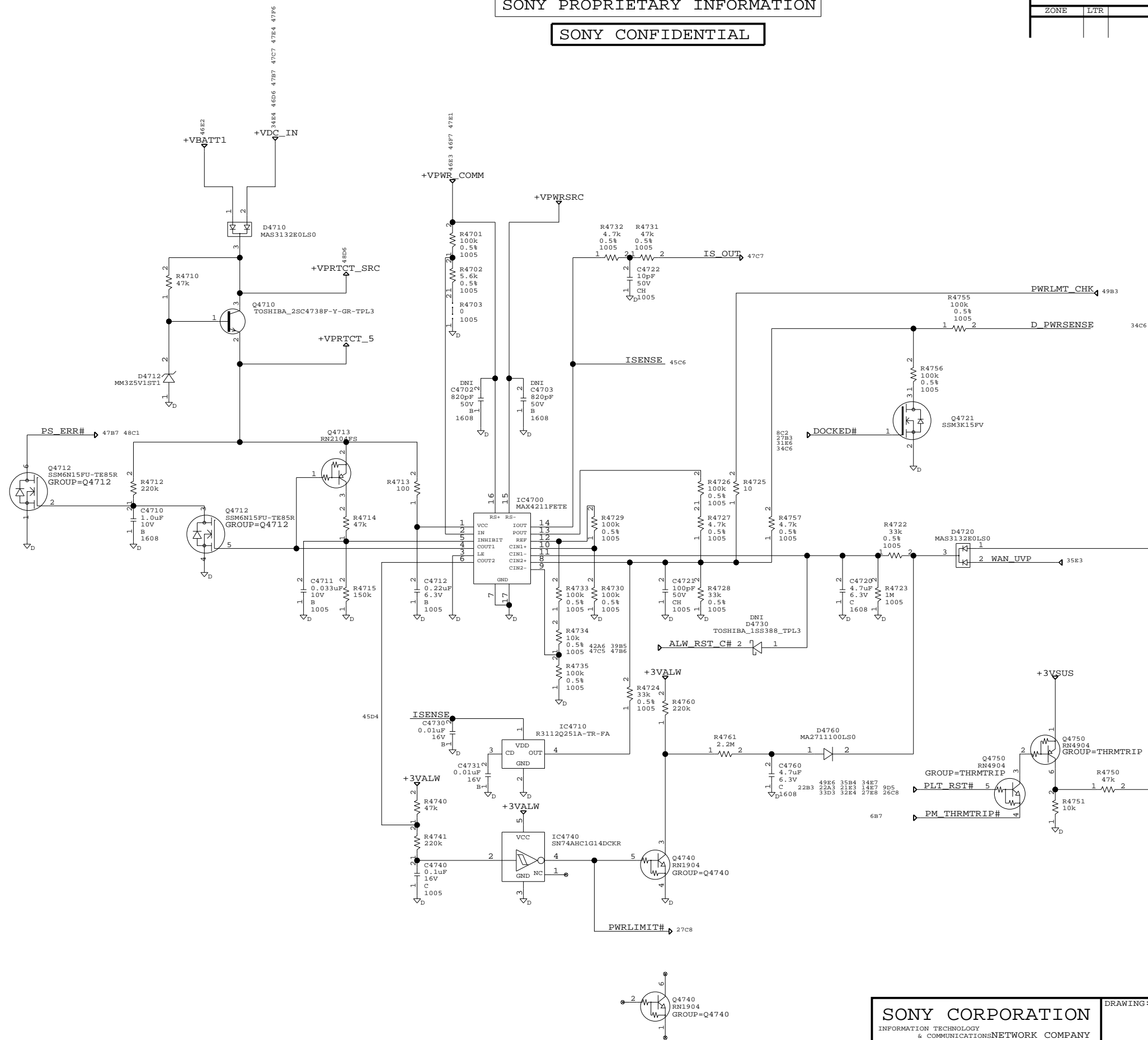
(POWER-8)
SHEET 44 OF 50

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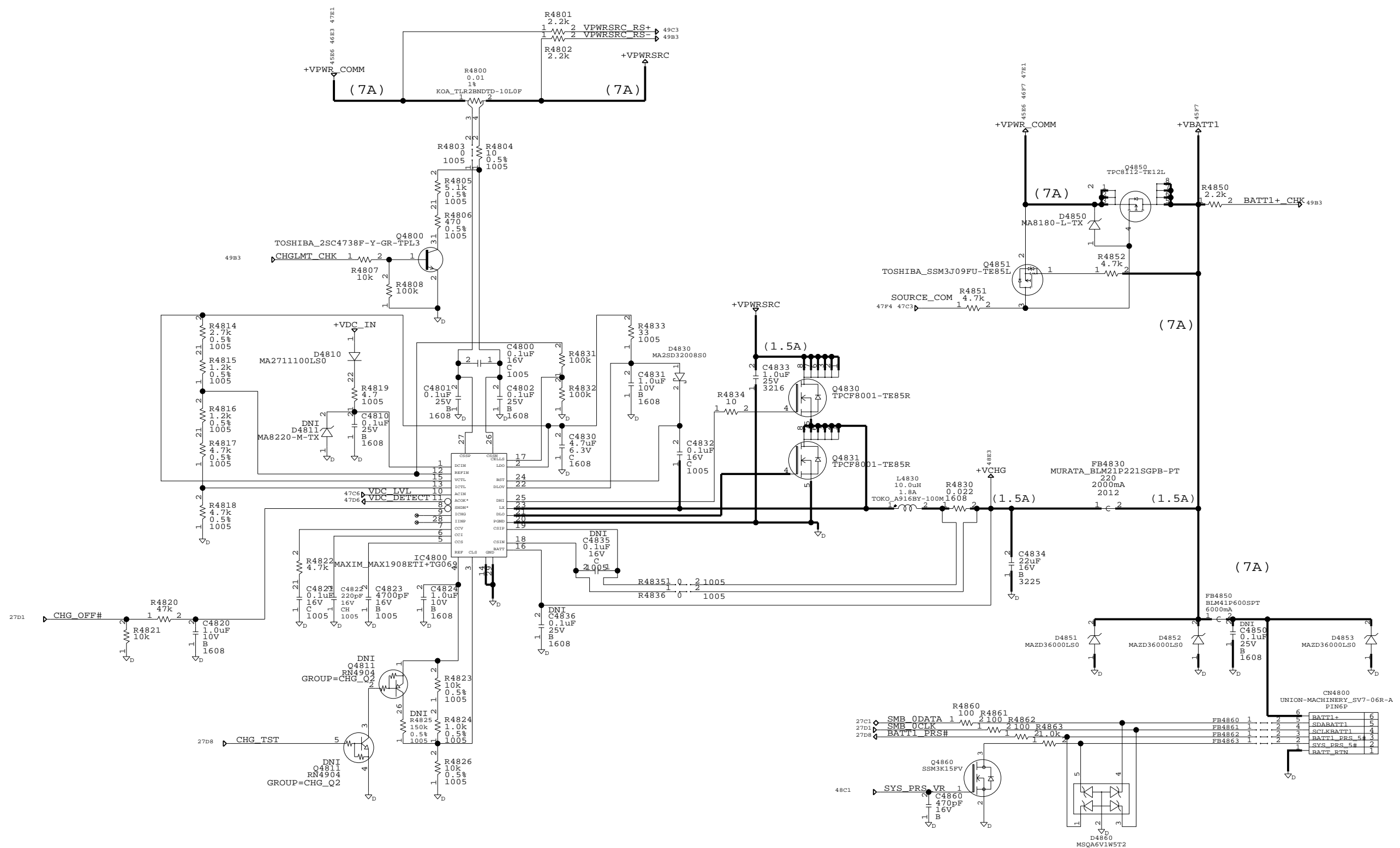
ZONE	LTR	DESCRIPTION	DATE	APPR.



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		MBX-147	
		POWER LIMITER	
		LAST_MODIFIED=Mon Jan 16 20:46:41 2006	
		REV.	PART NO.
		1.130	-
		QTY 1	-
SIZE	DESIGNER	(POWER-9)	
C	-	SHEET 45 OF 50	



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LAST_MODIFIED=Mon Jan 16 20:46:43 2006

	-	QTY 1	REV. 1.154	PART NO. -
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SIZE	C	DESIGNER	-	-	SI
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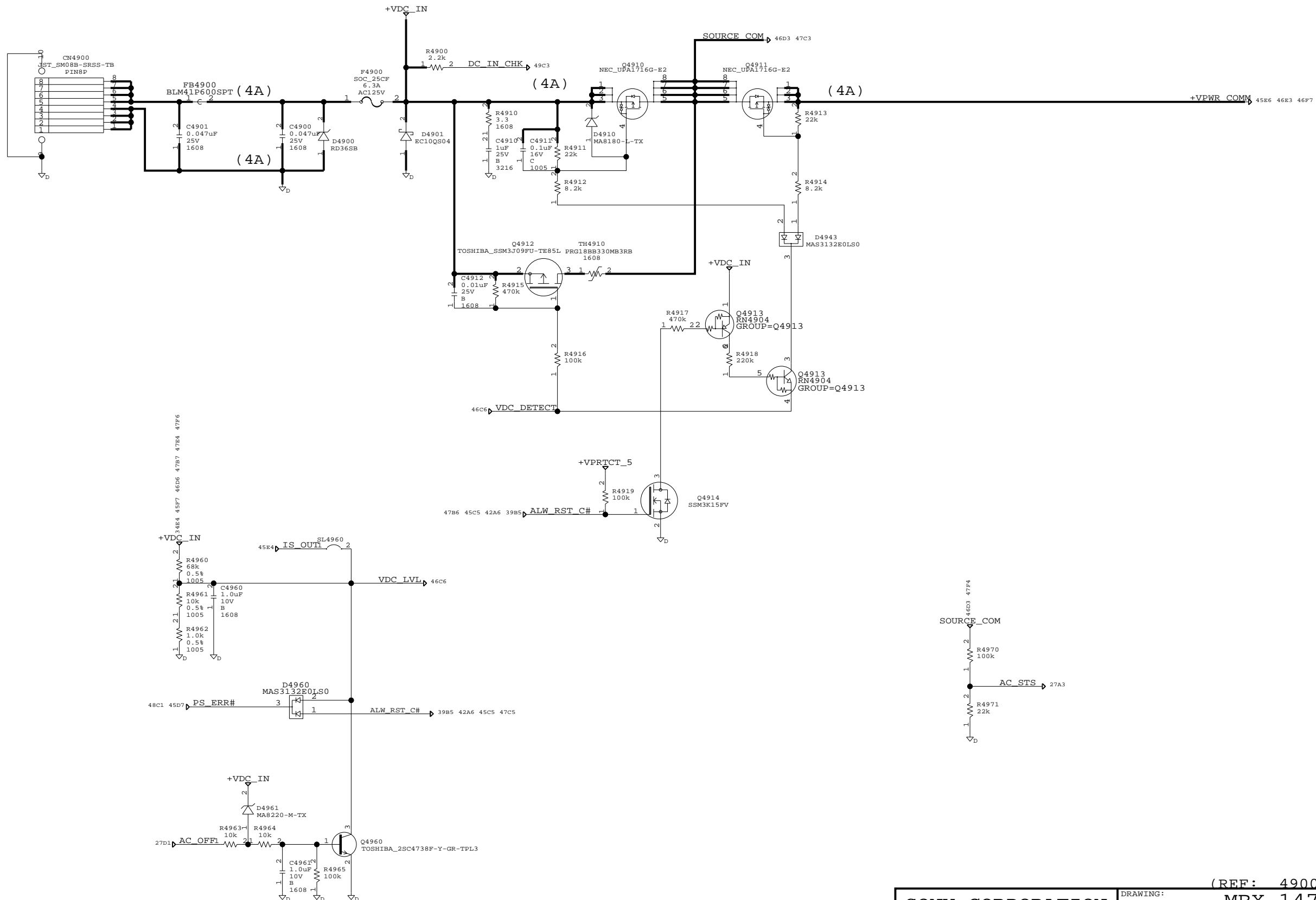
(POWER-10)
SHEET 46 OF 50

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REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPR.

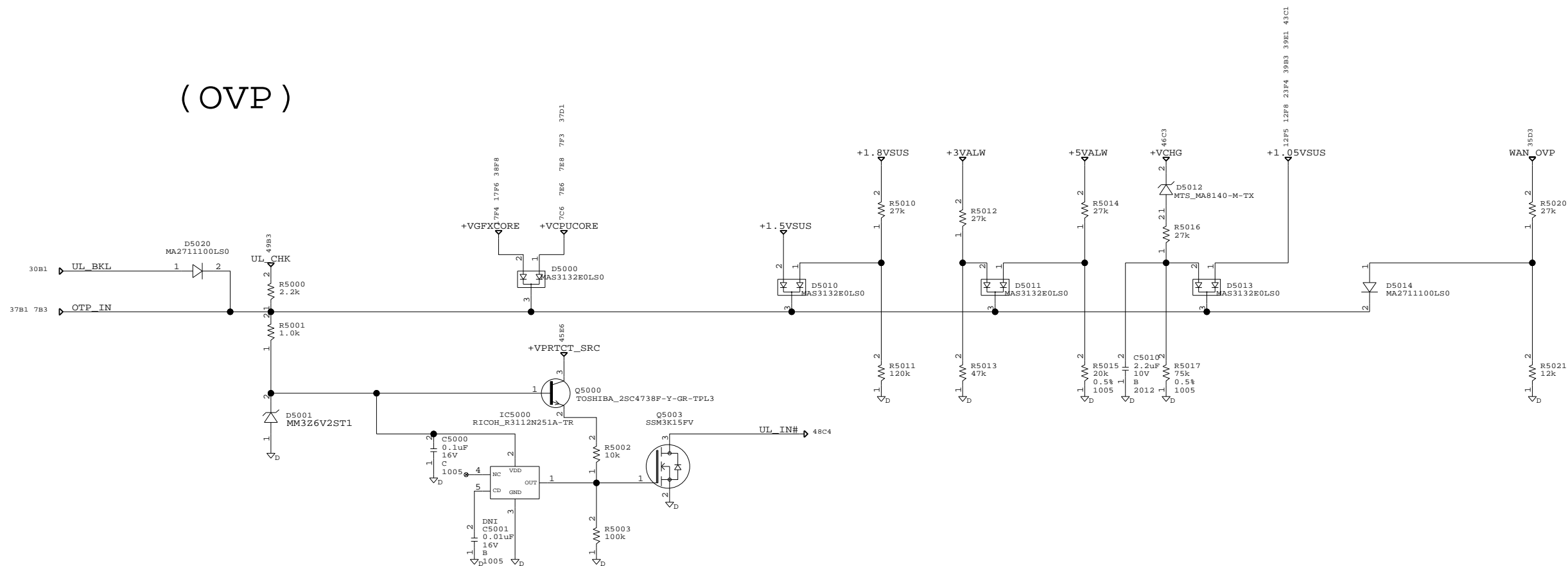


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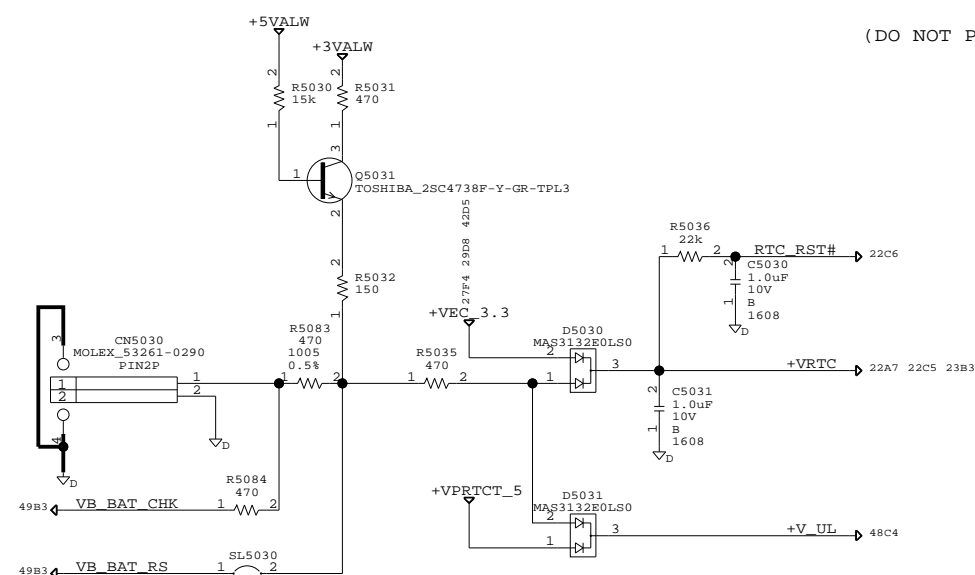
DRAWING:		(REF: 4900- 4999)	
MBX-147		DC_IN	
LAST_MODIFIED=Mon Jan 16 20:46:43 2006			
SIZE	DESIGNER	REV.	PART NO.
C	-	1.152	-
		QTY 1	
		(POWER-11) SHEET 47 OF 50	

(OVP)

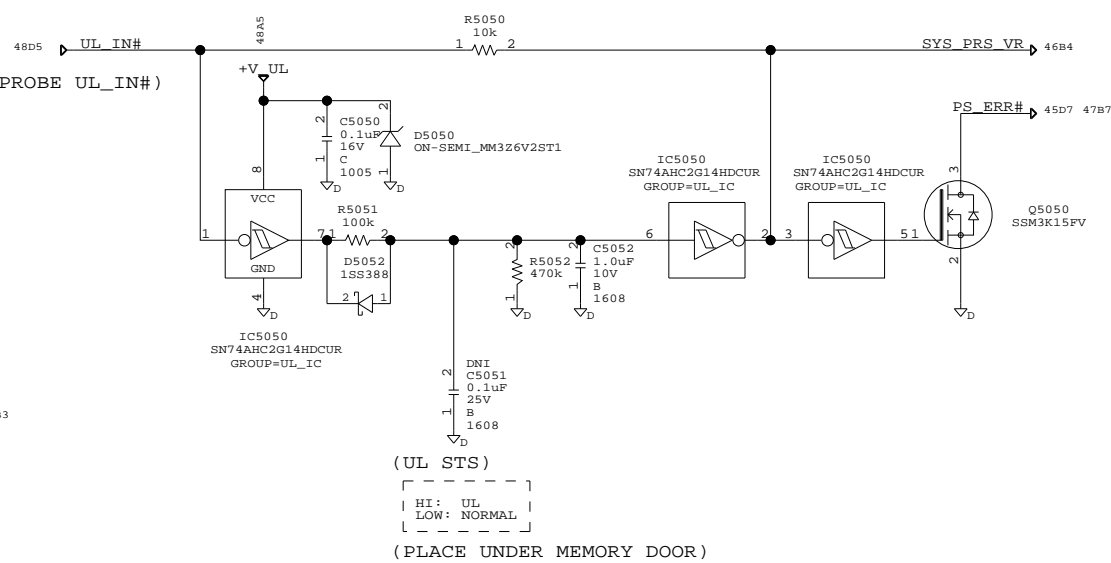


(UL CIRCUIT)

(BACKUP BATTERY)



```
(DO NOT PROBE UL_IN#)
```



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DRAWING: MBX-147
PROTECTION(UL CIRCUIT)
LAST_MODIFIED=Mon Jan 16 20:46:44 2006

-	QTY 1	REV. 0.220	PART NO. -
SIZE C	DESIGNER -	-	(POWER-1) SHEET 48 OF 50

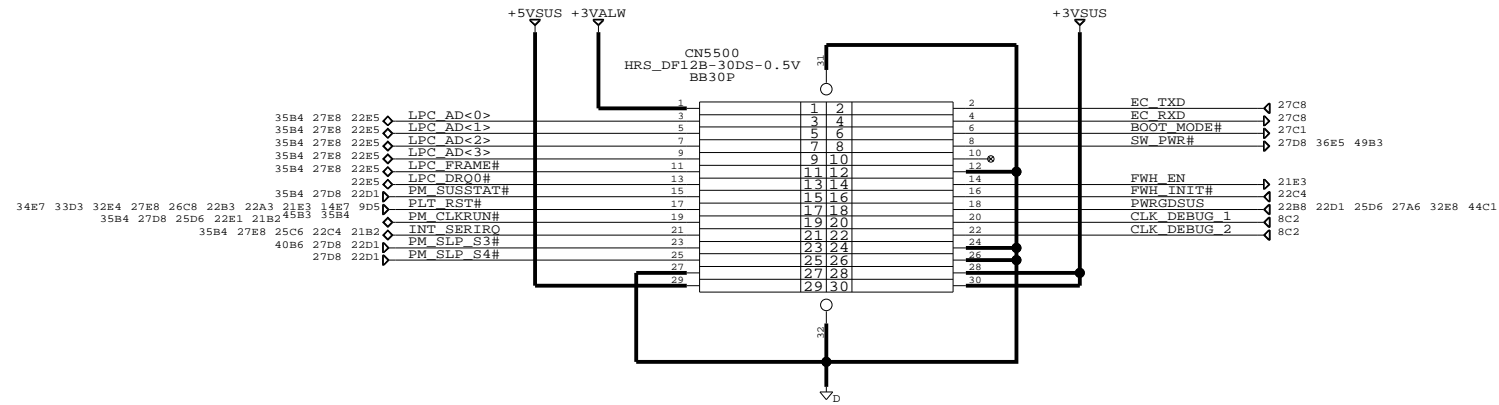
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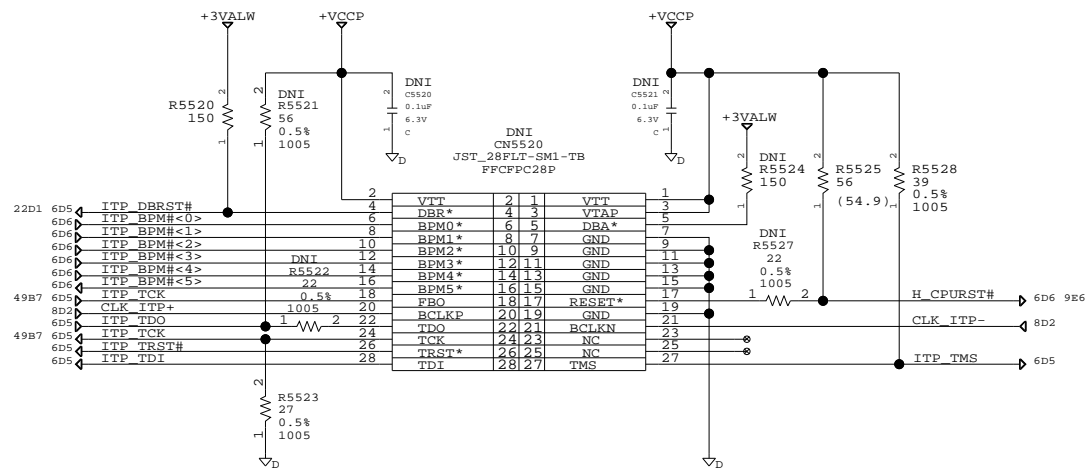
REVISIONS

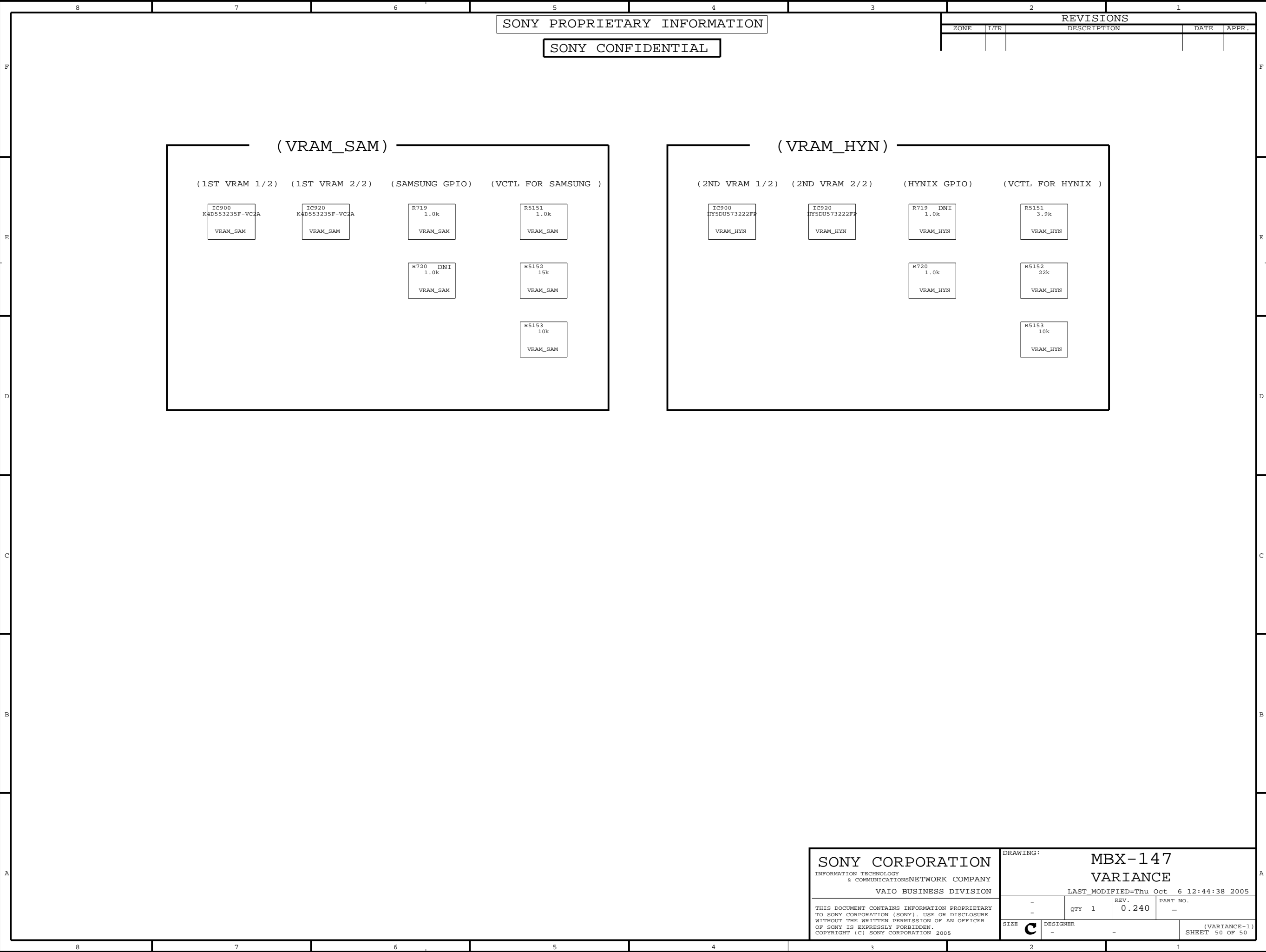
ZONE	LTR	DESCRIPTION	DATE	APPR.

(DEBUG CONNECTOR)



(ITP CONNECTOR)





8		7		6		5		4		3		2		1									
Title: Basenet Report Design: mbx147 Date: Jan 16 20:49:06 2006												REVISIONS											
Base nets and synonyms for pwb_lib.MBX147(@pwb_lib.mbx147(sch_1))												ZONE				LTR		DESCRIPTION		DATE		APPR.	
Base Signal		Synonyms			Location([Zone][dir])																		
+1.2VSUS		+1.2VSUS - @pwb_lib.MBX147			26E4 41C3 43C4																		
+1.05VSUS		+1.05VSUS - @pwb_lib.MBX147			12F5 12F8 23F4 39B3 39E1 43C1 48E2																		
+1.5VSUS		+1.5VSUS - @pwb_lib.MBX147			7D6 11B4 11C5 11D4 11D5 11D8 11F5 11F6 12E3 13D5 13E4 21F7 23C7 23F8 32F3 33F5 39D7 43D1 48E5																		
+1.8VSUS		+1.8VSUS - @pwb_lib.MBX147			9B6 9E1 12F5 19F8 26E4 40B5 40B6 40E3 43C5 48E4 11D6 11E7 11E7 11F4 11F4																		
+2.5VSUS		+2.5VSUS - @pwb_lib.MBX147			26E4 41E4 43C2 43C6 21D2 22B4 22E4 23C3 23D3 27B4 27B5 27B6 27C6 27E3 27E3 27E4 27E6 27E6 27F5 28B2 29D5 30D6 30D7 32F7 34D7 34D7 34E7 36B7 36E5 36F3 36F3 40E4 40E6 41D5 41D6 42D1 43C7 43D5 43E1 44B6 44C2 44E3 44E3 44E6 44E7 45B5 45B6 45C4 48C6 48E4 49C6 49C7 49E5																		
+3VALW		+3VALW - @pwb_lib.MBX147																					
+3VSUS		+3VSUS - @pwb_lib.MBX147			6F5 8E5 8E5 9E5 9E5 11C4 14B3 14B8 14C2 14F2 16F2 19B1 19B4 19B8 21D3 22A4 22A5 22A7 22B2 22B2 22B2 22B2 22C1 22D5 23C3 23C3 23E4 24D2 24D3 24E7 25B6 25C7 25D6 25F3 25F5 26D7 26E5 27B4 27E3 27E3 27E6 28E3 29D4 30E7 31B3 31C3 31C4 31D4 32F3 32F3 33E7 33F2 33F4 35C2 35C7 35E3 35E7 36B7 36E4 36E7 36F2 37C7 38C6 41E5 43D1 43D2 43D4 44B7 44C3 45C2 49E4 6B6 23F6 27B2 27B2 27F5 29E4 30C1 30E2 31E5 32F7 35E7 36F3 38F5 39E4 40C5 42E6 42F1 43D7 43E2 48C7 48E3																		
+5VALW		+5VALW - @pwb_lib.MBX147			7C4 23F6 27E3 28E3 29E7 30C1 31D3 31D7 31E7 35F2 36C7 36F2 37B7 37D3 37D5 37F3 37F6 43D2 43D6 49E5 45F7 46E2 6B8 6D4 6D5 6D5 6E5 7D3 7D6 9A7 9B7 9B7 9B8 9E6 11F1 22C4 22C5 23E4 37C7 37F7 39B2 49C6 49C7 46C3 48E3 7C6 7E6 7E8 7F3 37D1 48E6 34E4 45F7 46D6 47B7 47C7 47E4 47F6 20E4 40B4 27F4 29D8 42D5 48B6 18B5 18C2 18C6 17D3 26E5 29E4 30D7 35C7 39B3 43C7 43D7 43E6 17F4 17F6 38F8 48E6 15B8 15C1 15E3 15F1 15F5 18B5 18E3 18E3 18E7 18E7 38F1 14E4 17D5 15B4 17B7 17F6 17F8 43C4 17C6 43C5 17B5 17C5 17C6 17D6 43C5 14C5 14D4 14F7 14F8 16D2 16D4 16D7 16D8 16E3 16E3 16E4 16E7 16F3 17B5 17C5 17D4 17F4 43C6 25F3 32E6 42B8 45E6 47C5 48A6 45E6 48D6 35F2 37D4 37F4 38F4 38F5 38F6 39E5 42B8 42D4 42E7 42F4 43F7 45E5 46D4 46F5 45E6 46E3 46F7 47E1 22A7 22C5 23B3 48B5 9E1 19E4 19E7 40B4 48A5 48C4 35C6 32E2 32E2 32D4 32E2 32E6																		
+VBATT1		+VBATT1 - @pwb_lib.MBX147																					
+VCCP		+VCCP - @pwb_lib.MBX147																					
+VCHG		+VCHG - @pwb_lib.MBX147																					
+VCPUCORE		+VCPUCORE - @pwb_lib.MBX147																					
+VDC_IN		+VDC_IN - @pwb_lib.MBX147																					
+VDDRVT		+VDDRVT - @pwb_lib.MBX147																					
+VEC_3.3		+VEC_3.3 - @pwb_lib.MBX147																					
+VFBAVREF		+VFBAVREF - @pwb_lib.MBX147																					
+VFETDRV		+VFETDRV - @pwb_lib.MBX147																					
+VGFXCORE		+VGFXCORE - @pwb_lib.MBX147																					
+VGFXMEMVCC		+VGFXMEMVCC - @pwb_lib.MBX147																					
+VGFXTMDS_3.3		+VGFXTMDS_3.3 - @pwb_lib.MBX147																					
+VGFX_1.2		+VGFX_1.2 - @pwb_lib.MBX147																					
+VGFX_1.8		+VGFX_1.8 - @pwb_lib.MBX147																					
+VGFX_2.5		+VGFX_2.5 - @pwb_lib.MBX147																					
+VGFX_3.3		+VGFX_3.3 - @pwb_lib.MBX147																					
+VPCCDVCC		+VPCCDVCC - @pwb_lib.MBX147																					
+VPRTCT_5		+VPRTCT_5 - @pwb_lib.MBX147																					
+VPRTCT_SRC		+VPRTCT_SRC - @pwb_lib.MBX147																					
+VPWRSRC		+VPWRSRC - @pwb_lib.MBX147																					
+VPWR_COMM		+VPWR_COMM - @pwb_lib.MBX147																					
+VRTC		+VRTC - @pwb_lib.MBX147																					
+VSMVREF		+VSMVREF - @pwb_lib.MBX147																					
+V_UL		+V_UL - @pwb_lib.MBX147																					
+=VCAM		+=VCAM - @pwb_lib.MBX147																					
+=VEXCD15		+=VEXCD15 - @pwb_lib.MBX147																					
+=VEXCD33		+=VEXCD33 - @pwb_lib.MBX147																					
+=VEXCDAUX		+=VEXCDAUX - @pwb_lib.MBX147																					
+=VPCCDVPP		+=VPCCDVPP - @pwb_lib.MBX147																					
												</											

		8		7		6		5		4		3		2		1				
														REVISIONS						
														ZONE		LTR				
																DESCRIPTION				
																DATE				
																APPR.				
F	=VCD31	=VCD31 - @pwb_lib.MBX147	30D2	CAM_PRS#	CAM_PRS# - @pwb_lib.MBX147	27B3	35D6													
	=VCD32	=VCD32 - @pwb_lib.MBX147	30D2	CHGLMT_CHK	CHGLMT_CHK - @pwb_lib.MBX147	46E7	49B3													
	=VGA_CRTDET#	=VGA_CRTDET# - @pwb_lib.MBX147	31C4	CHG_OFF#	CHG_OFF# - @pwb_lib.MBX147	27D1	46C8													
	=VGA_VSYNC	=VGA_VSYNC - @pwb_lib.MBX147	31E4	CHG_TST	CHG_TST - @pwb_lib.MBX147	27D8	46B7													
	=VGTLLREF0	=VGTLLREF0 - @pwb_lib.MBX147	6C4	CHK_+1.2VSUS	CHK_+1.2VSUS - @pwb_lib.MBX147	41A3	49B3													
	=WL_CH_CLK_PCIE_F	=WL_CH_CLK_PCIE_F - @pwb_lib.MBX147	33D7	CHK_+1.05VSUS	CHK_+1.05VSUS - @pwb_lib.MBX147	39C1	49B3													
	A20GATE	A20GATE - @pwb_lib.MBX147	21B2 22D4 27D8	CHK_+1.5VSUS	CHK_+1.5VSUS - @pwb_lib.MBX147	39C8	49B3													
	ACC_X	ACC_X - @pwb_lib.MBX147	27D8 29C8	CHK_+3VALW	CHK_+3VALW - @pwb_lib.MBX147	42C1	49B3													
	ACC_Y	ACC_Y - @pwb_lib.MBX147	27D8 29C5	CHK_+5VALW	CHK_+5VALW - @pwb_lib.MBX147	42E1	49B3													
	ACC_Z	ACC_Z - @pwb_lib.MBX147	27D8 29C5	CHK_+VCPUCORE	CHK_+VCPUCORE - @pwb_lib.MBX147	37E1	49B3													
E	AC_OFF	AC_OFF - @pwb_lib.MBX147	27D1 47A7	CHK_+VDDRVT	CHK_+VDDRVT - @pwb_lib.MBX147	40B3	49B3													
	AC_STS	AC_STS - @pwb_lib.MBX147	27A3 47B3	CHK_+VGFXCORE	CHK_+VGFXCORE - @pwb_lib.MBX147	38D7	49B3													
	ALW_DETECT	ALW_DETECT - @pwb_lib.MBX147	37B4 42A6 44D4	CHK_+VGFXMEMVCC	CHK_+VGFXMEMVCC - @pwb_lib.MBX147	38D1	49B3													
	ALW_RST_C#	ALW_RST_C# - @pwb_lib.MBX147	39B5 42A6 45C5 47B6 47C5	CLKREQ_EXCD#	CLKREQ_EXCD# - @pwb_lib.MBX147	8C2	32C5													
	ANL66_ID	ANL66_ID - @pwb_lib.MBX147	27B3 36C4	CLKREQ_MCH#	CLKREQ_MCH# - @pwb_lib.MBX147	8C2	9D4													
	AVSS_EC	AVSS_EC - @pwb_lib.MBX147	27C5 27E4 27F5 29C6	CLKREQ_MNCD#	CLKREQ_MNCD# - @pwb_lib.MBX147	8C2	33D6													
	AZ_BCLK_AUD	AZ_BCLK_AUD - @pwb_lib.MBX147	22E5 36E4	CLKREQ_PEG#	CLKREQ_PEG# - @pwb_lib.MBX147	8C2	14A8 27A3													
	AZ_BCLK_MDM	AZ_BCLK_MDM - @pwb_lib.MBX147	22E5 35E6	CLKREQ_SATA#	CLKREQ_SATA# - @pwb_lib.MBX147	8C2	22C6													
	AZ_RST_AUD#	AZ_RST_AUD# - @pwb_lib.MBX147	22E5 36E4	CLK_27MHZ	CLK_27MHZ - @pwb_lib.MBX147	8C1	14A8													
	AZ_RST_MDM#	AZ_RST_MDM# - @pwb_lib.MBX147	22E5 35E6	CLK_27MHZ_SS	CLK_27MHZ_SS - @pwb_lib.MBX147	8C1	14A8													
D	AZ_SDIN0	AZ_SDIN0 - @pwb_lib.MBX147	22E5 36E4	CLK_BSEL0	CLK_BSEL0 - @pwb_lib.MBX147	6C4	8C2 9E4													
	AZ_SDIN1	AZ_SDIN1 - @pwb_lib.MBX147	22D5 35E6	CLK_BSEL1	CLK_BSEL1 - @pwb_lib.MBX147	6C4	8B2 9E4													
	AZ_SDOUT_AUD	AZ_SDOUT_AUD - @pwb_lib.MBX147	22D5 36E4	CLK_BSEL2	CLK_BSEL2 - @pwb_lib.MBX147	6C4	9E4													
	AZ_SDOUT_MDM	AZ_SDOUT_MDM - @pwb_lib.MBX147	22D5 35E6	CLK_CARD	CLK_CARD - @pwb_lib.MBX147	8C2	25D6													
	AZ_SYNC_AUD	AZ_SYNC_AUD - @pwb_lib.MBX147	22D5 36E4	CLK_CPU_BCLK+	CLK_CPU_BCLK+ - @pwb_lib.MBX147	6C5	8D2													
	AZ_SYNC_MDM	AZ_SYNC_MDM - @pwb_lib.MBX147	22D5 35E6	CLK_CPU_BCLK-	CLK_CPU_BCLK- - @pwb_lib.MBX147	6C5	8D2													
	A_A<0>	A_A<0> - @pwb_lib.MBX147	25E4 32B5	CLK_DEBUG_1	CLK_DEBUG_1 - @pwb_lib.MBX147	8C2	49E3													
	A_A<1>	A_A<1> - @pwb_lib.MBX147	25E4 32B5	CLK_DEBUG_2	CLK_DEBUG_2 - @pwb_lib.MBX147	8C2	49E3													
	A_A<2>	A_A<2> - @pwb_lib.MBX147	25E4 32B5	CLK_DREFCLK+	CLK_DREFCLK+ - @pwb_lib.MBX147	8C2	9D1													
	A_A<3>	A_A<3> - @pwb_lib.MBX147	25E4 32B7	CLK_DREFCLK-	CLK_DREFCLK- - @pwb_lib.MBX147	8C2	9D1													
C	A_A<4>	A_A<4> - @pwb_lib.MBX147	25E4 32B7	CLK_DREF_SS+	CLK_DREF_SS+ - @pwb_lib.MBX147	8C2	9D1													
	A_A<5>	A_A<5> - @pwb_lib.MBX147	25E4 32C7	CLK_DREF_SS-	CLK_DREF_SS- - @pwb_lib.MBX147	8C2	9D1													
	A_A<6>	A_A<6> - @pwb_lib.MBX147	25E4 32C7	CLK_EC	CLK_EC - @pwb_lib.MBX147	8C2	27E8													
	A_A<7>	A_A<7> - @pwb_lib.MBX147	25E4 32C5	CLK_ENABLE#	CLK_ENABLE# - @pwb_lib.MBX147	8D6	37C6													
	A_A<8>	A_A<8> - @pwb_lib.MBX147	25E4 32C7	CLK_ICH14	CLK_ICH14 - @pwb_lib.MBX147	8C2	22D5													
	A_A<9>	A_A<9> - @pwb_lib.MBX147	25E4 32C7	CLK_ITP+	CLK_ITP+ - @pwb_lib.MBX147	8D2	49B7													
	A_A<10>	A_A<10> - @pwb_lib.MBX147	25E4 32C5	CLK_ITP-	CLK_ITP- - @pwb_lib.MBX147	8D2	49B5													
	A_A<11>	A_A<11> - @pwb_lib.MBX147	25E4 32C7	CLK_MCH_BCLK+	CLK_MCH_BCLK+ - @pwb_lib.MBX147	8D2	9C8													
	A_A<12>	A_A<12> - @pwb_lib.MBX147	25E4 32C5	CLK_MCH_BCLK-	CLK_MCH_BCLK- - @pwb_lib.MBX147	8D2	9C8													
	A_A<13>	A_A<13> - @pwb_lib.MBX147	25E4 32C5	CLK_MCH_GPLL+	CLK_MCH_GPLL+ - @pwb_lib.MBX147	8D2	9E1													
B	A_A<14>	A_A<14> - @pwb_lib.MBX147	25E4 32C5	CLK_MCH_GPLL-	CLK_MCH_GPLL- - @pwb_lib.MBX147	8D2	9E1													
	A_A<15>	A_A<15> - @pwb_lib.MBX147	25E4 32C5	CLK_MS48	CLK_MS48 - @pwb_lib.MBX147	8C2	24C4													
	A_A<16>	A_A<16> - @pwb_lib.MBX147	25E3 32C7	CLK_PCIE_DOCK+	CLK_PCIE_DOCK+ - @pwb_lib.MBX147	8D2	34D6													
	A_A<17>	A_A<17> - @pwb_lib.MBX147	25E4 32C5	CLK_PCIE_DOCK-	CLK_PCIE_DOCK- - @pwb_lib.MBX147	8D2	34D6													
	A_A<18>	A_A<18> - @pwb_lib.MBX147	25E4 32C7	CLK_PCIE_EXCD+	CLK_PCIE_EXCD+ - @pwb_lib.MBX147	8D2	32C1													
	A_A<19>	A_A<19> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_EXCD-	CLK_PCIE_EXCD- - @pwb_lib.MBX147	8D2	32C1													
	A_A<20>	A_A<20> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_ICH+	CLK_PCIE_ICH+ - @pwb_lib.MBX147	8D2	21E8													
	A_A<21>	A_A<21> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_ICH-	CLK_PCIE_ICH- - @pwb_lib.MBX147	8D2	21E8													
	A_A<22>	A_A<22> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_LAN+	CLK_PCIE_LAN+ - @pwb_lib.MBX147	8D2	26C7													
	A_A<23>	A_A<23> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_LAN-	CLK_PCIE_LAN- - @pwb_lib.MBX147	8D2	26C7													
A	A_A<24>	A_A<24> - @pwb_lib.MBX147	25F4 32C7	CLK_PCIE_MNCD+	CLK_PCIE_MNCD+ - @pwb_lib.MBX147	8D2	33D6													
	A_A<25>	A_A<25> - @pwb_lib.MBX147	25F4 32C5	CLK_PCIE_MNCD-	CLK_PCIE_MNCD- - @pwb_lib.MBX147	8D2	33D6													
	A_BVD1	A_BVD1 - @pwb_lib.MBX147	25D3 32B5	CLK_PCIE_PEG+	CLK_PCIE_PEG+ - @pwb_lib.MBX147	8D2	14D8													
	A_BVD2	A_BVD2 - @pwb_lib.MBX147	25D3 32B7	CLK_PCIE_PEG-	CLK_PCIE_PEG- - @pwb_lib.MBX147	8D2	14D8													
	A_CD1#	A_CD1# - @pwb_lib.MBX147	25D4 32D5	CLK_PCIE_SATA+	CLK_PCIE_SATA+ - @pwb_lib.MBX147	8D2	22D6													
	A_CD2#	A_CD2# - @pwb_lib.MBX147	25D4 32B5	CLK_PCIE_SATA-	CLK_PCIE_SATA- - @pwb_lib.MBX147	8D2	22D6													
	A_CE1#	A_CE1# - @pwb_lib.MBX147	25D4 32C5	CLK_PCI_ICH	CLK_PCI_ICH - @pwb_lib.MBX147	8C2	21E3													
	A_CE2#	A_CE2# - @pwb_lib.MBX147	25D4 32C7	CLK_TPM	CLK_TPM - @pwb_lib.MBX147	8C2	35B4													
	A_D<0>	A_D<0> - @pwb_lib.MBX147	25D4 32B7	CLK_USB48	CLK_USB48 - @pwb_lib.MBX147	8C2	21D5													
	A_D<1>	A_D<1> - @pwb_lib.MBX147	25D4 32B7	CPU_VCCSENSE	CPU_VCCSENSE - @pwb_lib.MBX147	7C5	37D8													
													SONY CORPORATION		-					
													NETWORK COMPANY		-					
													-		QTY 1		REV.		PART NO.	
													-		-		-		-	
													SIZE		DESIGNER		-		52	
													C		-		-		SHEET 52 OF <TOTAL DESIGN SHEETS>	

8		7		6		5		4		3		2		1			
												REVISIONS					
												ZONE		LTR			
												DESCRIPTION		DATE			
														APPR.			
ETHER_RST#	ETHER_RST# - @pwb_lib.MBX147	26C8	27D1	FBAD63	FBAD63 - @pwb_lib.MBX147	15C7	18D4										
EXCD_DET	EXCD_DET - @pwb_lib.MBX147	32E1	39B7	FBADQM0	FBADQM0 - @pwb_lib.MBX147	15C7	18D5										
EXTCOM_PWREN	EXTCOM_PWREN - @pwb_lib.MBX147	27B3	35E3 36A8	FBADQM1	FBADQM1 - @pwb_lib.MBX147	15C7	18D5										
EXTSMI#	EXTSMI# - @pwb_lib.MBX147	22E1	27E8	FBADQM2	FBADQM2 - @pwb_lib.MBX147	15C7	18D5										
EXT_DEV_SENSE	EXT_DEV_SENSE - @pwb_lib.MBX147	27C8	31B2	FBADQM3	FBADQM3 - @pwb_lib.MBX147	15C7	18D5										
FAN_PWM	FAN_PWM - @pwb_lib.MBX147	6A6	27E1	FBADQM4	FBADQM4 - @pwb_lib.MBX147	15C7	18D1										
FBA A0	FBA A0 - @pwb_lib.MBX147	15D5	18D2 18D6	FBADQM5	FBADQM5 - @pwb_lib.MBX147	15C7	18D1										
FBA A1	FBA A1 - @pwb_lib.MBX147	15C5	18D2 18D6	FBADQM6	FBADQM6 - @pwb_lib.MBX147	15C7	18D1										
FBA A2	FBA A2 - @pwb_lib.MBX147	15D5	18D6	FBADQM7	FBADQM7 - @pwb_lib.MBX147	15C7	18D1										
FBA A3	FBA A3 - @pwb_lib.MBX147	15D5	18D6	FBADQS0	FBADQS0 - @pwb_lib.MBX147	15C8	18D5										
FBA A4	FBA A4 - @pwb_lib.MBX147	15C5	18D6	FBADQS1	FBADQS1 - @pwb_lib.MBX147	15C7	18C5										
FBA A5	FBA A5 - @pwb_lib.MBX147	15C5	18D6	FBADQS2	FBADQS2 - @pwb_lib.MBX147	15B7	18D5										
FBA A6	FBA A6 - @pwb_lib.MBX147	15C5	18D2 18D6	FBADQS3	FBADQS3 - @pwb_lib.MBX147	15B8	18C5										
FBA A7	FBA A7 - @pwb_lib.MBX147	15C5	18D2 18D6	FBADQS4	FBADQS4 - @pwb_lib.MBX147	15B8	18C1										
FBA A8	FBA A8 - @pwb_lib.MBX147	15C5	18D2 18D6	FBADQS5	FBADQS5 - @pwb_lib.MBX147	15B7	18C1										
FBA A9	FBA A9 - @pwb_lib.MBX147	15C5	18D2 18D6	FBADQS6	FBADQS6 - @pwb_lib.MBX147	15B7	18D1										
FBA A10	FBA A10 - @pwb_lib.MBX147	15C5	18D2 18D6	FBADQS7	FBADQS7 - @pwb_lib.MBX147	15B8	18D1										
FBA A11	FBA A11 - @pwb_lib.MBX147	15C5	18D2 18D6	FBB A2	FBB A2 - @pwb_lib.MBX147	15C5	18D2										
FBA BA0	FBA BA0 - @pwb_lib.MBX147	15C5	18D2 18D6	FBB A3	FBB A3 - @pwb_lib.MBX147	15C5	18D2										
FBA BA1	FBA BA1 - @pwb_lib.MBX147	15C5	18D2 18D6	FBB A4	FBB A4 - @pwb_lib.MBX147	15C5	18D2										
FBA CAS#	FBA CAS# - @pwb_lib.MBX147	15C5	18C2 18C6	FBB A5	FBB A5 - @pwb_lib.MBX147	15C5	18D2										
FBA CKE	FBA CKE - @pwb_lib.MBX147	15C5	18C2 18C5	FP_PR#	FP_PR# - @pwb_lib.MBX147	21C2 22E1	28D4										
FBA CS0#	FBA CS0# - @pwb_lib.MBX147	15C5	18C2 18C6	FWH_EN	FWH_EN - @pwb_lib.MBX147	21E3	49E3										
FBA RAS#	FBA RAS# - @pwb_lib.MBX147	15C5	18C2 18C6	FWH_INIT#	FWH_INIT# - @pwb_lib.MBX147	22C4	49E3										
FBA WE#	FBA WE# - @pwb_lib.MBX147	15C5	18C2 18C6	GFX_HI_LO#	GFX_HI_LO# - @pwb_lib.MBX147	16D3	38C6										
FBACLK0+	FBACLK0+ - @pwb_lib.MBX147	15B5	18C5	GFX_VGA_BLU	GFX_VGA_BLU - @pwb_lib.MBX147	14C1	31E6										
FBACLK0-	FBACLK0- - @pwb_lib.MBX147	15B5	18C5	GFX_VGA_DDCCLK	GFX_VGA_DDCCLK - @pwb_lib.MBX147	14B1	31C5										
FBACLK1+	FBACLK1+ - @pwb_lib.MBX147	15B5	18C1	GFX_VGA_DDCDATA	GFX_VGA_DDCDATA - @pwb_lib.MBX147	14B1	31C5										
FBACLK1-	FBACLK1- - @pwb_lib.MBX147	15B5	18C1	GFX_VGA_GRN	GFX_VGA_GRN - @pwb_lib.MBX147	14C1	31E6										
FBAD0	FBAD0 - @pwb_lib.MBX147	15E7	18D8	GFX_VGA_HSYNC	GFX_VGA_HSYNC - @pwb_lib.MBX147	14B1	31D8										
FBAD1	FBAD1 - @pwb_lib.MBX147	15E7	18D8	GFX_VGA_RED	GFX_VGA_RED - @pwb_lib.MBX147	14C1	31E6										
FBAD2	FBAD2 - @pwb_lib.MBX147	15E8	18D8	GFX_VGA_VSYNC	GFX_VGA_VSYNC - @pwb_lib.MBX147	14B1	31E8										
FBAD3	FBAD3 - @pwb_lib.MBX147	15E7	18D8	GSENSE_PR#	GSENSE_PR# - @pwb_lib.MBX147	27B3 27D8	29C8										
FBAD4	FBAD4 - @pwb_lib.MBX147	15E7	18D8	HOTPLUG	HOTPLUG - @pwb_lib.MBX147	16F3	34C2										
FBAD5	FBAD5 - @pwb_lib.MBX147	15E7	18D8	H_A#<3>	H_A#<3> - @pwb_lib.MBX147	6E8	9F6										
FBAD6	FBAD6 - @pwb_lib.MBX147	15E7	18D8	H_A#<4>	H_A#<4> - @pwb_lib.MBX147	6E8	9F6										
FBAD7	FBAD7 - @pwb_lib.MBX147	15E7	18D8	H_A#<5>	H_A#<5> - @pwb_lib.MBX147	6E8	9F6										
FBAD8	FBAD8 - @pwb_lib.MBX147	15E7	18D8	H_A#<6>	H_A#<6> - @pwb_lib.MBX147	6E8	9F6										
FBAD9	FBAD9 - @pwb_lib.MBX147	15E7	18C8	H_A#<7>	H_A#<7> - @pwb_lib.MBX147	6E8	9F6										
FBAD10	FBAD10 - @pwb_lib.MBX147	15E7	18C8	H_A#<8>	H_A#<8> - @pwb_lib.MBX147	6E8	9F6										
FBAD11	FBAD11 - @pwb_lib.MBX147	15E7	18C8	H_A#<9>	H_A#<9> - @pwb_lib.MBX147	6E8	9F6										
FBAD12	FBAD12 - @pwb_lib.MBX147	15E7	18C8	H_A#<10>	H_A#<10> - @pwb_lib.MBX147	6E8	9F6										
FBAD13	FBAD13 - @pwb_lib.MBX147	15E7	18C8	H_A#<11>	H_A#<11> - @pwb_lib.MBX147	6E8	9F6										
FBAD14	FBAD14 - @pwb_lib.MBX147	15E7	18C8	H_A#<12>	H_A#<12> - @pwb_lib.MBX147	6E8	9E6										
FBAD15	FBAD15 - @pwb_lib.MBX147	15E7	18C8	H_A#<13>	H_A#<13> - @pwb_lib.MBX147	6E8	9E6										
FBAD16	FBAD16 - @pwb_lib.MBX147	15E7	18D8	H_A#<14>	H_A#<14> - @pwb_lib.MBX147	6E8	9E6										
FBAD17	FBAD17 - @pwb_lib.MBX147	15E7	18D8	H_A#<15>	H_A#<15> - @pwb_lib.MBX147	6E8	9E6										
FBAD18	FBAD18 - @pwb_lib.MBX147	15E7	18D8	H_A#<16>	H_A#<16> - @pwb_lib.MBX147	6D8	9E6										
FBAD19	FBAD19 - @pwb_lib.MBX147	15E7	18D8	H_A#<17>	H_A#<17> - @pwb_lib.MBX147	6D8	9E6										
FBAD20	FBAD20 - @pwb_lib.MBX147	15E7	18D8	H_A#<18>	H_A#<18> - @pwb_lib.MBX147	6D8	9E6										
FBAD21	FBAD21 - @pwb_lib.MBX147	15E7	18D8	H_A#<19>	H_A#<19> - @pwb_lib.MBX147	6D8	9E6										
FBAD22	FBAD22 - @pwb_lib.MBX147	15D7	18D8	H_A#<20>	H_A#<20> - @pwb_lib.MBX147	6D8	9E6										
FBAD23	FBAD23 - @pwb_lib.MBX147	15D7	18D8	H_A#<21>	H_A#<21> - @pwb_lib.MBX147	6D8	9E6										
FBAD24	FBAD24 - @pwb_lib.MBX147	15D7	18D8	H_A#<22>	H_A#<22> - @pwb_lib.MBX147	6D8	9E6										
FBAD25	FBAD25 - @pwb_lib.MBX147	15D7	18D8	H_A#<23>	H_A#<23> - @pwb_lib.MBX147	6D8	9E6										
FBAD26	FBAD26 - @pwb_lib.MBX147	15D7	18D8	H_A#<24>	H_A#<24> - @pwb_lib.MBX147	6D8	9E6										
FBAD27	FBAD27 - @pwb_lib.MBX147	15D8	18D8	H_A#<25>	H_A#<25> - @pwb_lib.MBX147	6D8	9E6										
FBAD28	FBAD28 - @pwb_lib.MBX147	15D7	18D8	H_A#<26>	H_A#<26> - @pwb_lib.MBX147	6D8	9E6										
FBAD29	FBAD29 - @pwb_lib.MBX147	15D7	18D8	H_A#<27>	H_A#<27> - @pwb_lib.MBX147	6D8	9E6										
FBAD30	FBAD30 - @pwb_lib.MBX147	15D7	18D8	H_A#<28>	H_A#<28> - @pwb_lib.MBX147	6D8	9E6										
FBAD31	FBAD31 - @pwb_lib.MBX147	15D7	18D8	H_A#<29>	H_A#<29> - @pwb_lib.MBX147	6D8	9E6										
FBAD32	FBAD32 - @pwb_lib.MBX147	15D7	18D4	H_A#<30>	H_A#<30> - @pwb_lib.MBX147	6D8	9E6										
FBAD33	FBAD33 - @pwb_lib.MBX147	15D7	18D4	H_A#<31>	H_A#<31> - @pwb_lib.MBX147	6D8	9E6										
FBAD34	FBAD34 - @pwb_lib.MBX147	15D7	18D4	H_A20M#	H_A20M# - @pwb_lib.MBX147	6C8	22D4										
FBAD35	FBAD35 - @pwb_lib.MBX147	15D7	18D4	H_ADS#	H_ADS# - @pwb_lib.MBX147	6E6	9E6										
FBAD36	FBAD36 - @pwb_lib.MBX147	15D7	18D4	H_ADSTB#<0>	H_ADSTB#<0> - @pwb_lib.MBX147	6D8	9E6										
FBAD37	FBAD37 - @pwb_lib.MBX147	15D7	18D4	H_ADSTB#<1>	H_ADSTB#<1> - @pwb_lib.MBX147	6D8	9E6										
FBAD38	FBAD38 - @pwb_lib.MBX147	15D8	18D4	H_BNR#	H_BNR# - @pwb_lib.MBX147	6E6	9E6										
FBAD39	FBAD39 - @pwb_lib.MBX147	15D7	18D4	H_BPRI#	H_BPRI# - @pwb_lib.MBX147	6E6	9E6										
FBAD40	FBAD40 - @pwb_lib.MBX147	15D7	18C4	H_BREQ#0	H_BREQ#0 - @pwb_lib.MBX147	6E6	9E6										
FBAD41	FBAD41 - @pwb_lib.MBX147	15D7	18C4	H_CPURST#	H_CPURST# - @pwb_lib.MBX147	6D6	9E6 49B5										
FBAD42	FBAD42 - @pwb_lib.MBX147	15D7	18C4	H_CPUSLP_MCH#	H_CPUSLP_MCH# - @pwb_lib.MBX147	6C1	9C6										
FBAD43	FBAD43 - @pwb_lib.MBX147	15D7	18C4	H_D#<0>	H_D#<0> - @pwb_lib.MBX147	6E4	9F8										
FBAD44	FBAD44 - @pwb_lib.MBX147	15D7	18C4	H_D#<1>	H_D#<1> - @pwb_lib.MBX147	6E4	9F8										
FBAD45	FBAD45 - @pwb_lib.MBX147	15D7	18D4	H_D#<2>	H_D#<2> - @pwb_lib.MBX147	6E4	9F8										
FBAD46	FBAD46 - @pwb_lib.MBX147	15D7	18C4	H_D#<3>	H_D#<3> - @pwb_lib.MBX147	6E4	9F8										
FBAD47	FBAD47 - @pwb_lib.MBX147	15D7	18C4	H_D#<4>	H_D#<4> - @pwb_lib.MBX147	6E4	9F8										
FBAD48	FBAD48 - @pwb_lib.MBX147	15D7	18D4	H_D#<5>	H_D#<5> - @pwb_lib.MBX147	6E4	9F8										
FBAD49	FBAD49 - @pwb_lib.MBX147	15C7	18D4	H_D#<6>	H_D#<6> - @pwb_lib.MBX147	6E4	9F8										
FBAD50	FBAD50 - @pwb_lib.MBX147	15C7	18D4	H_D#<7>	H_D#<7> - @pwb_lib.MBX147	6E4	9F8										
FBAD51	FBAD51 - @pwb_lib.MBX147	15C7	18D4	H_D#<8>	H_D#<8> - @pwb_lib.MBX147	6E4	9F8										
FBAD52	FBAD52 - @pwb_lib.MBX147	15C7	18D4	H_D#<9>	H_D#<9> - @pwb_lib.MBX147	6E4	9E8										
FBAD53	FBAD53 - @pwb_lib.MBX147	15C7	18D4	H_D#<10>	H_D#<10> - @pwb_lib.MBX147	6E4	9E8										
FBAD54	FBAD54 - @pwb_lib.MBX147	15C7	18D4	H_D#<11>	H_D#<11> - @pwb_lib.MBX147	6E4	9E8										
FBAD55	FBAD55 - @pwb_lib.MBX147	15C7	18D4	H_D#<12>	H_D#<12> - @pwb_lib.MBX147	6E4	9E8										
FBAD56	FBAD56 - @pwb_lib.MBX147	15C8	18D4	H_D#<13>	H_D#<13> - @pwb_lib.MBX147	6D4	9E8										
FBAD57	FBAD57 - @pwb_lib.MBX147	15C7	18D4	H_D#<14>	H_D#<14> - @pwb_lib.MBX147	6D4	9E8										
FBAD58	FBAD58 - @pwb_lib.MBX147	15C7	18D4	H_D#<15>	H_D#<15> - @pwb_lib.MBX147	6D4	9E8										
FBAD59	FBAD59 - @pwb_lib.MBX147	15C7	18D4	H_D#<16>	H_D#<16> - @pwb_lib.MBX147	6D4	9E8										
FBAD60	FBAD60 - @pwb_lib.MBX147	15C7	18D4	H_D#<17>	H_D#<17> - @pwb_lib.MBX147	6D4	9E8										
FBAD61	FBAD61 - @pwb_lib.MBX147	15C7	18D4	H_D#<18>	H_D#<18> - @pwb_lib.MBX147	6D4	9E8										
FBAD62	FBAD62 - @pwb_lib.MBX147	15C7	18D4	H_D#<19>	H_D#<19> - @pwb_lib.MBX147	6D4	9E8										
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NETWORK COMPANY										-							
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										53		SHEET 53 OF <TOTAL DESIGN SHEETS>					

8		7		6		5		4		3		2		1					
F	H_D#<20>	H_D#<20> - @pwb_lib.MBX147	6D4	9E8	H_XRCOMP	H_XRCOMP - @pwb_lib.MBX147	9A8	9C8	REVISONS						ZONE	LTR	DESCRIPTION	DATE	APPR.
	H_D#<21>	H_D#<21> - @pwb_lib.MBX147	6D4	9E8	H_XSCOMP	H_XSCOMP - @pwb_lib.MBX147	9A8	9C8											
	H_D#<22>	H_D#<22> - @pwb_lib.MBX147	6D4	9E8	H_XSWING	H_XSWING - @pwb_lib.MBX147	9B6	9C8											
	H_D#<23>	H_D#<23> - @pwb_lib.MBX147	6D4	9E8	H_YRCOMP	H_YRCOMP - @pwb_lib.MBX147	9A7	9C8											
	H_D#<24>	H_D#<24> - @pwb_lib.MBX147	6D4	9E8	H_YSCOMP	H_YSCOMP - @pwb_lib.MBX147	9A7	9C8											
	H_D#<25>	H_D#<25> - @pwb_lib.MBX147	6D4	9E8	H_YSWING	H_YSWING - @pwb_lib.MBX147	9A6	9C8											
	H_D#<26>	H_D#<26> - @pwb_lib.MBX147	6D4	9E8	ICH_PWROK	ICH_PWROK - @pwb_lib.MBX147	9D5	22B1 22D1											
	H_D#<27>	H_D#<27> - @pwb_lib.MBX147	6D4	9E8	ICH_PWROK_EC	ICH_PWROK_EC - @pwb_lib.MBX147	9D5	22B3 22D1 27D1											
	H_D#<28>	H_D#<28> - @pwb_lib.MBX147	6D4	9E8	ICH_SPI_CLK	ICH_SPI_CLK - @pwb_lib.MBX147	22B3	22E5											
	H_D#<29>	H_D#<29> - @pwb_lib.MBX147	6D4	9E8	ICH_SPI_CS#	ICH_SPI_CS# - @pwb_lib.MBX147	22B6	22E5											
E	H_D#<30>	H_D#<30> - @pwb_lib.MBX147	6D4	9E8	ICH_SPI_MISO	ICH_SPI_MISO - @pwb_lib.MBX147	22B6	22E5											
	H_D#<31>	H_D#<31> - @pwb_lib.MBX147	6D4	9E8	ICH_SPI_MOSI	ICH_SPI_MOSI - @pwb_lib.MBX147	22B3	22E5											
	H_D#<32>	H_D#<32> - @pwb_lib.MBX147	6E2	9E8	IDE_PDADR<0>	IDE_PDADR<0> - @pwb_lib.MBX147	22D8	29C3											
	H_D#<33>	H_D#<33> - @pwb_lib.MBX147	6E2	9E8	IDE_PDADR<1>	IDE_PDADR<1> - @pwb_lib.MBX147	22D8	29C3											
	H_D#<34>	H_D#<34> - @pwb_lib.MBX147	6E2	9E8	IDE_PDADR<2>	IDE_PDADR<2> - @pwb_lib.MBX147	22D8	29C1											
	H_D#<35>	H_D#<35> - @pwb_lib.MBX147	6E2	9E8	IDE_PDCS0#	IDE_PDCS0# - @pwb_lib.MBX147	22D8	29C3											
	H_D#<36>	H_D#<36> - @pwb_lib.MBX147	6E2	9E8	IDE_PDCS1#	IDE_PDCS1# - @pwb_lib.MBX147	22D8	29C1											
	H_D#<37>	H_D#<37> - @pwb_lib.MBX147	6E2	9D8	IDE_PDDACK#	IDE_PDDACK# - @pwb_lib.MBX147	22D8	29C1											
	H_D#<38>	H_D#<38> - @pwb_lib.MBX147	6E2	9D8	IDE_PDDREQ_5	IDE_PDDREQ_5 - @pwb_lib.MBX147	22D8	29C1											
	H_D#<39>	H_D#<39> - @pwb_lib.MBX147	6E2	9D8	IDE_PDD_5<0>	IDE_PDD_5<0> - @pwb_lib.MBX147	22E8	29C3											
D	H_D#<40>	H_D#<40> - @pwb_lib.MBX147	6E2	9D8	IDE_PDD_5<1>	IDE_PDD_5<1> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<41>	H_D#<41> - @pwb_lib.MBX147	6E2	9D8	IDE_PDD_5<2>	IDE_PDD_5<2> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<42>	H_D#<42> - @pwb_lib.MBX147	6E2	9D8	IDE_PDD_5<3>	IDE_PDD_5<3> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<43>	H_D#<43> - @pwb_lib.MBX147	6E2	9D8	IDE_PDD_5<4>	IDE_PDD_5<4> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<44>	H_D#<44> - @pwb_lib.MBX147	6E2	9D8	IDE_PDD_5<5>	IDE_PDD_5<5> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<45>	H_D#<45> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<6>	IDE_PDD_5<6> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<46>	H_D#<46> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<7>	IDE_PDD_5<7> - @pwb_lib.MBX147	22E8	29C3											
	H_D#<47>	H_D#<47> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<8>	IDE_PDD_5<8> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<48>	H_D#<48> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<9>	IDE_PDD_5<9> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<49>	H_D#<49> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<10>	IDE_PDD_5<10> - @pwb_lib.MBX147	22E8	29C1											
C	H_D#<50>	H_D#<50> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<11>	IDE_PDD_5<11> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<51>	H_D#<51> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<12>	IDE_PDD_5<12> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<52>	H_D#<52> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<13>	IDE_PDD_5<13> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<53>	H_D#<53> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<14>	IDE_PDD_5<14> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<54>	H_D#<54> - @pwb_lib.MBX147	6D2	9D8	IDE_PDD_5<15>	IDE_PDD_5<15> - @pwb_lib.MBX147	22E8	29C1											
	H_D#<55>	H_D#<55> - @pwb_lib.MBX147	6D2	9D8	IDE_PDIOR#	IDE_PDIOR# - @pwb_lib.MBX147	22D8	29C1											
	H_D#<56>	H_D#<56> - @pwb_lib.MBX147	6D2	9D8	IDE_PDIORDY	IDE_PDIORDY - @pwb_lib.MBX147	22D8	29C4											
	H_D#<57>	H_D#<57> - @pwb_lib.MBX147	6D2	9D8	IDE_PDIOW#	IDE_PDIOW# - @pwb_lib.MBX147	22D8	29C3											
	H_D#<58>	H_D#<58> - @pwb_lib.MBX147	6D2	9D8	IDE_RST_5#	IDE_RST_5# - @pwb_lib.MBX147	27B3	27D1 29C5											
	H_D#<59>	H_D#<59> - @pwb_lib.MBX147	6D2	9D8	IDSELCB	IDSELCB - @pwb_lib.MBX147	25D6	25D6											
B	H_D#<60>	H_D#<60> - @pwb_lib.MBX147	6D2	9D8	IMVP_OTP_1	IMVP_OTP_1 - @pwb_lib.MBX147	37B2	37C8 37E2											
	H_D#<61>	H_D#<61> - @pwb_lib.MBX147	6D2	9D8	IMVP_OTP_2	IMVP_OTP_2 - @pwb_lib.MBX147	37B2	37C8 37D2											
	H_D#<62>	H_D#<62> - @pwb_lib.MBX147	6D2	9D8	IMVP_PWRGD	IMVP_PWRGD - @pwb_lib.MBX147	22D1	27A3 37A6 37C6											
	H_D#<63>	H_D#<63> - @pwb_lib.MBX147	6D2	9D8	INTVRMEN	INTVRMEN - @pwb_lib.MBX147	22A8	22C6											
	H_DBSY#	H_DBSY# - @pwb_lib.MBX147	6E6	9D6	INT_GFX_VGA_BLU	INT_GFX_VGA_BLU - @pwb_lib.MBX147	13C7	14C4											
	H_DEFER#	H_DEFER# - @pwb_lib.MBX147	6E6	9D6	INT_GFX_VGA_DDCCLK	INT_GFX_VGA_DDCCLK - @pwb_lib.MBX14	13C6	14A4											
	H_DINV#<0>	H_DINV#<0> - @pwb_lib.MBX147	6D4	9D6	INT_GFX_VGA_DDCDATA	INT_GFX_VGA_DDCDATA - @pwb_lib.MBX1	13C6	14A4											
	H_DINV#<1>	H_DINV#<1> - @pwb_lib.MBX147	6D4	9D6	47														
	H_DINV#<2>	H_DINV#<2> - @pwb_lib.MBX147	6D2	9D6	INT_GFX_VGA_GRN	INT_GFX_VGA_GRN - @pwb_lib.MBX147	13C7	14C4											
	H_DINV#<3>	H_DINV#<3> - @pwb_lib.MBX147	6D2	9D6	INT_GFX_VGA_HSYNC	INT_GFX_VGA_HSYNC - @pwb_lib.MBX147	13C6	14B4											
A	H_DPRSTP#	H_DPRSTP# - @pwb_lib.MBX147	6C2	22D4	INT_GFX_VGA_RED	INT_GFX_VGA_RED - @pwb_lib.MBX147	13C7	14B4											
	H_DPSLP#	H_DPSLP# - @pwb_lib.MBX147	6C2	22D4	INT_GFX_VGA_VSYNC	INT_GFX_VGA_VSYNC - @pwb_lib.MBX147	13C6	14B4											
	H_DPWR#	H_DPWR# - @pwb_lib.MBX147	6C2	9D6	INT_IDEIRQ	INT_IDEIRQ - @pwb_lib.MBX147	21B2	22D8 29C4											
	H_DRDY#	H_DRDY# - @pwb_lib.MBX147	6E6	9D6	INT_LCD_BKLCTR	INT_LCD_BKLCTR - @pwb_lib.MBX147	13E7	16E3											
	H_DSTB0#+	H_DSTB0#+ - @pwb_lib.MBX147	6D4	9D6	INT_LCD_BKLEN	INT_LCD_BKLEN - @pwb_lib.MBX147	13E7	16E3											
	H_DSTB1#+	H_DSTB1#+ - @pwb_lib.MBX147	6D4	9D6	INT_LCD_PNLEN	INT_LCD_PNLEN - @pwb_lib.MBX147	13E7	16E3											
	H_DSTB1#-	H_DSTB1#- - @pwb_lib.MBX147	6D4	9D6	INT_LVDS_ACLK+	INT_LVDS_ACLK+ - @pwb_lib.MBX147	13D6	14E4											
	H_DSTB2#+	H_DSTB2#+ - @pwb_lib.MBX147	6D2	9D6	INT_LVDS_ACLK-	INT_LVDS_ACLK- - @pwb_lib.MBX147	13D6	14E4											
	H_DSTB2#-	H_DSTB2#- - @pwb_lib.MBX147	6D2	9D6	INT_LVDS_ADATA0+	INT_LVDS_ADATA0+ - @pwb_lib.MBX147	13D6	14F4											
	H_DSTB3#+	H_DSTB3#+ - @pwb_lib.MBX147	6D2	9D6	INT_LVDS_ADATA0-	INT_LVDS_ADATA0- - @pwb_lib.MBX147	13D6	14F4											
	H_DSTB3#-	H_DSTB3#- - @pwb_lib.MBX147	6D2	9D6	INT_LVDS_ADATA1+	INT_LVDS_ADATA1+ - @pwb_lib.MBX147	13D6	14E4											
	H_FERR#	H_FERR# - @pwb_lib.MBX147	6C8	22C5	INT_LVDS_ADATA1-	INT_LVDS_ADATA1- - @pwb_lib.MBX147	13D6	14E4											
	H_HIT#	H_HIT# - @pwb_lib.MBX147	6D6	9D6	INT_LVDS_ADATA2+	INT_LVDS_ADATA2+ - @pwb_lib.MBX147	13D6	14E4											
	H_HITM#	H_HITM# - @pwb_lib.MBX147	6D6	9D6	INT_LVDS_ADATA2-	INT_LVDS_ADATA2- - @pwb_lib.MBX147	13D6	14E4											
	H_IGNNE#	H_IGNNE# - @pwb_lib.MBX147	6C8	22D4	INT_PIRQA#	INT_PIRQA# - @pwb_lib.MBX147	21D2	21D3											
	H_INIT#	H_INIT# - @pwb_lib.MBX147	6E6	22C4	INT_PIRQB#	INT_PIRQB# - @pwb_lib.MBX147	21D2	21D3											
	H_INTR	H_INTR - @pwb_lib.MBX147	6C8	22C4	INT_PIRQC#	INT_PIRQC# - @pwb_lib.MBX147	21D2	21D3											
	H_LOCK#	H_LOCK# - @pwb_lib.MBX147	6D6	9D6	INT_PIRQD#	INT_PIRQD# - @pwb_lib.MBX147	21D2	21D3											
	H_NMI	H_NMI - @pwb_lib.MBX147	6C8	22C4	INT_PIRQE#	INT_PIRQE# - @pwb_lib.MBX147	21C2	21D3 25C6											
	H_PROCHOT#	H_PROCHOT# - @pwb_lib.MBX147	6D5	6F3 37B6	INT_PIRQF#	INT_PIRQF# - @pwb_lib.MBX147	21C2	21D3 24D8											
	H_PROCHOT_EC#	H_PROCHOT_EC# - @pwb_lib.MBX147	6F5	27A6	INT_PIRQG#	INT_PIRQG# - @pwb_lib.MBX147	21C2	21D3 24D4											
	H_PWRGDCPU	H_PWRGDCPU - @pwb_lib.MBX147	6C1	22D1	INT_PIRQH#	INT_PIRQH# - @pwb_lib.MBX147	21C2	21D3											
	H_REQ#<0>	H_REQ#<0> - @pwb_lib.MBX147	6D8	9C6	INT_SERIRQ	INT_SERIRQ - @pwb_lib.MBX147	21B2	22C4 25C6 27E8 35B4											
	H_REQ#<1>	H_REQ#<1> - @pwb_lib.MBX147	6D8	9C6	49E6														
	H_REQ#<2>	H_REQ#<2> - @pwb_lib.MBX147	6D8	9C6	ISENSE	ISENSE - @pwb_lib.MBX147	45C6	45D4											
	H_REQ#<3>	H_REQ#<3> - @pwb_lib.MBX147	6D8	9C6	IS_OUT	IS_OUT - @pwb_lib.MBX147	45E4	47C7											
	H_REQ#<4>	H_REQ#<4> - @pwb_lib.MBX147	6D8	9C6	ITP_BPM#<0>	ITP_BPM#<0> - @pwb_lib.MBX147	6D6	49B7											
	H_RS#<0>	H_RS#<0> - @pwb_lib.MBX147	6D6	9C6	ITP_BPM#<1>	ITP_BPM#<1> - @pwb_lib.MBX147	6D6	49B7											
	H_RS#<1>	H_RS#<1> - @pwb_lib.MBX147	6D6	9C6	ITP_BPM#<2>	ITP_BPM#<2> - @pwb_lib.MBX147	6D6	49B7											
	H_RS#<2>	H_RS#<2> - @pwb_lib.MBX147	6D6	9C6	ITP_BPM#<3>	ITP_BPM#<3> - @pwb_lib.MBX147	6D6	49B7											
	H_RST_CPU#	H_RST_CPU# - @pwb_lib.MBX147	22C4	27D8	ITP_BPM#<4>	ITP_BPM#<4> - @pwb_lib.MBX147	6D6	49B7											
	H_SMI#	H_SMI# - @pwb_lib.MBX147	6C8	22C4	ITP_BPM#<5>	ITP_BPM#<5> - @pwb_lib.MBX147	6D6	49B7											
	H_STPCLK#	H_STPCLK# - @pwb_lib.MBX147	6C8	22C4	ITP_DBRST#	ITP_DBRST# - @pwb_lib.MBX147	6D5	22D1 49B7											
	H_TRDY#	H_TRDY# - @pwb_lib.MBX147	6D6	9C6	ITP_TCK	ITP_TCK - @pwb_lib.MBX147	6D5	49B7 49B7											
	H_VID<0>	H_VID<0> - @pwb_lib.MBX147	7C6	37F8	ITP_TDI	ITP_TDI - @pwb_lib.MBX147	6D5	49B7											
	H_VID<1>	H_VID<1> - @pwb_lib.MBX147	7C6	37E8	ITP_TDO	ITP_TDO - @pwb_lib.MBX147	6D5	49B7											
	H_VID<2>	H_VID<2> - @pwb_lib.MBX147	7C6	37E8	ITP_TMS	ITP_TMS - @pwb_lib.MBX147	6D5	49B5											
	H_VID<3>	H_VID<3> - @pwb_lib.MBX147	7C6	37E8	ITP_TRST#	ITP_TRST# - @pwb_lib.MBX147	6D5	49B7											
	H_VID<4>	H_VID<4> - @pwb_lib.MBX147	7C6	37E8	KSI<0>	KSI<0> - @pwb_lib.MBX147	27E1	28D5											
	H_VID<5>	H_VID<5> - @pwb_lib.MBX147	7C6	37E8	KSI<1>	KSI<1> - @pwb_lib.MBX147	27E1	28D5											
	H_VID<6>	H_VID<6> - @pwb_lib.MBX147	7C6	37E8	KSI<2>	KSI<2> - @pwb_lib.MBX147	27E1	28D5											
	SONY CORPORATION												-						
NETWORK COMPANY												-							
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8		7		6		5		4		3		2		1		
												REVISIONS				
												ZONE	LTR	DESCRIPTION	DATE	APPR.
F	KSI<3>	KSI<3> - @pwb_lib.MBX147	27E1	28D5		M_A_DM<7>	M_A_DM<7> - @pwb_lib.MBX147	10D5	19B7							
	KSI<4>	KSI<4> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<0>	M_A_DQ<0> - @pwb_lib.MBX147	10E7	19E7							
	KSI<5>	KSI<5> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<1>	M_A_DQ<1> - @pwb_lib.MBX147	10E7	19E7							
	KSI<6>	KSI<6> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<2>	M_A_DQ<2> - @pwb_lib.MBX147	10D7	19E7							
	KSI<7>	KSI<7> - @pwb_lib.MBX147	27E1	28D5		M_A_DQ<3>	M_A_DQ<3> - @pwb_lib.MBX147	10D7	19E7							
	KSO<0>	KSO<0> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<4>	M_A_DQ<4> - @pwb_lib.MBX147	10D7	19E5							
	KSO<1>	KSO<1> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<5>	M_A_DQ<5> - @pwb_lib.MBX147	10D7	19E5							
	KSO<2>	KSO<2> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<6>	M_A_DQ<6> - @pwb_lib.MBX147	10D7	19E5							
	KSO<3>	KSO<3> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<7>	M_A_DQ<7> - @pwb_lib.MBX147	10D7	19E5							
	KSO<4>	KSO<4> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<8>	M_A_DQ<8> - @pwb_lib.MBX147	10D7	19E7							
	KSO<5>	KSO<5> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<9>	M_A_DQ<9> - @pwb_lib.MBX147	10D7	19E7							
	KSO<6>	KSO<6> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<10>	M_A_DQ<10> - @pwb_lib.MBX147	10D7	19E7							
	KSO<7>	KSO<7> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<11>	M_A_DQ<11> - @pwb_lib.MBX147	10D7	19E7							
	KSO<8>	KSO<8> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<12>	M_A_DQ<12> - @pwb_lib.MBX147	10D7	19E5							
E	KSO<9>	KSO<9> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<13>	M_A_DQ<13> - @pwb_lib.MBX147	10D7	19E5							
	KSO<10>	KSO<10> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<14>	M_A_DQ<14> - @pwb_lib.MBX147	10D7	19E5							
	KSO<11>	KSO<11> - @pwb_lib.MBX147	27B6	28D5		M_A_DQ<15>	M_A_DQ<15> - @pwb_lib.MBX147	10D7	19E5							
	KSO<12>	KSO<12> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<16>	M_A_DQ<16> - @pwb_lib.MBX147	10D7	19D7							
	KSO<13>	KSO<13> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<17>	M_A_DQ<17> - @pwb_lib.MBX147	10D7	19D7							
	KSO<14>	KSO<14> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<18>	M_A_DQ<18> - @pwb_lib.MBX147	10D7	19D7							
	KSO<15>	KSO<15> - @pwb_lib.MBX147	27A6	28D5		M_A_DQ<19>	M_A_DQ<19> - @pwb_lib.MBX147	10D7	19D7							
	LCD_BKLCTR	LCD_BKLCTR - @pwb_lib.MBX147	16E1	30B3	30E3	M_A_DQ<20>	M_A_DQ<20> - @pwb_lib.MBX147	10D7	19D5							
	LCD_BKLEN	LCD_BKLEN - @pwb_lib.MBX147	16E1	30E8		M_A_DQ<21>	M_A_DQ<21> - @pwb_lib.MBX147	10D7	19D5							
	LCD_PNLEN	LCD_PNLEN - @pwb_lib.MBX147	16E1	30C8		M_A_DQ<22>	M_A_DQ<22> - @pwb_lib.MBX147	10D7	19D5							
	LED_BATT1#	LED_BATT1# - @pwb_lib.MBX147	27D8	36D8		M_A_DQ<23>	M_A_DQ<23> - @pwb_lib.MBX147	10D7	19D5							
	LED_BRIGHTNESS	LED_BRIGHTNESS - @pwb_lib.MBX147	27E1	36B3		M_A_DQ<24>	M_A_DQ<24> - @pwb_lib.MBX147	10D7	19D7							
	LED_CAPS#	LED_CAPS# - @pwb_lib.MBX147	27D8	36E8		M_A_DQ<25>	M_A_DQ<25> - @pwb_lib.MBX147	10D7	19D7							
	LED_DOCK_IN_USE#	LED_DOCK_IN_USE# - @pwb_lib.MBX147	27B3	34C7		M_A_DQ<26>	M_A_DQ<26> - @pwb_lib.MBX147	10D7	19D7							
D	LED_GFX_EXT#	LED_GFX_EXT# - @pwb_lib.MBX147	27B3	36B8		M_A_DQ<27>	M_A_DQ<27> - @pwb_lib.MBX147	10D7	19D7							
	LED_GFX_INT#	LED_GFX_INT# - @pwb_lib.MBX147	27B3	36B8		M_A_DQ<28>	M_A_DQ<28> - @pwb_lib.MBX147	10D7	19D5							
	LED_MS	LED_MS - @pwb_lib.MBX147	24D1	28D4		M_A_DQ<29>	M_A_DQ<29> - @pwb_lib.MBX147	10C7	19D5							
	LED_NUM#	LED_NUM# - @pwb_lib.MBX147	27D8	36E8		M_A_DQ<30>	M_A_DQ<30> - @pwb_lib.MBX147	10C7	19D5							
	LED_PWR#	LED_PWR# - @pwb_lib.MBX147	27D8	36D8		M_A_DQ<31>	M_A_DQ<31> - @pwb_lib.MBX147	10C7	19D5							
	LED_SATA#	LED_SATA# - @pwb_lib.MBX147	22C6	36D8		M_A_DQ<32>	M_A_DQ<32> - @pwb_lib.MBX147	10C7	19C7							
	LED_SCRL#	LED_SCRL# - @pwb_lib.MBX147	27D8	36E8		M_A_DQ<33>	M_A_DQ<33> - @pwb_lib.MBX147	10C7	19C7							
	LED_STNDBY#	LED_STNDBY# - @pwb_lib.MBX147	27D8	36D8		M_A_DQ<34>	M_A_DQ<34> - @pwb_lib.MBX147	10C7	19C7							
	LED_WLAN#	LED_WLAN# - @pwb_lib.MBX147	33C3	36D8		M_A_DQ<35>	M_A_DQ<35> - @pwb_lib.MBX147	10C7	19C7							
	LINKALERT#	LINKALERT# - @pwb_lib.MBX147	21D1	22D6		M_A_DQ<36>	M_A_DQ<36> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<0>	LPC_AD<0> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<37>	M_A_DQ<37> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<1>	LPC_AD<1> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<38>	M_A_DQ<38> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<2>	LPC_AD<2> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<39>	M_A_DQ<39> - @pwb_lib.MBX147	10C7	19C5							
	LPC_AD<3>	LPC_AD<3> - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<40>	M_A_DQ<40> - @pwb_lib.MBX147	10C7	19C7							
C	LPC_DRQ0#	LPC_DRQ0# - @pwb_lib.MBX147	22E5	49E6		M_A_DQ<41>	M_A_DQ<41> - @pwb_lib.MBX147	10C7	19C7							
	LPC_FRAME#	LPC_FRAME# - @pwb_lib.MBX147	22E5	27E8	35B4 49E6	M_A_DQ<42>	M_A_DQ<42> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ACLK+	LVDS_ACLK+ - @pwb_lib.MBX147	14E1	30E5		M_A_DQ<43>	M_A_DQ<43> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ACLK-	LVDS_ACLK- - @pwb_lib.MBX147	14E1	30E5		M_A_DQ<44>	M_A_DQ<44> - @pwb_lib.MBX147	10C7	19C5							
	LVDS_ADATA0+	LVDS_ADATA0+ - @pwb_lib.MBX147	14F1	30D5		M_A_DQ<45>	M_A_DQ<45> - @pwb_lib.MBX147	10C7	19C5							
	LVDS_ADATA0-	LVDS_ADATA0- - @pwb_lib.MBX147	14F1	30D5		M_A_DQ<46>	M_A_DQ<46> - @pwb_lib.MBX147	10C7	19B5							
	LVDS_ADATA1+	LVDS_ADATA1+ - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<47>	M_A_DQ<47> - @pwb_lib.MBX147	10C7	19B5							
	LVDS_ADATA1-	LVDS_ADATA1- - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<48>	M_A_DQ<48> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ADATA2+	LVDS_ADATA2+ - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<49>	M_A_DQ<49> - @pwb_lib.MBX147	10C7	19B7							
	LVDS_ADATA2-	LVDS_ADATA2- - @pwb_lib.MBX147	14E1	30D5		M_A_DQ<50>	M_A_DQ<50> - @pwb_lib.MBX147	10C7	19B7							
	MCH_ICH_SYNC#	MCH_ICH_SYNC# - @pwb_lib.MBX147	9D4	22D1		M_A_DQ<51>	M_A_DQ<51> - @pwb_lib.MBX147	10C7	19B7							
	MDL_ID<0>	MDL_ID<0> - @pwb_lib.MBX147	22E1	22E1		M_A_DQ<52>	M_A_DQ<52> - @pwb_lib.MBX147	10C7	19B5							
	MDL_ID<1>	MDL_ID<1> - @pwb_lib.MBX147	22E1	22E1		M_A_DQ<53>	M_A_DQ<53> - @pwb_lib.MBX147	10C7	19B5							
	MNCD_RF_OFF#	MNCD_RF_OFF# - @pwb_lib.MBX147	27A6	33D3		M_A_DQ<54>	M_A_DQ<54> - @pwb_lib.MBX147	10C7	19B5							
B	MODEL_ID_EC<0>	MODEL_ID_EC<0> - @pwb_lib.MBX147	27B3	27D1		M_A_DQ<55>	M_A_DQ<55> - @pwb_lib.MBX147	10C7	19B5							
	MSK_FPBACK_5#	MSK_FPBACK_5# - @pwb_lib.MBX147	27B3	27D1	30E8	M_A_DQ<56>	M_A_DQ<56> - @pwb_lib.MBX14									

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	M_B_A<9>	M_B_A<9> - @pwb_lib.MBX147	10C2	19D4	20D4					M_B_DQS5-	M_B_DQS5- - @pwb_lib.MBX147	10D2	19C1					
	M_B_A<10>	M_B_A<10> - @pwb_lib.MBX147	10C2	19C4	20C4					M_B_DQS6+	M_B_DQS6+ - @pwb_lib.MBX147	10D2	19B4					
	M_B_A<11>	M_B_A<11> - @pwb_lib.MBX147	10C2	19D1	20D4					M_B_DQS6-	M_B_DQS6- - @pwb_lib.MBX147	10D2	19B4					
	M_B_A<12>	M_B_A<12> - @pwb_lib.MBX147	10C2	19D4	20D4					M_B_DQS7+	M_B_DQS7+ - @pwb_lib.MBX147	10D2	19B1					
	M_B_A<13>	M_B_A<13> - @pwb_lib.MBX147	10C2	19C1	20C4					M_B_DQS7-	M_B_DQS7- - @pwb_lib.MBX147	10C2	19B1					
	M_B_BS<0>	M_B_BS<0> - @pwb_lib.MBX147	10E2	19C4	20C4					M_B_RAS#	M_B_RAS# - @pwb_lib.MBX147	10C2	19C1	20C4				
	M_B_BS<1>	M_B_BS<1> - @pwb_lib.MBX147	10E2	19C1	20C4					M_B_WE#	M_B_WE# - @pwb_lib.MBX147	10C2	19C4	20C4				
	M_B_BS<2>	M_B_BS<2> - @pwb_lib.MBX147	10D2	19D4	20D4					M_CKE0	M_CKE0 - @pwb_lib.MBX147	9E2	19D7	20C6				
	M_B_CAS#	M_B_CAS# - @pwb_lib.MBX147	10D2	19C4	20C4					M_CKE1	M_CKE1 - @pwb_lib.MBX147	9E2	19D5	20C6				
	M_B_DM<0>	M_B_DM<0> - @pwb_lib.MBX147	10D2	19E1						M_CKE2	M_CKE2 - @pwb_lib.MBX147	9E2	19D4	20D4				
	M_B_DM<1>	M_B_DM<1> - @pwb_lib.MBX147	10D2	19E1						M_CKE3	M_CKE3 - @pwb_lib.MBX147	9E2	19D1	20D4				
	M_B_DM<2>	M_B_DM<2> - @pwb_lib.MBX147	10D2	19D1						M_CLK0+	M_CLK0+ - @pwb_lib.MBX147	9F2	19E5					
	M_B_DM<3>	M_B_DM<3> - @pwb_lib.MBX147	10D2	19D4						M_CLK0-	M_CLK0- - @pwb_lib.MBX147	9F2	19E5					
	M_B_DM<4>	M_B_DM<4> - @pwb_lib.MBX147	10D2	19C1						M_CLK1+	M_CLK1+ - @pwb_lib.MBX147	9F2	19B5					
	M_B_DM<5>	M_B_DM<5> - @pwb_lib.MBX147	10D2	19C4						M_CLK1-	M_CLK1- - @pwb_lib.MBX147	9F2	19B5					
	M_B_DM<6>	M_B_DM<6> - @pwb_lib.MBX147	10D2	19B1						M_CLK2+	M_CLK2+ - @pwb_lib.MBX147	9F2	19B1					
	M_B_DM<7>	M_B_DM<7> - @pwb_lib.MBX147	10D2	19B4						M_CLK2-	M_CLK2- - @pwb_lib.MBX147	9F2	19B1					
	M_B_DQ<0>	M_B_DQ<0> - @pwb_lib.MBX147	10B7	10E4	19E4					M_CLK3+	M_CLK3+ - @pwb_lib.MBX147	9F2	19E1					
	M_B_DQ<1>	M_B_DQ<1> - @pwb_lib.MBX147	10E4	19E4						M_CLK3-	M_CLK3- - @pwb_lib.MBX147	9F2	19E1					
	M_B_DQ<2>	M_B_DQ<2> - @pwb_lib.MBX147	10D4	19E4						M_CS#0	M_CS#0 - @pwb_lib.MBX147	9E2	19C5	20D6				
	M_B_DQ<3>	M_B_DQ<3> - @pwb_lib.MBX147	10D4	19E4						M_CS#1	M_CS#1 - @pwb_lib.MBX147	9E2	19C7	20D6				
	M_B_DQ<4>	M_B_DQ<4> - @pwb_lib.MBX147	10D4	19E1						M_CS#2	M_CS#2 - @pwb_lib.MBX147	9E2	19C1	20C4				
	M_B_DQ<5>	M_B_DQ<5> - @pwb_lib.MBX147	10D4	19E1						M_CS#3	M_CS#3 - @pwb_lib.MBX147	9E2	19C4	20C4				
	M_B_DQ<6>	M_B_DQ<6> - @pwb_lib.MBX147	10D4	19E1						M_ODT0	M_ODT0 - @pwb_lib.MBX147	9E2	19C5	20D6				
	M_B_DQ<7>	M_B_DQ<7> - @pwb_lib.MBX147	10D4	19E1						M_ODT1	M_ODT1 - @pwb_lib.MBX147	9E2	19C7	20D6				
	M_B_DQ<8>	M_B_DQ<8> - @pwb_lib.MBX147	10D4	19E4						M_ODT2	M_ODT2 - @pwb_lib.MBX147	9E2	19C1	20C4				
	M_B_DQ<9>	M_B_DQ<9> - @pwb_lib.MBX147	10D4	19E4						M_ODT3	M_ODT3 - @pwb_lib.MBX147	9E2	19C4	20C4				
	M_B_DQ<10>	M_B_DQ<10> - @pwb_lib.MBX147	10D4	19E4						M_RCOMP	M_RCOMP - @pwb_lib.MBX147	9A5	9E2					
	M_B_DQ<11>	M_B_DQ<11> - @pwb_lib.MBX147	10D4	19E4						M_RCOMP	M_RCOMP - @pwb_lib.MBX147	9A5	9E2					
	M_B_DQ<12>	M_B_DQ<12> - @pwb_lib.MBX147	10D4	19E1						OPT_OFF#	OPT_OFF# - @pwb_lib.MBX147	27A6	29D5					
	M_B_DQ<13>	M_B_DQ<13> - @pwb_lib.MBX147	10D4	19E1						OTP_IN	OTP_IN - @pwb_lib.MBX147	7B3	37B1	48E8				
	M_B_DQ<14>	M_B_DQ<14> - @pwb_lib.MBX147	10D4	19E1						PANEL_ID<0>	PANEL_ID<0> - @pwb_lib.MBX147	21B2	22C6	30C3				
	M_B_DQ<15>	M_B_DQ<15> - @pwb_lib.MBX147	10D4	19E1						PANEL_ID<1>	PANEL_ID<1> - @pwb_lib.MBX147	21B2	22D1	30D3				
	M_B_DQ<16>	M_B_DQ<16> - @pwb_lib.MBX147	10D4	19D4						PANEL_ID<2>	PANEL_ID<2> - @pwb_lib.MBX147	21B2	22D1	30D3				
	M_B_DQ<17>	M_B_DQ<17> - @pwb_lib.MBX147	10D4	19D4						PCIE_DOCK1_RX+	PCIE_DOCK1_RX+ - @pwb_lib.MBX147	21D8	34D6					
	M_B_DQ<18>	M_B_DQ<18> - @pwb_lib.MBX147	10D4	19D4						PCIE_DOCK1_RX-	PCIE_DOCK1_RX- - @pwb_lib.MBX147	21D8	34D6					
	M_B_DQ<19>	M_B_DQ<19> - @pwb_lib.MBX147	10D4	19D4						PCIE_DOCK1_TX+	PCIE_DOCK1_TX+ - @pwb_lib.MBX147	21D8	34C6					
	M_B_DQ<20>	M_B_DQ<20> - @pwb_lib.MBX147	10D4	19D1						PCIE_DOCK1_TX-	PCIE_DOCK1_TX- - @pwb_lib.MBX147	21D8	34C6					
	M_B_DQ<21>	M_B_DQ<21> - @pwb_lib.MBX147	10D4	19D1						PCIE_EXCD_RX+	PCIE_EXCD_RX+ - @pwb_lib.MBX147	21E8	32C1					
	M_B_DQ<22>	M_B_DQ<22> - @pwb_lib.MBX147	10D4	19D1						PCIE_EXCD_RX-	PCIE_EXCD_RX- - @pwb_lib.MBX147	21E8	32C1					
	M_B_DQ<23>	M_B_DQ<23> - @pwb_lib.MBX147	10D4	19D1						PCIE_EXCD_TX+	PCIE_EXCD_TX+ - @pwb_lib.MBX147	21D8	32C5					
	M_B_DQ<24>	M_B_DQ<24> - @pwb_lib.MBX147	10D4	19D4						PCIE_EXCD_TX-	PCIE_EXCD_TX- - @pwb_lib.MBX147	21E8	32C5					
	M_B_DQ<25>	M_B_DQ<25> - @pwb_lib.MBX147	10D4	19D4						PCIE_LAN_RX+	PCIE_LAN_RX+ - @pwb_lib.MBX147	21D8	26D7					
	M_B_DQ<26>	M_B_DQ<26> - @pwb_lib.MBX147	10D4	19D4						PCIE_LAN_RX-	PCIE_LAN_RX- - @pwb_lib.MBX147	21D8	26D7					
	M_B_DQ<27>	M_B_DQ<27> - @pwb_lib.MBX147	10D4	19D4						PCIE_LAN_TX+	PCIE_LAN_TX+ - @pwb_lib.MBX147	21D8	26C7					
	M_B_DQ<28>	M_B_DQ<28> - @pwb_lib.MBX147	10D4	19D1						PCIE_LAN_TX-	PCIE_LAN_TX- - @pwb_lib.MBX147	21D8	26D7					
	M_B_DQ<29>	M_B_DQ<29> - @pwb_lib.MBX147	10C4	19D1						PCIE_MNCD_RX+	PCIE_MNCD_RX+ - @pwb_lib.MBX147	21D8	33D7					
	M_B_DQ<30>	M_B_DQ<30> - @pwb_lib.MBX147	10C4	19D1						PCIE_MNCD_RX-	PCIE_MNCD_RX- - @pwb_lib.MBX147	21D8	33D7					
	M_B_DQ<31>	M_B_DQ<31> - @pwb_lib.MBX147	10C4	19D1						PCIE_MNCD_TX+	PCIE_MNCD_TX+ - @pwb_lib.MBX147	21D8	33C7					
	M_B_DQ<32>	M_B_DQ<32> - @pwb_lib.MBX147	10C4	19C4						PCIE_MNCD_TX-	PCIE_MNCD_TX- - @pwb_lib.MBX147	21D8	33D7					
	M_B_DQ<33>	M_B_DQ<33> - @pwb_lib.MBX147	10C4	19C4						PCIE_WAKE#	PCIE_WAKE# - @pwb_lib.MBX147	22B8	26C7	32C5	33D6	34C6		
	M_B_DQ<34>	M_B_DQ<34> - @pwb_lib.MBX147	10C4	19C4						PCIE_WAKE_F#	PCIE_WAKE_F# - @pwb_lib.MBX147	21B1	22B7	22D1	22E1			
	M_B_DQ<35>	M_B_DQ<35> - @pwb_lib.MBX147	10C4	19C4						PCI_AD<0>	PCI_AD<0> - @pwb_lib.MBX147	21E1	25E6					
	M_B_DQ<36>	M_B_DQ<36> - @pwb_lib.MBX147	10C4	19C1						PCI_AD<1>	PCI_AD<1> - @pwb_lib.MBX147	21E1	25E6					
	M_B_DQ<37>	M_B_DQ<37> - @pwb_lib.MBX147	10C4	19C1						PCI_AD<2>	PCI_AD<2> - @pwb_lib.MBX147	21E1	25E6					
	M_B_DQ<38>	M_B_DQ<38> - @pwb_lib.MBX147	10C4	19C1						PCI_AD<3>	PCI_AD<3> - @pwb_lib.MBX147	21E1	25E6					
	M_B_DQ<39>	M_B_DQ<39> - @pwb_lib.MBX147	10C4	19C1						PCI_AD<4>	PCI_AD<4> - @pwb_lib.MBX147	21E1	25E6					
	M_B_DQ<40>	M_B_DQ<40> - @pwb_lib.MBX147	10C4	19C4						PCI_AD<5>	PCI_AD<5> - @pwb_lib.MBX147	21E1	25E6					
	M_B_DQ<41>	M_B_DQ<41> - @pwb_lib.MBX147	10C4	19C4						PCI_AD<6>	PCI_AD<6> - @pwb_lib.MBX							

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C1 C_0603 mbx147[6C6] C2 C_0603 mbx147[6C5] C50 C_1005 mbx147[6E5] C52 C_1005 mbx147[6F5] C60 C_2012 mbx147[6B6] C100 C_2012 mbx147[7E3] C101 C_2012 mbx147[7E3] C102 C_2012 mbx147[7E3] C103 C_2012 mbx147[7E2] C104 C_2012 mbx147[7E2] C105 C_2012 mbx147[7E2] C106 C_2012 mbx147[7E2] C107 C_2012 mbx147[7E2] C108 C_2012 mbx147[7E1] C109 C_2012 mbx147[7E1] C110 C_2012 mbx147[7E3] C111 C_2012 mbx147[7E3] C112 C_2012 mbx147[7E3] C113 C_2012 mbx147[7E2] C114 C_2012 mbx147[7E2] C115 C_2012 mbx147[7E2] C116 C_2012 mbx147[7E2] C117 C_2012 mbx147[7E2] C118 C_2012 mbx147[7E1] C119 C_2012 mbx147[7E1] C120 C_2012 mbx147[7E3] C121 C_2012 mbx147[7E3] C122 C_2012 mbx147[7E3] C123 C_2012 mbx147[7E2] C124 C_2012 mbx147[7E2] C125 C_2012 mbx147[7E2] C150 CE_3528 mbx147[7C3] C151 C_0603 mbx147[7C3] C152 C_0603 mbx147[7C2] C153 C_0603 mbx147[7C2] C154 C_0603 mbx147[7C2] C155 C_0603 mbx147[7C2] C156 C_0603 mbx147[7C2] C160 C_2012 mbx147[7C6] C161 C_1005 mbx147[7C6] C210 C_0603 mbx147[8E6] C211 C_0603 mbx147[8E6] C212 C_0603 mbx147[8E6] C213 C_0603 mbx147[8E6] C214 C_1005 mbx147[8E5] C216 C_0603 mbx147[8E5] C217 C_1005 mbx147[8E4] C220 C_1005 mbx147[8D7] C221 C_1005 mbx147[8D7] C301 C_0603 mbx147[9C8] C302 C_0603 mbx147[9C8] C307 C_0603 mbx147[9D6] C321 C_0603 mbx147[9B7] C322 C_0603 mbx147[9A7] C340 C_1005 mbx147[9D2] C341 C_1005 mbx147[9D2] C342 C_0603 mbx147[9D2] C343 C_0603 mbx147[9D2] C344 C_1005 mbx147[9D2] C345 C_1005 mbx147[9D2] C346 C_1005 mbx147[9E1] C347 C_1005 mbx147[9E2] C348 C_1005 mbx147[9E2] C400 C_0603 mbx147[11F4] C401 C_2012 mbx147[11C4] C402 C_0603 mbx147[11C4] C403 C_0603 mbx147[11D6] C404 C_1005 mbx147[11D6] C405 C_1608 mbx147[11F4] C410 C_2012 mbx147[11E7] C411 C_0603 mbx147[11F4] C412 C_0603 mbx147[11E7] C413 C_1005 mbx147[11E7] C414 C_2012 mbx147[11F4] C420 CE_3528 mbx147[11E5] C421 C_2012 mbx147[11E5] C422 C_2012 mbx147[11E5] C423 C_0603 mbx147[11E5] C424 C_2012 mbx147[11E6] C425 C_0603 mbx147[11E6] C426 C_0603 mbx147[11E6] C430 C_0603 mbx147[11C4] C431 C_1005 mbx147[11C4] C436 C_2012 mbx147[11D4] C451 CE_3528 mbx147[11D7] C452 C_0603 mbx147[11D7] C453 CE_3528 mbx147[11D7]																		C454 C_0603 mbx147[11D7]	C455 C_2012 mbx147[11C7]	C456 C_0603 mbx147[11C7]	C457 C_2012 mbx147[11C7]	C458 C_0603 mbx147[11C7]	C461 C_0603 mbx147[11B4]	C462 C_1608 mbx147[11E2]	C463 C_1608 mbx147[11E2]	C464 C_1005 mbx147[11C2]	C465 C_1005 mbx147[11C2]	C466 C_1005 mbx147[11C2]	C468 CE_3528 mbx147[11E2]	C471 C_2012 mbx147[11C4]	C472 C_0603 mbx147[11C4]	C475 C_2012 mbx147[12F6]	C476 C_2012 mbx147[12F5]	C477 CE_3528 mbx147[12F5]	C478 C_1005 mbx147[12D6]	C479 C_1005 mbx147[12C6]	C480 C_2012 mbx147[12E8]	C481 C_2012 mbx147[12E8]	C482 C_2012 mbx147[12E8]	C483 C_0603 mbx147[12E8]	C484 C_0603 mbx147[12E7]	C485 C_0603 mbx147[12E7]	C486 C_2012 mbx147[12F6]	C487 C_2012 mbx147[12F6]	C489 C_1005 mbx147[12E6]	C490 C_1005 mbx147[12E6]	C491 C_1005 mbx147[12D6]	C492 C_1005 mbx147[12C6]	C493 C_1005 mbx147[12A6]	C494 C_1005 mbx147[12A6]	C495 CE_3528 mbx147[12E8]	C496 C_0603 mbx147[12D6]	C497 C_0603 mbx147[12D6]	C498 C_0603 mbx147[12D5]	C500 C_0603 mbx147[13C4]	C501 C_0603 mbx147[13D4]	C502 C_0603 mbx147[13C4]	C503 C_0603 mbx147[13D4]	C504 C_0603 mbx147[13C4]	C505 C_0603 mbx147[13C4]	C506 C_0603 mbx147[13C4]	C507 C_0603 mbx147[13C4]	C508 C_0603 mbx147[13C4]	C509 C_0603 mbx147[13C4]	C510 C_0603 mbx147[13C4]	C511 C_0603 mbx147[13C4]	C512 C_0603 mbx147[13C4]	C513 C_0603 mbx147[13C4]	C514 C_0603 mbx147[13C4]	C515 C_0603 mbx147[13C4]	C516 C_0603 mbx147[13C4]	C517 C_0603 mbx147[13C4]	C518 C_0603 mbx147[13C4]	C519 C_0603 mbx147[13C4]	C520 C_0603 mbx147[13C4]	C521 C_0603 mbx147[13C4]	C522 C_0603 mbx147[13C4]	C523 C_0603 mbx147[13C4]	C524 C_0603 mbx147[13B4]	C525 C_0603 mbx147[13C4]	C526 C_0603 mbx147[13B4]	C527 C_0603 mbx147[13C4]	C528 C_0603 mbx147[13B4]	C529 C_0603 mbx147[13C4]	C530 C_0603 mbx147[13B4]	C531 C_0603 mbx147[13C4]	C602 C_1005 mbx147[14D8]	C603 C_1005 mbx147[14D8]	C604 C_1005 mbx147[14B5]	C606 C_1005 mbx147[14D4]	C607 C_1005 mbx147[14D4]	C610 C_0603 mbx147[14C7]	C611 C_0603 mbx147[14C7]	C612 C_0603 mbx147[14C7]	C613 C_0603 mbx147[14C7]	C614 C_0603 mbx147[14C7]	C615 C_0603 mbx147[14C7]	C616 C_0603 mbx147[14C7]	C617 C_0603 mbx147[14C7]	C618 C_0603 mbx147[14C7]	C619 C_0603 mbx147[14C7]	C620 C_0603 mbx147[14C7]	C621 C_0603 mbx147[14B7]	C622 C_0603 mbx147[14B7]	C623 C_0603 mbx147[14B7]	C624 C_0603 mbx147[14B7]	C625 C_0603 mbx147[14B7]	C630 C_0603 mbx147[14B7]	C631 C_0603 mbx147[14B7]
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F	C632	C_0603	mbx147[14B7]				C874	C_1608	mbx147[17C6]				REVISIONS						
	C633	C_0603	mbx147[14B7]				C875	C_1005	mbx147[17C6]				ZONE	LTR	DESCRIPTION	DATE	APPR.		
	C634	C_0603	mbx147[14B7]				C876	C_0603	mbx147[17C6]										
	C635	C_0603	mbx147[14B7]				C880	C_1608	mbx147[17C6]										
	C636	C_0603	mbx147[14B7]				C881	C_1608	mbx147[17C6]										
	C637	C_0603	mbx147[14B7]				C882	C_1005	mbx147[17C5]										
	C638	C_0603	mbx147[14B7]				C883	C_0603	mbx147[17C5]										
	C639	C_0603	mbx147[14B7]				C884	C_1608	mbx147[17C5]										
	C640	C_0603	mbx147[14B7]				C885	C_1005	mbx147[17C5]										
	C641	C_0603	mbx147[14B7]				C886	C_0603	mbx147[17C5]										
E	C642	C_0603	mbx147[14B7]				C887	C_1005	mbx147[17C5]										
	C643	C_0603	mbx147[14B7]				C888	C_0603	mbx147[17C5]										
	C644	C_0603	mbx147[14B7]				C889	C_1005	mbx147[17B4]										
	C645	C_0603	mbx147[14B7]				C890	C_1608	mbx147[17C4]										
	C646	C_0603	mbx147[14A8]				C891	C_1005	mbx147[17C4]										
	C650	C_0603	mbx147[14F3]				C892	C_0603	mbx147[17C4]										
	C651	C_0603	mbx147[14F2]				C893	C_1005	mbx147[17C4]										
	C652	C_0603	mbx147[14F2]				C894	C_1608	mbx147[17B5]										
	C653	C_0603	mbx147[14F2]				C895	C_1608	mbx147[17B4]										
	C654	C_0603	mbx147[14C3]				C896	C_0603	mbx147[17B4]										
D	C655	C_0603	mbx147[14B3]				C897	C_1005	mbx147[17B4]										
	C660	C_1005	mbx147[15D5]				C898	C_1608	mbx147[17B5]										
	C661	C_1005	mbx147[15D5]				C899	C_0603	mbx147[17B5]										
	C662	C_1005	mbx147[15D5]				C900	C_1005	mbx147[18C6]										
	C663	C_1005	mbx147[15D4]				C901	C_2012	mbx147[18E8]										
	C664	C_1005	mbx147[15D4]				C902	C_1005	mbx147[18E8]										
	C665	C_1005	mbx147[15D4]				C903	C_1005	mbx147[18E8]										
	C666	C_1608	mbx147[15D4]				C904	C_1005	mbx147[18E7]										
	C667	C_1005	mbx147[15D4]				C905	C_1005	mbx147[18E7]										
	C668	C_1005	mbx147[15D3]				C906	C_1005	mbx147[18E7]										
C	C670	C_1005	mbx147[15B8]				C907	C_1005	mbx147[18E6]										
	C671	C_1005	mbx147[15B8]				C908	C_1005	mbx147[18E6]										
	C680	C_0603	mbx147[15B5]				C909	C_2012	mbx147[18E6]										
	C681	C_1005	mbx147[15B5]				C920	C_1005	mbx147[18C2]										
	C682	C_1608	mbx147[15B5]				C921	C_2012	mbx147[18E4]										
	C702	C_1005	mbx147[16D4]				C922	C_1005	mbx147[18E4]										
	C750	C_1005	mbx147[16D2]				C923	C_1005	mbx147[18E4]										
	C751	C_1005	mbx147[16D3]				C924	C_1005	mbx147[18E4]										
	C760	C_0603	mbx147[16F2]				C925	C_1005	mbx147[18E3]										
	C801	C_2012	mbx147[17E8]				C926	C_1005	mbx147[18E3]										
B	C802	C_1608	mbx147[17E7]				C927	C_1005	mbx147[18E3]										
	C803	C_1608	mbx147[17E7]				C928	C_1005	mbx147[18E2]										
	C804	C_1608	mbx147[17E7]				C929	C_2012	mbx147[18E2]										
	C805	C_0603	mbx147[17E7]				C1000	C_1608	mbx147[19F7]										
	C806	C_0603	mbx147[17E7]				C1001	C_1608	mbx147[19F7]										
	C810	C_2012	mbx147[17E7]				C1002	C_1608	mbx147[19F7]										
	C811	C_1608	mbx147[17E7]				C1003	C_1608	mbx147[19F7]										
	C812	C_1608	mbx147[17E7]				C1004	C_1608	mbx147[19F7]										
	C813	C_1608	mbx147[17E6]				C1005	C_1005	mbx147[19F6]										
	C814	C_0603	mbx147[17E6]				C1006	C_1005	mbx147[19F6]										
A	C815	C_0603	mbx147[17E6]				C1007	C_1005	mbx147[19F6]										
	C817	C_3225	mbx147[17F8]				C1008	C_1005	mbx147[19F6]										
	C818	C_3225	mbx147[17F8]				C1009	C_1608	mbx147[19E8]										
	C820	C_1608	mbx147[17F6]				C1010	C_1005	mbx147[19E7]										
	C821	C_0603	mbx147[17F6]				C1015	C_1005	mbx147[19A7]										
	C830	C_1608	mbx147[17F5]				C1020	C_1608	mbx147[19F3]										
	C831	C_1005	mbx147[17F5]				C1021	C_1608	mbx147[19F3]										
	C832	C_1005	mbx147[17F5]				C1022	C_1608	mbx147[19F3]										
	C833	C_1005	mbx147[17F5]				C1023	C_1608	mbx147[19F2]										
	C834	C_1005	mbx147[17F5]				C1024	C_1608	mbx147[19F2]										
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F	C4092	CE_7343	mbx147[37D1]				C4722	C_1005	mbx147[45E5]																	
	C4093	CE_7343	mbx147[37D1]				C4730	C_1005	mbx147[45C6]																	
	C4099	C_1005	mbx147[37E7]				C4731	C_1005	mbx147[45B5]																	
	C4100	C_1608	mbx147[39E6]				C4740	C_1005	mbx147[45B6]																	
	C4101	C_3626	mbx147[39E5]				C4760	C_1608	mbx147[45B4]																	
	C4102	C_3626	mbx147[39E3]				C4800	C_1005	mbx147[46D6]																	
	C4103	C_1608	mbx147[39E3]				C4801	C_1608	mbx147[46D6]																	
	C4104	C_1608	mbx147[39E4]				C4802	C_1608	mbx147[46D6]																	
	C4105	C_1608	mbx147[39E4]				C4810	C_1608	mbx147[46D6]																	
	C4106	C_0603	mbx147[39D4]				C4820	C_1608	mbx147[46C7]																	
E	C4107	C_1005	mbx147[39D5]				C4821	C_1005	mbx147[46C7]																	
	C4108	C_1005	mbx147[39D4]				C4822	C_1005	mbx147[46C7]																	
	C4120	C_1005	mbx147[39D6]				C4823	C_1005	mbx147[46C6]																	
	C4121	C_1005	mbx147[39D6]				C4824	C_1608	mbx147[46C6]																	
	C4122	C_1005	mbx147[39C6]				C4830	C_1608	mbx147[46D5]																	
	C4123	C_0603	mbx147[39C5]				C4831	C_1608	mbx147[46D5]																	
	C4124	C_0603	mbx147[39C5]				C4832	C_1005	mbx147[46C5]																	
	C4130	C_0603	mbx147[39D7]				C4833	C_3216	mbx147[46D4]																	
	C4131	CE_6032	mbx147[39D7]				C4834	C_3225	mbx147[46C3]																	
	C4150	C_1005	mbx147[39D2]				C4835	C_1005	mbx147[46C5]																	
D	C4151	C_1005	mbx147[39D3]				C4836	C_1608	mbx147[46C5]																	
	C4160	C_0603	mbx147[39D1]				C4850	C_1608	mbx147[46C2]																	
	C4161	CE_6032	mbx147[39D2]				C4860	C_0603	mbx147[46B3]																	
	C4190	C_1005	mbx147[39B7]				C4900	C_1608	mbx147[47E7]																	
	C4199	C_1005	mbx147[39B3]				C4901	C_1608	mbx147[47E7]																	
	C4200	C_1005	mbx147[40D5]				C4910	C_3216	mbx147[47E6]																	
	C4201	C_1608	mbx147[40E5]				C4911	C_1005	mbx147[47E5]																	
	C4202	C_1608	mbx147[40E6]				C4912	C_1608	mbx147[47D6]																	
	C4203	C_0603	mbx147[40E6]				C4960	C_1608	mbx147[47C7]																	
	C4204	C_1005	mbx147[40E5]				C4961	C_1608	mbx147[47A7]																	
C	C4205	C_1005	mbx147[40D4]				C5000	C_1005	mbx147[48D6]																	
	C4206	C_0603	mbx147[40E4]				C5001	C_1005	mbx147[48D6]																	
	C4207	C_2012	mbx147[40E4]				C5010	C_2012	mbx147[48D3]																	
	C4208	C_1005	mbx147[40D5]				C5030	C_1608	mbx147[48B5]																	
	C4209	CE_6032	mbx147[40D3]				C5031	C_1608	mbx147[48B5]																	
	C4250	C_1608	mbx147[40B5]				C5050	C_1005	mbx147[48C4]																	
	C4251	C_2012	mbx147[40B5]				C5051	C_1608	mbx147[48B3]																	
	C4252	C_1005	mbx147[40B6]				C5052	C_1608	mbx147[48B3]																	
	C4253	C_1005	mbx147[40B5]				C5100	C_1608	mbx147[38E5]																	
	C4254	C_2012	mbx147[40B4]				C5101	C_1608	mbx147[38E5]																	
B	C4255	C_2012	mbx147[40B4]				C5102	C_0603	mbx147[38E4]																	
	C4270	C_0603	mbx147[40D3]				C5110	C_3626	mbx147[38E6]																	
	C4300	C_1608	mbx147[41E5]				C5111	C_1608	mbx147[38E6]																	
	C4301	C_0603	mbx147[41E4]				C5120	C_1005	mbx147[38D7]																	
	C4302	C_2012	mbx147[41E4]				C5121	C_0603	mbx147[38D6]																	
	C4303	C_0603	mbx147[41E4]				C5122	C_0603	mbx147[38C5]																	
	C4350	CE_3528	mbx147[41B3]				C5123	C_0603	mbx147[38C5]																	
	C4351	C_0603	mbx147[41B3]				C5124	C_1005	mbx147[38C5]																	
	C4352	C_0603	mbx147[41C5]				C5130	CE_6032	mbx147[38E8]																	
	C4353	C_2012	mbx147[41C4]				C5131	CE_6032	mbx147[38D8]																	
A	C4354	C_1005	mbx147[41C4]				C5132	C_1608	mbx147[38E8]																	
	C4355	C_1005	mbx147[41B5]				C5140	C_3216	mbx147[38E4]																	
	C4356	C_1005	mbx147[41B5]				C5141	C_1608	mbx147[38E3]																	
	C4357	C_1608	mbx147[41C5]				C5151	C_1005	mbx147[38D3]																	
	C4358	C_1608	mbx147[41C6]				C5152	C_1608	mbx147[38D4]																	
	C4359	C_0603	mbx147[41C6]				C5160	CE_6032	mbx147[38E2]																	
	C4360	C_1005	mbx147[41C5]				C5161	C_1608	mbx147[38E1]																	
	C4410	C_3216	mbx147[42E4]				C5162	C_1005	mbx147[38E1]																	
	C4411	C_1608	mbx147[42E3]				C5163	C_1005	mbx147[38E2]																	
	C4412	C_1005	mbx147[42E4]				C5233	C_0603	mbx147[38E8]																	
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D60	SHOTTKY_SMD	mbx147[6B6]	FB881	FB_1005	mbx147[17C4]																		
D110	D_SMD	mbx147[7B3]	FB890	FB_1005	mbx147[17C5]																		
D3000	ZD7_TSSOP8	mbx147[28C7]	FB891	FB_1005	mbx147[17B5]																		
D3001	ZD7_TSSOP8	mbx147[28C6]	FB892	FB_1005	mbx147[17B5]																		
D3002	ZD4_SC88A	mbx147[28C6]	FB1610	FB_1608	mbx147[23F7]																		
D3003	ZD4_SC88A	mbx147[28C6]	FB1620	FB_1608	mbx147[23D7]																		
D3004	ZD4_SC88A	mbx147[28C5]	FB1800	FB_1005	mbx147[24E7]																		
D3260	SHOTTKY_SMD	mbx147[31D3]	FB1801	CONDUCTOR_1608	mbx147[24E7]																		
D3900	D_SMD	mbx147[36C8]	FB3050	CONDUCTOR_1005	mbx147[28D3]																		
D4000	D2_SSSM3AAK	mbx147[37C8]	FB3800	CONDUCTOR_1005	mbx147[35E6]																		
D4050	SHOTTKY_SMD4525	mbx147[37E3]	FB3850	CONDUCTOR_1005	mbx147[35B2]																		
D4051	SHOTTKY_SMD	mbx147[37E6]	FB4830	FB_2012	mbx147[46C2]																		
D4070	SHOTTKY_SMD4525	mbx147[37C3]	FB4850	FB_4516	mbx147[46C2]																		
D4071	SHOTTKY_SMD	mbx147[37D4]	FB4860	CONDUCTOR_1005	mbx147[46B2]																		
D4091	D2_SSSM3AAK	mbx147[37B2]	FB4861	CONDUCTOR_1005	mbx147[46B2]																		
D4100	SHOTTKY_SMD	mbx147[39E5]	FB4862	CONDUCTOR_1005	mbx147[46B2]																		
D4101	SHOTTKY_SMD	mbx147[39E4]	FB4863	CONDUCTOR_1005	mbx147[46B2]																		
D4110	SHOTTKY_SMD	mbx147[39D7]	FB4900	FB_4516	mbx147[47E7]																		
D4140	SHOTTKY_SMD	mbx147[39D2]	FL651	RB4_2010	mbx147[14D3]																		
D4170	D2_SSSM3AAK	mbx147[39B5]	FL652	RB4_2010	mbx147[14D3]																		
D4410	D2_SSSM3AAK	mbx147[42D4]	FL2000	LF2_SMD-D	mbx147[26D6]																		
D4411	SHOTTKY_SC79AK	mbx147[42E4]	FL3220	LF4_2010	mbx147[30D4]																		
D4421	SHOTTKY_SC79AK	mbx147[42C4]	FL3221	LF4_2010	mbx147[30D4]																		
D4471	D2_SSSM3AAK	mbx147[42C7]	FL3260	RB4_2010	mbx147[31E3]																		
D4472	D_SMD	mbx147[42A6]	FL3350	LF2_SMD-D	mbx147[32C3]																		
D4473	D_SMD	mbx147[42B8]	FL3351	LF2_SMD-D	mbx147[32C3]																		
D4550	SHOTTKY_SMD	mbx147[43E2]	FL3352	LF2_SMD-D	mbx147[32C1]																		
D4552	SHOTTKY_SMD	mbx147[43D2]	FL3400	LF2_SMD-D	mbx147[33C7]																		
D4553	SHOTTKY_SMD	mbx147[43D2]	FL3600	LF2_SMD-D	mbx147[34D5]																		
D4554	SHOTTKY_SMD	mbx147[43C2]	FL3830	LF2_SMD-D	mbx147[35D2]																		
D4570	D_SMD	mbx147[43F7]	IC50	LM86_TSSOP8	mbx147[6E5]																		
D4571	ZD_SC76AK	mbx147[43E7]	IC200	ICS9LPR335_QFN73	mbx147[8C5]																		
D4631	SHOTTKY_SMD	mbx147[44D2]	IC300	GMCH_1466_CSP1466	mbx147[9D7 9E3]																		
D4650	SHOTTKY_SMD	mbx147[44C2]	IC300	GMCH_1466_CSP1466	mbx147[10C3 10C6]																		
D4660	SHOTTKY_SMD	mbx147[44B6]	IC300	GMCH_1466_CSP1466	mbx147[11D3]																		
D4710	D2_SSSM3AAK	mbx147[45E7]	IC300	GMCH_1466_CSP1466	mbx147[12C7 12D1 12D3 12D4]																		
D4712	ZD_SOD323AK	mbx147[45D7]	IC300	GMCH_1466_CSP1466	mbx147[13D4]																		
D4720	D2_SSSM3AAK	mbx147[45C3]	IC600	G7XM_CSP820	mbx147[14C6]																		
D4730	SHOTTKY_SMD	mbx147[45C4]	IC600	G7XM_CSP820	mbx147[15D6 15D2]																		
D4760	D_SMD	mbx147[45B4]	IC600	G7XM_CSP820	mbx147[16D5]																		
D4810	D_SMD	mbx147[46D6]	IC600	G7XM_CSP820	mbx147[17A5 17E5]																		
D4811	ZD_SC90	mbx147[46D7]	IC611	SN74CBTLV3257_SSOP16	mbx147[14C2]																		
D4830	SHOTTKY_SC79AK	mbx147[46D5]	IC612	SN74CBTLV3257_SSOP16	mbx147[14B3]																		
D4850	ZD_SC90	mbx147[46E2]	IC620	7408_SC88A	mbx147[14E7]																		
D4851	ZD_SMD	mbx147[46B2]	IC631	PI3DBV40_TSSOP48	mbx147[14E2]																		
D4852	ZD_SMD	mbx147[46B2]	IC640	74125_TSSOP8	mbx147[14A7]																		
D4853	ZD_SMD	mbx147[46B1]	IC750	LM89_TSSOP8	mbx147[16D2]																		
D4860	ZD4_SC88A	mbx147[46B2]	IC751	7408_SC88A	mbx147[16F3]																		
D4900	ZD_SC76	mbx147[47E6]	IC760	SN74CBTLV3257_SSOP16	mbx147[16E2]																		
D4901	SHOTTKY_SMD4525	mbx147[47E6]	IC900	DDR_SDRAM256MX32_CSP	mbx147[18D7]																		
D4910	ZD_SC90	mbx147[47E5]	144																				
D4943	D2_SSSM3AAK	mbx147[47E4]	IC920	DDR_SDRAM256MX32_CSP	mbx147[18D3]																		
D4960	D2_SSSM3AAK	mbx147[47B7]	144																				
D4961	ZD_SC90	mbx147[47B7]	IC1400	ICH7_M_CSP652_C																			

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												ZONE	LTR	DESCRIPTION	DATE	APPR.				
F	IC4630	7414_TSSOP8	mbx147[44C1	44E2	44C3]	Q4053	M_FET_NCH_E_5P_SON5	mbx147[37E3]								
	IC4660	7414_TSSOP8	mbx147[44A6	44B6	44B5]	Q4054	M_FET_NCH_E_5P_SON5	mbx147[37E4]								
	IC4700	MAX4211_QFN17	mbx147[45C5				Q4070	M_FET_NCH_E_5P_SON5	mbx147[37C3]								
	IC4710	R3112_SC82	mbx147[45B5				Q4071	M_FET_NCH_E_5P_SON5	mbx147[37C3]								
	IC4740	7414_SC88A	mbx147[45B5				Q4072	M_FET_NCH_E_5P_SON5	mbx147[37C3]								
	IC4800	MAX1908_QFN29	mbx147[46C6				Q4073	M_FET_NCH_E_5P_SON5	mbx147[37C3]								
	IC5000	R3112_SOT23-5	mbx147[48D6				Q4074	M_FET_NCH_E_5P_SON5	mbx147[37C4]								
	IC5050	7414_TSSOP8	mbx147[48B4	48B2	48B2]	Q4110	M_FET_NCH_E_5P_SON5	mbx147[39E6]								
	IC5100	MAX1541_QFN41	mbx147[38D5				Q4111	M_FET_NCH_E_5P_SON5	mbx147[39D6]								
	JR303	CONDUCTOR_1005	mbx147[9D5				Q4140	M_FET_NCH_E_5P_SON5	mbx147[39E3]								
E	JR340	CONDUCTOR_1005	mbx147[9E1				Q4141	M_FET_NCH_E_5P_SON5	mbx147[39D3]								
	JR401	FB_1005	mbx147[11D4				Q4190	M_FET_NCH_E_3P_VMT3G	mbx147[39B7]								
	JR620	CONDUCTOR_0603	mbx147[14F7				SD												
	JR621	CONDUCTOR_0603	mbx147[14E7				Q4198	M_FET_NCH_E_3P_VMT3G	mbx147[39B3]								
	JR622	CONDUCTOR_0603	mbx147[14E7				SD												
	JR640	CONDUCTOR_0603	mbx147[14A7				Q4199	M_FET_NCH_E_5P_SON5	mbx147[39B3]								
	JR641	CONDUCTOR_0603	mbx147[14A7				Q4200	M_FET_NCH_E_3P_VMT3G	mbx147[40D3]								
	JR705	CONDUCTOR_1005	mbx147[16D4				SD												
	JR707	CONDUCTOR_1005	mbx147[16D4				Q4320	2M_FET_NCH_E_6P_ES6	mbx147[41B3	41D4]							
	JR1561	CONDUCTOR_1005	mbx147[22D2				Q4430	SI4830_SOP8	mbx147[42C3]								
D	JR2001	CONDUCTOR_1005	mbx147[26E4				Q4450	SI4830_SOP8	mbx147[42E3]								
	JR2002	CONDUCTOR_1005	mbx147[26E4				Q4500	M_FET_NCH_E_3P_VMT3G	mbx147[43D7]								
	JR4070	CONDUCTOR_1005	mbx147[37C6				SD												
	JR4071	CONDUCTOR_1005	mbx147[37C6				Q4510	2M_FET_NCH_E_8P_TSSO	mbx147[43D4	43D6]							
	JR4170	CONDUCTOR_0603	mbx147[39C4				P8												
	JR4255	CONDUCTOR_0603	mbx147[40B4				Q4511	2M_FET_NCH_E_6P_SC88	mbx147[43D6	43D4]							
	JR4610	CONDUCTOR_0603	mbx147[44E6				SGDSGD												
	L60	LP_50H28	mbx147[6B6				Q4570	DTR-NPN-PNP-RBE_SC88	mbx147[43E7	43E7]							
	L450	FB_1608	mbx147[11D7				Q4574	TR-NPN_SC89BEC	mbx147[43E7]								
	L453	FB_1608	mbx147[11D7				Q4590	2M_FET_NCH_E_8P_TSSO	mbx147[43B4	43B6]							
C	L4050	LP_10H40	mbx147[37E2				P8												
	L4070	LP_10H40	mbx147[37C2				Q4591	2M_FET_NCH_E_8P_SON8	mbx147[43B5	43B6]							
	L4110	LP_10H43	mbx147[39D6				Q4592	M_FET_NCH_E_3P_VMT3G	mbx147[43B7]								
	L4140	LP_10H45	mbx147[39D2				SD												
	L4201	LP_60H35	mbx147[40D4				Q4593	2M_FET_NCH_E_6P_SC88	mbx147[43B4	43B6]							
	L4350	LP_60H35	mbx147[41C4				SGDSGD												
	L4430	LP_10H45	mbx147[42C2				Q4594	2M_FET_NCH_E_6P_SC88	mbx147[43B5	43B5]							
	L4450	LP_10H43	mbx147[42E2				SGDSGD												
	L4830	LP_60H35	mbx147[46C3				Q4710	TR-NPN_SC89BEC	mbx147[45E7]								
	L5110	LP_10H40	mbx147[38E7				Q4712	2M_FET_NCH_E_6P_SC88	mbx147[45D8	45D7]							
B	L5140	LP_10H43	mbx147[38E2				SGDSGD												
	Q41	TR-NPN_SC89BEC	mbx147[6B7				Q4713	DTR-PNP-RBE_FSM3BEC	mbx147[45D6]								
	Q50	DTR-PNP-RBE_FSM3BEC	mbx147[6F4				Q4721	M_FET_NCH_E_3P_VMT3G	mbx147[45D3]								
	Q51	M_FET_NCH_E_3P_VMT3G	mbx147[6F3				SD												
	SD							Q4740	DTR2-NPN-RBE_SC88	mbx147[45B5	45A5]							
	Q60	M_FET_NCH_E_3P_SC59G	mbx147[6A6				Q4750	DTR-NPN-PNP-RBE_SC88	mbx147[45B3	45B2]							
	SD							Q4800	TR-NPN_SC89BEC	mbx147[46E6]								
	Q610	2M_FET_NCH_E_6P_SC88	mbx147[14C4	14C4]		Q4811	DTR-NPN-PNP-RBE_SC88	mbx147[46B6	46B6]							
	SGDSGD							Q4830	M_FET_NCH_E_8P_SON8	mbx147[46D4]								
	Q880	M_FET_NCH_E_3P_SC70G	mbx147[17D4				Q4831	M_FET_NCH_E_8P_SON8	mbx147[46C4]								
A	SD							Q4850	M_FET_PCH_E_8P_SOP8	mbx147[46E2]								
	Q881	M_FET_NCH_E_3P_VMT3G	mbx147[17C4				Q4851	M_FET_PCH_E_3P_SC70G	mbx147[46D3]								
	SD							SD												
	Q1580	DTR2-NPN-RBE_SC88	mbx147[22B7	22B7]		Q4860	M_FET_NCH_E_3P_VMT3G	mbx147[46B3]								
	Q3100	DTR-NPN-PNP-RBE_SC88	mbx147[29D8	29D8]		SD												
	Q3150	M_FET_NCH_E_8P_SON8	mbx147[29E3				Q4910	M_FET_PCH_E_74XX_8P_	mbx147[47E5]								
	Q3151	2M_FET_NCH_E_6P_SC88	mbx147[29D3	29D4]		SOP8												
	SGDSGD							Q4911	M_FET_PCH_E_74XX_8P_	mbx147[47E4]								
	Q3210	M_FET_NCH_E_3P_VMT3G	mbx147[30C7				SOP8												
	SD							Q4912	M_FET_PCH_E_3P_SC70G	mbx147[47D5]								
Q3211	2M_FET_NCH_E_6P_ES6	mbx147[30C7	30C6]		SD													
Q3212	M_FET_NCH_E_8P_SON8	mbx147[30D6				Q4913	DTR-NPN-PNP-RBE_SC88	mbx147[47D4	47D4]								
Q3260	2M_FET_NCH_E_6P_SC88	mbx147[31E7				Q4914	M_FET_NCH_E_3P_VMT3G	mbx147[47C5]									
	SGDSGD							SD												
	Q3261	2M_FET_NCH_E_6P_SC88	mbx147[31D7				Q4960	TR-NPN_SC89BEC	mbx147[47A6]								
	SGDSGD							Q5000	TR-NPN_SC89BEC	mbx147[48D6]								
	Q3280	DTR-NPN-PNP-RBE_SC88	mbx147[31B4				Q5003	M_FET_NCH_E_3P_VMT3G	mbx147[48D5]								
	Q3610	DTR-PNP-RBE_FSM3BEC	mbx147[34C7				SD												
	Q3810	M_FET_NCH_E_8P_SON8	mbx147[35C6				Q5031	TR-NPN_SC89BEC	mbx147[48B6]								
	Q3811	2M_FET_NCH_E_6P_SC88	mbx147[35B8	35B6]		Q5050	M_FET_NCH_E_3P_VMT3G	mbx147[48B2]								
	SGDSGD							SD												
	Q3900	DTR2-PNP-RBE_SC88EBC	mbx147[36E6	36E5]		Q5110	M_FET_NCH_E_5P_SON5	mbx147[38E6]								
	EBC							Q5111	M_FET_NCH_E_5P_SON5	mbx147[38E6]								
Q3901	DTR2-PNP-RBE_SC88EBC	mbx147[36E6	36D6]		Q5112	M_FET_NCH_E_5P_SON5	mbx147[38E6]									
EBC							Q5113	M_FET_NCH_E_5P_SON5	mbx147[38E6]									
Q3902	DTR2-PNP-RBE_SC88EBC	mbx147[36D6	36D7]		Q5140	SI4830_SOP8	mbx147[38E3]									
EBC							Q5170	DTR2-NPN-RBE_SC88	mbx147[38B6	38B5]								
Q3903	DTR2-PNP-RBE_SC88EBC	mbx147[36D7	36D7]		R1	R_0603	mbx147[6E5]									
EBC							R2	R_1005	mbx147[6D5]									
Q3904	DTR-NPN-PNP-RBE_SC88	mbx147[36C7	36B7]		R3	R_1005	mbx147[6D5]									
Q3905	DTR2-PNP-RBE_SC88EBC	mbx147[36B7	36B7]		R5	R_1005	mbx147[6C5]									
EBC							R30	R_1005	mbx147[6C4]									
Q3906	DTR-NPN-PNP-RBE_SC88	mbx147[36A7	36A7]		R31	R_1005	mbx147[6C4]									
Q3950	M_FET_NCH_E_3P_VMT3G	mbx147[36B2				R32	R_0603	mbx147[6C3]									
SD							R33	R_1005	mbx147[6C3]									
Q4000	2M_FET_NCH_E_6P_SC88	mbx147[37A6	37B5]		R34	R_1005	mbx147[6C2]									
SGDSGD							R35	R_1005	mbx147[6C2]									
Q4010	M_FET_NCH_E_3P_VMT3G	mbx147[37B7				R36	R_1005	mbx147[6C2]									
SD							R37	R_1005	mbx147[6C2]									
Q4050	M_FET_NCH_E_5P_SON5	mbx147[37E3				R41	R_0603	mbx147[6B8]									
Q4051	M_FET_NCH_E_5P_SON5	mbx147[37E3				R42	R_0603	mbx147[6B7]									
Q4052	M_FET_NCH_E_5P_SON5	mbx147[37E3				R50	R_0603	mbx147[6E4]									
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F	R60	R_0603	mbx147[6A6]				R730	R_0603	mbx147[16D7]										F					
	R61	R_0603	mbx147[6A6]				R731	R_0603	mbx147[16C7]															
	R100	R_1005	mbx147[7C6]				R732	R_0603	mbx147[16D7]															
	R101	R_1005	mbx147[7B6]				R733	R_0603	mbx147[16C7]															
	R110	R_1005	mbx147[7B4]				R742	R_0603	mbx147[16D8]															
	R200	R_1005	mbx147[8D7]				R743	R_0603	mbx147[16C8]															
	R201	CONDUCTOR_1005	mbx147[8D7]				R750	R_0603	mbx147[16E4]															
	R202	R_1005	mbx147[8D3]				R751	R_0603	mbx147[16E4]															
	R203	R_1005	mbx147[8B4]				R752	R_0603	mbx147[16E1]															
	R204	R_1005	mbx147[8B4]				R753	R_0603	mbx147[16E1]															
E	R205	R_1005	mbx147[8B4]				R754	R_0603	mbx147[16E1]															
	R206	R_1005	mbx147[8B4]				R755	R_0603	mbx147[16D4]															
	R207	R_1005	mbx147[8B4]				R756	R_0603	mbx147[16F3]															
	R210	R_1005	mbx147[8D3]				R760	R_0603	mbx147[16C4]															
	R211	R_1005	mbx147[8D3]				R761	R_0603	mbx147[16C4]															
	R212	R_1005	mbx147[8D3]				R776	R_0603	mbx147[16D3]															
	R213	R_0603	mbx147[8C3]				R777	R_0603	mbx147[16D3]															
	R214	R_1005	mbx147[8C3]				R778	R_0603	mbx147[16D3]															
	R215	R_0603	mbx147[8B3]				R779	R_0603	mbx147[16D3]															
	R250	R_1005	mbx147[8C2]				R780	R_0603	mbx147[16E3]															
D	R251	R_0603	mbx147[8C2]				R801	R_0603	mbx147[17B5]															
	R252	R_0603	mbx147[8C2]				R802	R_0603	mbx147[17B5]															
	R253	R_0603	mbx147[8C2]				R881	R_0603	mbx147[17D3]															
	R254	R_1005	mbx147[8C3]				R912	R_0603	mbx147[18C5]															
	R255	R_1005	mbx147[8C3]				R982	R_0603	mbx147[18C5]															
	R300	R_0603	mbx147[9E5]				R985	R_0603	mbx147[18C1]															
	R301	R_1005	mbx147[9B8]				R990	R_1005	mbx147[18B5]															
	R302	R_1005	mbx147[9B8]				R991	R_1005	mbx147[18B5]															
	R303	R_0603	mbx147[9E5]				R1400	R_1005	mbx147[21F7]															
	R304	R_1005	mbx147[9A8]				R1401	R_1005	mbx147[21F7]															
C	R305	R_1005	mbx147[9A8]				R1403	R_0603	mbx147[21D3]															
	R306	R_0603	mbx147[9E5]				R1404	R_1005	mbx147[22B2]															
	R307	R_1005	mbx147[9D6]				R1425	R_1005	mbx147[21E6]															
	R308	R_1005	mbx147[9D6]				R1450	R_0603	mbx147[21A3]															
	R309	R_0603	mbx147[9E5]				R1451	R_0603	mbx147[21A3]															
	R311	R_1005	mbx147[9B7]				R1460	R_0603	mbx147[21C2]															
	R312	R_1005	mbx147[9B7]				R1461	R_0603	mbx147[21B2]															
	R314	R_1005	mbx147[9A7]				R1462	R_0603	mbx147[21C2]															
	R315	R_1005	mbx147[9A7]				R1500	R_1608	mbx147[22C5]															
	R321	R_1005	mbx147[9B7]				R1501	CONDUCTOR_1005	mbx147[22C6]															
B	R322	R_1005	mbx147[9B7]				R1502	R_0603	mbx147[22C7]															
	R323	R_1005	mbx147[9A7]				R1504	R_1005	mbx147[22C6]															
	R324	R_1005	mbx147[9A7]				R1505	R_1005	mbx147[22C6]															
	R331	R_1005	mbx147[9B6]				R1506	R_1005	mbx147[22C5]															
	R332	R_1005	mbx147[9B6]				R1507	R_1005	mbx147[22C5]															
	R334	R_1005	mbx147[9A6]				R1508	R_1005	mbx147[22C1]															
	R335	R_1005	mbx147[9A6]				R1509	R_1005	mbx147[22C1]															
	R340	R_0603	mbx147[9E5]				R1510	R_1005	mbx147[22D6]															
	R342	R_0603	mbx147[9D4]				R1511	R_1005	mbx147[22D6]															
	R343	R_1005	mbx147[9E1]				R1512	R_0603	mbx147[22C6]															
A	R344	R_1005	mbx147[9E1]				R1520	R_0603	mbx147[22E4]															
	R501	R_1005	mbx147[13E6]				R1521	R_1005	mbx147[22E4]															
	R503	CONDUCTOR_1005	mbx147[13E6]				R1522	R_1005	mbx147[22E4]															
	R504	CONDUCTOR_1005	mbx147[13E6]				R1550	R_0603	mbx147[22D4]															
	R512	R_1005	mbx147[13C5]				R1551	R_0603	mbx147[22C5]															
	R513	R_0603	mbx147[13B5]				R1553	R_0603	mbx147[22C3]															
	R520	R_1005	mbx147[13E4]				R1554	R_0603	mbx147[22C4]															
	R521	R_1005	mbx147[13E4]				R1562	R_0603	mbx147[22C2]															
	R530	R_0603	mbx147[13E7]				R1581	R_1005	mbx147[22B5]															
	R532	R_0603	mbx147[13E7]				R1590	R_0603	mbx147[22A7]															
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F	R2530	R_0603	mbx147[27B2]				R4071	R_1005	mbx147[37C5]												
	R2531	R_0603	mbx147[27B2]				R4072	R_1005	mbx147[37D3]												
	R2550	R_0603	mbx147[27B6]				R4077	CONDUCTOR_1005	mbx147[37C4]												
	R2580	CONDUCTOR_1005	mbx147[27C3]				R4078	CONDUCTOR_1005	mbx147[37C4]												
	R2581	R_1005	mbx147[27C4]				R4079	CONDUCTOR_1005	mbx147[37C3]												
	R3051	R_0603	mbx147[28E3]				R4083	R_0603	mbx147[37C5]												
	R3052	R_0603	mbx147[28D3]				R4090	R_0603	mbx147[37E1]												
	R3060	R_0603	mbx147[28B2]				R4101	R_0603	mbx147[39E4]												
	R3061	R_0603	mbx147[28B3]				R4102	R_1005	mbx147[39D4]												
	R3150	R_0603	mbx147[29E4]				R4103	R_1005	mbx147[39D5]												
E	R3151	R_0603	mbx147[29E3]				R4120	R_1005	mbx147[39D6]												
	R3152	R_0603	mbx147[29D3]				R4122	R_1005	mbx147[39D5]												
	R3153	R_0603	mbx147[29D2]				R4123	R_1005	mbx147[39C6]												
	R3154	R_1005	mbx147[29C4]				R4124	R_1005	mbx147[39C6]												
	R3200	R_0603	mbx147[30E6]				R4125	R_1005	mbx147[39C6]												
	R3210	R_0603	mbx147[30C7]				R4126	R_1005	mbx147[39C6]												
	R3211	R_0603	mbx147[30D7]				R4127	R_1005	mbx147[39C6]												
	R3212	R_0603	mbx147[30C6]				R4128	R_1005	mbx147[39C5]												
	R3260	CONDUCTOR_1005	mbx147[31E7]				R4129	R_1005	mbx147[39C5]												
	R3261	CONDUCTOR_1005	mbx147[31D7]				R4131	R_0603	mbx147[39C7]												
D	R3265	R_0603	mbx147[31D3]				R4150	R_1005	mbx147[39D3]												
	R3266	R_0603	mbx147[31D3]				R4152	R_1005	mbx147[39D4]												
	R3280	R_0603	mbx147[31C4]				R4153	R_1005	mbx147[39C3]												
	R3281	R_0603	mbx147[31C3]				R4154	R_1005	mbx147[39C3]												
	R3282	R_0603	mbx147[31B3]				R4155	R_1005	mbx147[39C3]												
	R3350	R_0603	mbx147[32E3]				R4161	R_0603	mbx147[39C2]												
	R3351	R_0603	mbx147[32E3]				R4170	R_0603	mbx147[39C5]												
	R3361	R_1005	mbx147[32E1]				R4171	R_0603	mbx147[39B5]												
	R3362	R_0603	mbx147[32E1]				R4172	R_0603	mbx147[39B5]												
	R3370	R_1005	mbx147[32C4]				R4173	R_0603	mbx147[39B4]												
C	R3371	R_1005	mbx147[32C4]				R4190	R_1005	mbx147[39B7]												
	R3400	R_0603	mbx147[33D7]				R4191	R_1005	mbx147[39B7]												
	R3610	R_0603	mbx147[34D7]				R4192	R_0603	mbx147[39B7]												
	R3611	R_0603	mbx147[34C7]				R4199	R_0603	mbx147[39B3]												
	R3801	R_1005	mbx147[35E7]				R4201	R_0603	mbx147[40E6]												
	R3802	CONDUCTOR_1005	mbx147[35E6]				R4202	R_1005	mbx147[40D5]												
	R3803	CONDUCTOR_1005	mbx147[35E6]				R4203	R_1005	mbx147[40D4]												
	R3812	R_0603	mbx147[35C7]				R4205	R_1005	mbx147[40D3]												
	R3813	R_0603	mbx147[35C6]				R4206	R_1005	mbx147[40D4]												
	R3832	R_1005	mbx147[35E3]				R4207	R_1005	mbx147[40D4]												
B	R3850	R_1005	mbx147[35C2]				R4208	R_1005	mbx147[40D4]												
	R3901	R_0603	mbx147[36C7]				R4250	R_0603	mbx147[40B4]												
	R3902	R_0603	mbx147[36B8]				R4255	R_1005	mbx147[41B5]												
	R3910	R_0603	mbx147[36D4]				R4320	R_1005	mbx147[41E4]												
	R3911	R_0603	mbx147[36D4]				R4350	R_0603	mbx147[41C6]												
	R3912	R_0603	mbx147[36D4]				R4354	R_1005	mbx147[41B6]												
	R3913	R_0603	mbx147[36D4]				R4356	R_1005	mbx147[41B4]												
	R3914	R_0603	mbx147[36D4]				R4357	R_1005	mbx147[41B4]												
	R3915	R_0603	mbx147[36D4]				R4358	R_1005	mbx147[41C4]												
	R3916	R_0603	mbx147[36D4]				R4359	R_0603	mbx147[41A3]												
A	R3918	R_1005	mbx147[36D4]				R4370	R_1005	mbx147[41B3]												
	R3919	R_0603	mbx147[36D4]				R4411	R_1005	mbx147[42E4]												
	R3921	R_0603	mbx147[36C4]				R4412	R_1005	mbx147[42E4]												
	R3922	R_0603	mbx147[36C4]				R4413	R_1005	mbx147[42D4]												
	R3923	R_1005	mbx147[36C4]				R4414	R_1005	mbx147[42E4]												
	R3930	R_0603	mbx147[36D4]				R4418	R_0603	mbx147[42D8]												
	R3931	R_0603	mbx147[36D4]				R4421	R_1005	mbx147[42C4]												
	R3932	R_0603	mbx147[36D4]				R4422	R_1005	mbx147[42B4]												
	R3933	R_0603	mbx147[36D4]				R4423	R_1005	mbx147[42B4]												
	R3940	R_0603	mbx147[36E3]				R4424	R_1005	mbx147[42C4]												
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			R4651			R_0603			mbx147[44C2]			ZONE			LTR			DESCRIPTION			DATE			APPR.		
			R4652			R_0603			mbx147[44C2]																	
			R4660			R_0603			mbx147[44B7]																	
			R4661			R_0603			mbx147[44B6]																	
			R4662			R_0603			mbx147[44B6]																	
			R4701			R_1005			mbx147[45E6]																	
			R4702			R_1005			mbx147[45E6]																	
			R4703			CONDUCTOR_1005			mbx147[45E6]																	
			R4710			R_0603			mbx147[45E7]																	
			R4712			R_0603			mbx147[45D7]																	
			R4713			R_0603			mbx147[45D6]																	
			R4714			R_0603			mbx147[45D6]																	
			R4715			R_0603			mbx147[45C6]																	
			R4722			R_1005			mbx147[45C3]																	
			R4723			R_1005			mbx147[45C3]																	
			R4724			R_1005			mbx147[45C5]																	
			R4725			R_0603			mbx147[45D4]																	
			R4726			R_1005			mbx147[45D4]																	
			R4727			R_1005			mbx147[45D4]																	
			R4728			R_1005			mbx147[45C4]																	
			R4729			R_1005			mbx147[45D5]																	
			R4730			R_1005			mbx147[45C5]																	
			R4731			R_1005			mbx147[45E5]																	
			R4732			R_1005			mbx147[45E5]																	
			R4733			R_1005			mbx147[45C5]																	
			R4734			R_1005			mbx147[45C5]																	
			R4735			R_1005			mbx147[45C5]																	
			R4740			R_0603			mbx147[45B6]																	
			R4741			R_0603			mbx147[45B6]																	
			R4750			R_0603			mbx147[45B2]																	
			R4751			R_0603			mbx147[45B2]																	
			R4755			R_1005			mbx147[45E3]																	
			R4756			R_1005			mbx147[45D3]																	
			R4757			R_1005			mbx147[45D4]																	
			R4760			R_0603			mbx147[45C4]																	
			R4761			R_0603			mbx147[45B4]																	
			R4800			R4P_SMD			mbx147[46E6]																	
			R4801			R_0603			mbx147[46F5]																	
			R4802			R_0603			mbx147[46F5]																	
			R4803			CONDUCTOR_1005			mbx147[46E6]																	
			R4804			R_1005			mbx147[46E6]																	
			R4805			R_1005			mbx147[46E6]																	
			R4806			R_1005			mbx147[46E6]																	
			R4807			R_0603			mbx147[46E6]																	
			R4808			R_0603			mbx147[46D6]																	
			R4814			R_1005			mbx147[46D7]																	
			R4815			R_1005			mbx147[46D7]																	
			R4816			R_1005			mbx147[46D7]																	
			R4817			R_1005			mbx147[46C7]																	
			R4818			R_1005			mbx147[46C7]																	
			R4819			R_1005			mbx147[46D6]																	
			R4820			R_0603			mbx147[46C7]																	
			R4821			R_0603			mbx147[46B8]																	
			R4822			R_0603			mbx147[46C7]																	
			R4823			R_1005			mbx147[46B6]																	
			R4824			R_1005			mbx147[46B6]																	
			R4825			R_1005			mbx147[46B6]																	
			R4826			R_1005			mbx147[46B6]																	
			R4830			R_1608			mbx147[46C3]																	
			R4831			R_0603			mbx147[46D5]																	
			R4832			R_0603			mbx147[46D5]																	
			R4833			R_1005			mbx147[46D5]																	
			R4834			R_0603			mbx147[46D4]																	
			R4835			CONDUCTOR_1005			mbx147[46C5]																	
			R4836			CONDUCTOR_1005			mbx147[46C5]																	
			R4850			R_0603			mbx147[46E2]																	
			R4851			R_0603			mbx147[46D3]																	
			R4852			R_0603			mbx147[46D2]																	
			R4860			R_0603			mbx147[46B3]																	
			R4861			R_0603			mbx147[46B3]																	
			R4862			R_0603			mbx147[46B3]																	
			R4863			R_0603			mbx147[46B3]																	
			R4900			R_0603			mbx147[47F6]																	
			R4910			R_1608			mb																							

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F	RB1452	RB4_2010	mbx147[21C3]														TP382	TESTPAD_	mbx147[10B6]								
	RB1453	RB4_2010	mbx147[21C3]														TP385	TESTPAD_	mbx147[10B6]								
	RB1454	RB4_2010	mbx147[21B3]														TP391	TESTPAD_	mbx147[10C5]								
	RB1455	RB4_2010	mbx147[21B3]														TP392	TESTPAD_	mbx147[10C5]								
	RB1456	RB4_2010	mbx147[21B3]														TP393	TESTPAD_	mbx147[10C2]								
	RB1457	RB4_2010	mbx147[21B3]														TP394	TESTPAD_	mbx147[10C2]								
	RB1460	RB4_2010	mbx147[21D2]														TP503	TESTPAD_	mbx147[13E5]								
	RB1461	RB4_2010	mbx147[21C2]														TP600	TESTPAD_	mbx147[14D5]								
	RB1462	RB4_2010	mbx147[21C2]														TP601	TESTPAD_	mbx147[14D5]								
	RB1520	RB4_2010	mbx147[22E4]														TP610	TESTPAD_	mbx147[14C5]								
E	RB1521	RB4_2010	mbx147[22E4]														TP630	TESTPAD_	mbx147[14D7]								
	RB1540	RB4_2010	mbx147[22E4]														TP631	TESTPAD_	mbx147[14D7]								
	RB1544	RB4_2010	mbx147[22D4]														TP660	TESTPAD_	mbx147[15C5]								
	RB1550	RB4_2010	mbx147[22E1]														TP662	TESTPAD_	mbx147[15C5]								
	RB2510	RB4_2010	mbx147[27E6]														TP663	TESTPAD_	mbx147[15B5]								
	RB2511	RB4_2010	mbx147[27E4]														TP664	TESTPAD_	mbx147[15B5]								
	RB2512	RB4_2010	mbx147[27E3]														TP665	TESTPAD_	mbx147[15B5]								
	RB2513	RB4_2010	mbx147[27E3]														TP666	TESTPAD_	mbx147[15B5]								
	RB2521	RB4_2010	mbx147[27E6]														TP670	TESTPAD_	mbx147[15E8]								
	RB2530	RB4_2010	mbx147[27B2]														TP671	TESTPAD_	mbx147[15D8]								
D	RB2540	RB4_2010	mbx147[27C5]														TP672	TESTPAD_	mbx147[15D8]								
	RB2550	RB4_2010	mbx147[27B4]														TP673	TESTPAD_	mbx147[15C8]								
	RB3150	RB4_2010	mbx147[29D2]														TP674	TESTPAD_	mbx147[15C8]								
	RB3630	RB4_2010	mbx147[34D3]														TP675	TESTPAD_	mbx147[15B8]								
	RB3930	RB4_2010	mbx147[36E3]														TP676	TESTPAD_	mbx147[15B8]								
	SL1	SHORTLAND_SMD	mbx147[6D6]														TP677	TESTPAD_	mbx147[15B8]								
	SL50	SHORTLAND_025MM	mbx147[6F4]														TP680	TESTPAD_	mbx147[15C1]								
	SL51	SHORTLAND_025MM	mbx147[6E4]														TP722	TESTPAD_	mbx147[16D6]								
	SL81	SHORTLAND_SMD	mbx147[6C4]														TP723	TESTPAD_	mbx147[16D6]								
	SL82	SHORTLAND_SMD	mbx147[6C4]														TP724	TESTPAD_	mbx147[16D6]								
C	SL83	SHORTLAND_SMD	mbx147[6C4]														TP725	TESTPAD_	mbx147[16C6]								
	SL202	SHORTLAND_025MM	mbx147[8C3]														TP730	TESTPAD_	mbx147[16E4]								
	SL300	SHORTLAND_SMD	mbx147[9D4]														TP732	TESTPAD_	mbx147[16D4]								
	SL302	SHORTLAND_025MM	mbx147[9D5]														TP734	TESTPAD_	mbx147[16D4]								
	SL1508	SHORTLAND_SMD	mbx147[22C1]														TP736	TESTPAD_	mbx147[16D4]								
	SL1510	SHORTLAND_SMD	mbx147[22B5]														TP737	TESTPAD_	mbx147[16D4]								
	SL1560	SHORTLAND_025MM	mbx147[22D2]														TP738	TESTPAD_	mbx147[16D4]								
	SL1580	SHORTLAND_SMD	mbx147[22B8]														TP744	TESTPAD_	mbx147[16E6]								
	SL1581	SHORTLAND_SMD	mbx147[22B8]														TP748	TESTPAD_	mbx147[16D6]								
	SL1900	SHORTLAND_SMD	mbx147[25D5]														TP749	TESTPAD_	mbx147[16C6]								
B	SL2520	SHORTLAND_SMD	mbx147[27C5]														TP750	TESTPAD_	mbx147[16D6]								
	SL4000	SHORTLAND_SMD	mbx147[37F8]														TP900	TESTPAD_	mbx147[18D5]								
	SL4001	SHORTLAND_SMD	mbx147[37E8]														TP901	TESTPAD_	mbx147[18D5]								
	SL4002	SHORTLAND_SMD	mbx147[37E8]														TP902	TESTPAD_	mbx147[18D5]								
	SL4003	SHORTLAND_SMD	mbx147[37E8]														TP903	TESTPAD_	mbx147[18D5]								
	SL4004	SHORTLAND_SMD	mbx147[37E8]														TP904	TESTPAD_	mbx147[18D6]								
	SL4005	SHORTLAND_SMD	mbx147[37E8]														TP905	TESTPAD_	mbx147[18D5]								
	SL4006	SHORTLAND_SMD	mbx147[37E8]														TP906	TESTPAD_	mbx147[18D5]								
	SL4016	SHORTLAND_SMD	mbx147[37B6]														TP907	TESTPAD_	mbx147[18C5]								
	SL4120	SHORTLAND_01MM	mbx147[39D7]														TP930	TESTPAD_	mbx147[18D8]								
A	SL4129	SHORTLAND_01MM	mbx147[39D5]														TP931	TESTPAD_	mbx147[18D8]								
	SL4150	SHORTLAND_01MM	mbx147[39D2]														TP932	TESTPAD_	mbx147[18D6]								
	SL4151	SHORTLAND_SMD	mbx147[39C4]														TP933	TESTPAD_	mbx147[18D6]								
	SL4152	SHORTLAND_SMD	mbx147[39C4]														TP950	TESTPAD_	mbx147[18D2]								
	SL4159	SHORTLAND_01MM	mbx147[39D4]														TP951	TESTPAD_	mbx147[18D1]								
	SL4200	SHORTLAND_SMD	mbx147[40E6]														TP952	TESTPAD_	mbx147[18D1]								
	SL4350	SHORTLAND_SMD	mbx147[41C6]														TP953	TESTPAD_	mbx147[18D1]								
	SL4610	SHORTLAND_SMD	mbx147[44E7]														TP954	TESTPAD_	mbx147[18D2]								
	SL4611	SHORTLAND_SMD	mbx147[44D7]														TP955	TESTPAD_	mbx147[18D2]								
	SL4620	SHORTLAND_SMD	mbx147[44D7]														TP956	TESTPAD_	mbx147[18D1]								
																		TP957	TESTPAD_	mbx147[18C1]									
																		TP960	TESTPAD_	mbx147[18D4]									
																		TP961	TESTPAD_	mbx147[18D4]									
																		TP962	TESTPAD_	mbx147[18D2]									
																		TP963	TESTPAD_	mbx147[18D2]									
																		TP1561	TESTPAD_	mbx147[22D1]									
																		TP2500	TESTPAD_	mbx147[27D1]									
																		X200	XTAL_8045	mbx147[8D7]									
																		X1500	XTAL_7015	mbx147[22C5]									
																		X1800	XTAL_3225+2	mbx147[24C7]									
																		X2500	XTAL_8045	mbx147[27C4]									
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