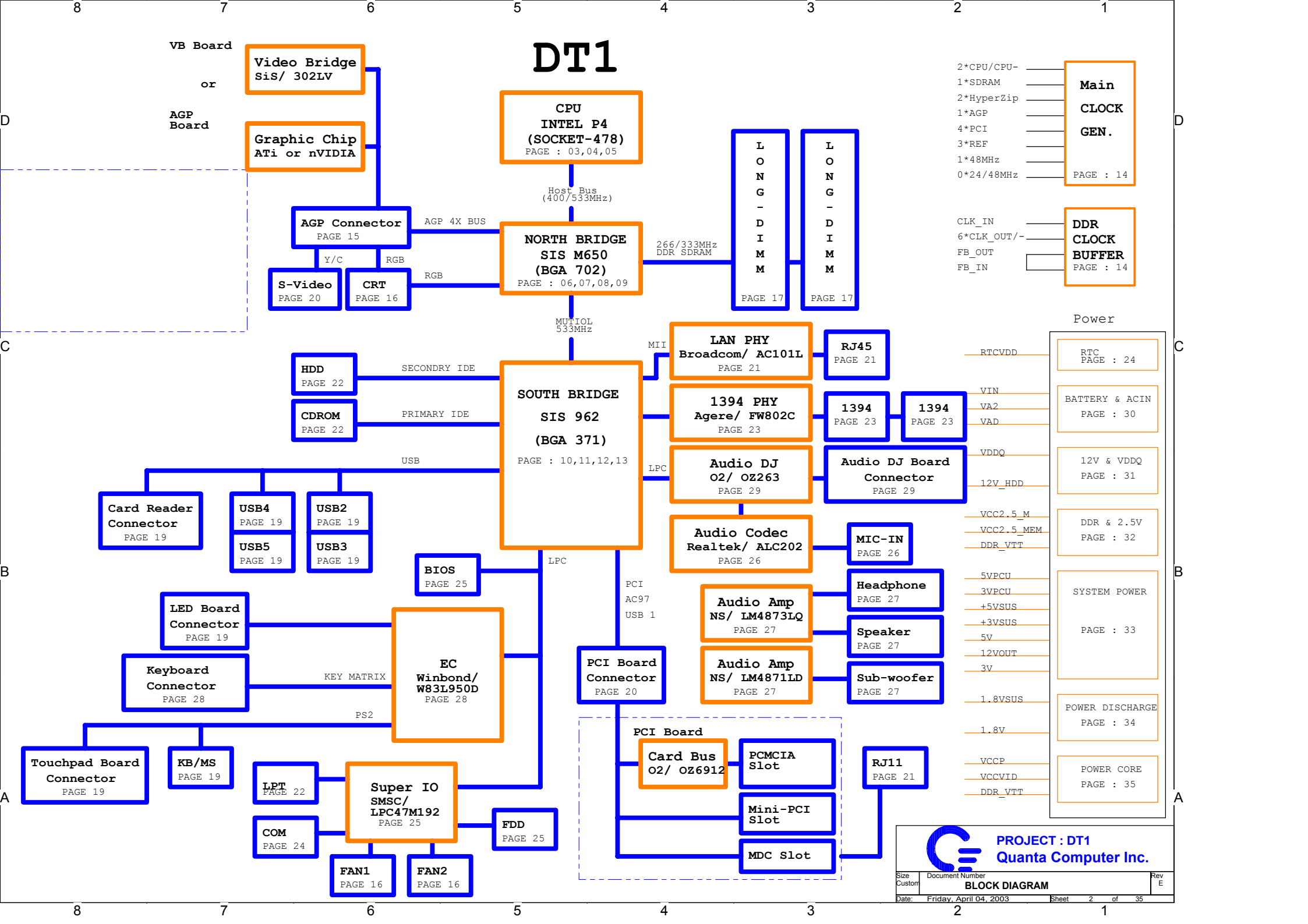
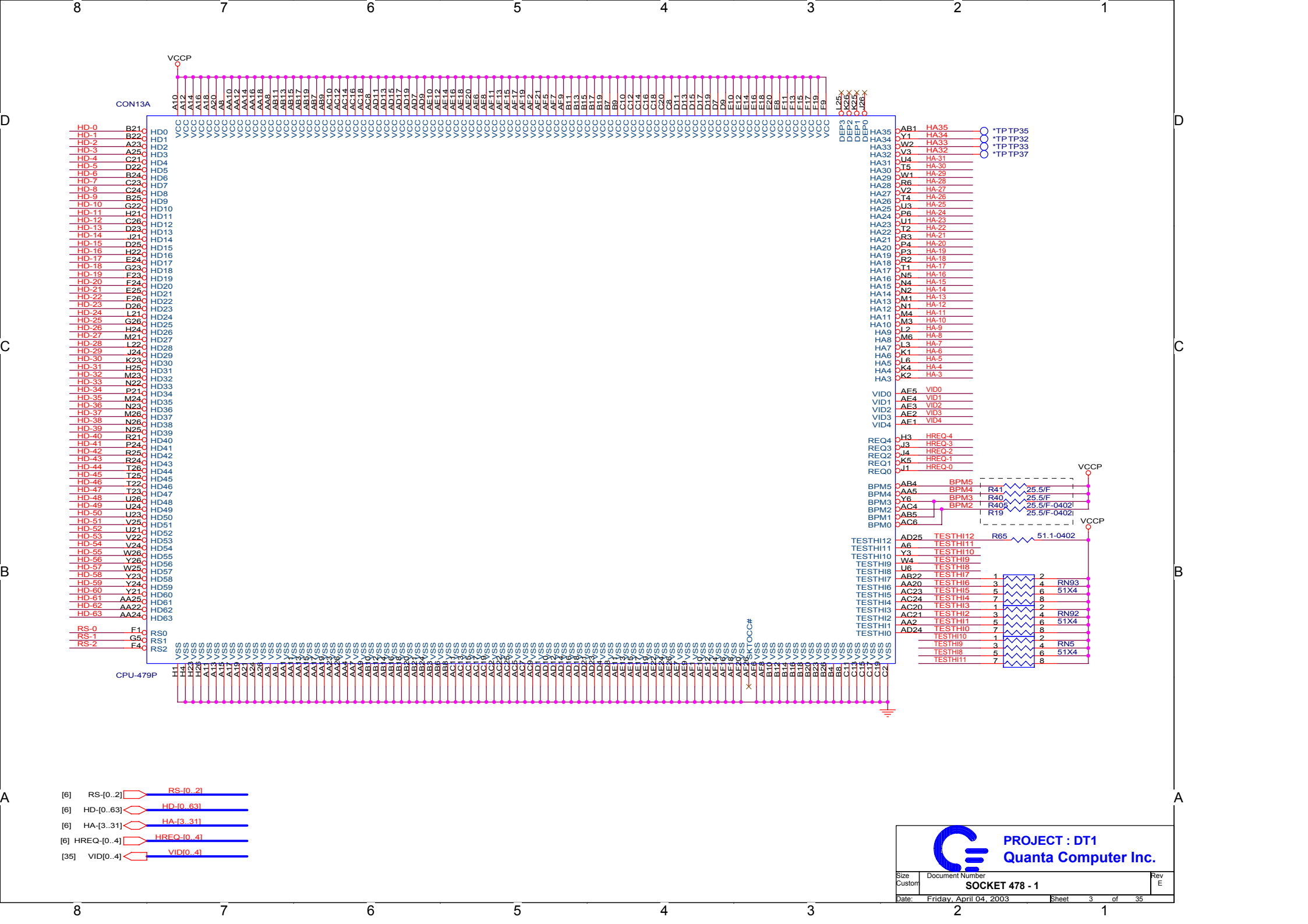


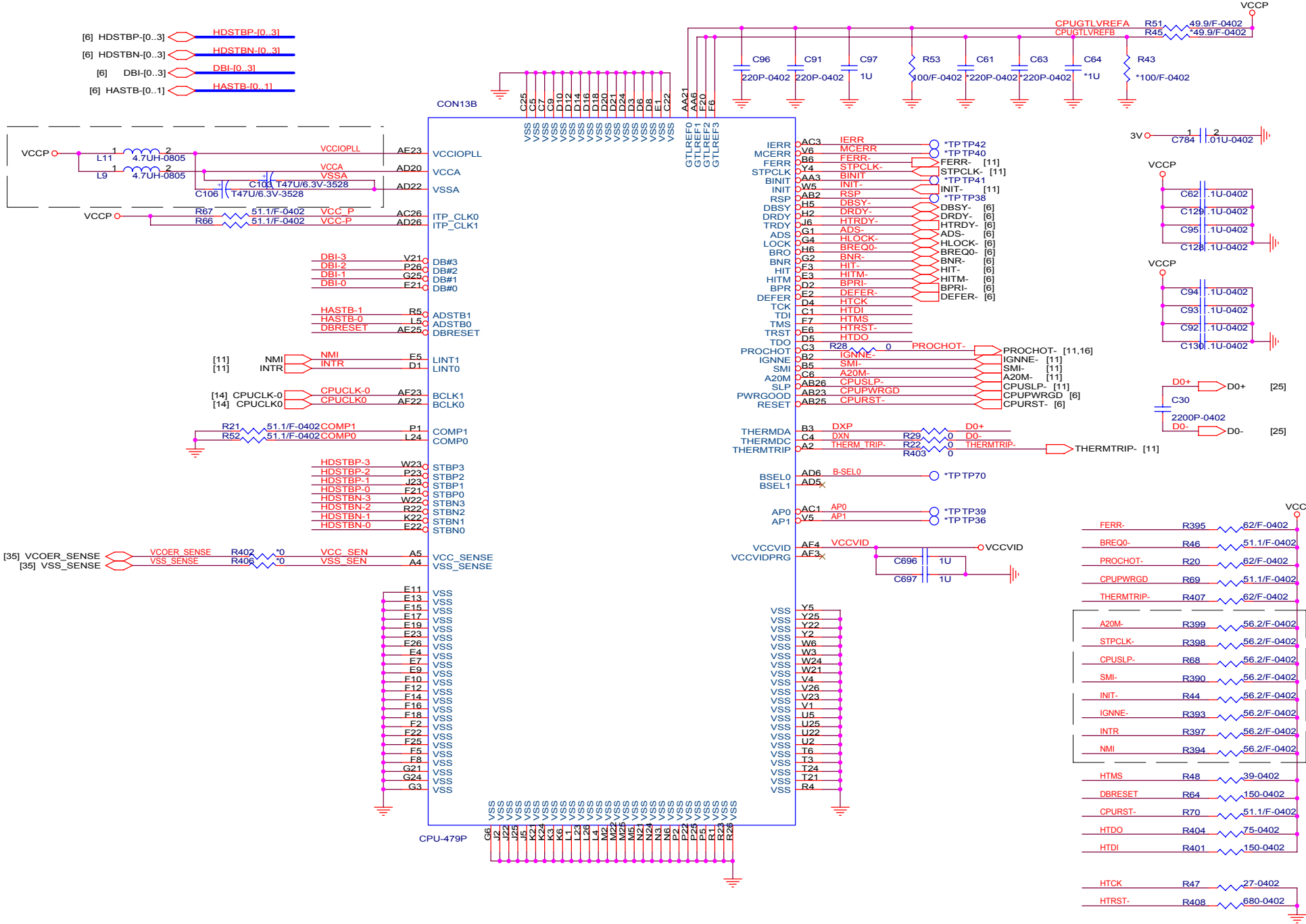
QUANTA COMPUTER INC.

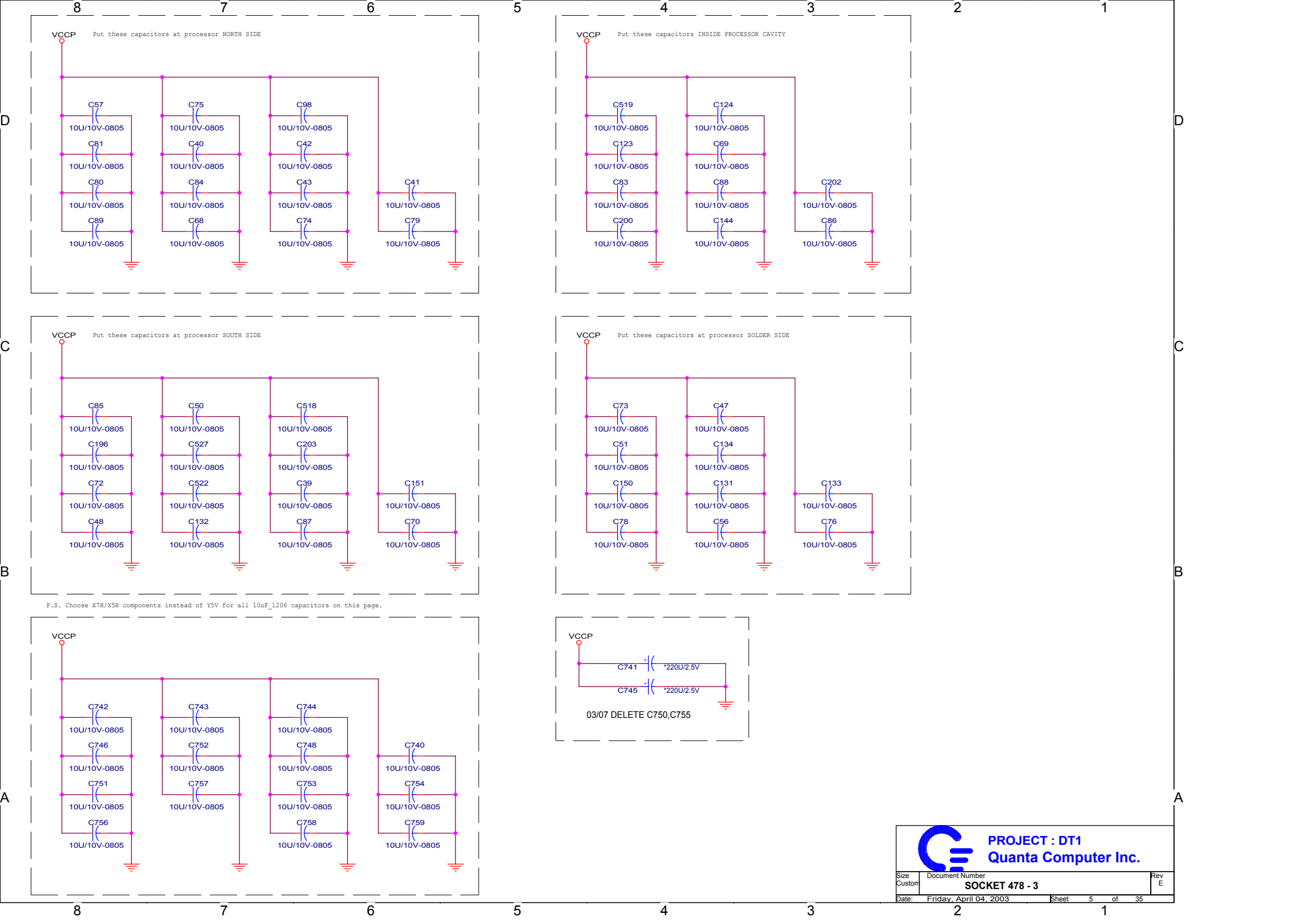
(March/31/2003) Rev : E

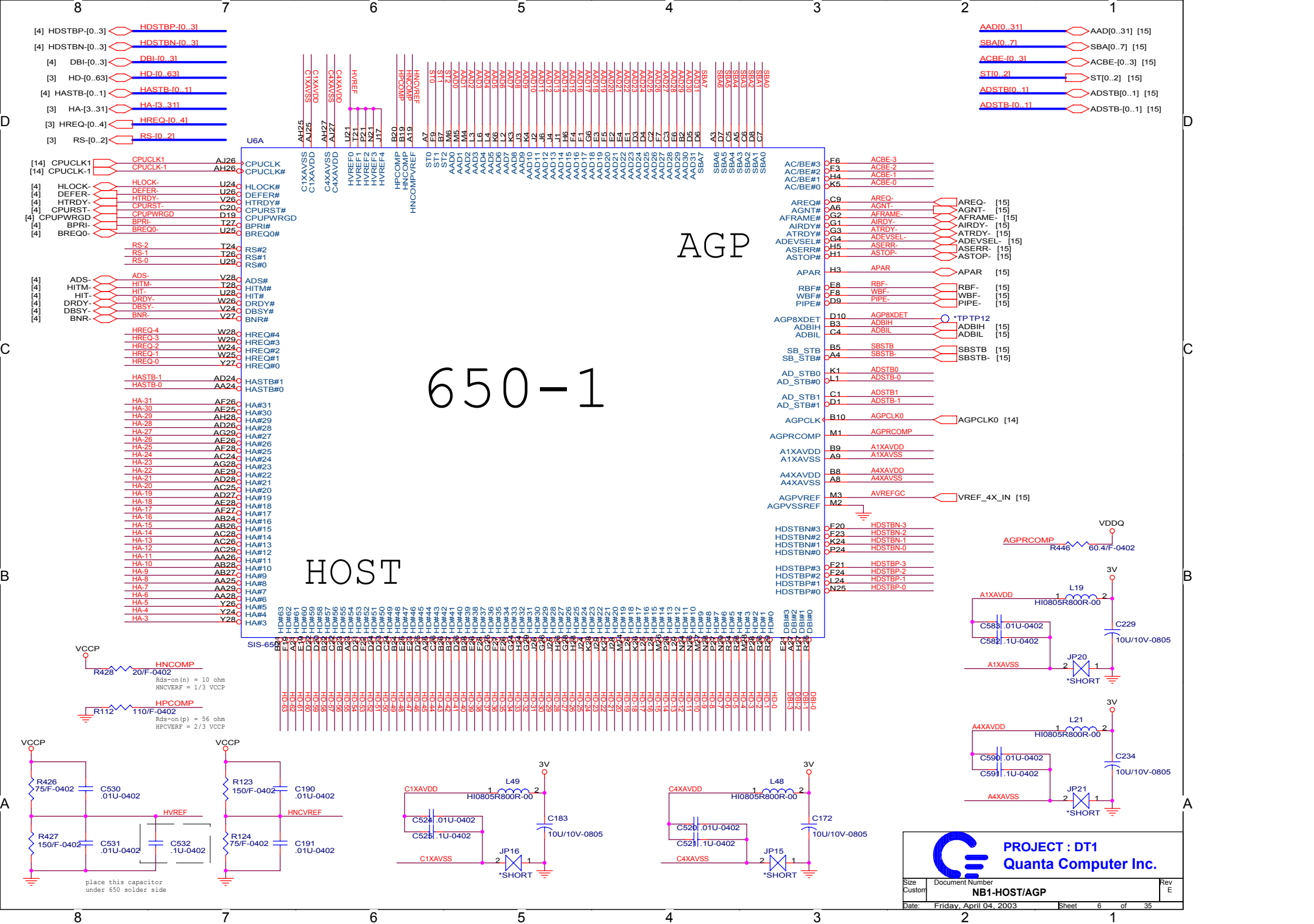
PAG	Content	PAG	Content
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2	650 System Block Diagram	32	POWER - DDR & 2.5V
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4	SOCKET 478 - 2	34	POWER DISCHARGE
5	SOCKET 478 - 3	35	POWER CORE
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7	650-2 (Memory for DDR)	37	
8	650-3 (HyperZip/VGA/Misc.)	38	
9	650-4 (Powers)	39	
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29	AUDIO DJ (0Z263)	59	
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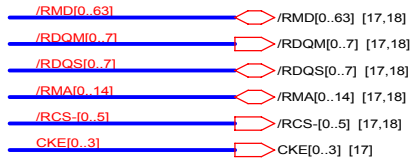




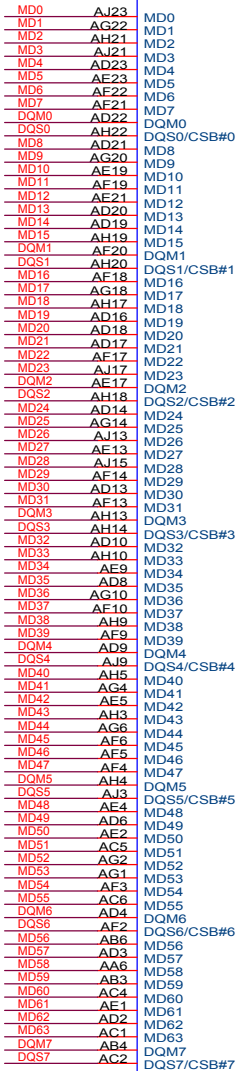
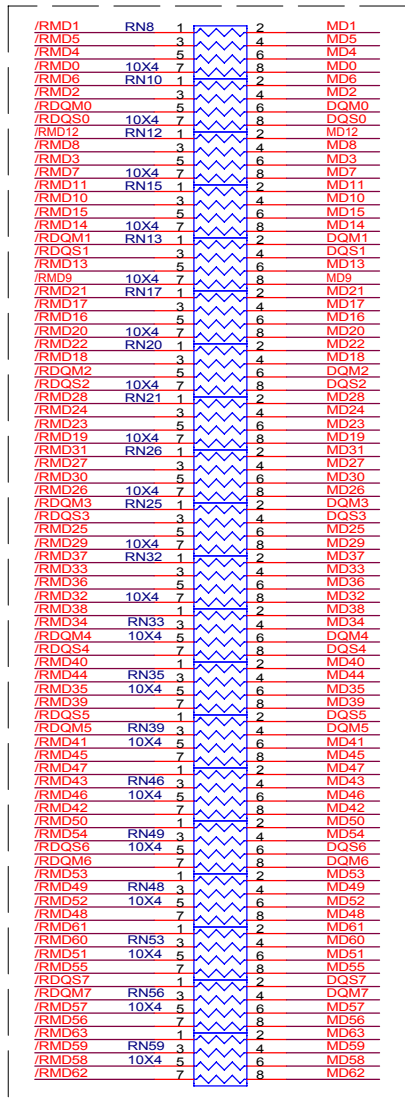








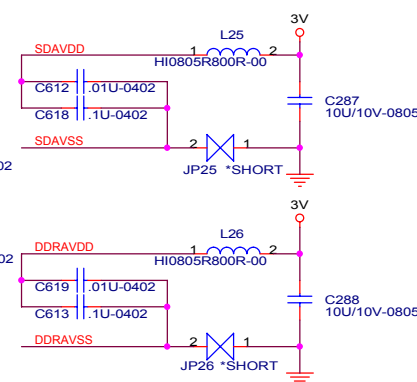
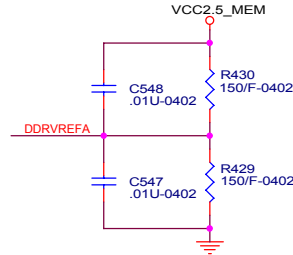
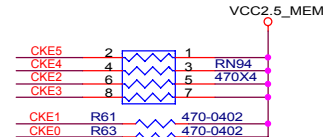
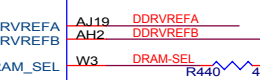
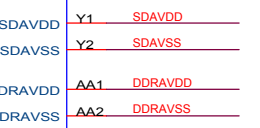
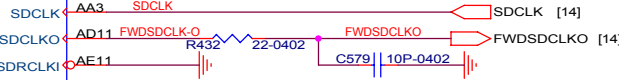
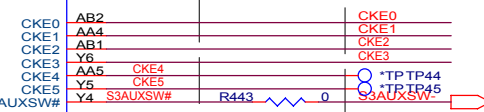
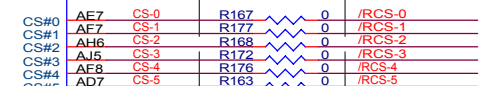
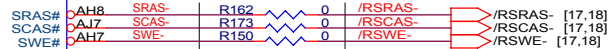
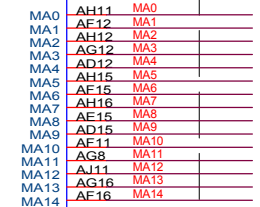
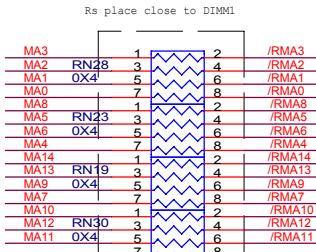
Rs place close to DIMM1



U6B

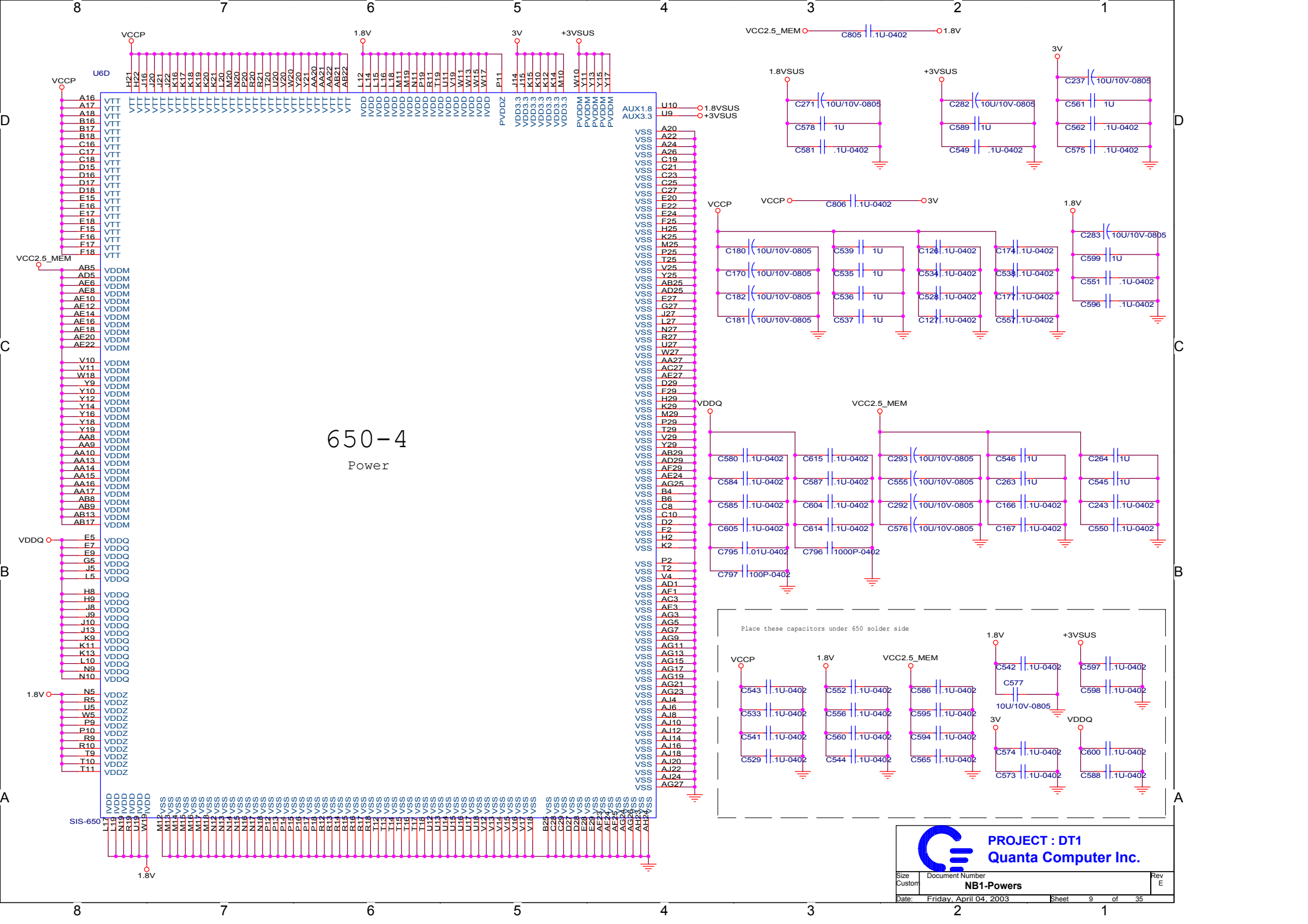
650-2

SIS-650

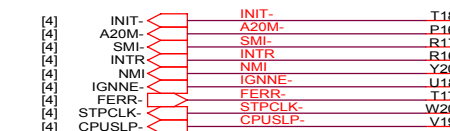


PROJECT : DT1
Quanta Computer Inc.

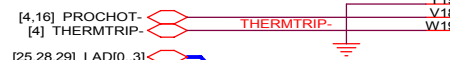
NB Hardware Trap Table				
				embedded pull-low (30~50K Ohm)
		0	1	Default
DILEN#	enable PLL	disable PLL	0	yes
DRAM_SEL	SDR	DDR	1(DDR)	yes
TRAP0	normal	NB debug mode	0	yes
TRAP1	TV selection, NTSC/PAL(0/1)			0
CSYNC	enable VB			0
RSYNC	enable VGA interrupt			1
LSYNC	enable panel link			0



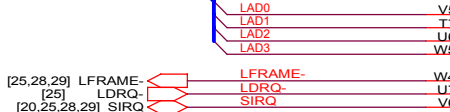
D



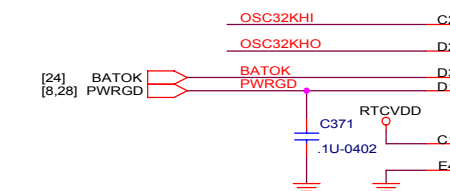
CPU_S



APIC

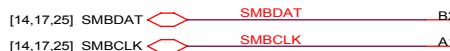


LPC

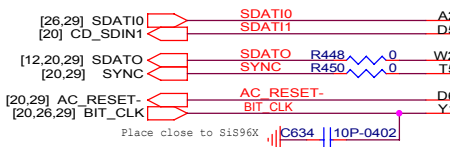


RTC

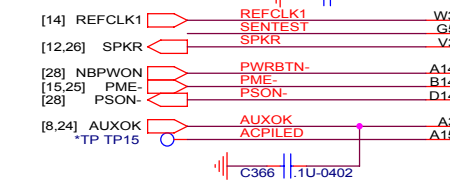
962-2



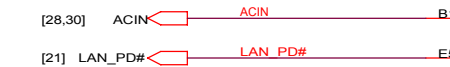
GPIO



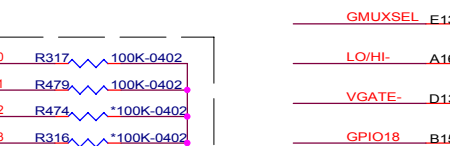
AC97



ACPI
/others



GPIO

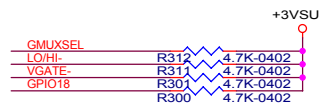
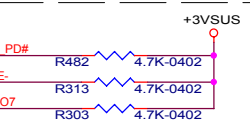


KBC
/geyserville

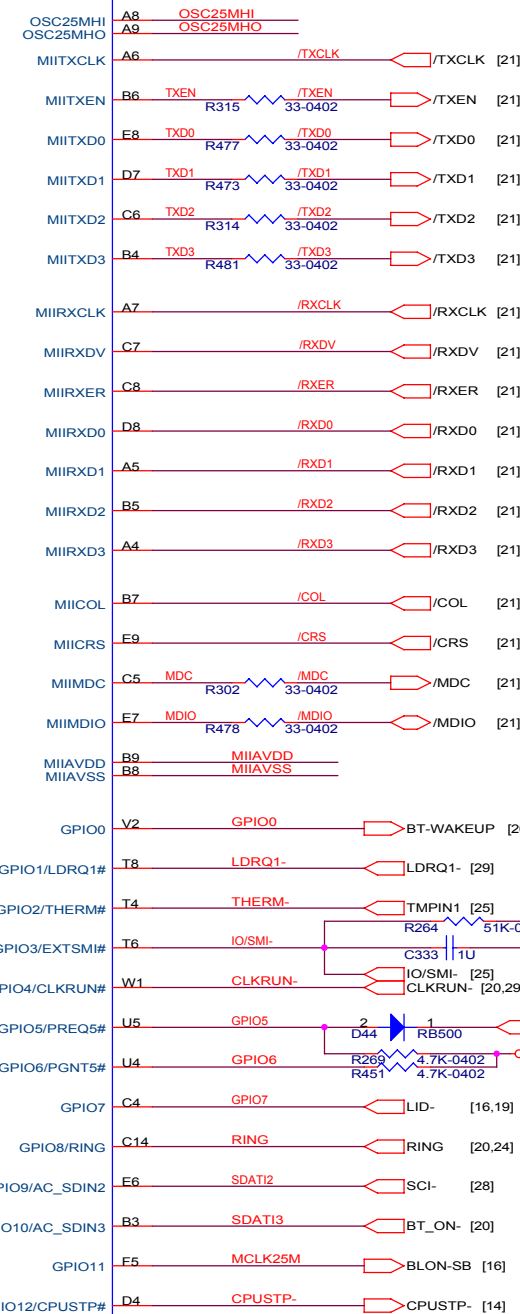
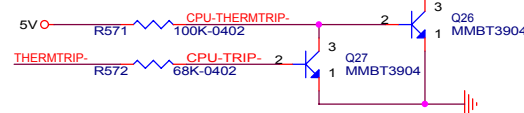


SIS-962

A

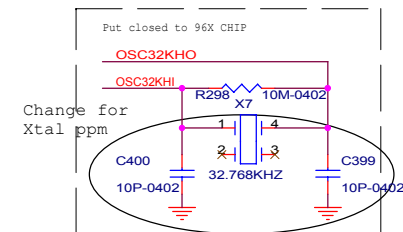


+3VSUS

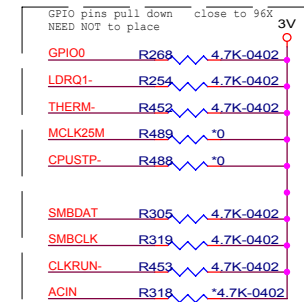
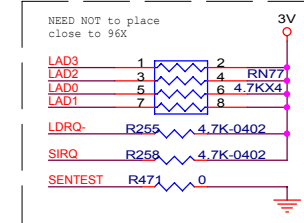
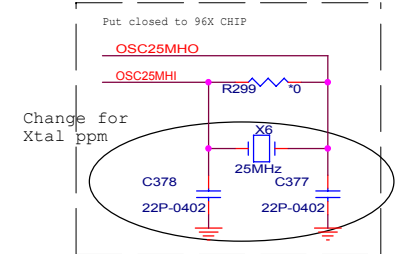


MII

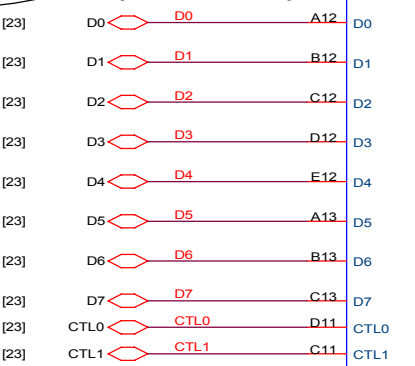
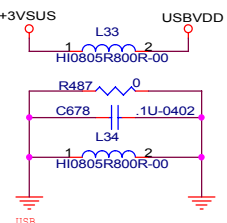
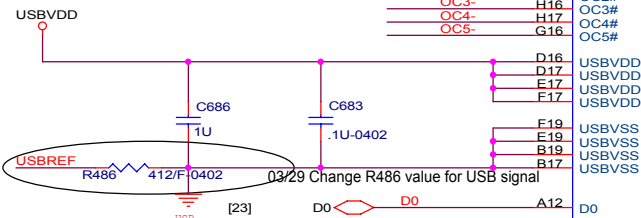
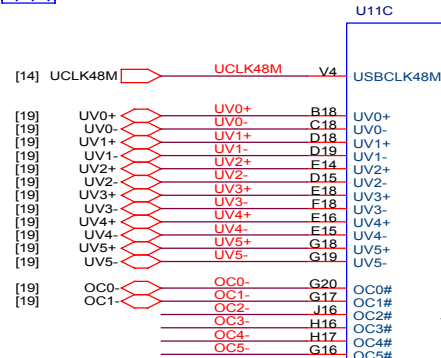
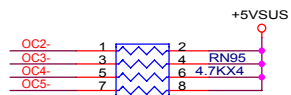
02/28 X7 COMPONENT FROM BG332768721 TO BG33768216



02/28 C399 FROM 20P TO 10P, C400 FROM 15P TO 10P



PROJECT : DT1
Quanta Computer Inc.



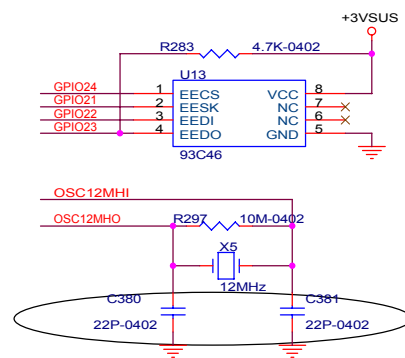
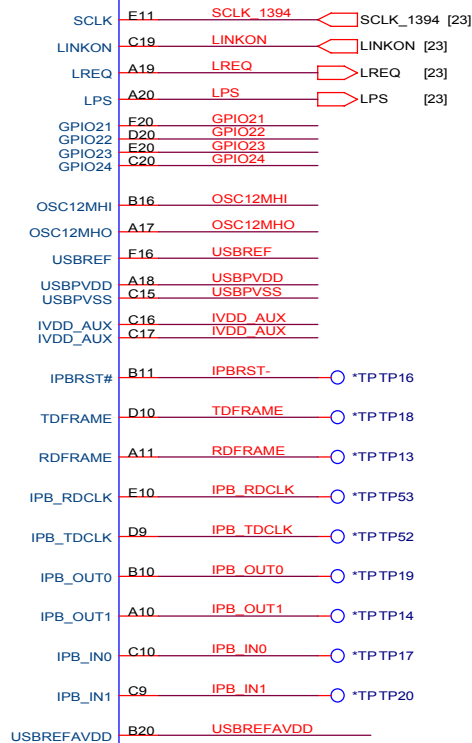
USB

962-3

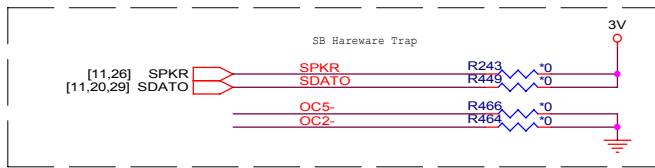
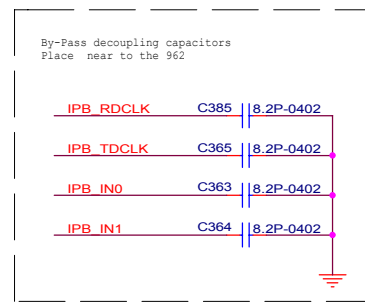
OTHERS

ACR-IPB
/ADSL
(For 962,963)

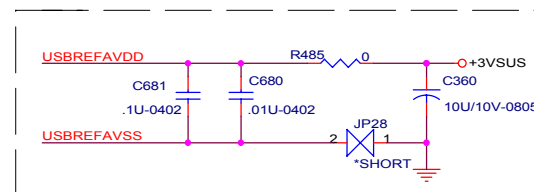
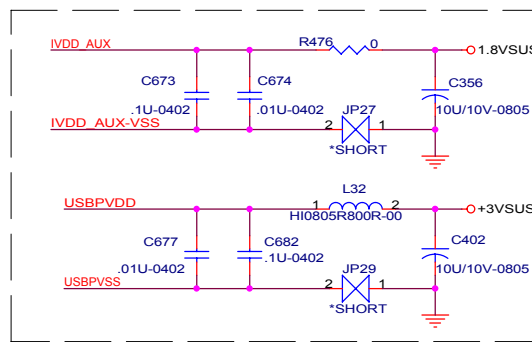
ACR-WIRELESS
/BLUETOOTH
(ONLY FOR 963)



3/29 Modify for Xtal

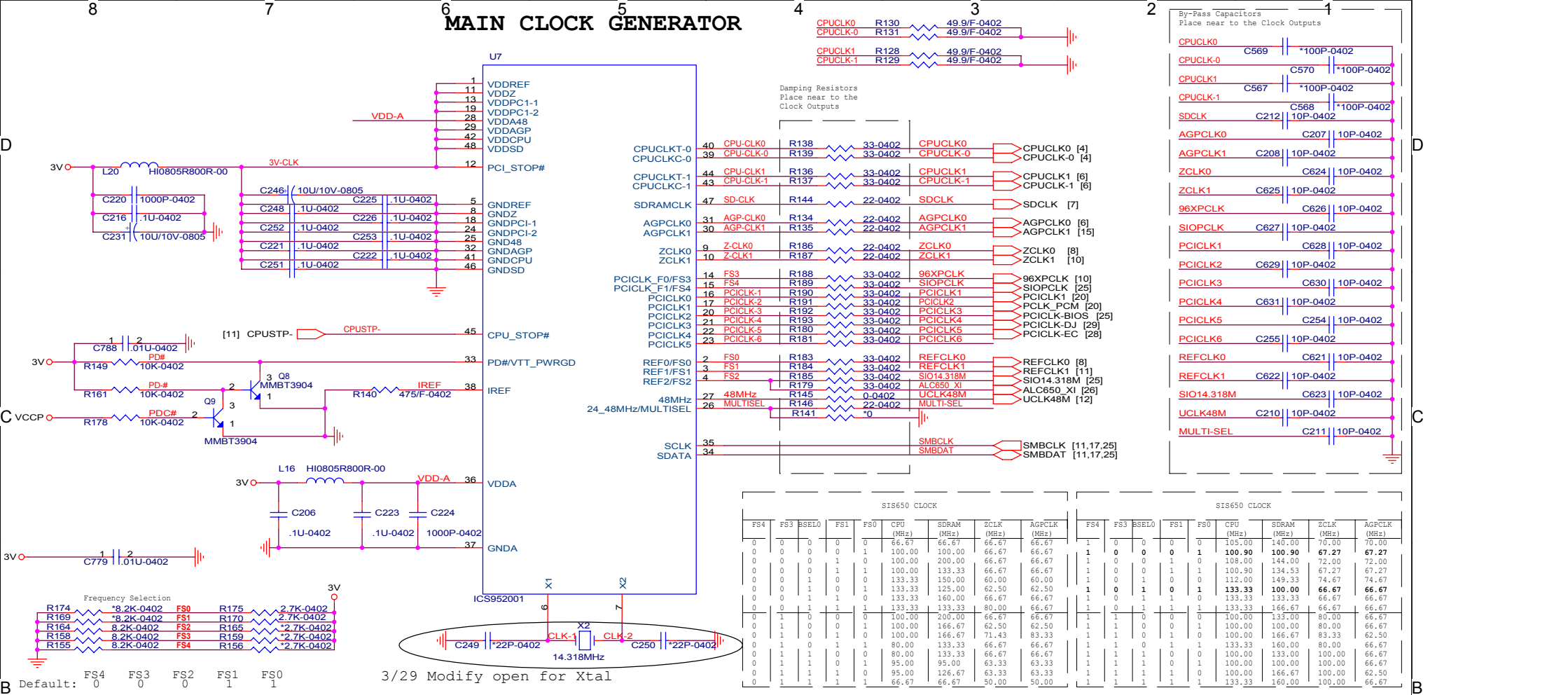


	0	1	Default
SPKR (LPC addr mapping)	disable	enable	0
SDATO (Trap mode)	ROM	PCI_AD	0
OC2-	enable	disable	1
OC5-	enable	disable	1

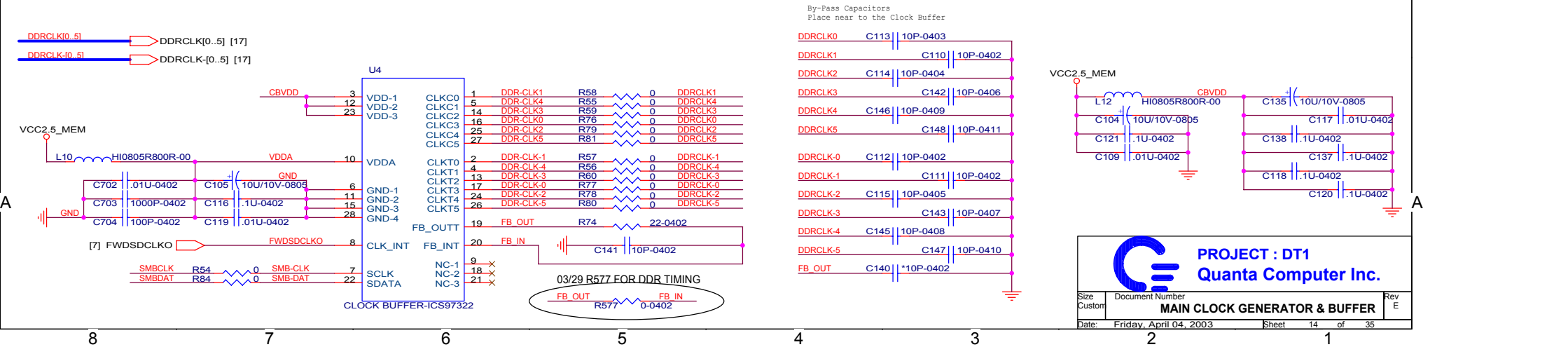


PROJECT : DT1
Quanta Computer Inc.

MAIN CLOCK GENERATOR

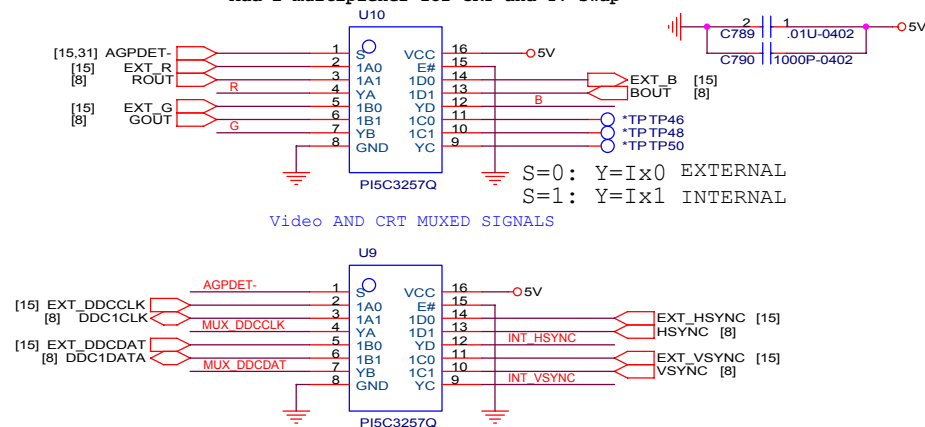
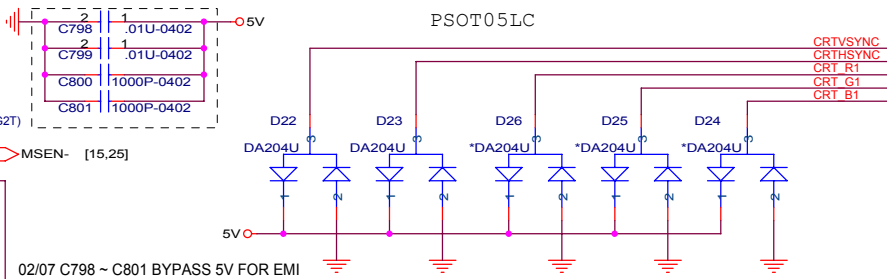
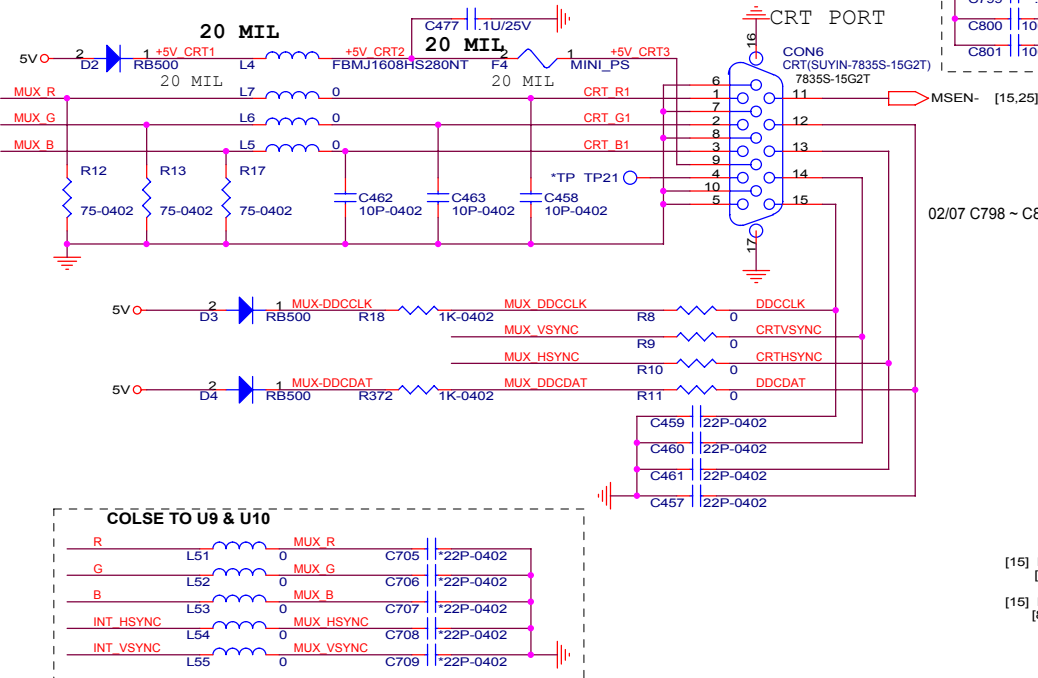


CLOCK BUFFER

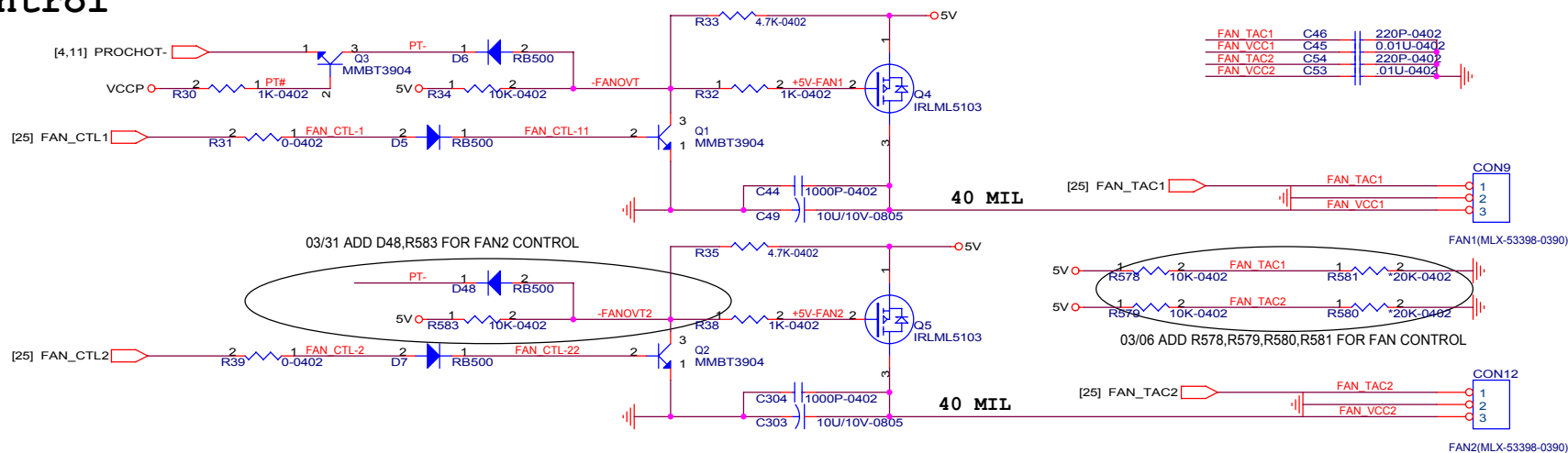


PROJECT : DT1
Quanta Computer Inc.

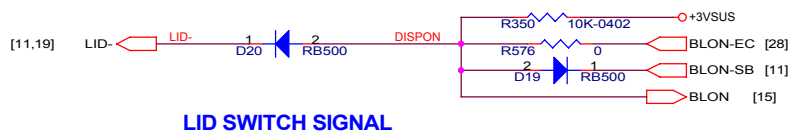
CRT



FAN Control

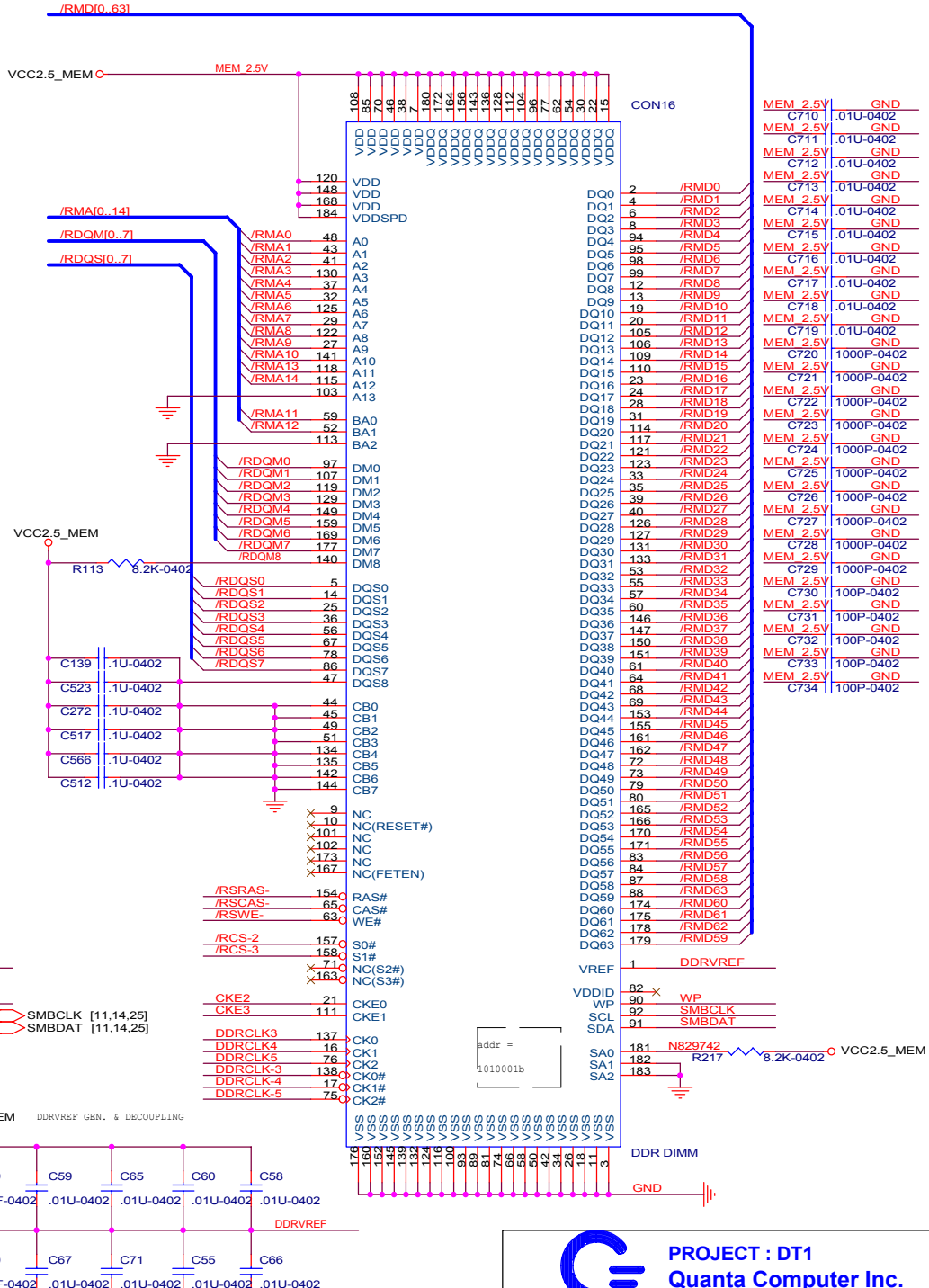
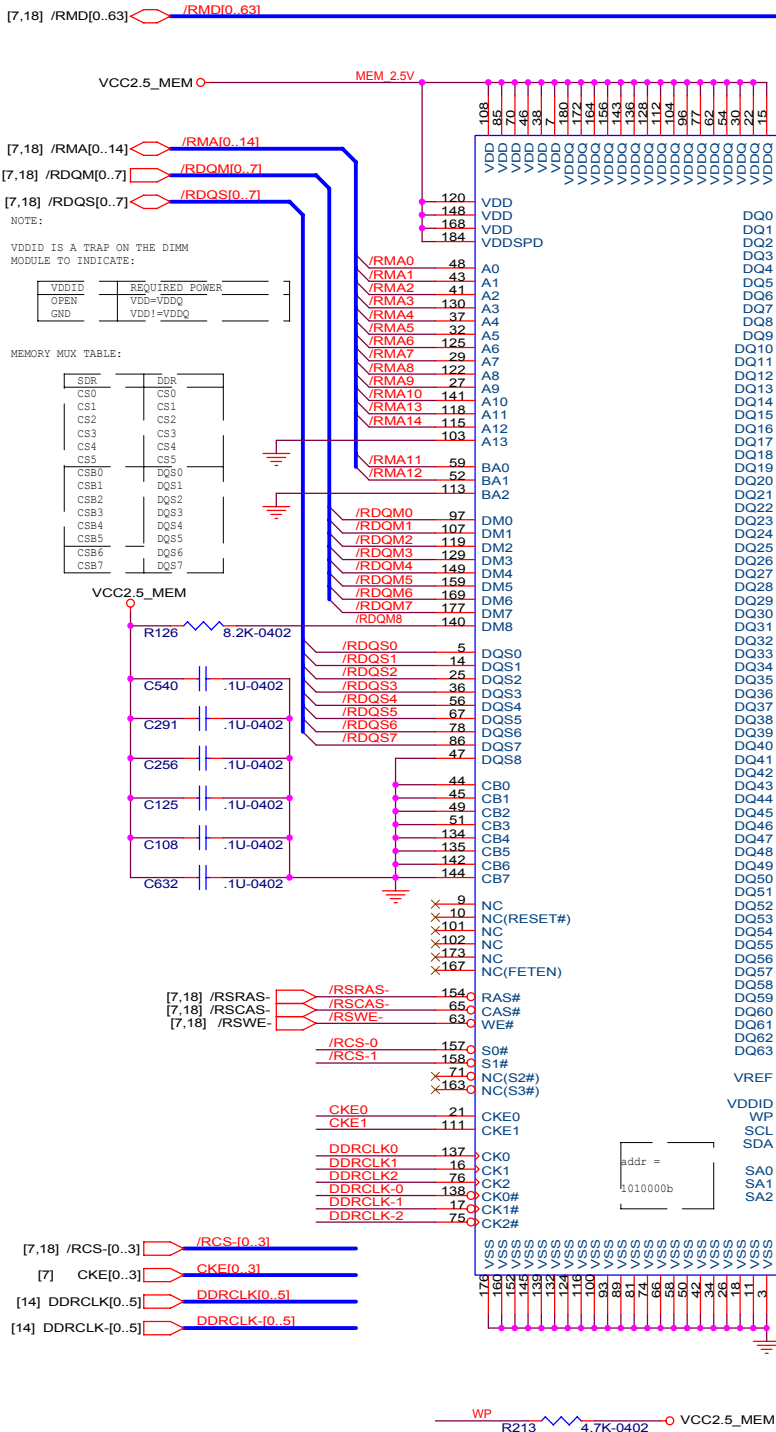


LCD-LID



PROJECT : DT1
Quanta Computer Inc.

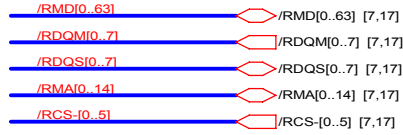
Size: Custom
Document Number: **CRT,LCD LID,BLON & FAN CONTROL**
Date: Friday, April 04, 2003
Sheet: 16 of 35
Rev: E



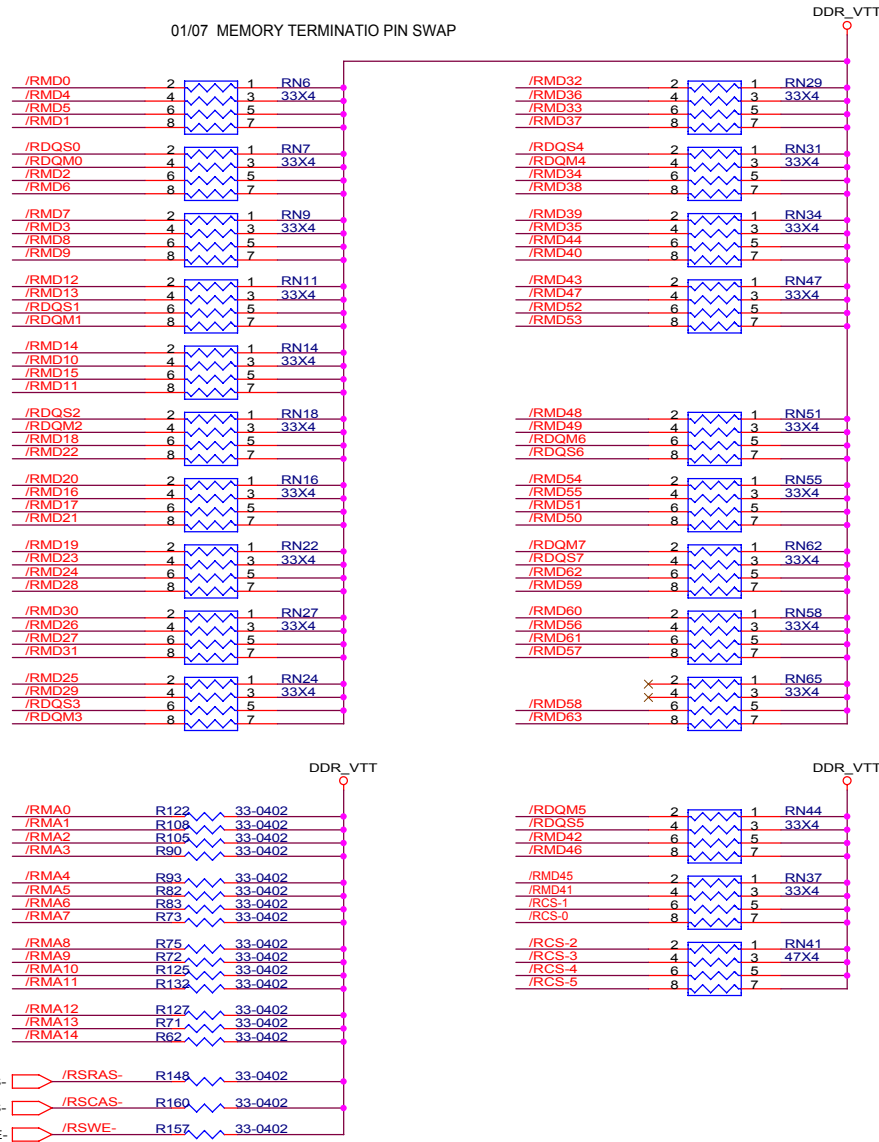
PROJECT : DT1
Quanta Computer Inc.

SSTL-2 Termination Resistors

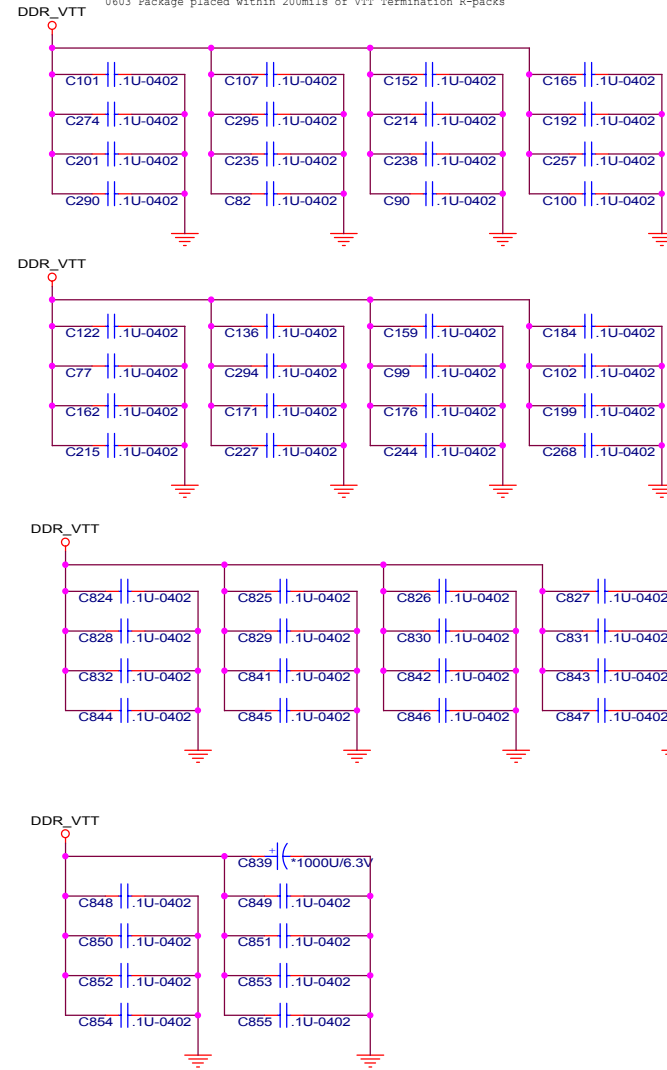
	SDR		DDR		
MD/DQM (/DQS)	LV-CMOS	Rs	SSTL-2	Rs	Rtt
MA/Control	LV-CMOS	0/10/-	SSTL-2	10	33
CS	LV-CMOS	10	SSTL-2	0	33
CKE	OD 3.3V	0	SSTL-2	0	47
			OD 2.5V		



01/07 MEMORY TERMINATIO PIN SWAP

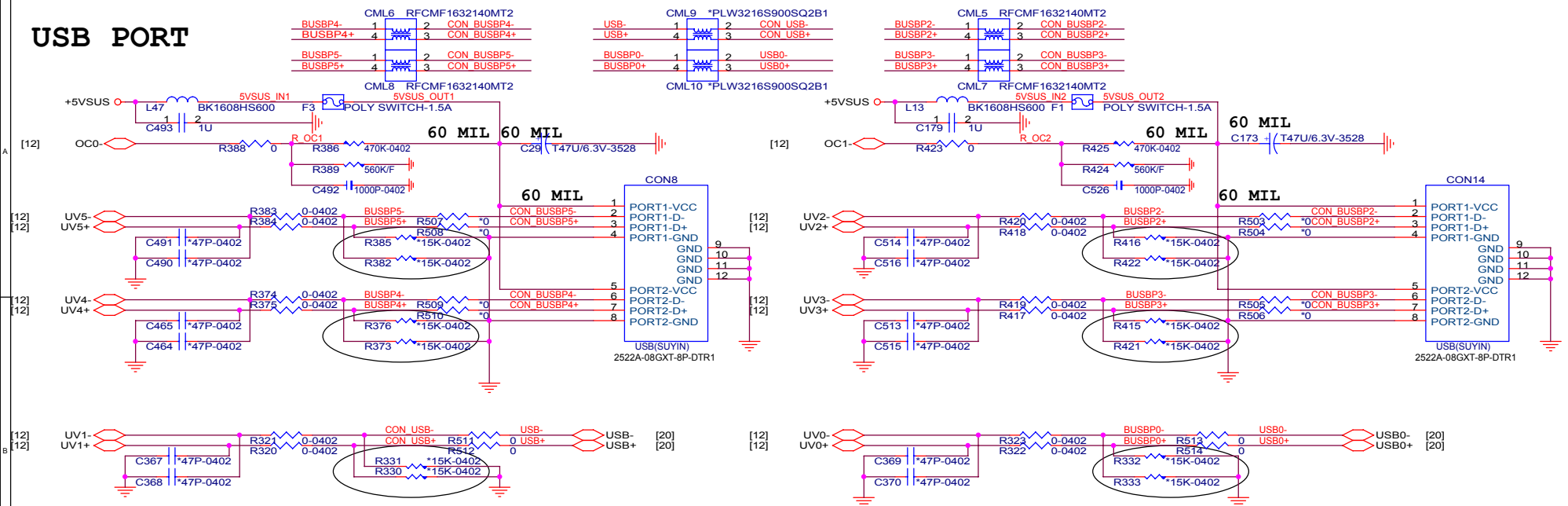


DECOUPLING CAPACITOR FOR SSTL-2 END TERMINATION VTT ISLAND
0603 Package placed within 200mils of VTT Termination R-packs



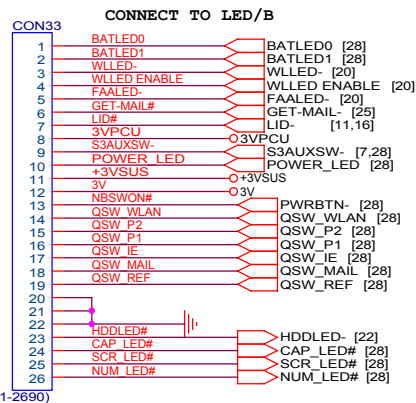
PROJECT : DT1
Quanta Computer Inc.

USB PORT

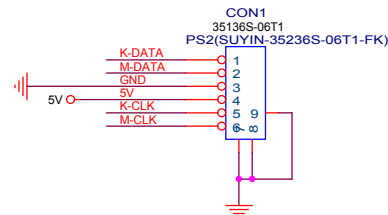


03/07 R330,R331,R332,R333,R373,R376,R382,R385,R416,R422 UNSTAFF

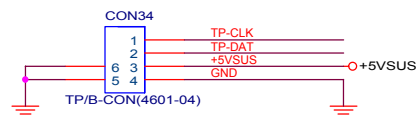
LED/B CONNECTOR



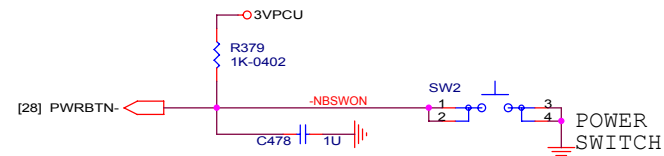
PS/2



TP/B CONNECTOR



POWER SW



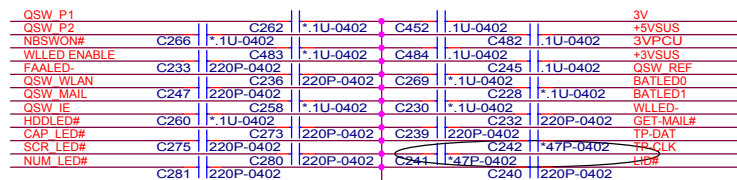
RESET SW



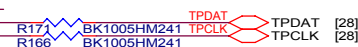
02/28 C45,C455,C456,C457 FROM 100P CHANGE TO 270P FOR EMI



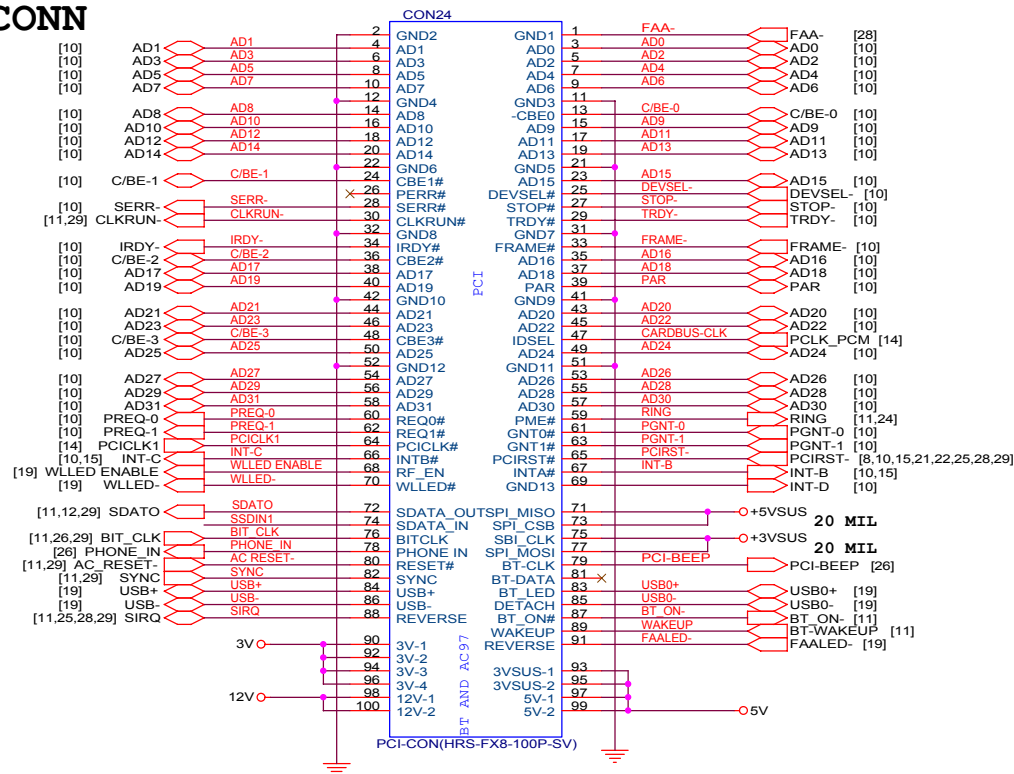
03/06 C453,C454,C455,C456 UNSTAFF



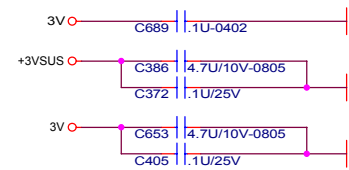
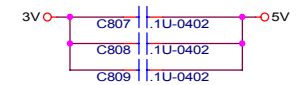
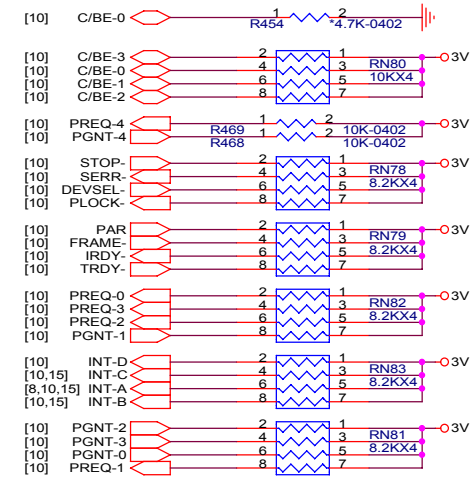
03/06 C241,C241 47P UNSTAFF



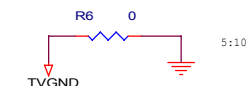
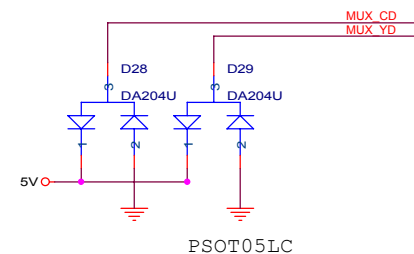
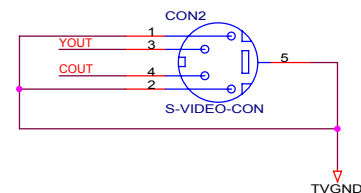
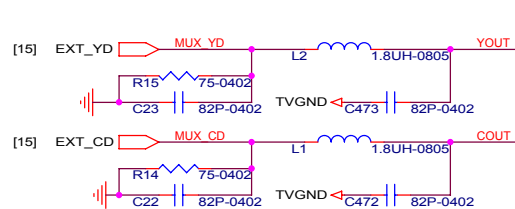
PCI-CONN



PCI PULL UP

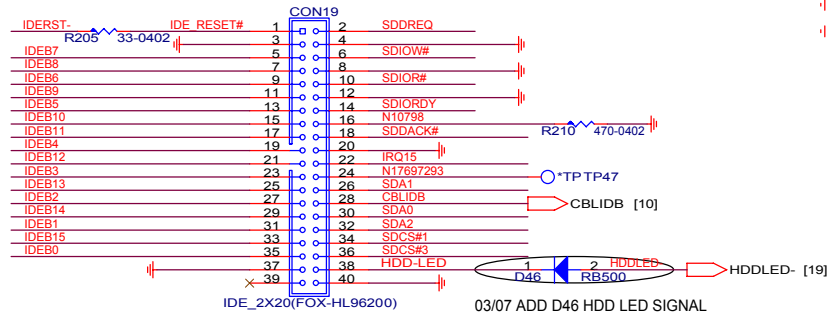


S-VIDEO (TV OUT)

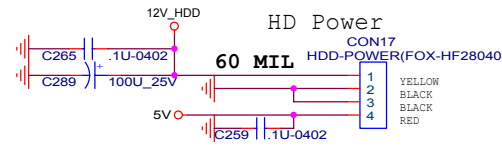


HDD,CD-ROM CONNECTOR

12/18 CON19 CHANGE COMPONENT



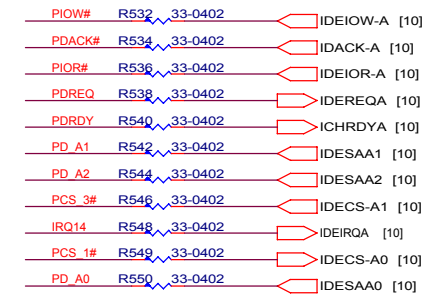
HDD POWER CONNECTOR



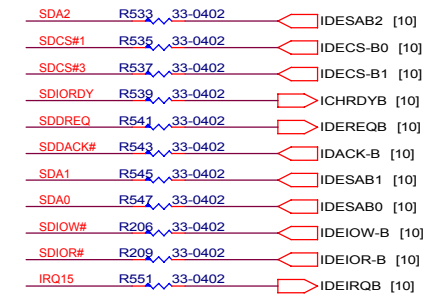
[10] IDE[A[0..15]] IDE[A0..15] IDE[B0..15] IDE[B[0..15]] [10]

PLACEMENT ON BOT SIDE

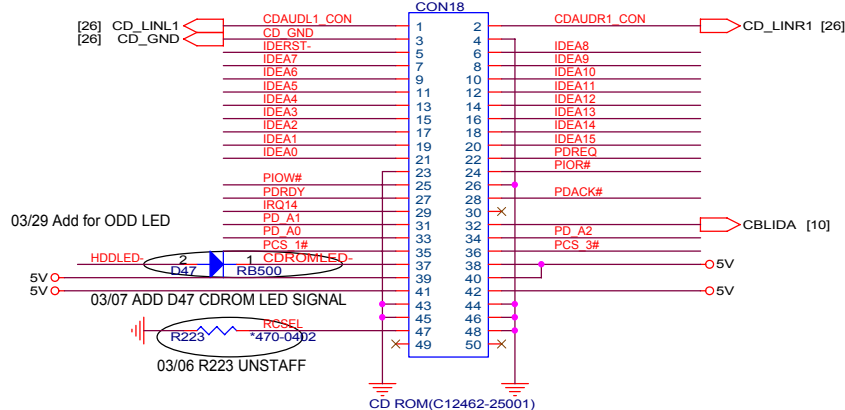
CLOSE TO CHIPSET SIDE



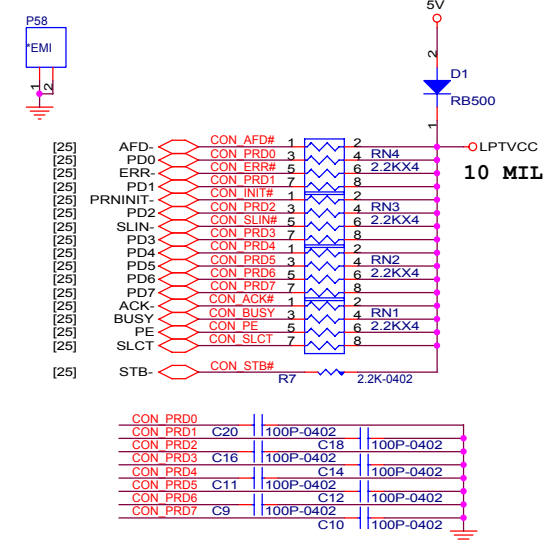
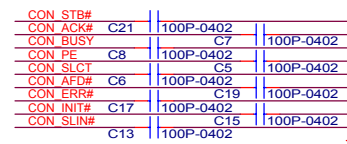
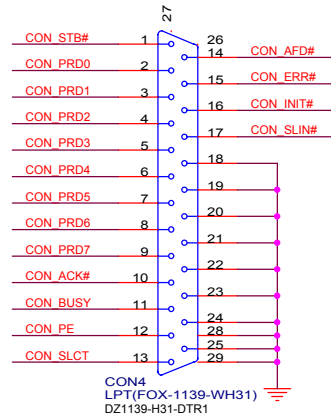
CLOSE TO CHIPSET SIDE



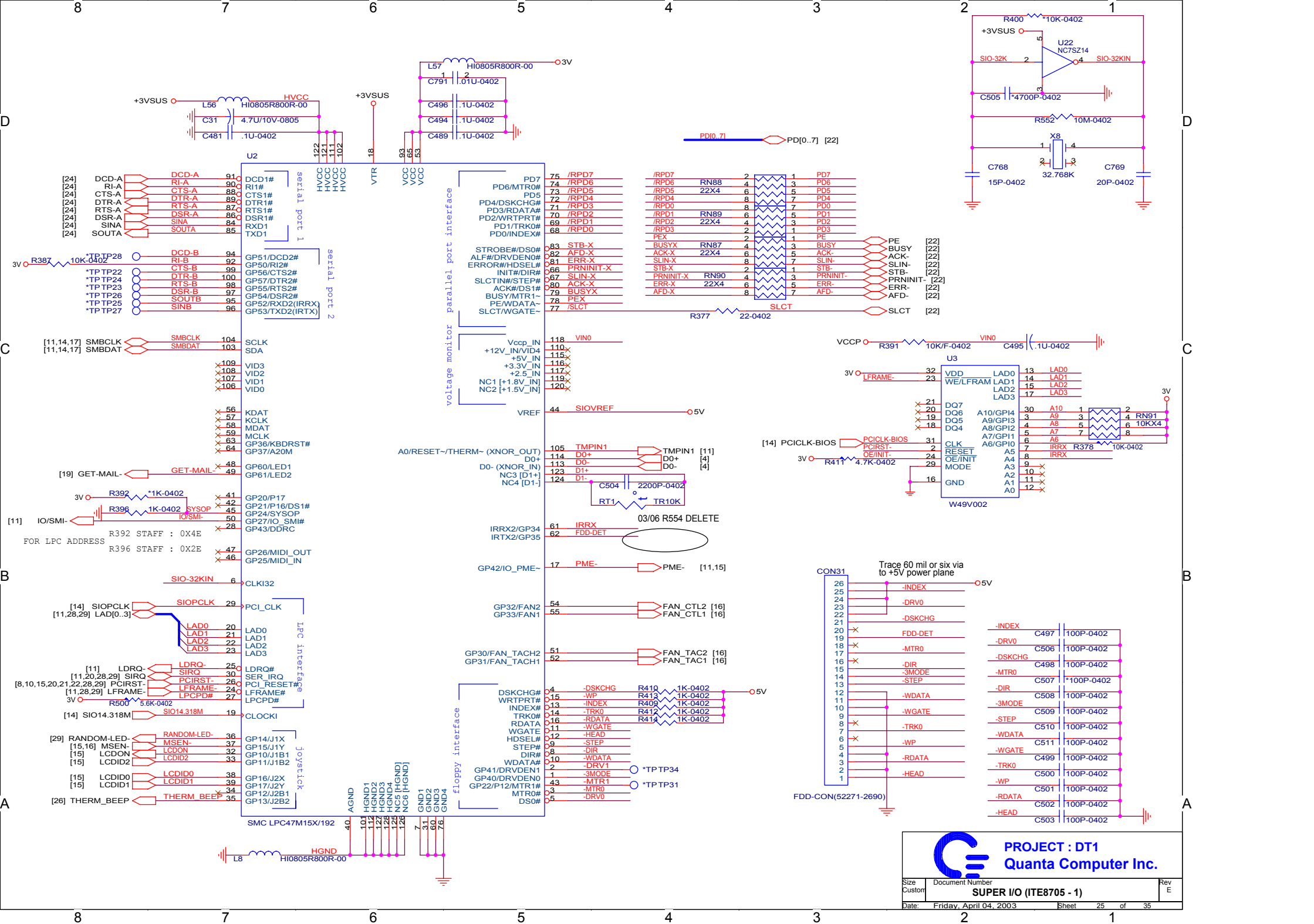
CD-ROM connector

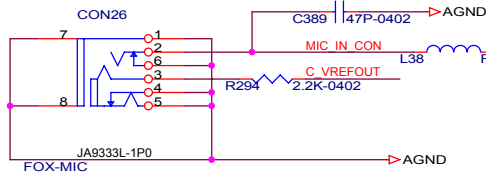
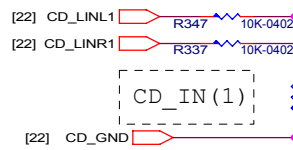
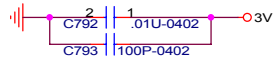


LPT CONN.

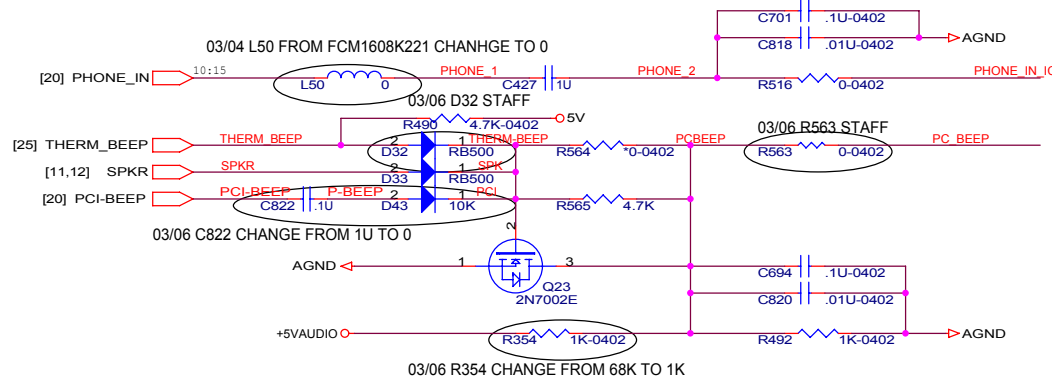


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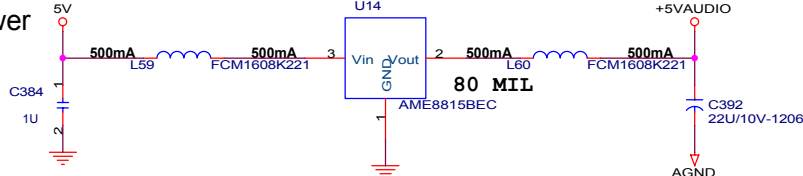




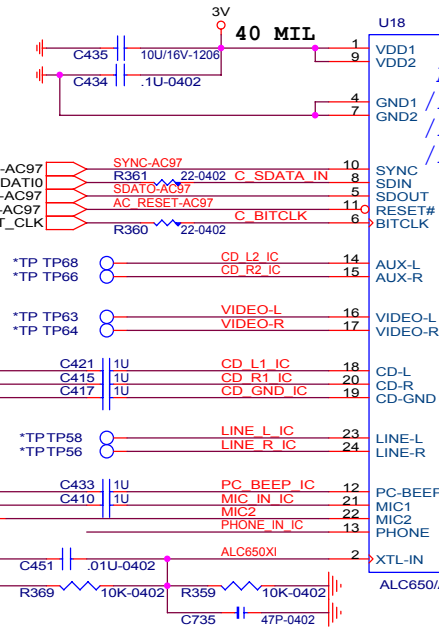
MIC_IN (PINK)



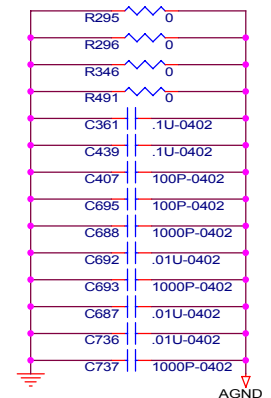
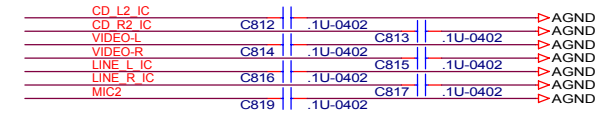
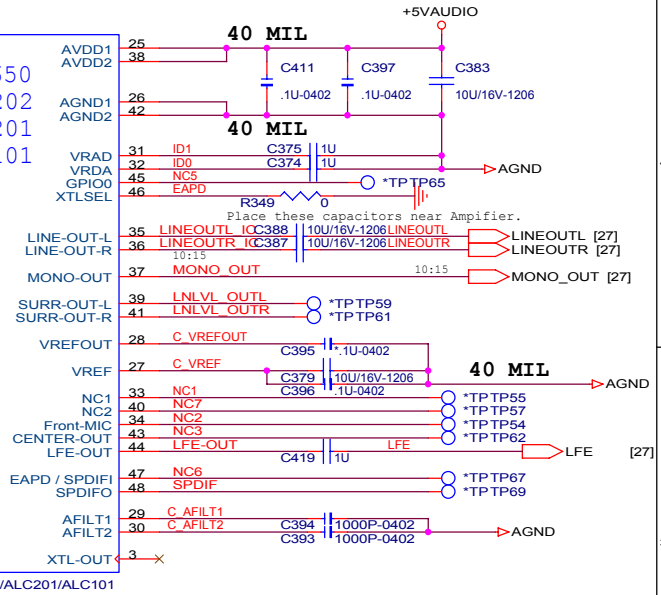
Audio Power



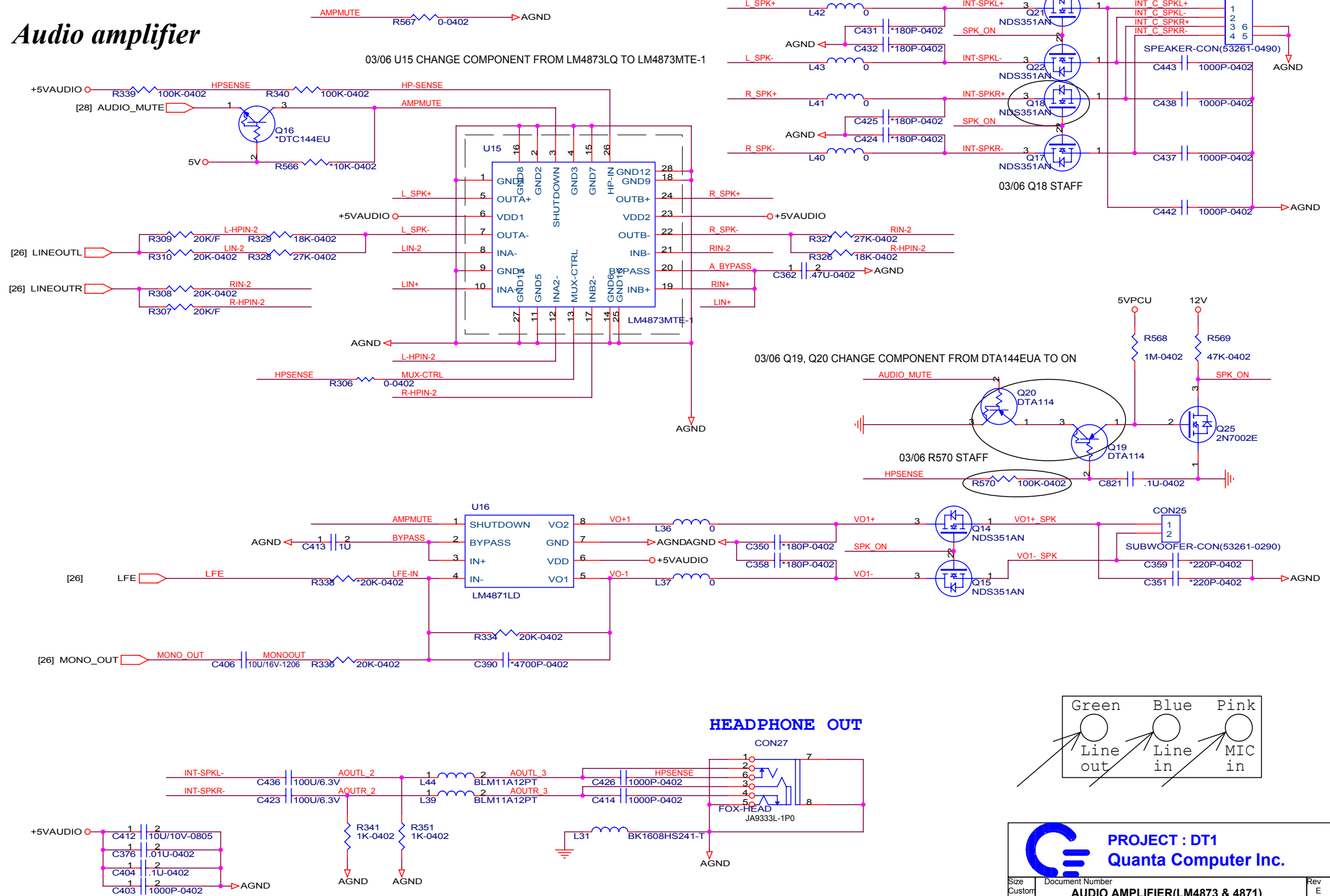
[29] SYNC-AC97
[11,29] SDAT10
[29] SDATO-AC97
[29] AC_RESET-AC97
[11,20,29] BIT_CLK




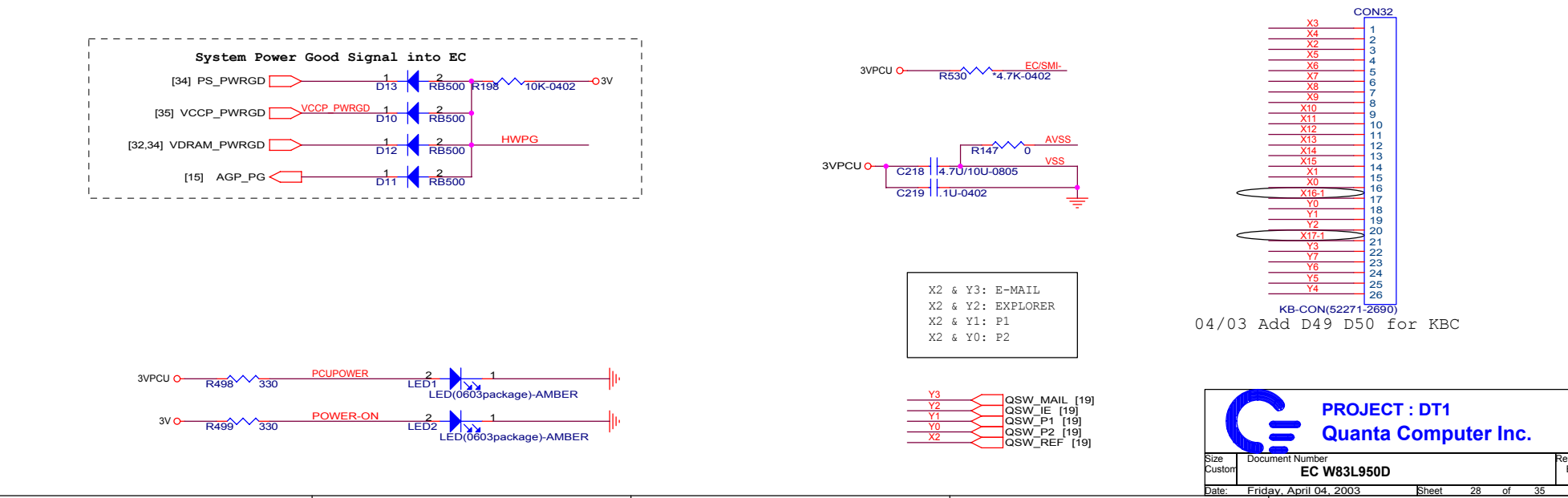
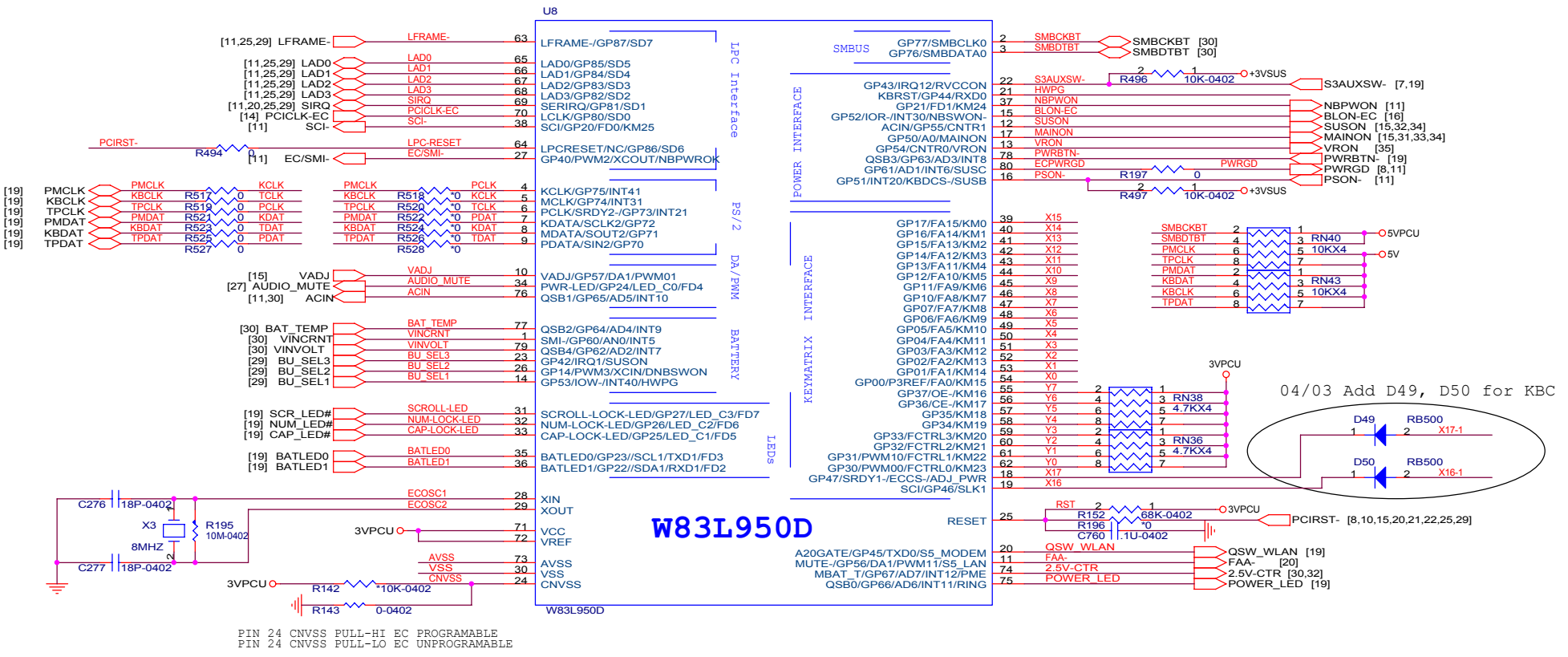
ALC650
/ALC202
/ALC201
/ALC101

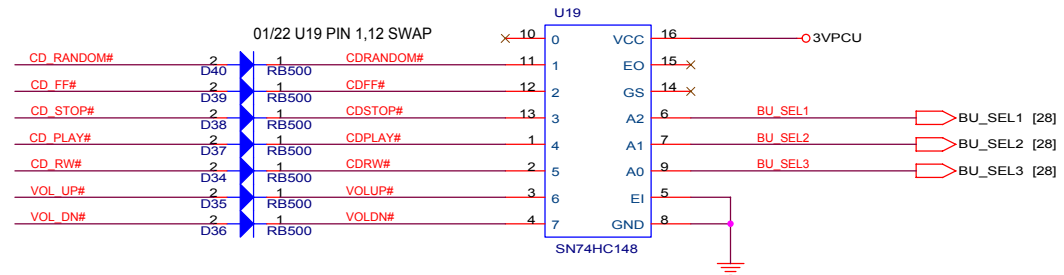
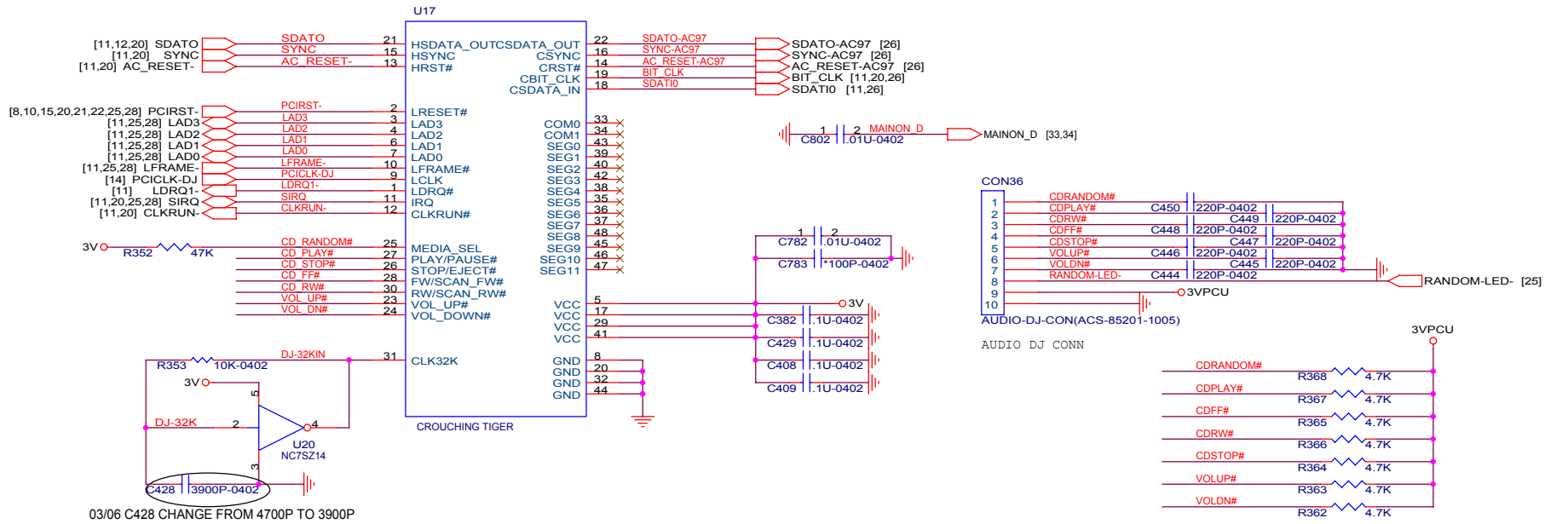


Audio amplifier

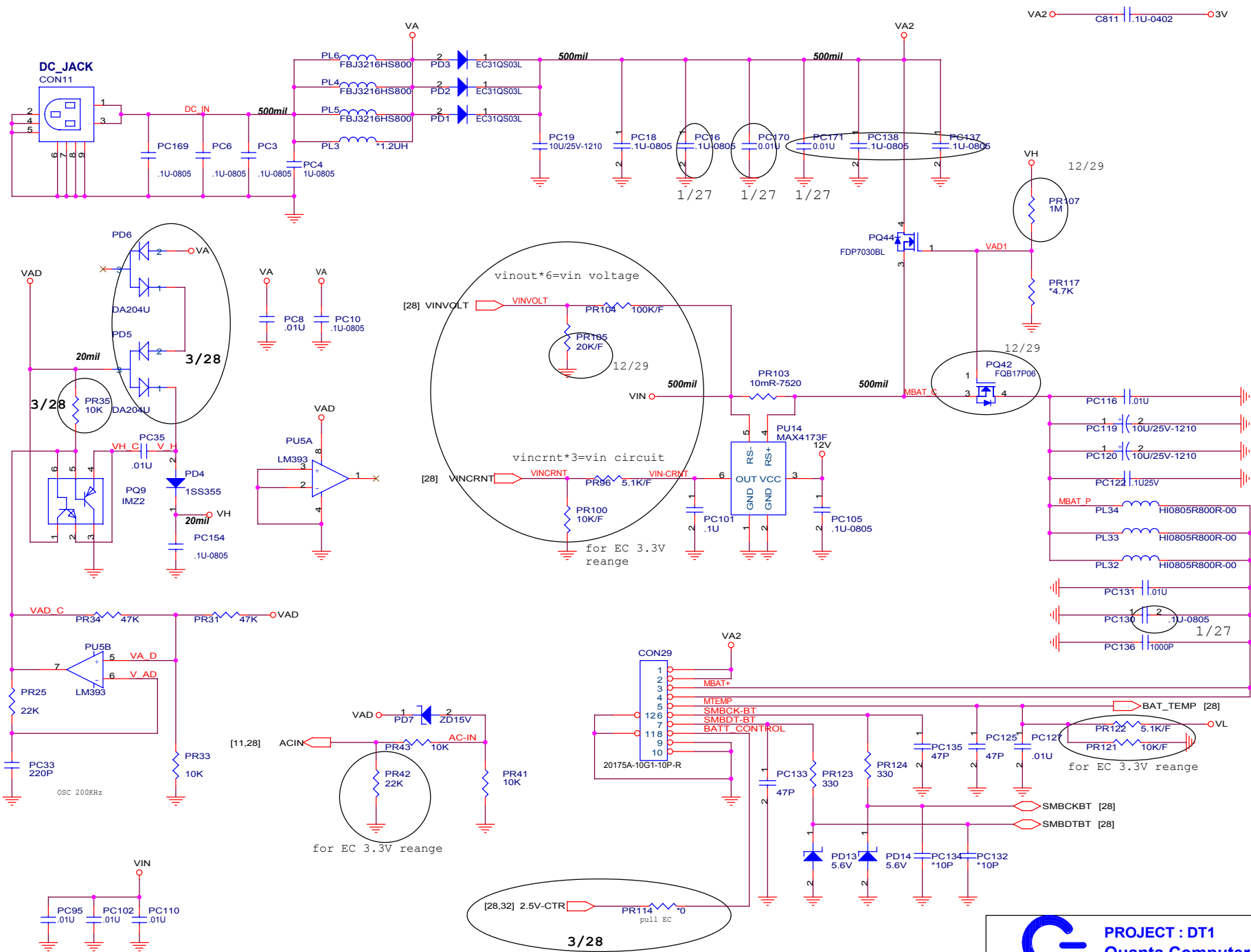


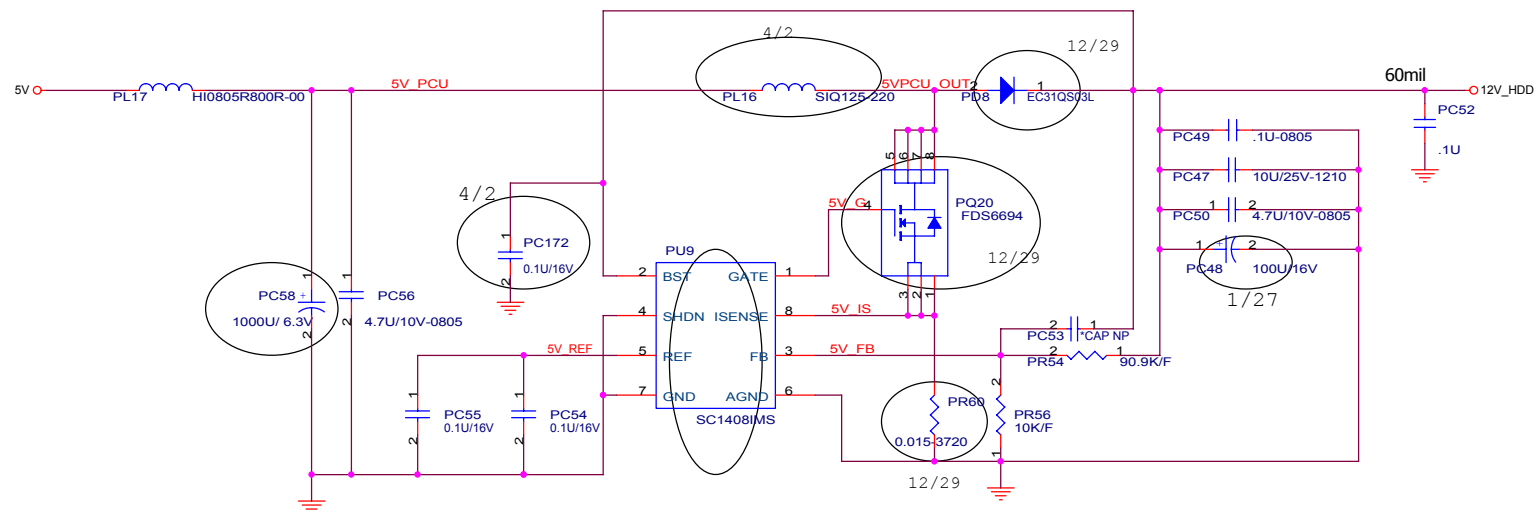
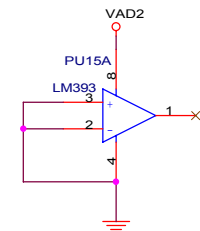
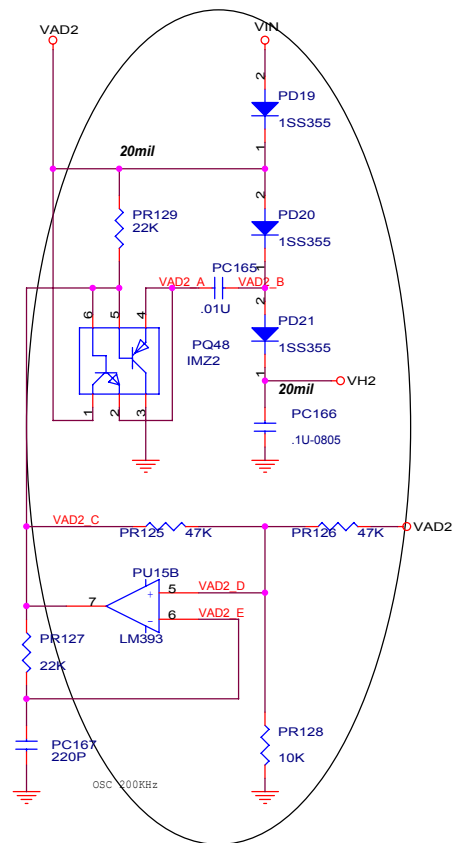
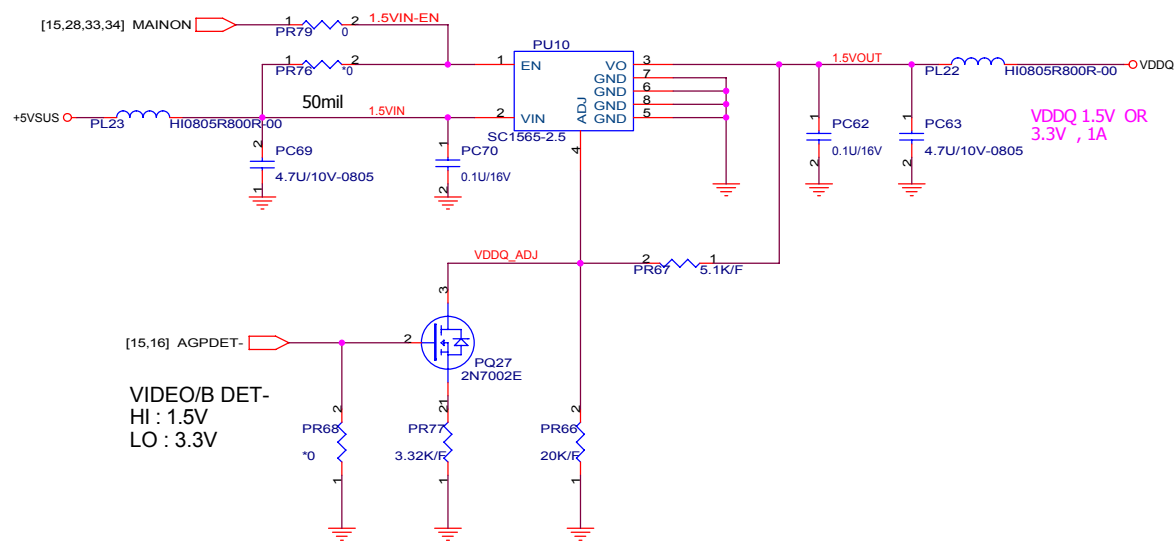
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AUDIO AMPLIFIER(LM4873 & 4871)			
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SN74HC148 TABLE												
EI	INPUTS							OUTPUTS				
	0	1	2	3	4	5	6	A2	A1	A0	GS	EO
H	X	X	X	X	X	X	X	H	H	H	H	H
L	H	H	H	H	H	H	H	H	H	H	H	L
L	X	X	X	X	X	X	L	L	L	L	L	H
L	X	X	X	X	X	L	H	L	H	L	L	H
L	X	X	X	X	L	H	H	L	H	L	L	H
L	X	X	X	L	H	H	H	H	L	L	L	H
L	X	X	L	H	H	H	H	H	L	L	L	H
L	X	L	H	H	H	H	H	H	H	L	L	H
L	L	H	H	H	H	H	H	H	H	H	L	H





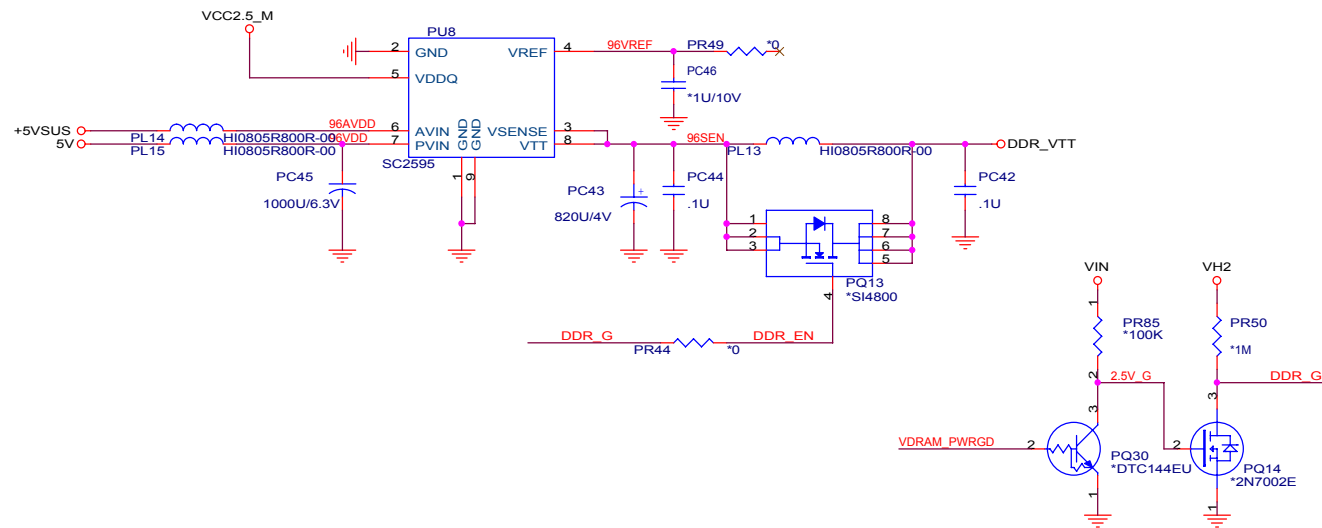
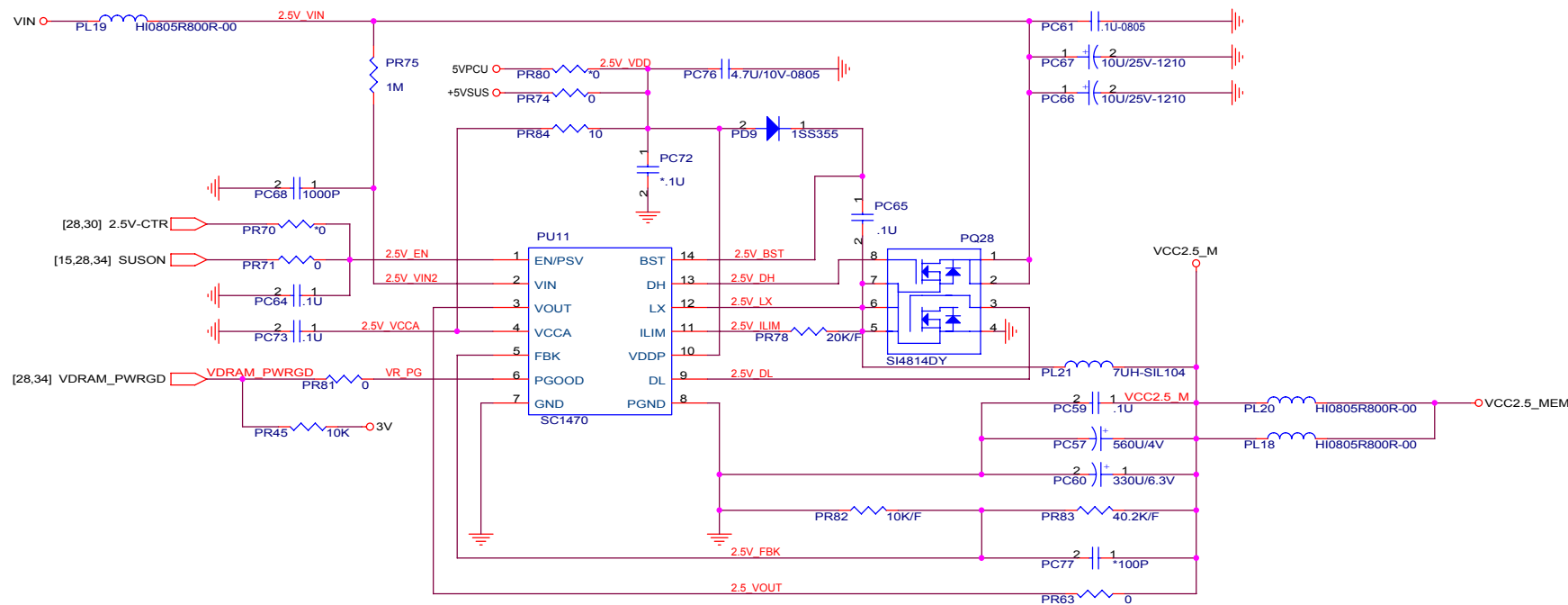
03/06 PC58 CHANGE COMPONENT TO 100U/16V

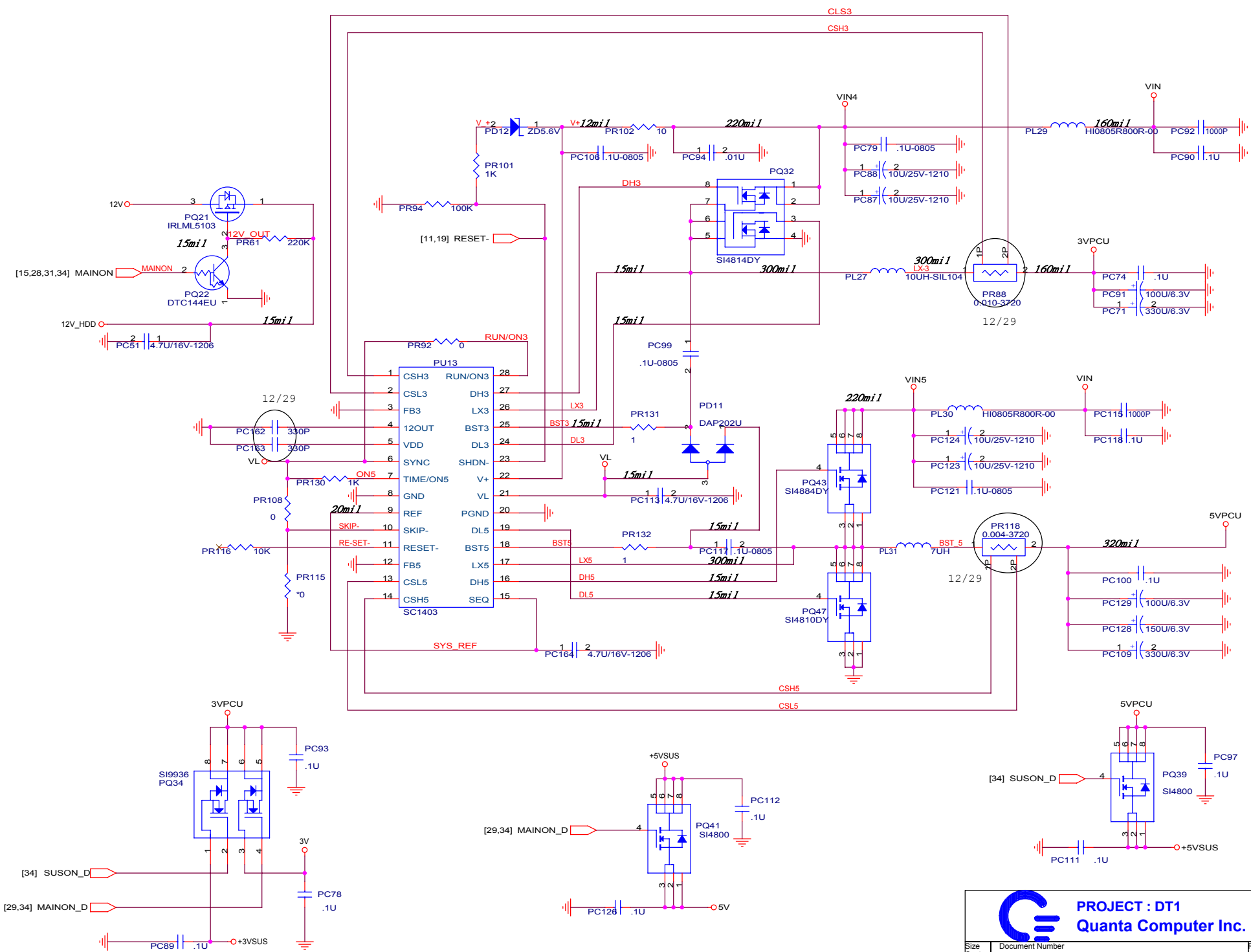
03/06 PR60 CHANGE FROM 10mohm TO 15mohm

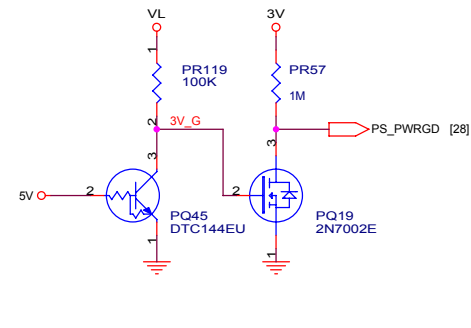
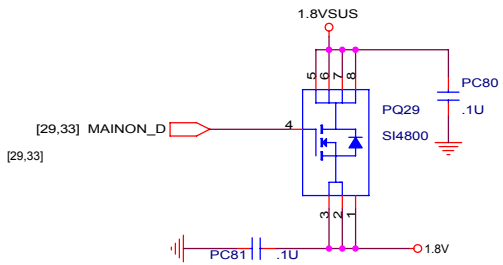
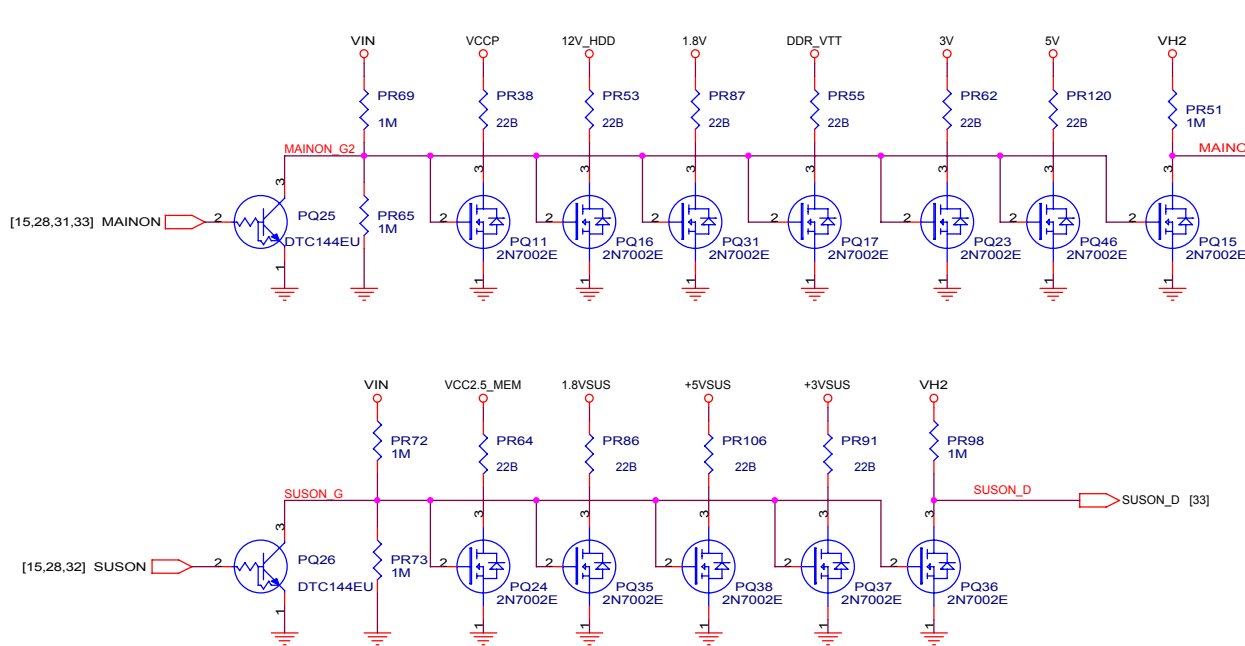
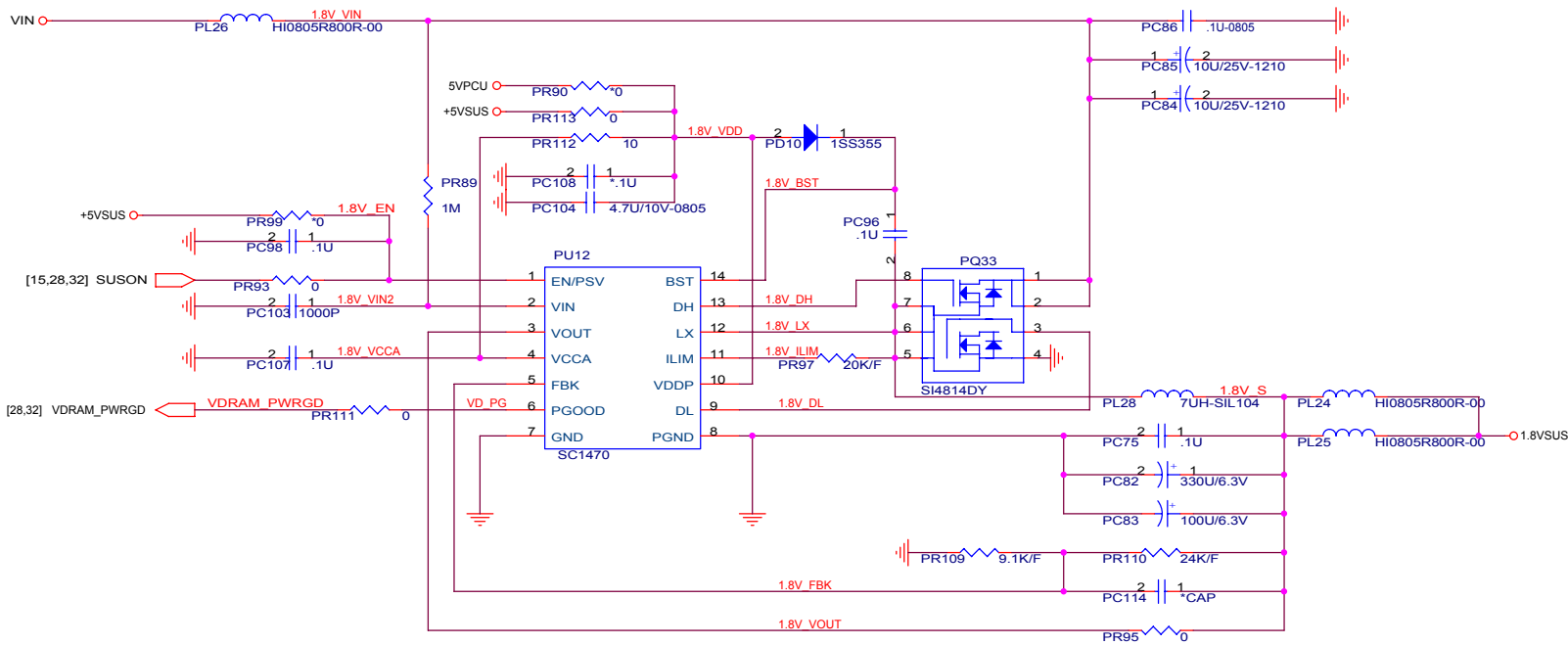


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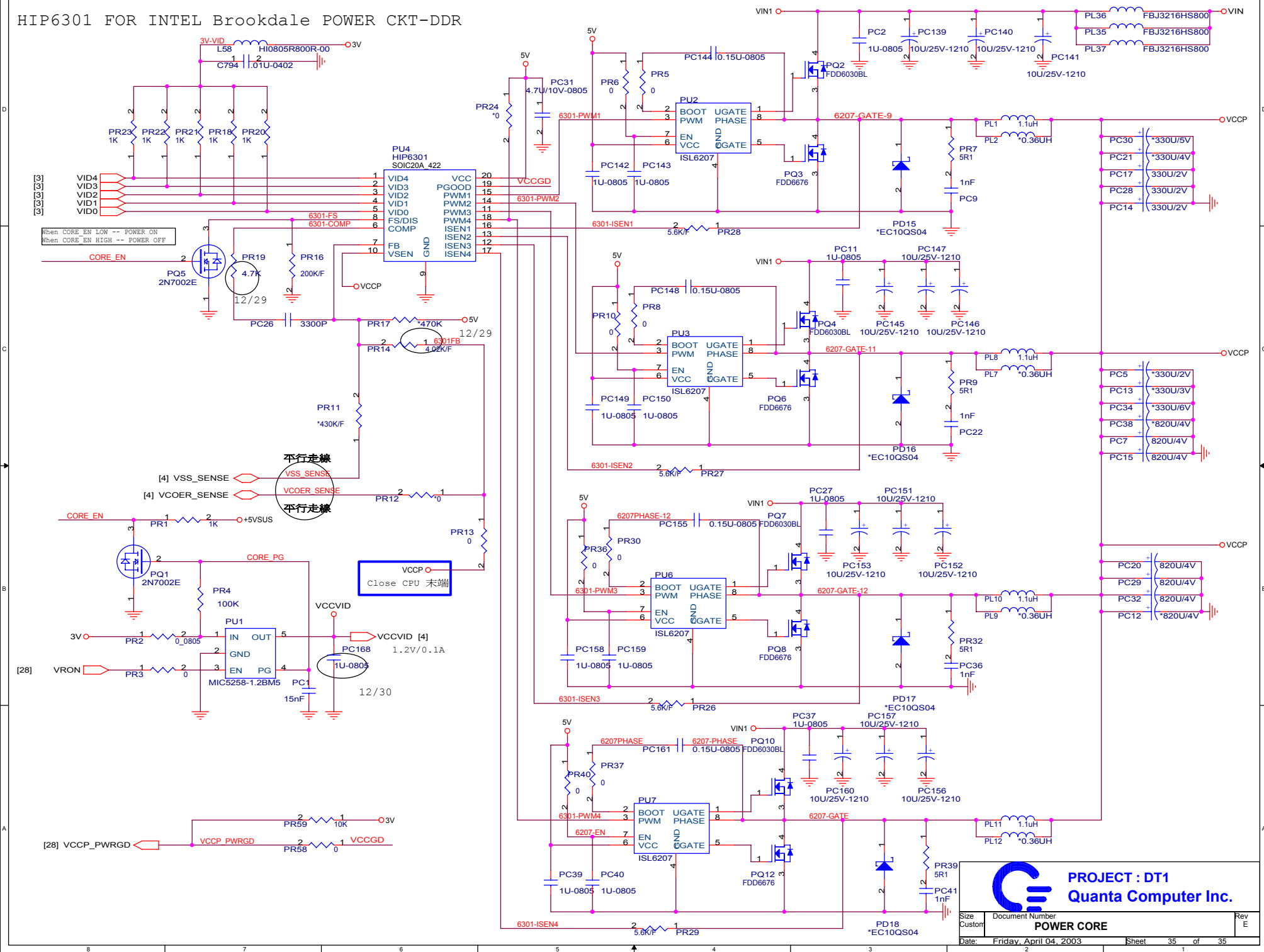






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POWER CORE

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