

Compal confidential

JBK00 LA-4093P Schematics Document

Mobile AMD S1G2 CPU with ATI
RX781 & SB700 core logic with M86-M

2009-03-25

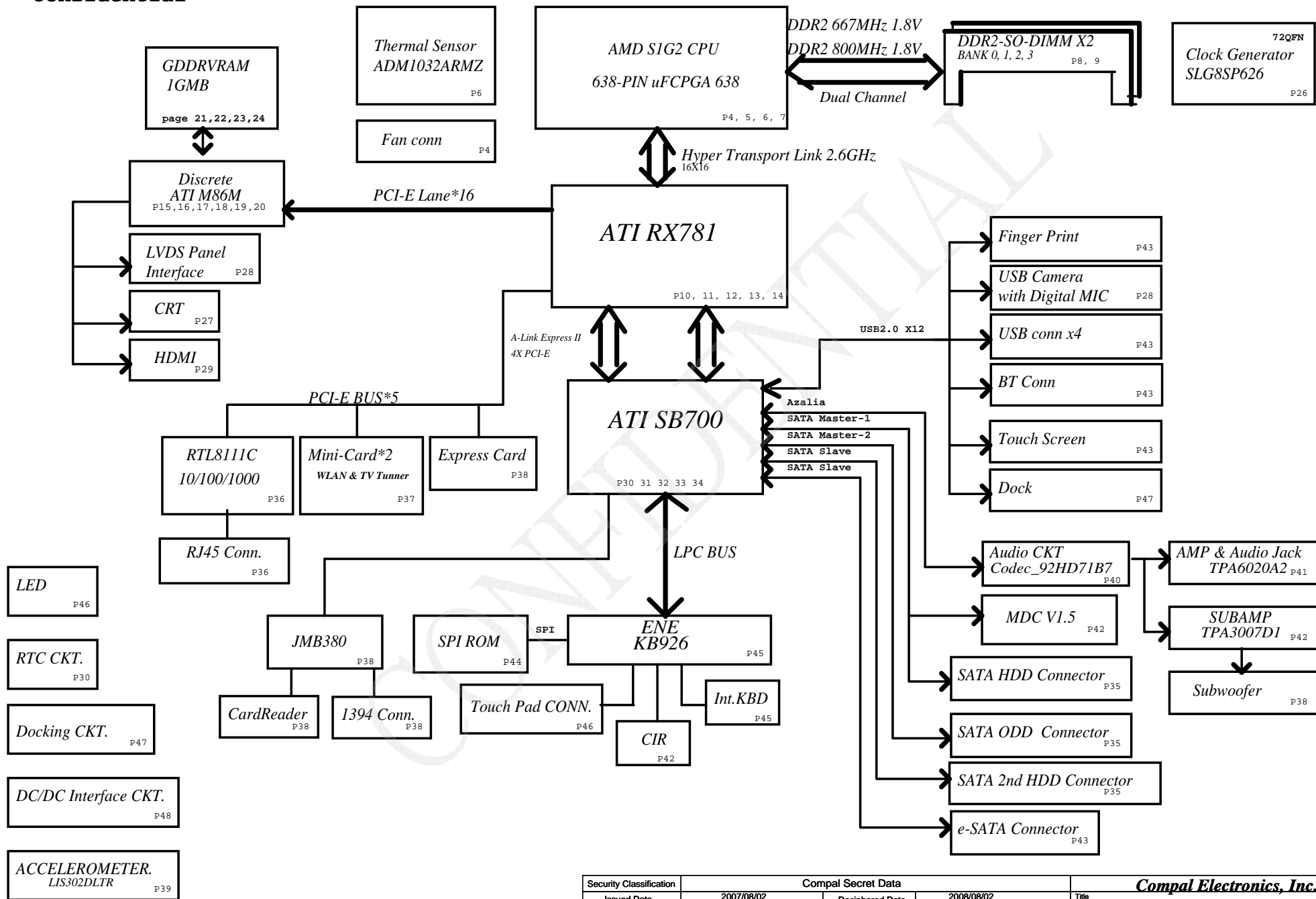
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Consumer AMD Discrete 17"



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Voltage Rails

power plane State	+B +3VL +5VL	+5VALW +3VALW +1.2VALW +3V_LAN	+1.8V +0.9V	+5VS +3VS +2.5VS +1.8VS +1.5VS +1.1VS +VGA_CORE +1.2V_HT +CPU_CORE_NB +CPU_CORE_0 +CPU_CORE_1
S0	O	O	O	O
S1	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

O MEANS ON X MEANS OFF

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	10100000
DDR SO-DIMM 1	A4	10100100
CLOCK GENERATOR (EXT.)	D2	11010010
ACCELEROMETER	3A	00111010

EC SM Bus1 address

EC SM Bus2 address

Device	HEX	Address	Device	HEX	Address
Smart Battery	16H	0001 011X b	AD1032-2 CPU	9AH	1001 101X b
24C16	A0H	1010 000X b	AD1032-1 VGA	98H	1001 100X b
CPU SIC interface	98H	1001 100X b			


Symbol Note :

 : means Digital Ground

 : means Analog Ground

@ : means just reserve , no build

DEBUG@ : means just reserve for debug.

 Layout Notes

@ : means just reserve , no build

45@ : means need be mounted when 45 level assy or rework stage.

RX781R1@ : means just reserve for R1 FRU BOM

SBR1 @ : means just reserve for R1 FRU BOM

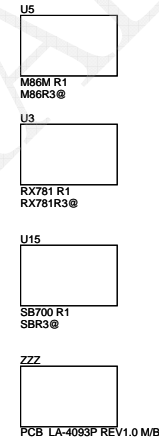
M86R1@ : means just reserve for R1 FRU BOM

M86M

RX781

SB700

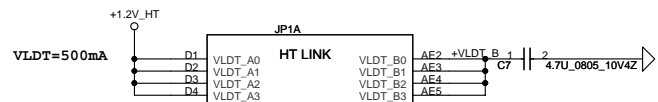
PCB



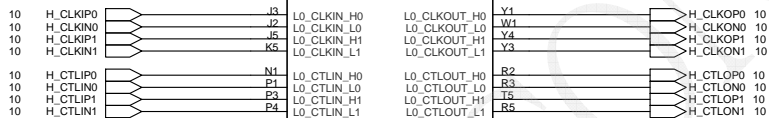
SMBUS Control Table

	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR CPU & ADM1032	SODIMM I / II	CLK CHIP	MINI CARD Slot 2	LCD	HDMI	G-Sensor
SMB_EC_CK1	KB926	X	V	V	X	X	X	X	X	X	X
SMB_EC_DA1											
SMB_EC_CK2	KB926	X	X	X	V	X	X	X	X	X	X
SMB_EC_DA2											
I2C_CLK	RS780M	X	X	X	X	X	X	X	V	X	X
I2C_DATA											
DDC_CLK0	RS780M	X	X	X	X	X	X	X	X	V	X
DDC_DATA0											
DDC_CLK1	RS780M	X	X	X	X	X	X	X	X	X	X
DDC_DATA1											
SCL0	SB700	X	X	X	X	V	V	X	X	X	V
SDA0											
SCL1	SB700	X	X	X	X	X	X	V	X	X	X
SDA1											
SCL2	SB700	X	X	X	X	X	X	X	X	X	X
SDA2											
SCL3	SB700	X	X	X	X	X	X	X	X	X	X
SDA3											

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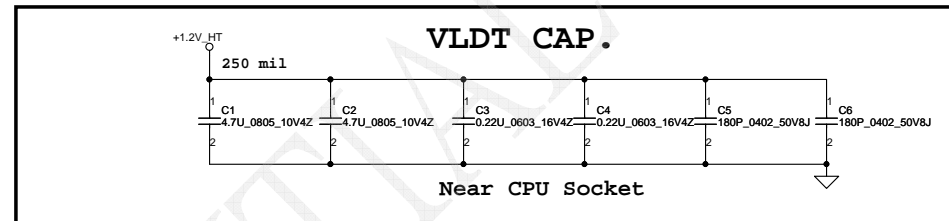


H_CADIP0	E3	L0_CADIN_H0	L0_CADOUT_H0	AD1	H_CADOP0
H_CADIN0	E2	L0_CADIN_L0	L0_CADOUT_L0	AC1	H_CADON0
H_CADIP1	F1	L0_CADIN_H1	L0_CADOUT_H1	AC2	H_CADOP1
H_CADIN1	F1	L0_CADIN_L1	L0_CADOUT_L1	AC3	H_CADON1
H_CADIP2	G3	L0_CADIN_H2	L0_CADOUT_H2	AA1	H_CADOP2
H_CADIN2	G2	L0_CADIN_L2	L0_CADOUT_L2	AA2	H_CADON2
H_CADIP3	G1	L0_CADIN_H3	L0_CADOUT_H3	AA3	H_CADOP3
H_CADIN3	H1	L0_CADIN_L3	L0_CADOUT_L3	W2	H_CADON3
H_CADIP4	K1	L0_CADIN_H4	L0_CADOUT_H4	W3	H_CADOP4
H_CADIN4	L3	L0_CADIN_L4	L0_CADOUT_L4	V1	H_CADON4
H_CADIP5	L2	L0_CADIN_H5	L0_CADOUT_H5	U1	H_CADOP5
H_CADIN5	L1	L0_CADIN_L5	L0_CADOUT_L5	U2	H_CADON5
H_CADIP6	M1	L0_CADIN_H6	L0_CADOUT_H6	U3	H_CADOP6
H_CADIN6	N3	L0_CADIN_L6	L0_CADOUT_L6	T1	H_CADON6
H_CADIP7	N2	L0_CADIN_H7	L0_CADOUT_H7	R1	H_CADOP7
H_CADIN7	E5	L0_CADIN_L7	L0_CADOUT_L7	AD4	H_CADON7
H_CADIP8	F5	L0_CADIN_H8	L0_CADOUT_H8	AD3	H_CADOP8
H_CADIN8	F3	L0_CADIN_L8	L0_CADOUT_L8	AD5	H_CADON8
H_CADIP9	F4	L0_CADIN_H9	L0_CADOUT_H9	AC5	H_CADOP9
H_CADIN9	G5	L0_CADIN_L9	L0_CADOUT_L9	AB4	H_CADON9
H_CADIP10	H5	L0_CADIN_H10	L0_CADOUT_H10	AB3	H_CADOP10
H_CADIN10	H3	L0_CADIN_L10	L0_CADOUT_L10	AB5	H_CADON10
H_CADIP11	H4	L0_CADIN_H11	L0_CADOUT_H11	AA5	H_CADOP11
H_CADIN11	K3	L0_CADIN_L11	L0_CADOUT_L11	Y5	H_CADON11
H_CADIP12	L5	L0_CADIN_H12	L0_CADOUT_H12	W5	H_CADOP12
H_CADIN12	L5	L0_CADIN_L12	L0_CADOUT_L12	V4	H_CADON12
H_CADIP13	M5	L0_CADIN_H13	L0_CADOUT_H13	V3	H_CADOP13
H_CADIN13	M3	L0_CADIN_L13	L0_CADOUT_L13	V5	H_CADON13
H_CADIP14	M4	L0_CADIN_H14	L0_CADOUT_H14	U5	H_CADOP14
H_CADIN14	N5	L0_CADIN_L14	L0_CADOUT_L14	T4	H_CADON14
H_CADIP15	P5	L0_CADIN_H15	L0_CADOUT_H15	T3	H_CADOP15
H_CADIN15	P5	L0_CADIN_L15	L0_CADOUT_L15		

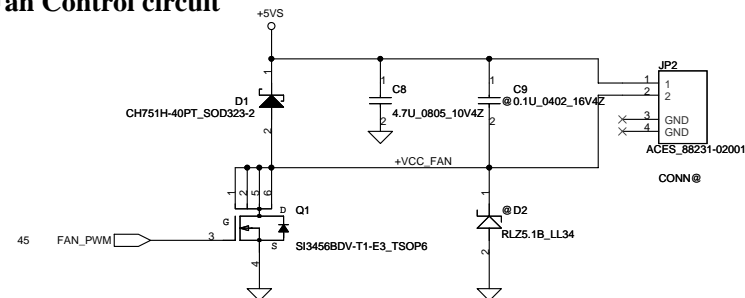


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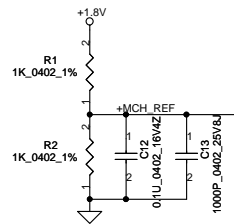


PWM Fan Control circuit

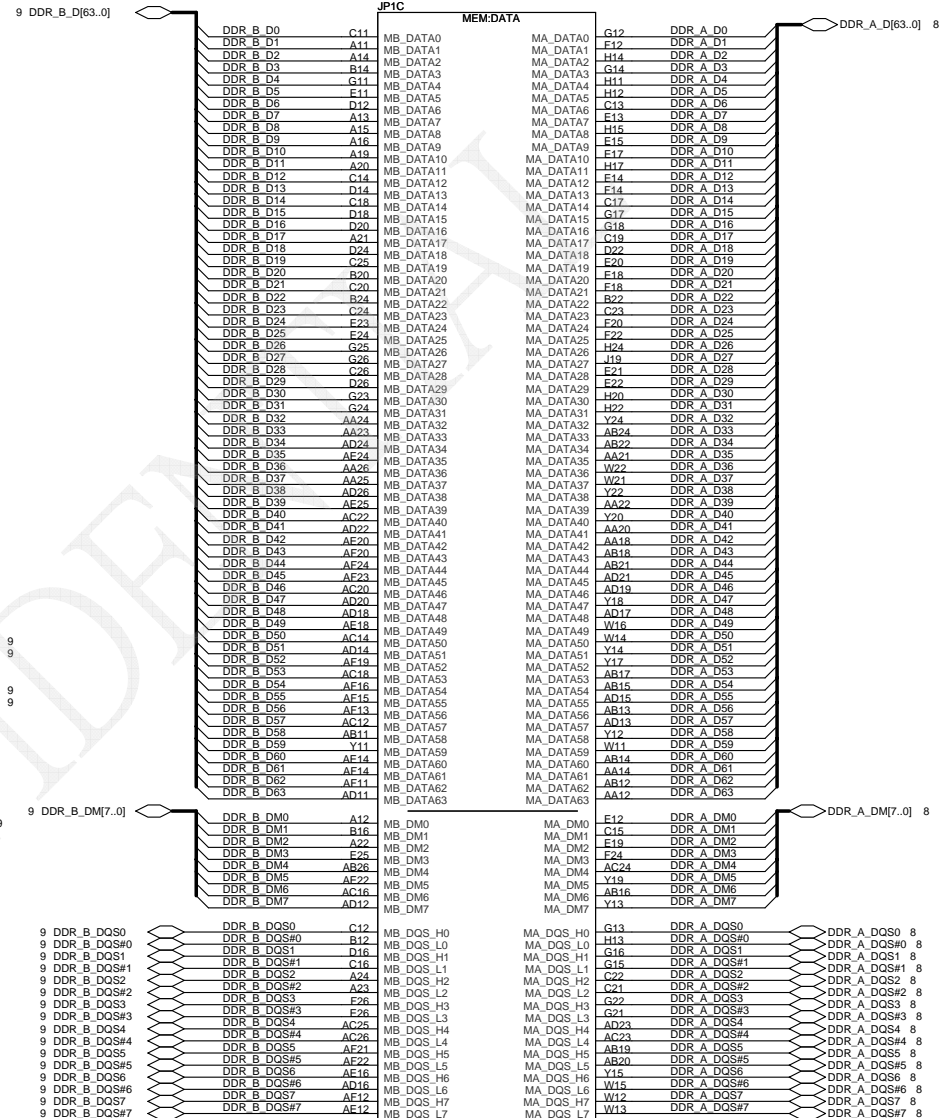
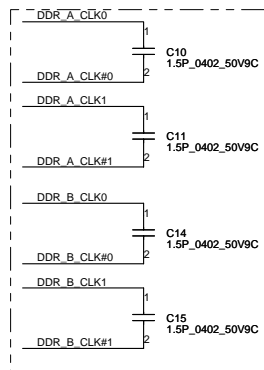


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Processor DDR2 Memory Interface



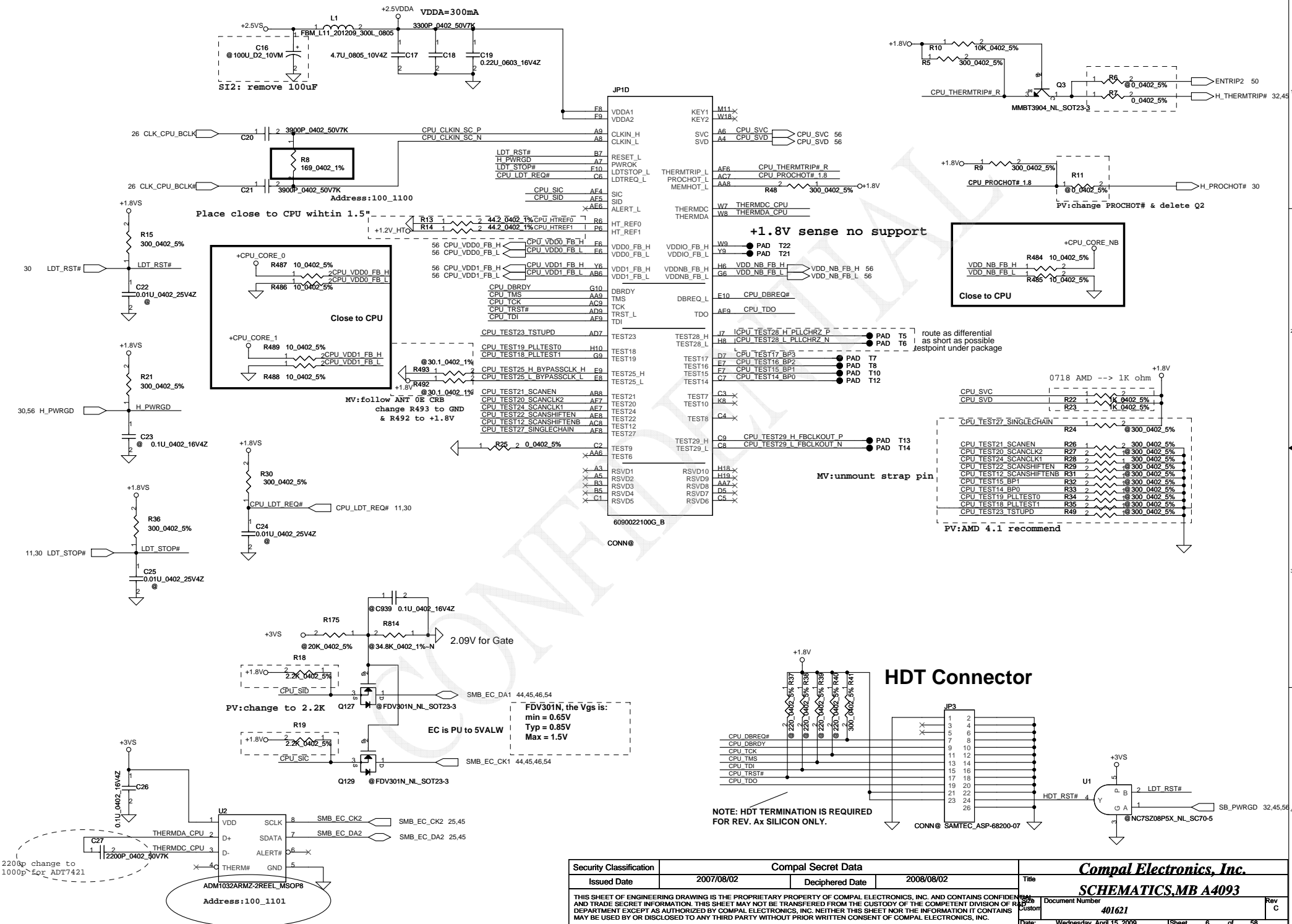
PLACE CLOSE TO PROCESSOR
WITHIN 1.5 INCH



CONN®

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VDD(+CPU_CORE) decoupling.

Near CPU Socket

Diagram illustrating the VDD(+CPU_CORE) decoupling components near the CPU socket for two cores:

- +CPU_CORE_0:** Decoupled using capacitor C30 (330U_X_2VM_R6M) and C28 (330U_X_2VM_R6M).
- +CPU_CORE_1:** Decoupled using capacitor C31 (330U_X_2VM_R6M) and C29 (330U_X_2VM_R6M).

Under CPU Socket

Diagram illustrating the VDD(+CPU_CORE) decoupling components under the CPU socket for two cores:

- +CPU_CORE_0:** Decoupled using capacitors C32 (22U_0805_6.3V6M), C33 (22U_0805_6.3V6M), C34 (22U_0805_6.3V6M), and C35 (22U_0805_6.3V6M). Additionally, C40 (0.22U_0603_16V4Z), C41 (0.01U_0402_25V4Z), and C42 (180P_0402_50V8J) are shown.
- +CPU_CORE_1:** Decoupled using capacitors C36 (22U_0805_6.3V6M), C37 (22U_0805_6.3V6M), C38 (22U_0805_6.3V6M), and C39 (22U_0805_6.3V6M). Additionally, C43 (0.22U_0603_16V4Z), C44 (0.01U_0402_25V4Z), and C45 (180P_0402_50V8J) are shown.

Under CPU Socket

Capacitors: C46 (22u_0805_6.3V6M), C47 (22u_0805_6.3V6M), C48 (0.22u_0603_16V4Z), C49 (0.22u_0603_16V4Z), C50 (180P_0402_50VB/J), C51 (180P_0402_50VB/J).

Between CPU Socket and DIMM

Capacitors: C55 (0.22u_0603_16V4Z), C56 (0.22u_0603_16V4Z), C57 (0.22u_0603_16V4Z), C58 (0.22u_0603_16V4Z).

180PF Qt'y follow the distance between CPU socket and DIMM0. <2.5inch>

Capacitors: C60 (0.01u_0402_25V4Z), C61 (0.01u_0402_25V4Z), C62 (180P_0402_50VB/J), C63 (180P_0402_50VB/J), C64 (180P_0402_50VB/J), C65 (180P_0402_50VB/J).

A: Add C165 and C176 to follow AMD Layout review recommend for EMI

Capacitors: C74 (4.7u_0805_10V4Z), C75 (4.7u_0805_10V4Z), C76 (4.7u_0805_10V4Z), C77 (4.7u_0805_10V4Z), C78 (220u_Y_4VM).

C: Change to NBO CAP

+CPU_CORE_NB decoupling.

+CPU_CORE_NB

C52 22U_0805_6.3V6M

C53 22U_0805_6.3V6M

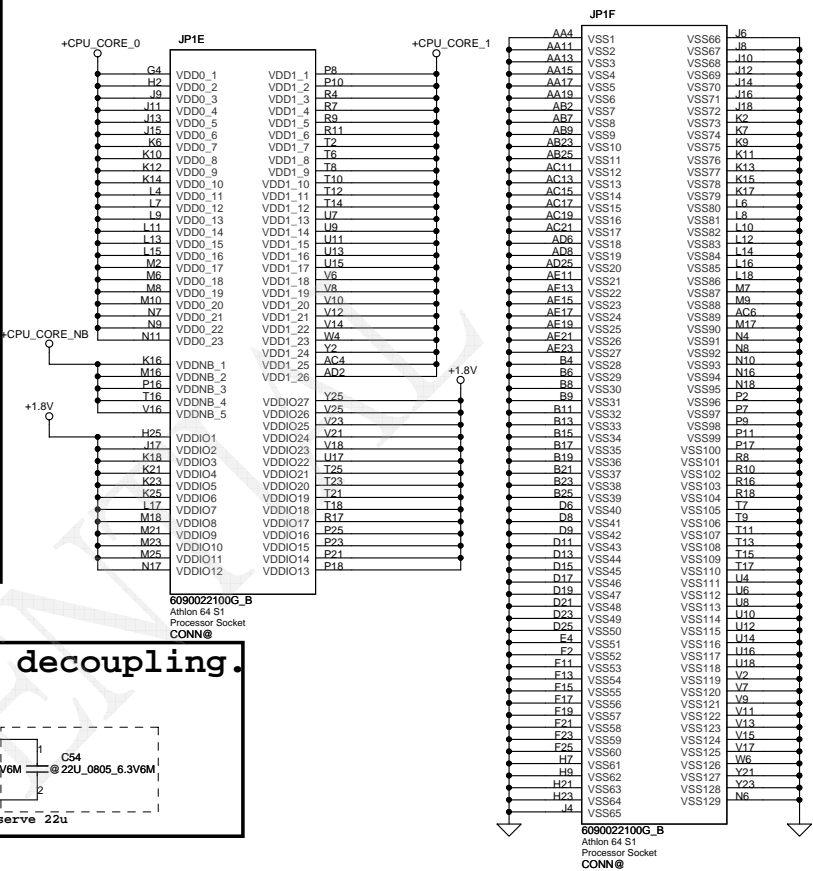
C54 22U_0805_6.3V6M

S12: reserve 22u

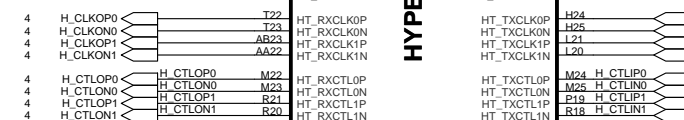
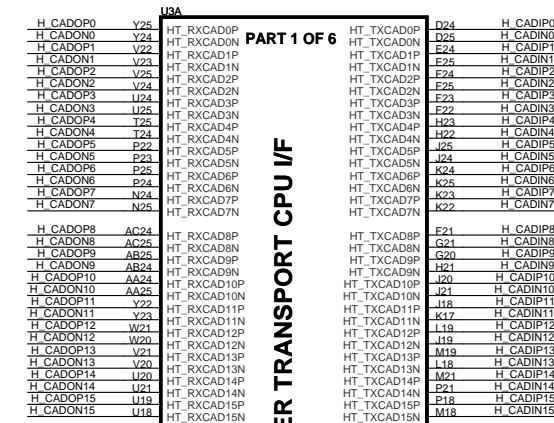
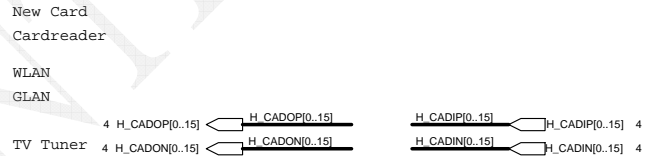
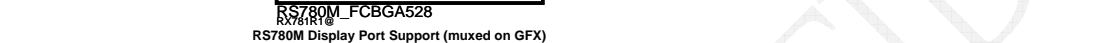
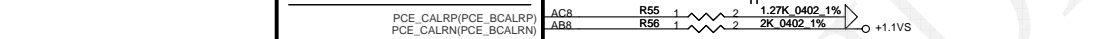
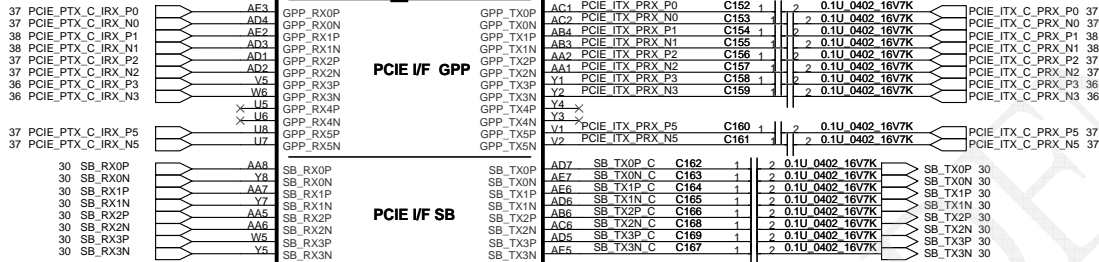
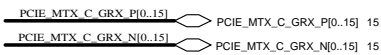
VTT decoupling.

The diagram illustrates the VTT decoupling circuit, showing three main sections connected to a +0.9V supply and ground:

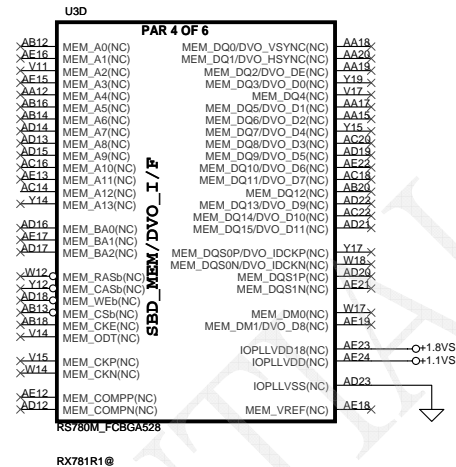
- Near Power Supply:** This section is enclosed in a dashed oval. It includes a capacitor C59 (220U_Y_4VM) connected to the +0.9V supply. A note indicates "C: Change to NBO CAP".
- Near CPU Socket Right side:** This section contains a series of components connected to the +0.9V supply and ground:
 - C66 (4.7U_0805_10V4Z)
 - C67 (4.7U_0805_10V4Z)
 - C68 (0.22U_0603_16V4Z)
 - C69 (0.22U_0603_16V4Z)
 - C70 (1000P_0402_25V8J)
 - C71 (1000P_0402_25V8J)
 - C72 (180P_0402_50V8J)
 - C73 (180P_0402_50V8J)
- Near CPU Socket Left side:** This section contains a series of components connected to the +0.9V supply and ground:
 - C79 (4.7U_0805_10V4Z)
 - C80 (4.7U_0805_10V4Z)
 - C81 (0.22U_0603_16V4Z)
 - C82 (0.22U_0603_16V4Z)
 - C83 (1000P_0402_25V8J)
 - C84 (1000P_0402_25V8J)
 - C85 (180P_0402_50V8J)
 - C86 (180P_0402_50V8J)



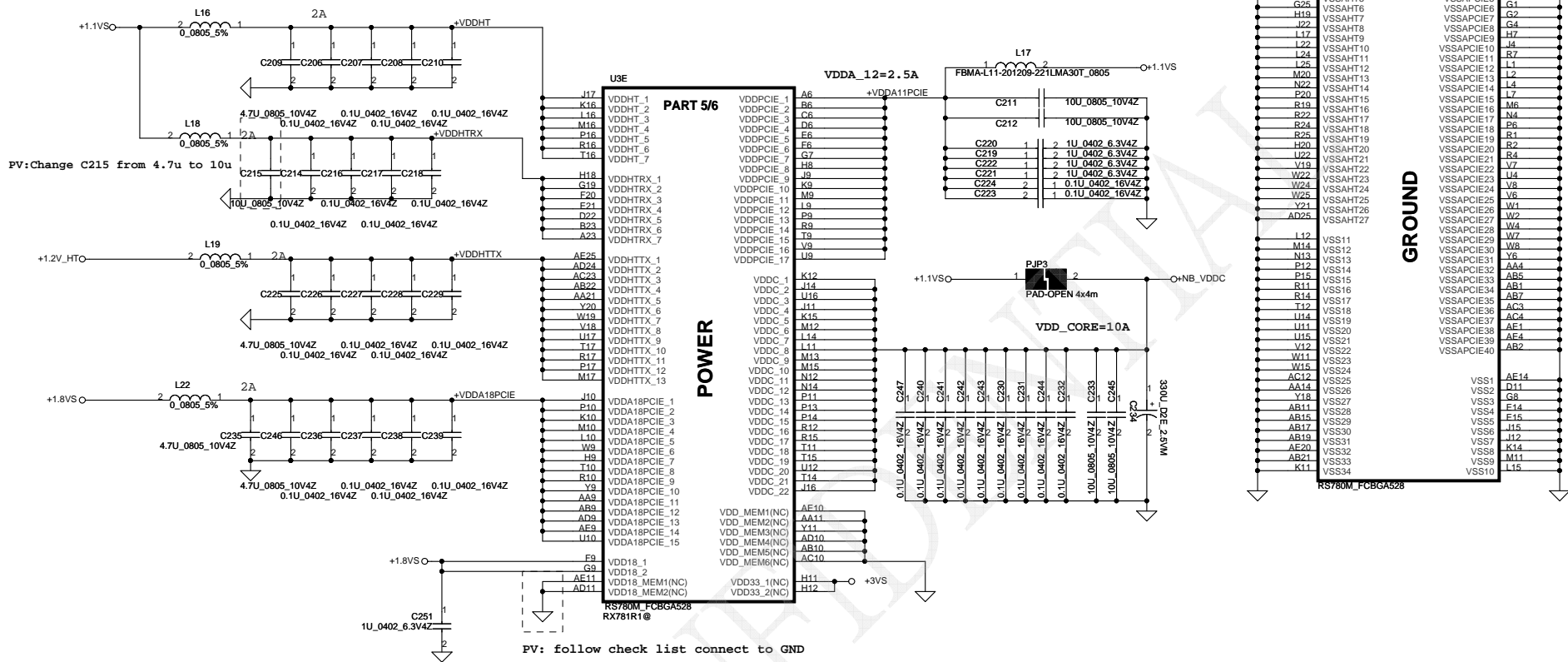
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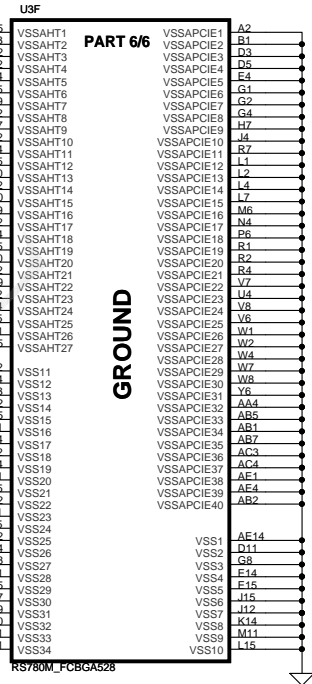
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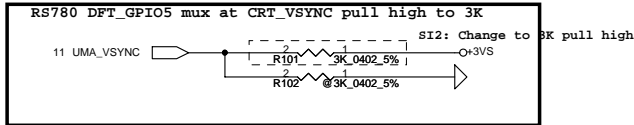


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PV: follow check list connect to GND





DFT_GPIO5:STRAP_DEBUG_BUS_GPIO_ENABLEb

Enables the Test Debug Bus using GPIO.

1 : Enable (RX780, RS780)

0 : Disable (RX780, RS780)

PIN: RS740-->RS780_AUX_CAL; RX780-->NB_TV_C; RS780--> VSYNC#

RS780 use register to control PCI-E configure

DFT_GPIO[4:2]: STRAP_PCIE_GPP_CFG[2:0]

These pin straps are used to configure PCI-E GPP mode.

000 : 00001

001 : 00010

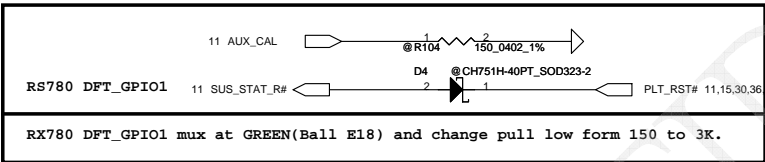
010 : 01011

011 : 00100

100 : 01010

101 : 01100

111 : 01011



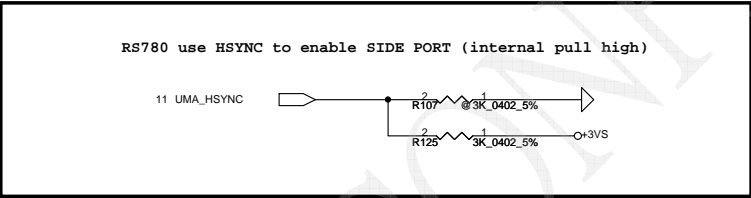
DFT_GPIO1: LOAD_EEPROM_STRAPS

Selects Loading of STRAPS from EPROM

1 : Bypass the loading of EEPROM straps and use Hardware Default Values

0 : I2C Master can load strap values from EEPROM if connected, or use default values if not connected

RS740/RX780: DFT_GPIO1 RS780:SUS_STAT



DFT_GPIO0: STRAP_DEBUG_BUS_PCIE_ENABLEb

RX780: Enables the Test Debug Bus using PCIE bus

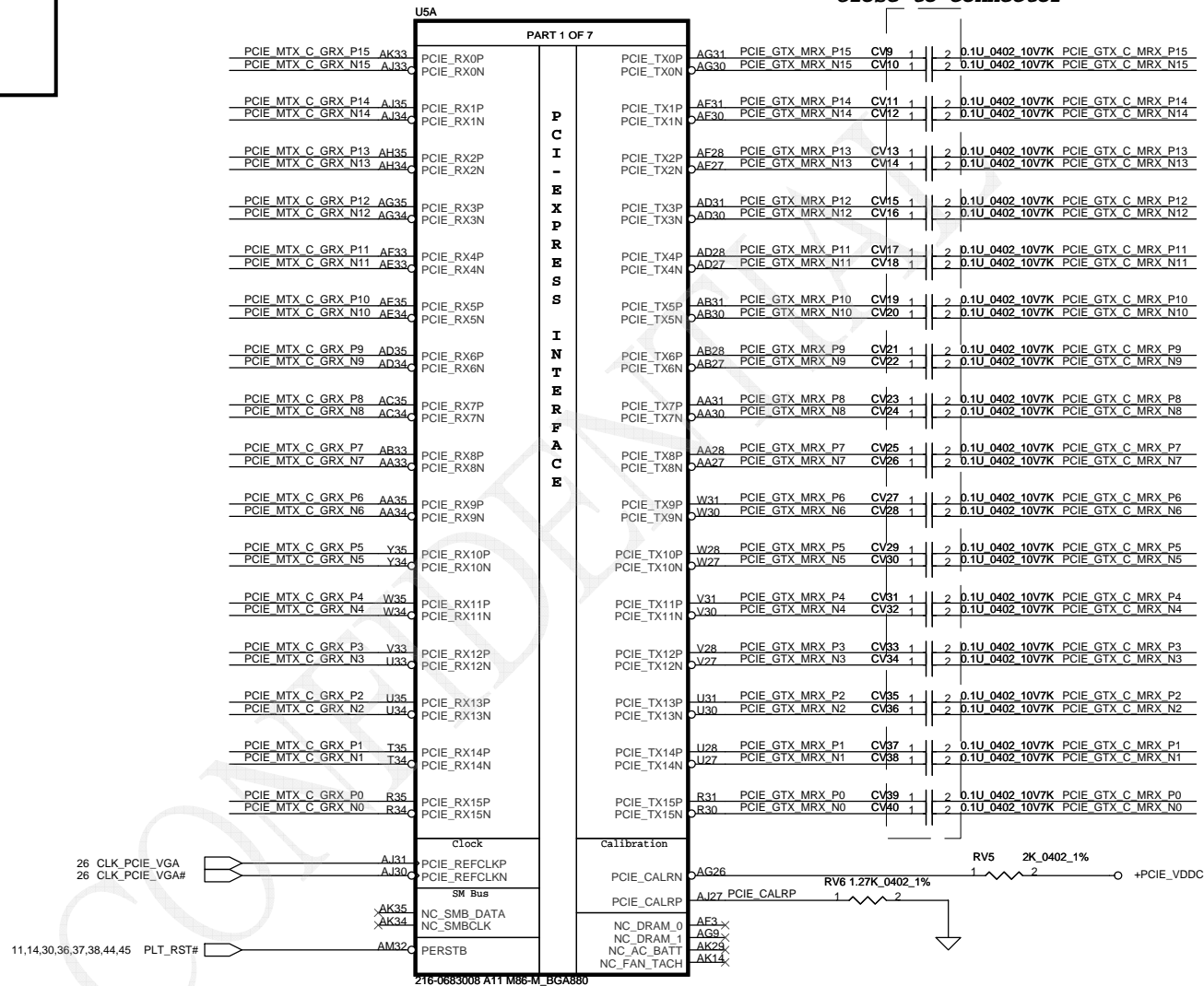
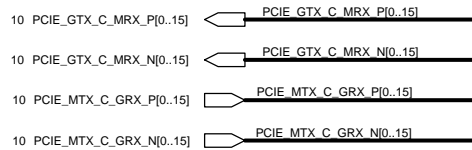
1 : Disable (Can still be enabled using nbcfg register access)

0 : Enable

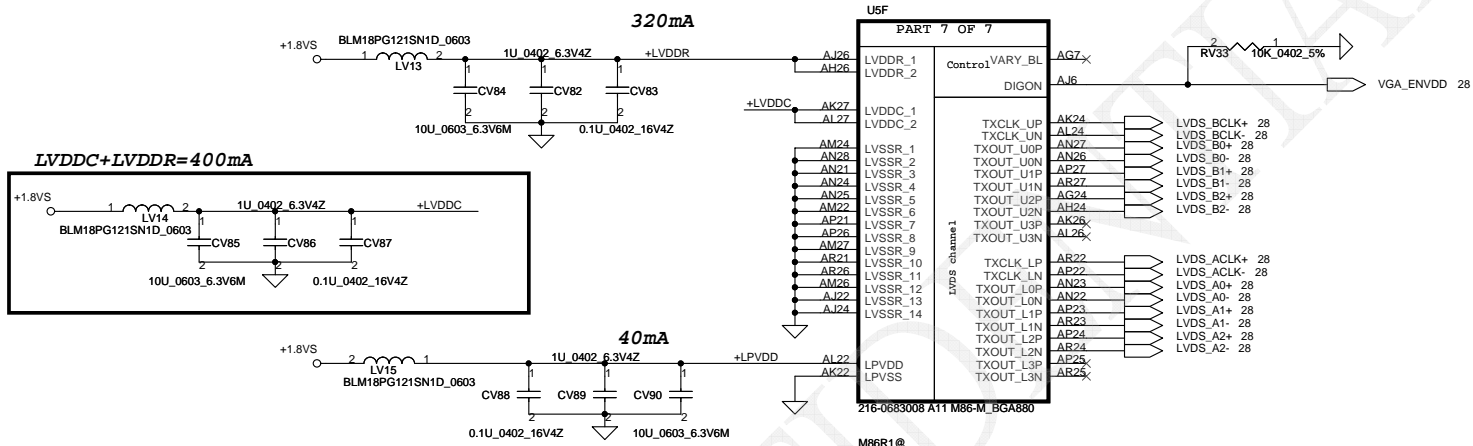
RS780: Enables Side port memory (RS780 use HSYNC#)

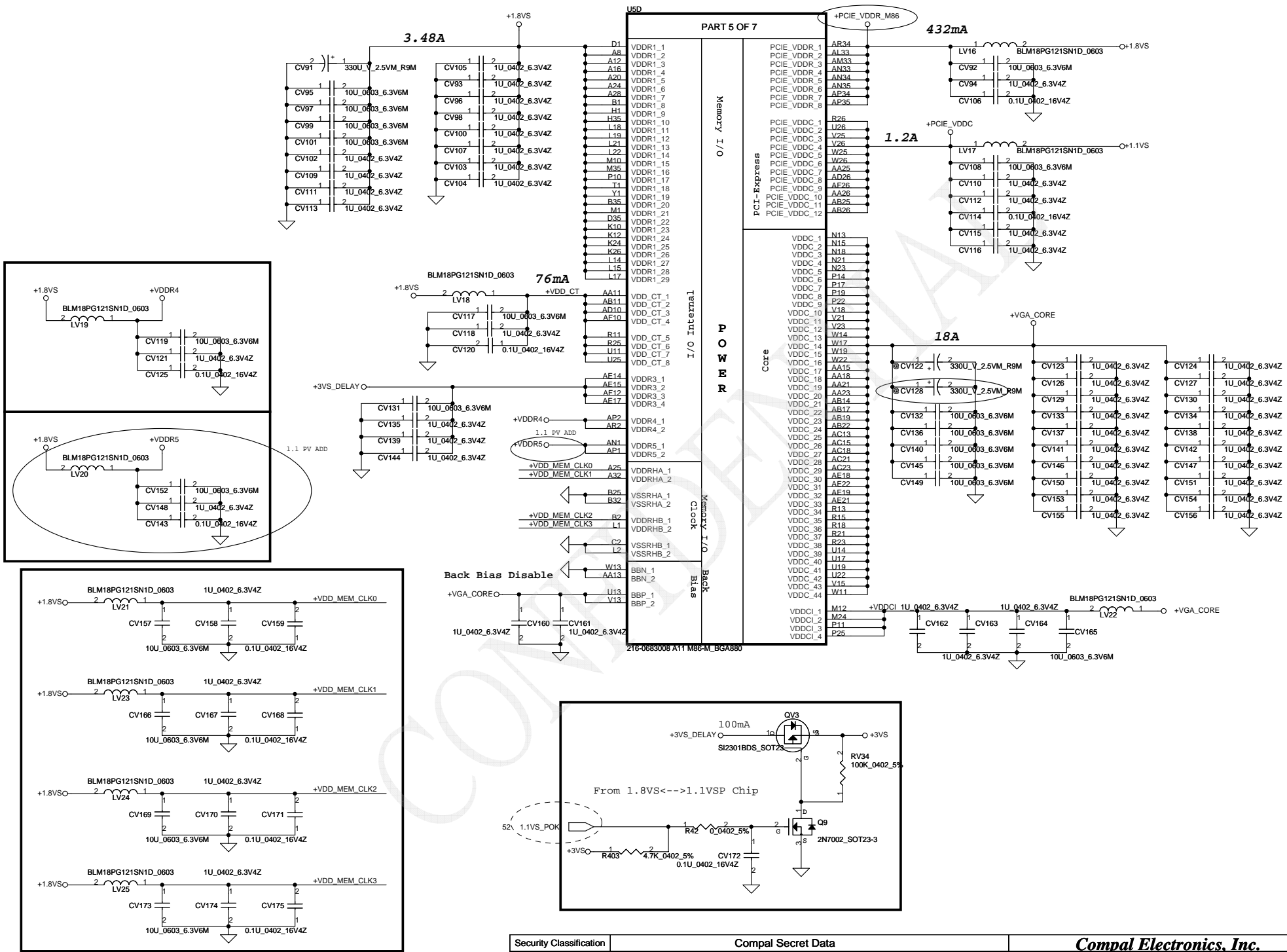
1. Disable (RS780)

0 : Enable (RS780)

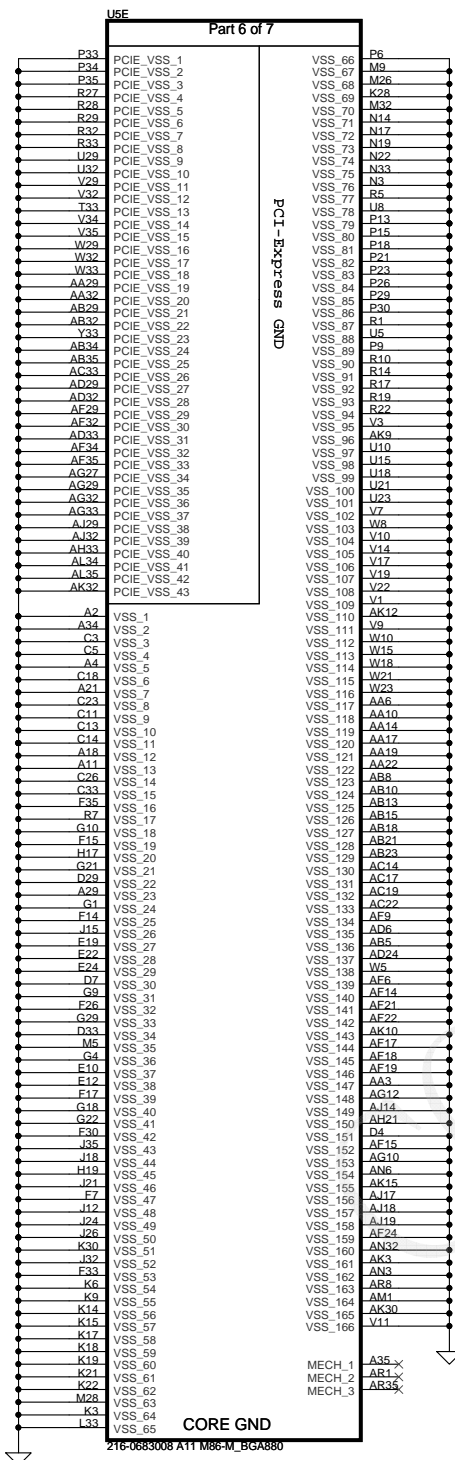


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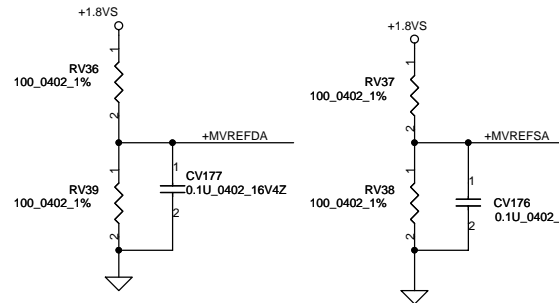
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				401621	C
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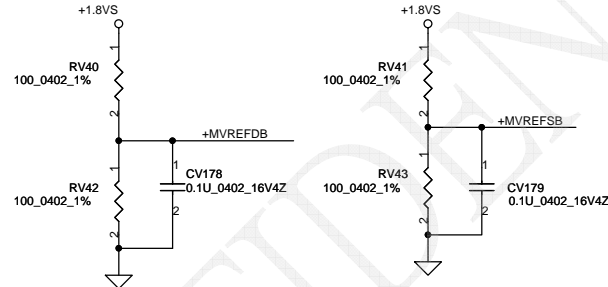
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DQMA#7..0] << DQMA#7..0] 21,22
QSA#7..0] << QSA#7..0] 21,22
QSA#7..0] << QSA#7..0] 21,22
MAA#12..0] << MAA#12..0] 21,22
BA#2..0] << BA#2..0] 21,22
MDA#63..0] << MDA#63..0] 21,22

Close to pin N34, N35



Close to pin A13, B14



MDB#63..0] << MDB#63..0] 23,24
MAB#12..0] << MAB#12..0] 23,24
BB#2..0] << BB#2..0] 23,24
QDMB#7..0] << QDMB#7..0] 23,24
QSB#7..0] << QSB#7..0] 23,24
QSB#7..0] << QSB#7..0] 23,24

USC

Part 3 of 7

MEMORY INTERFACE A

write strobe read strobe

MDA0 P27	DQA_0	MAA_0	C27	MAA0
MDA1 P28	DQA_1	MAA_1	B28	MAA1
MDA2 P31	DQA_2	MAA_2	B27	MAA2
MDA3 P32	DQA_3	MAA_3	G26	MAA3
MDA4 M27	DQA_4	MAA_4	F27	MAA4
MDA5 K29	DQA_5	MAA_5	F27	MAA5
MDA6 K31	DQA_6	MAA_6	D27	MAA6
MDA7 K32	DQA_7	MAA_7	J27	MAA7
MDA8 M33	DQA_8	MAA_8	F29	MAA8
MDA9 M34	DQA_9	MAA_9	C30	MAA9
MDA10 L34	DQA_10	MAA_10	E26	MAA10
MDA11 L35	DQA_11	MAA_11	A27	MAA11
MDA12 J36	DQA_12	MAA_12	G27	MAA12
MDA13 J34	DQA_13	MAA_12	D26	BA2
MDA14 H33	DQA_14	MAA_BA2	C28	BA0
MDA15 H34	DQA_15	MAA_BA0	B29	BA1
MDA16 K27	DQA_16	MAA_BA1		
MDA17 J29	DQA_17			
MDA18 J30	DQA_18			
MDA19 J31	DQA_19			
MDA20 F29	DQA_20			
MDA21 F32	DQA_21			
MDA22 D30	DQA_22			
MDA23 D32	DQA_23			
MDA24 G33	DQA_24			
MDA25 G34	DQA_25			
MDA26 G35	DQA_26			
MDA27 F34	DQA_27			
MDA28 D34	DQA_28			
MDA29 C34	DQA_29			
MDA30 C35	DQA_30			
MDA31 B34	DQA_31			
MDA32 C24	DQA_32			
MDA33 B24	DQA_33			
MDA34 B23	DQA_34			
MDA35 A23	DQA_35			
MDA36 C21	DQA_36			
MDA37 B21	DQA_37			
MDA38 C20	DQA_38			
MDA39 B20	DQA_39			
MDA40 J22	DQA_40			
MDA41 H22	DQA_41			
MDA42 F22	DQA_42			
MDA43 J19	DQA_43			
MDA45 G18	DQA_44			
MDA46 F19	DQA_45			
MDA47 D19	DQA_46			
MDA48 C19	DQA_47			
MDA49 B19	DQA_48			
MDA50 A19	DQA_49			
MDA51 B18	DQA_50			
MDA52 C16	DQA_51			
MDA53 B16	DQA_52			
MDA54 C15	DQA_53			
MDA55 A15	DQA_54			
MDA56 H18	DQA_55			
MDA57 F18	DQA_56			
MDA58 E18	DQA_57			
MDA59 D18	DQA_58			
MDA60 I17	DQA_59			
MDA61 G15	DQA_60			
MDA62 F15	DQA_61			
MDA63 D15	DQA_62			

+MVREFDA N35
+MVREFSA N34

AM34

216-0683008 A11 M86-M_BGA880

M86R1@

Part 4 of 7

MEMORY INTERFACE B

MDA0 H15	DOB_0	MAB_0	H2	MAB0
MDA1 G14	DOB_1	MAB_1	H3	MAB1
MDA2 F14	DOB_2	MAB_2	J5	MAB2
MDA3 D14	DOB_3	MAB_3	J6	MAB3
MDA4 H12	DOB_4	MAB_4	J4	MAB4
MDA5 G12	DOB_5	MAB_5	J6	MAB5
MDA6 F12	DOB_6	MAB_6	G6	MAB6
MDA7 D10	DOB_7	MAB_7	J8	MAB7
MDA8 B13	DOB_8	MAB_8	F3	MAB8
MDA9 C12	DOB_9	MAB_9	F4	MAB9
MDA10 B12	DOB_10	MAB_10	J1	MAB10
MDA11 B11	DOB_11	MAB_11	J2	MAB11
MDA12 C9	DOB_12	MAB_12	J7	MAB12
MDA13 B9	DOB_13	MAB_BA2	F1	BB2
MDA14 A9	DOB_14	MAB_BA0	G2	BB0
MDA15 B8	DOB_15	MAB_BA1	G3	BB1
MDA16 J10	DOB_16			
MDA17 H10	DOB_17			
MDA18 F10	DOB_18			
MDA19 D9	DOB_19			
MDA20 G7	DOB_20			
MDA21 G6	DOB_21			
MDA22 F6	DOB_22			
MDA23 D6	DOB_23			
MDA24 C8	DOB_24			
MDA25 C7	DOB_25			
MDA26 B7	DOB_26			
MDA27 A7	DOB_27			
MDA28 B5	DOB_28			
MDA29 A5	DOB_29			
MDA30 C4	DOB_30			
MDA31 B4	DOB_31			
MDA32 M3	DOB_32			
MDA33 M2	DOB_33			
MDA34 N2	DOB_34			
MDA35 M1	DOB_35			
MDA36 B3	DOB_36			
MDA37 R2	DOB_37			
MDA38 T3	DOB_38			
MDA39 T2	DOB_39			
MDA40 M8	DOB_40			
MDA41 M7	DOB_41			
MDA42 C5	DOB_42			
MDA43 P4	DOB_43			
MDA44 R9	DOB_44			
MDA45 R8	DOB_45			
MDA46 R6	DOB_46			
MDA47 U4	DOB_47			
MDA48 U3	DOB_48			
MDA49 U2	DOB_49			
MDA50 U1	DOB_50			
MDA51 V2	DOB_51			
MDA52 Y3	DOB_52			
MDA53 Y2	DOB_53			
MDA54 A2	DOB_54			
MDA55 A1	DOB_55			
MDA56 U9	DOB_56			
MDA57 U7	DOB_57			
MDA58 U6	DOB_58			
MDA59 V4	DOB_59			
MDA60 W9	DOB_60			
MDA61 W7	DOB_61			
MDA62 W6	DOB_62			
MDA63 V4	DOB_63			

+MVREFDB B14

+MVREFSB A13

TESTEN

TEST_MCLK

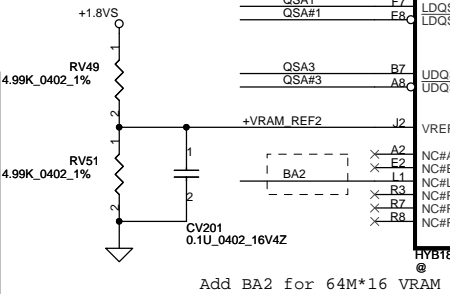
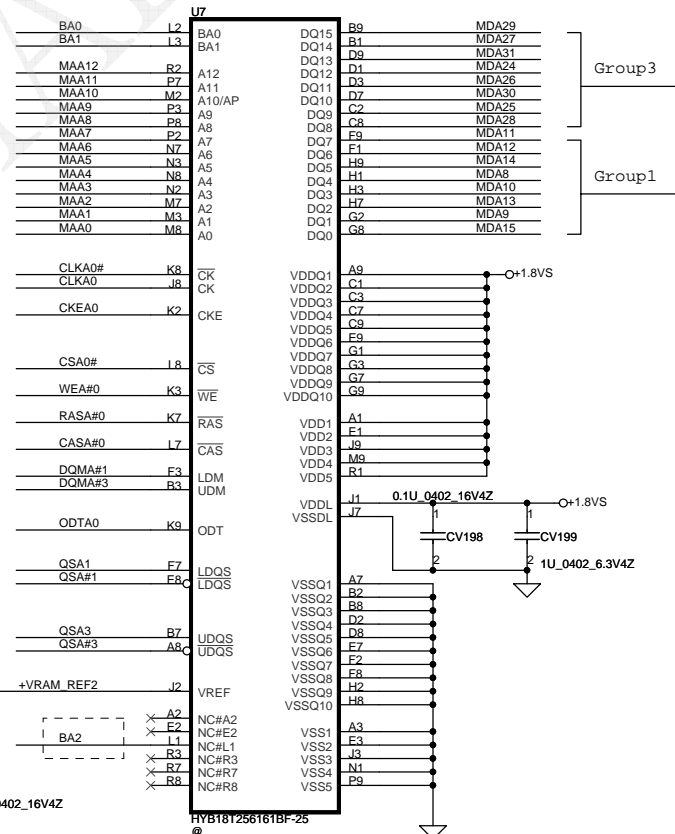
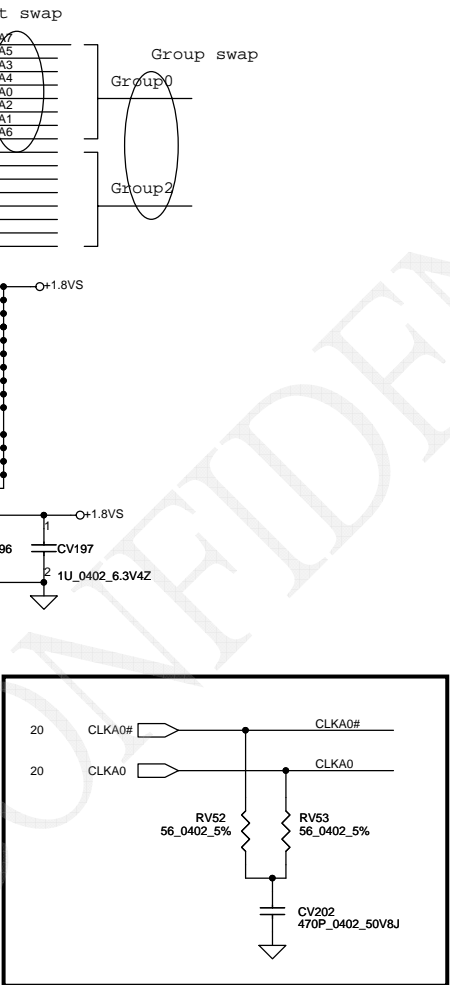
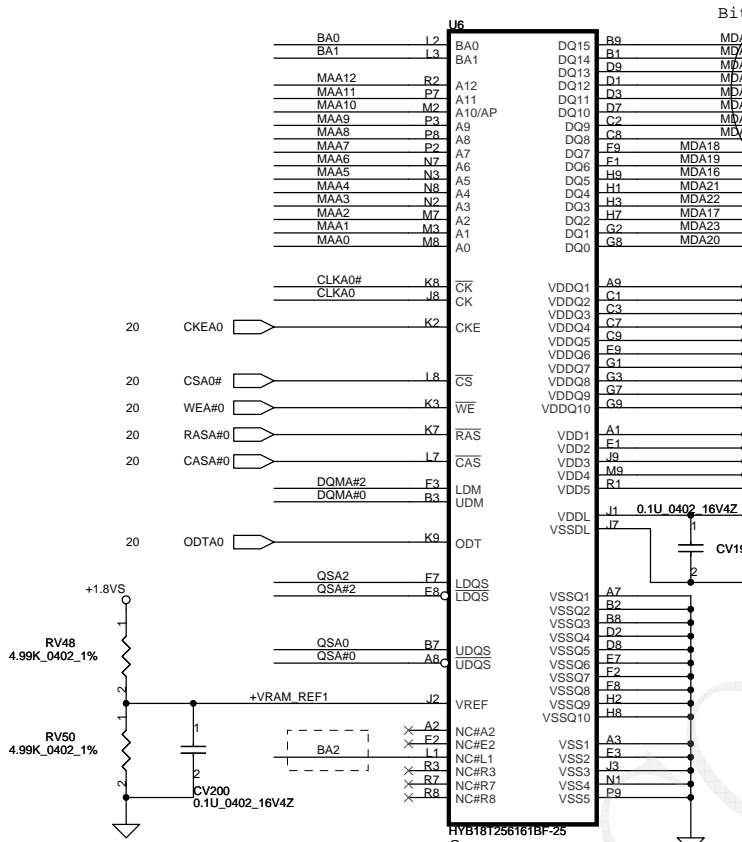
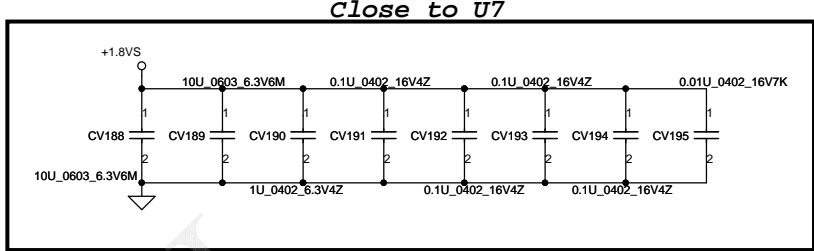
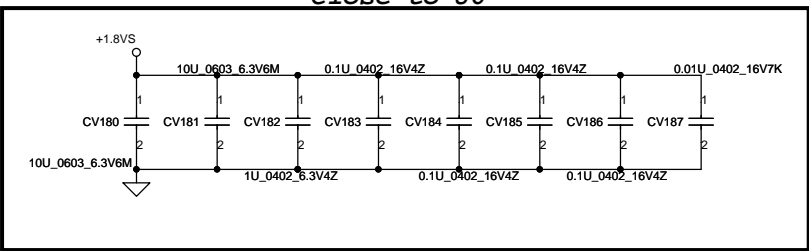
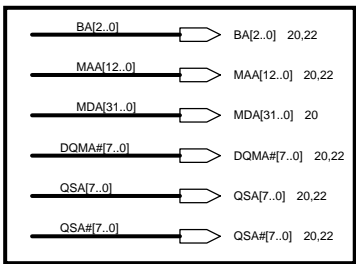
TEST_YCLK

MEMTEST

PLLTEST

216-0683008 A11 M86-M_BGA880

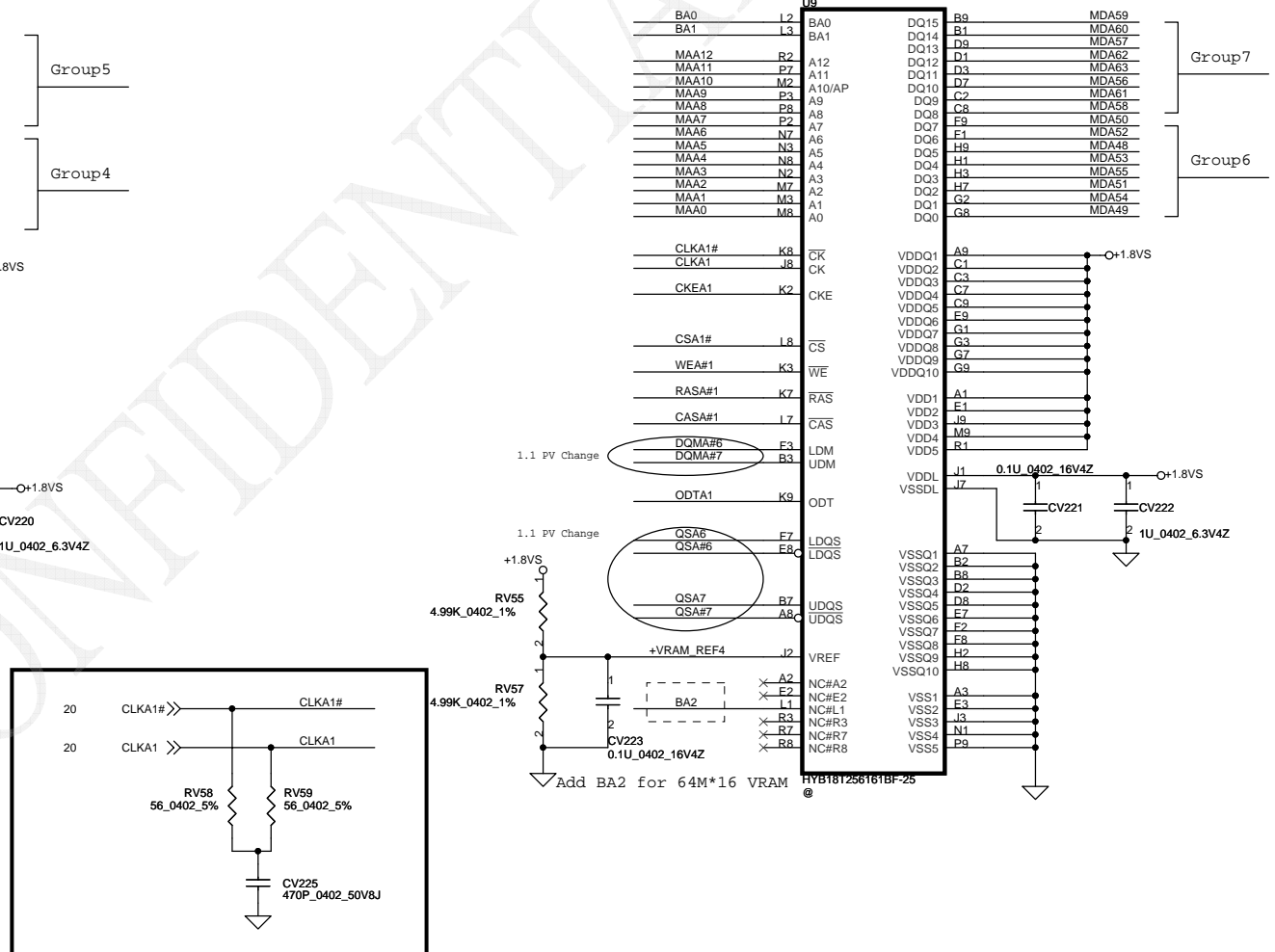
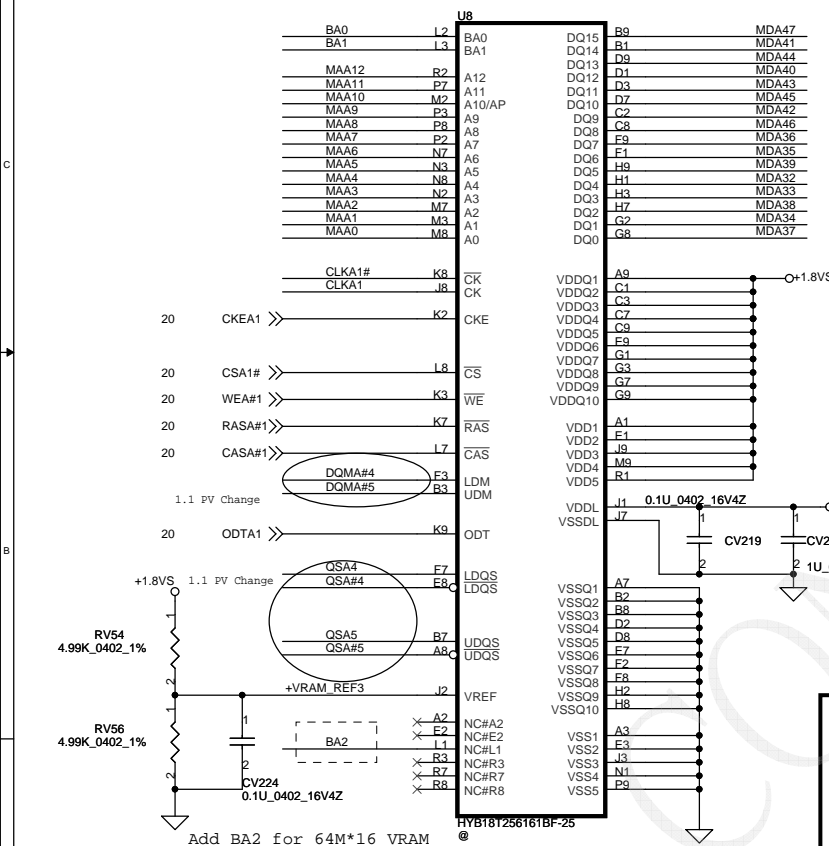
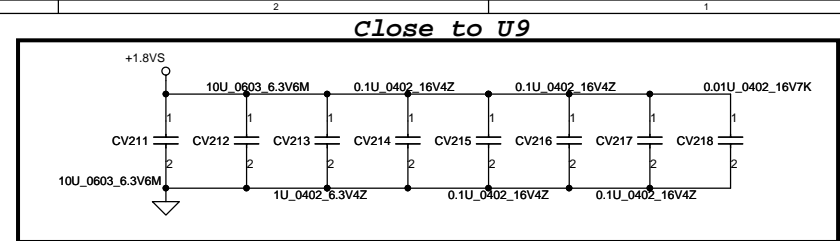
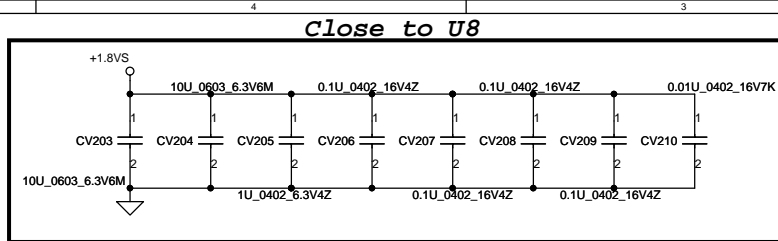
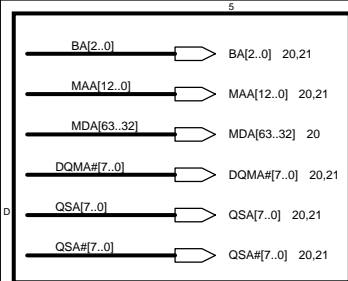
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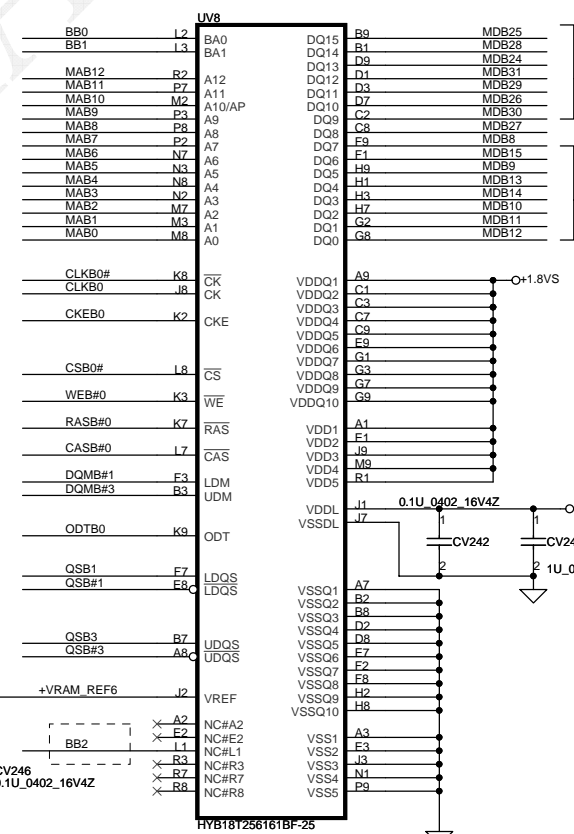
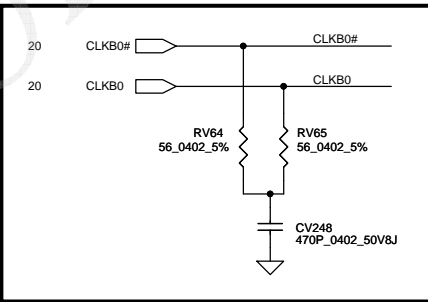
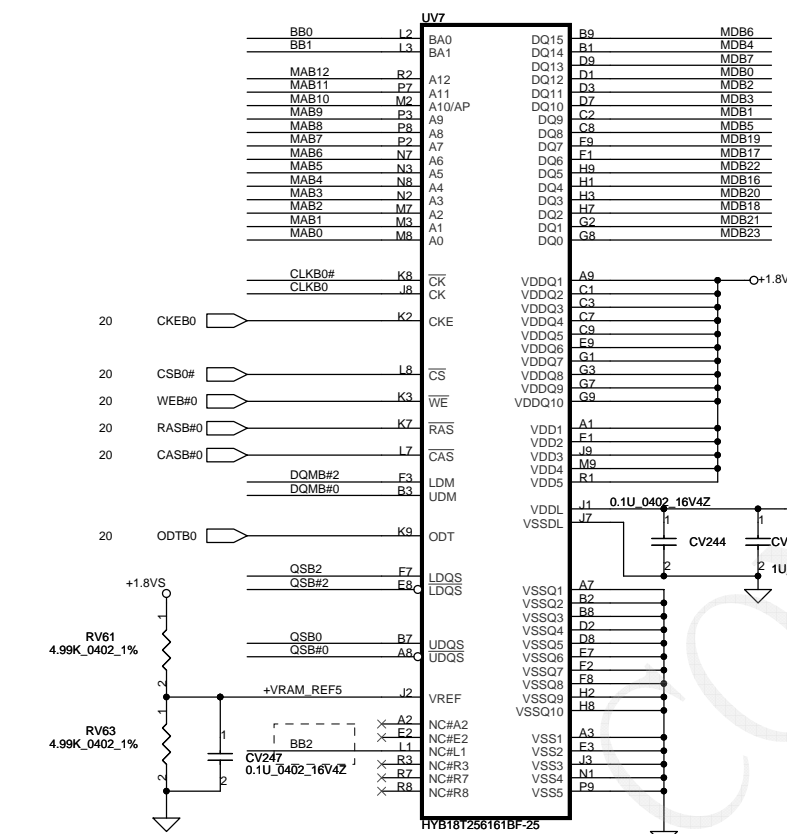
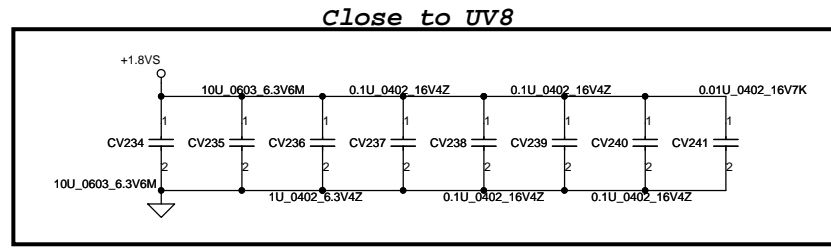
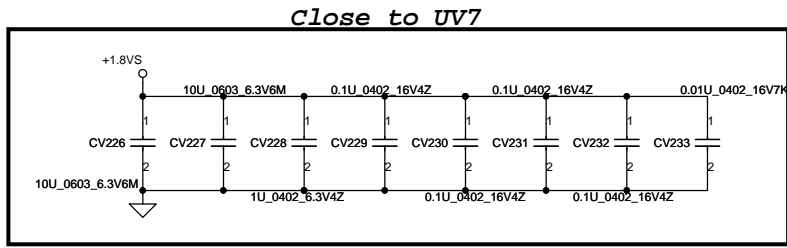
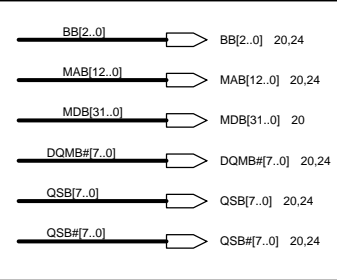


Add BA2 for 64M*16 VRAM

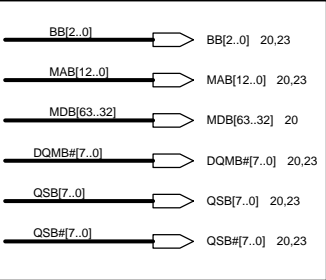
Add BA2 for 64M*16 VRAM

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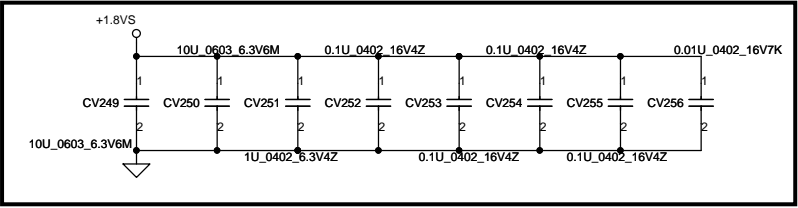




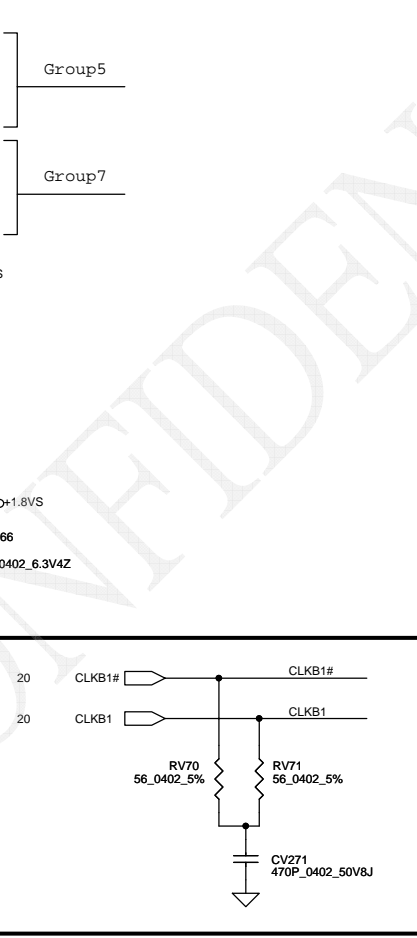
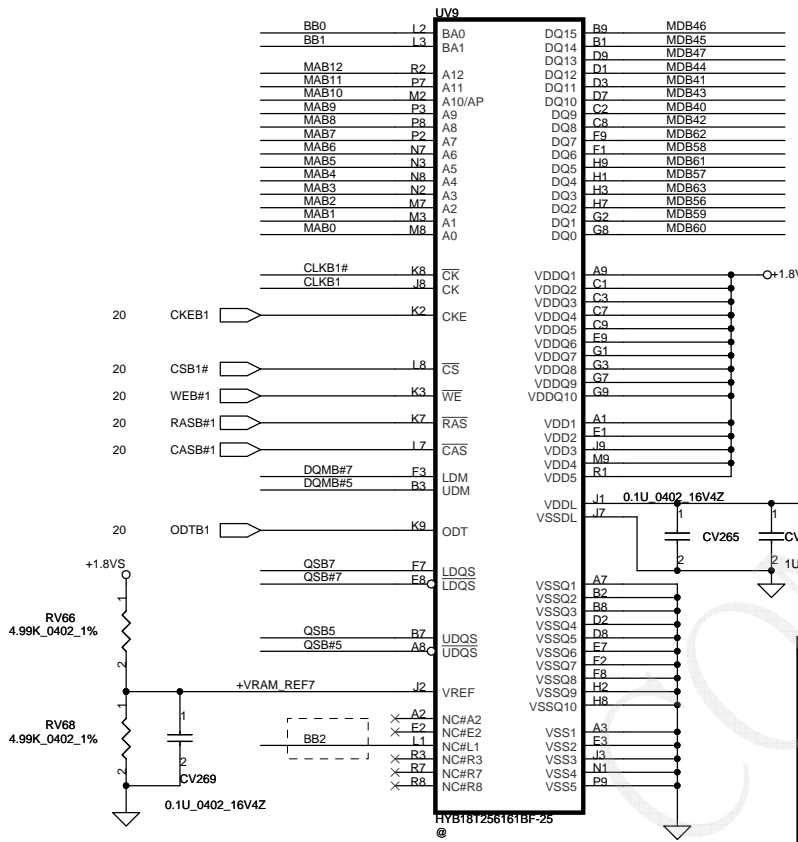
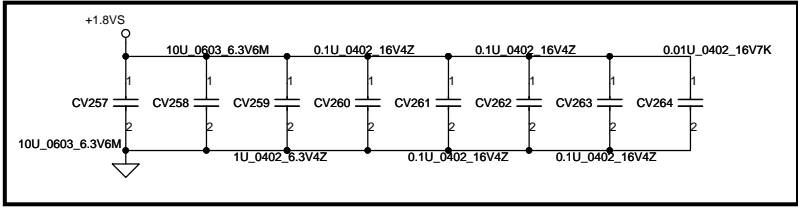
Add BA2 for 64M*16 VRAM



Close to UV9



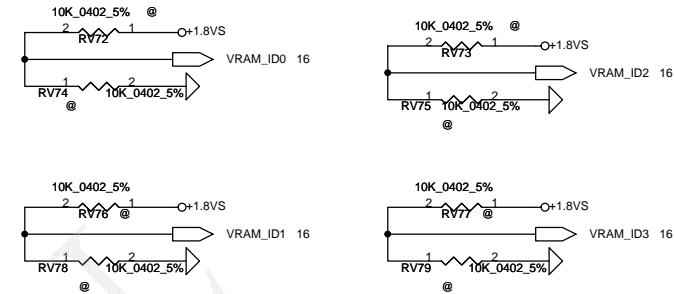
Close to UV10



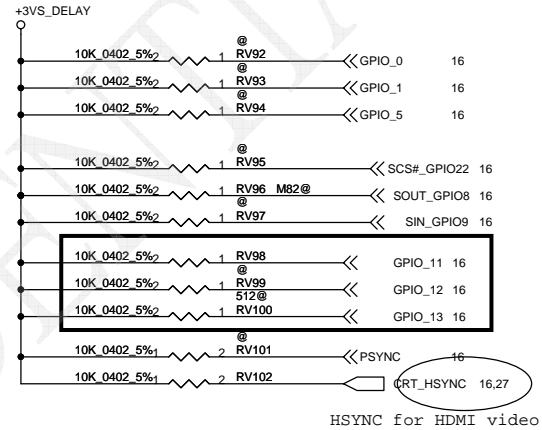
Add BA2 for 64M*16 VRAM

Add BA2 for 64M*16 VRAM

STRAPS	PIN	GPU	Project	VRAM size	Vendor Part Number#	Compal Part Number#	VRAM_ID 3,2,1,0
VRAM_ID[3:0]	DVPPDATA (23,22,21,20)	M82M-XT	JBK00 1.0a	512M(x4)	Samsung 64Mx16 1.8V	SA00002MD00	0 0 0 0
			JBK00 1.0	256M(X4)	Samsung 32Mx16 1.8V	SA00002AJ10	0 0 0 1
				512M(x4)	Hynix 64Mx16 1.8V		0 0 1 0
			JBK00 1.0	256M(X4)	Hynix 32Mx16 1.8V	SA00002DL00	0 0 1 1
			JBK00 1.0	256M(X4)	Qimonda 32Mx16 1.8V	SA00002A600	0 1 0 0
			JBK00 1.0a	512M(x4)	Qimonda 64Mx16 1.8V	SA00002MF00	0 1 0 1
		M86M	JBK00 1.1	512M(x8)	Samsung 32Mx16 1.8V	SA00002AJ10	0 1 1 0
			JBK00 1.1	512M(x8)	Hynix 32Mx16 1.8V	SA00002DL00	0 1 1 1
			JBK00 1.1	512M(x8)	Qimonda 32Mx16 1.8V	SA00002A600	1 0 0 0
				1G(x8)	Samsung 64Mx16 1.8V	SA00002MD00	1 0 0 1
				1G(x8)	Hynix 64Mx16 1.8V		1 0 1 0
				1G(x8)	Qimonda 64Mx16 1.8V	SA00002MF00	1 0 1 1

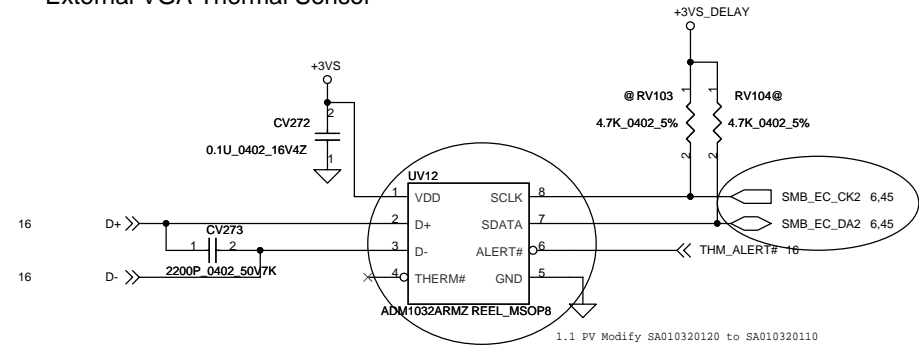


STRAPS	PIN	DESCRIPTION OF RECOMMENDED SETTING	RECOMMENDED M8X
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	0
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLES	0
BIF_DEBUG_ACCESS	GPIO4	DEBUG SIGNALS MUXED OUT	0
BIF_GEN2_EN_A	GPIO5	PCI-E 5.0GT/s or 2.5 GT/s select	0
DEBUG_I2C_ENABLE	GPIO6	Internal use only	0
BIF_AUDIO_EN	VIP3	ENABLE HD AUDIO M86-M ONLY	
BIF_AUDIO_EN	GPIO8	ENABLE HD AUDIO M82-M ONLY	
ROMIDCFG[3:0]	GPIO [9,13,12,11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	0 0 0 1
BIF_VGA_DIS	PSYNC	VGA ENABLED===0 is enable	0
BIF_HDMI_EN	HSYNC	HDMI ENABLE	1

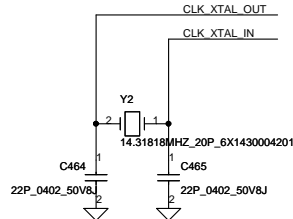
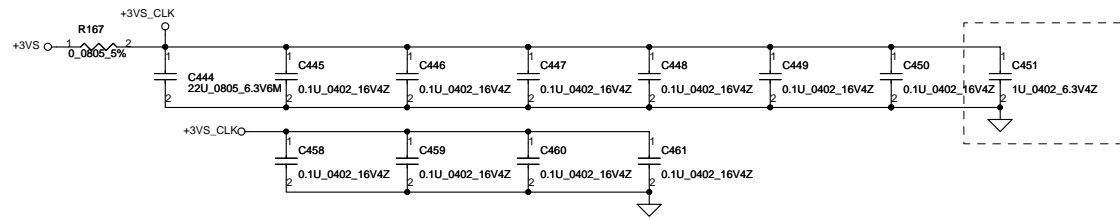
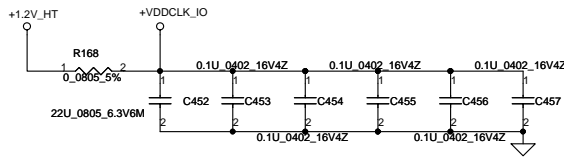


GPIO9 = 0 (BIOS_ROM_EN = 0)	
GPIO[13:11]	MEMORY SIZE
0 0 0	128MB
0 0 1	256MB
0 1 0	64MB
1 0 0	512MB

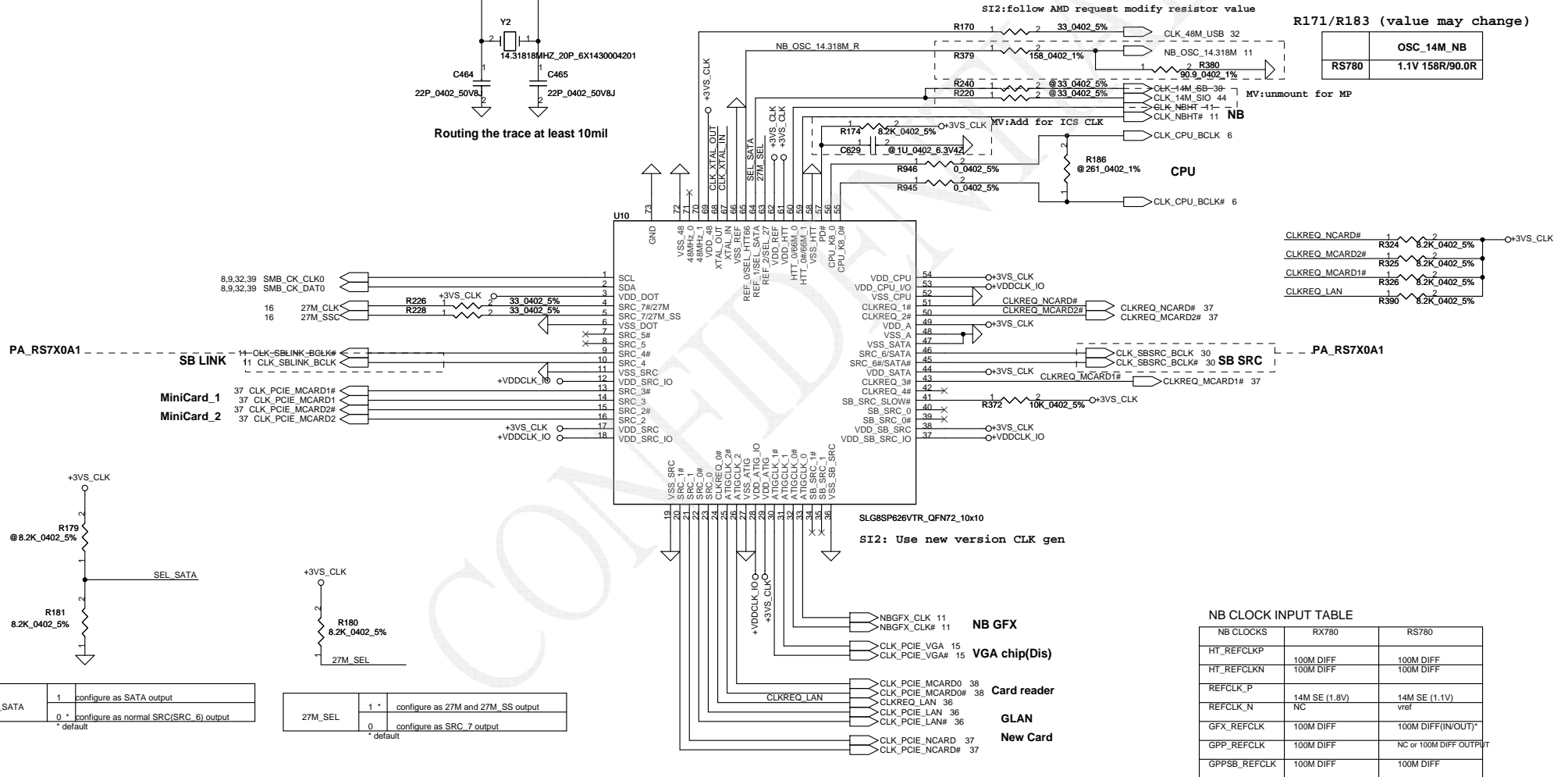
External VGA Thermal Sensor



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Routing the trace at least 10mil



NB CLOCK INPUT TABLE

NB CLOCKS	RX780	RS780
HT_REFCLKP	100M DIFF	100M DIFF
HT_REFCLKN	100M DIFF	100M DIFF
REFCLK_P	14M SE (1.8V)	14M SE (1.1V)
REFCLK_N	NC	vref
GFX_REFCLK	100M DIFF	100M DIFF(IN/OUT)*
GPP_REFCLK	100M DIFF	NC or 100M DIFF OUTPUT
GPPSB_REFCLK	100M DIFF	100M DIFF

Use voltage divider resistor R379 & R380 to pull low

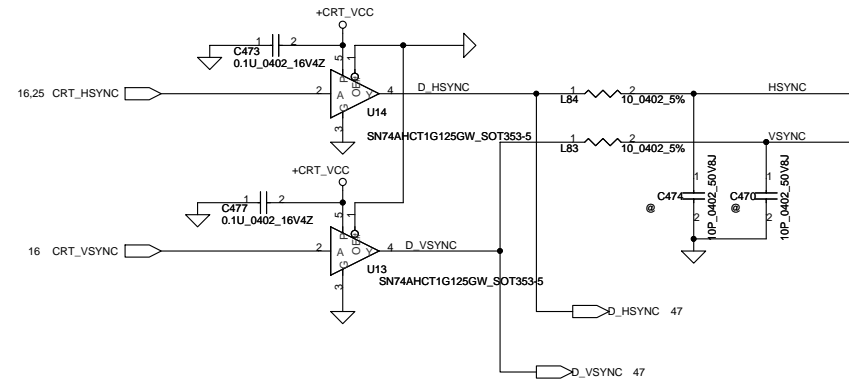
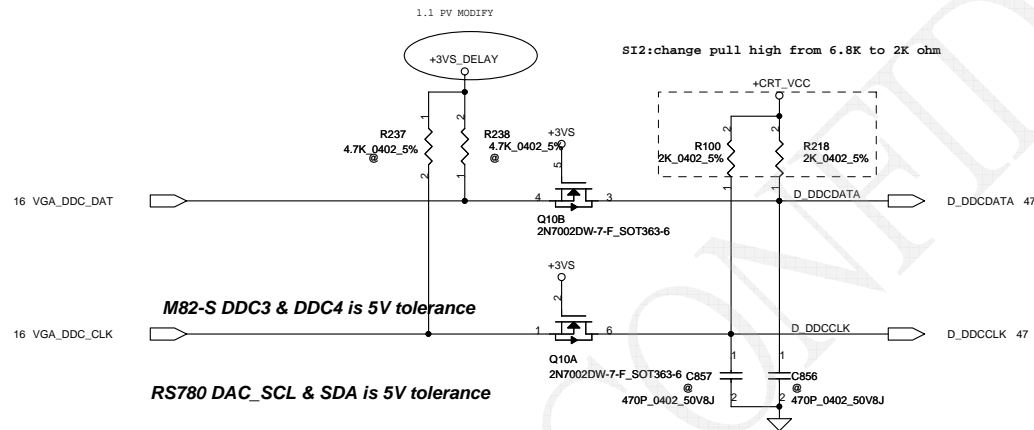
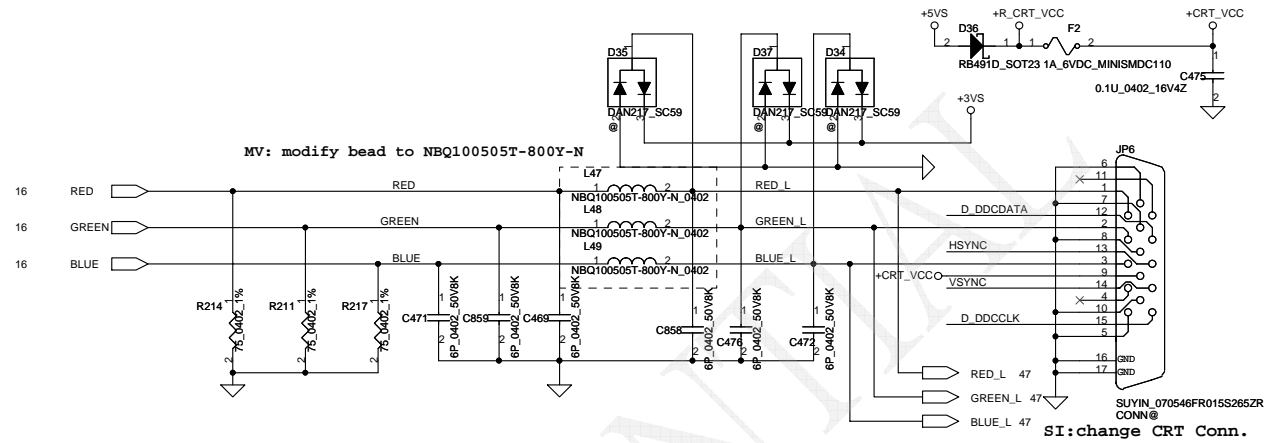
NB_OSC_14.318M	1	configure as single-ended 66MHz output
	0*	configure as differential 100MHz output
		* default

27M_SEL	1*	configure as 27M and 27M_SS output
	0	configure as SRC 7 output
		* default

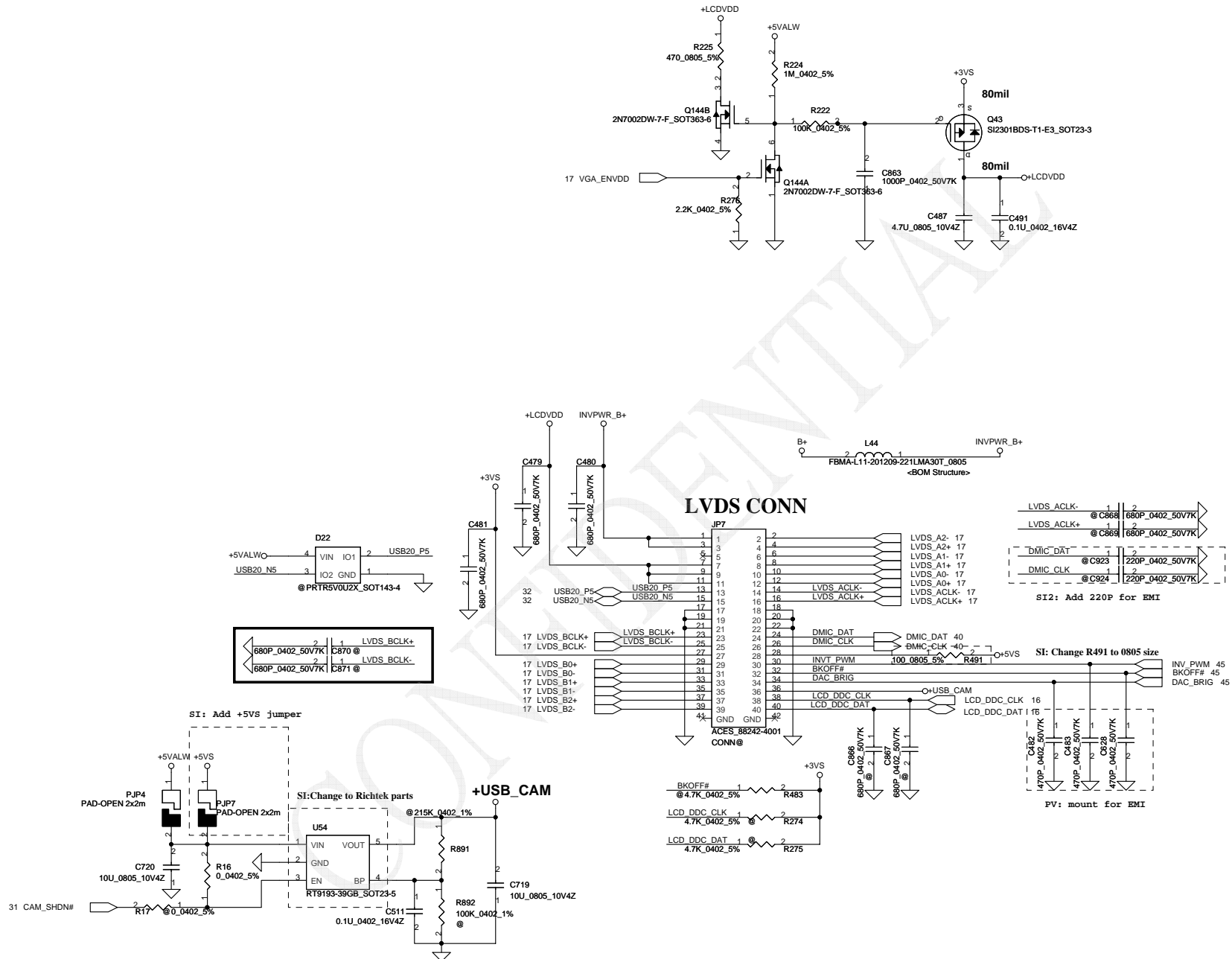
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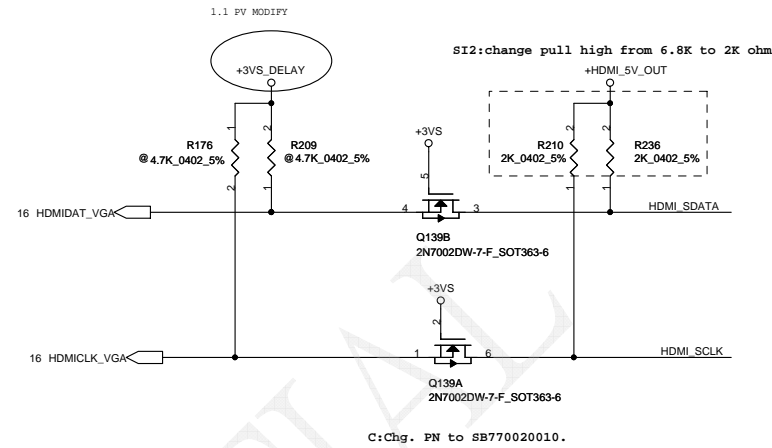
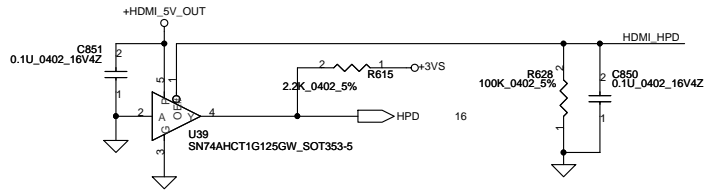
CRT CONNECTOR



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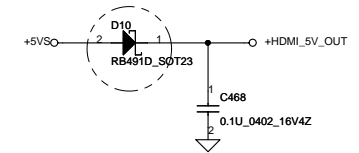


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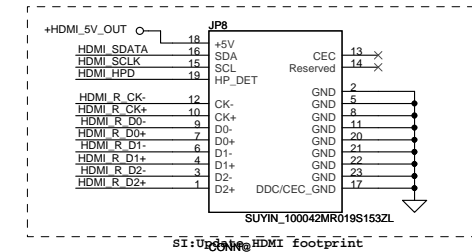


C:Chg. PN to SB770020010.

MP:Update D10 to meet HDMI.

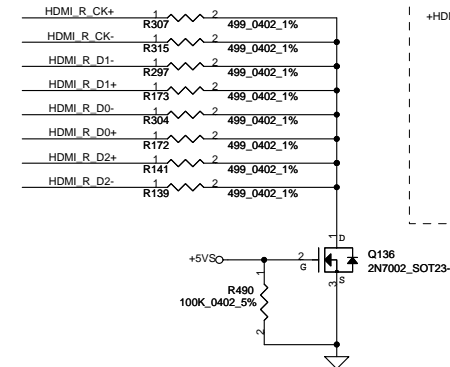
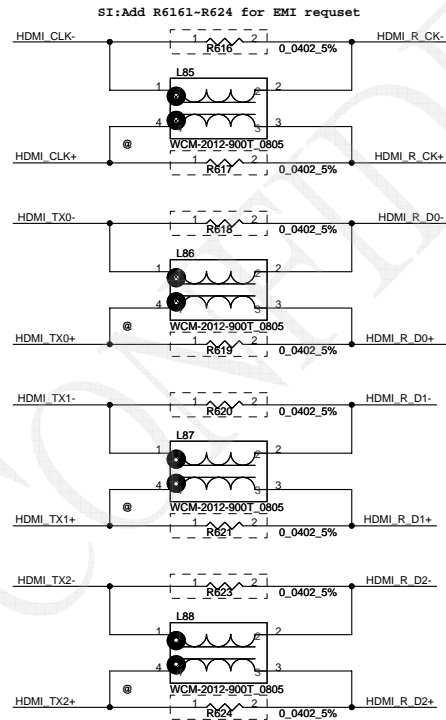


HDMI Connector



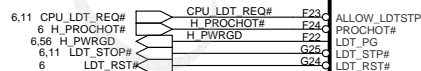
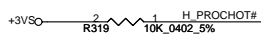
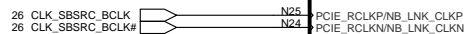
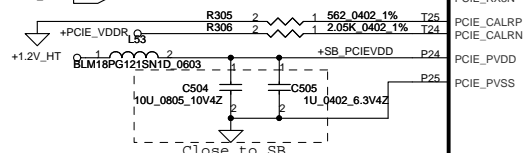
SI:Update HDMI footprint

16	HDMI_CLK+_VGA	C507	1	2	0.1U_0402_16V7K	HDMI_CLK+
16	HDMI_CLK-_VGA	C508	1	2	0.1U_0402_16V7K	HDMI_CLK-
16	HDMI_TX0+_VGA	C655	1	2	0.1U_0402_16V7K	HDMI_TX0+
16	HDMI_TX0-_VGA	C675	1	2	0.1U_0402_16V7K	HDMI_TX0-
16	HDMI_TX1+_VGA	C804	1	2	0.1U_0402_16V7K	HDMI_TX1+
16	HDMI_TX1-_VGA	C827	1	2	0.1U_0402_16V7K	HDMI_TX1-
16	HDMI_TX2+_VGA	C852	1	2	0.1U_0402_16V7K	HDMI_TX2+
16	HDMI_TX2-_VGA	C853	1	2	0.1U_0402_16V7K	HDMI_TX2-



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1 R300 2 NB_RST# R
@ 8.2K_0402_5%



SB700
Part 1 of 5

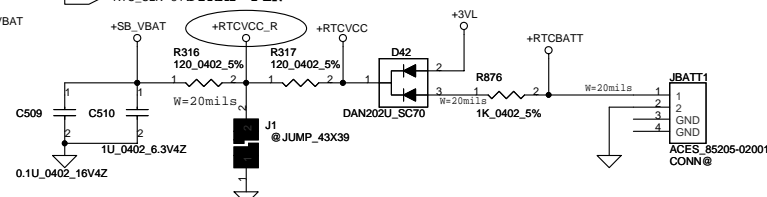
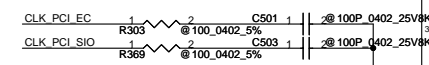
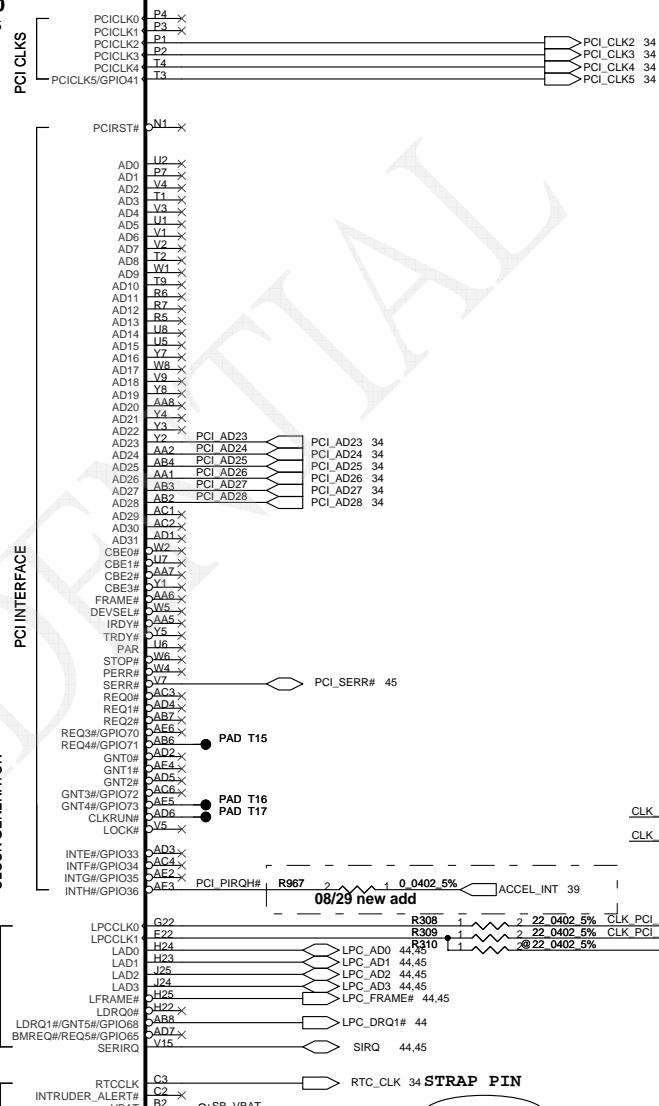
PCI EXPRESS INTERFACE

PCI INTERFACE

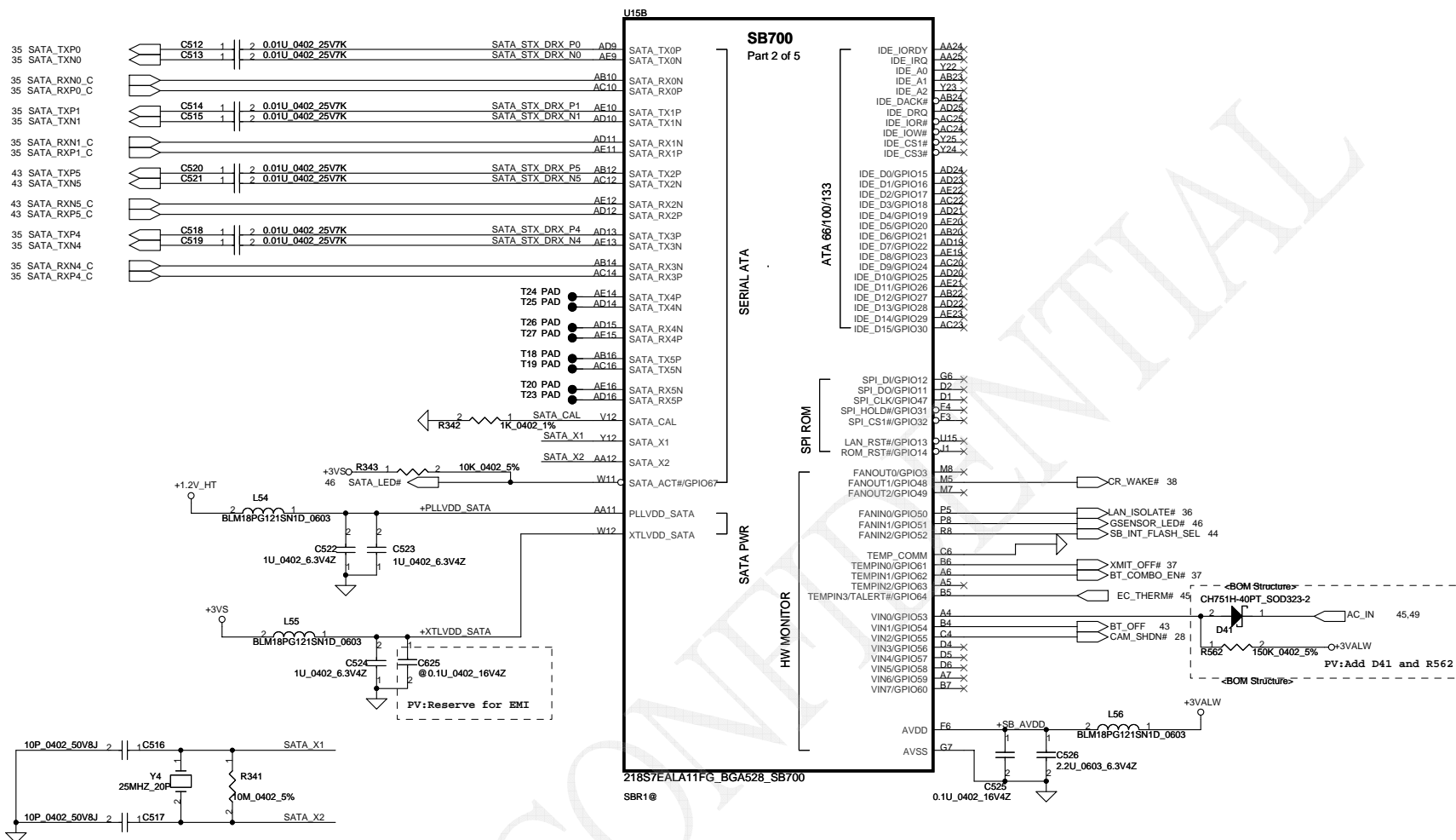
CLOCK GENERATOR

LPC

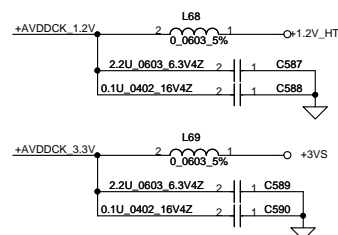
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						Docu- ment Number		401621		Rev C	
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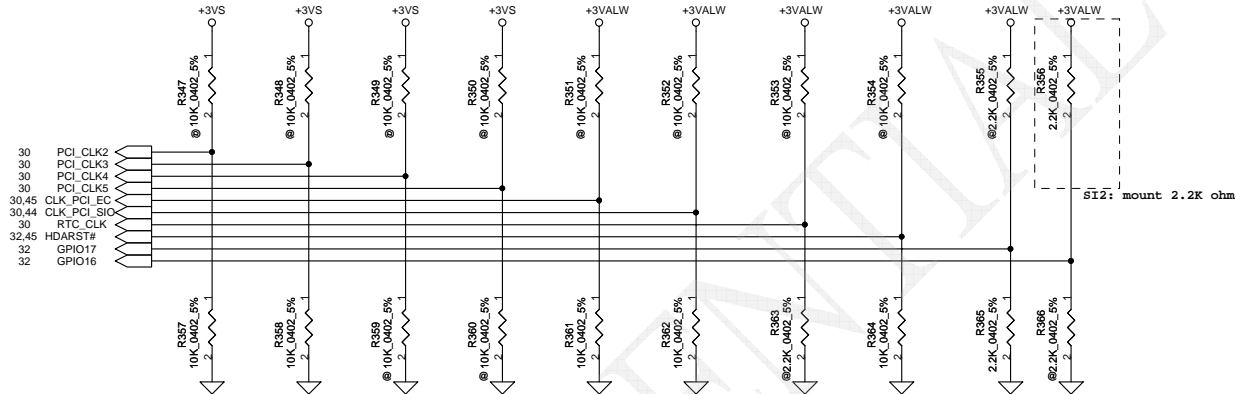


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REQUIRED STRAPS

NOTE: SB700 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK

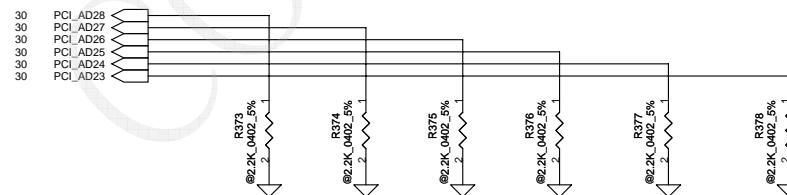
	PCI_CLK2	PCI_CLK3	PCI_CLK4	PCI_CLK5	LPC_CLK0	LPC_CLK1	RTC_CLK	AZ_RST_CD#	GP17	GP16
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	ENABLE PCI MEM BOOT	CLKGEN ENABLED	INTERNAL RTC DEFAULT	EC ENABLED	Internal pull up H,H = Reserved H,L = SPI ROM	
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			DISABLE PCI MEM BOOT DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	EC DISABLED DEFAULT		



DEBUG STRAPS

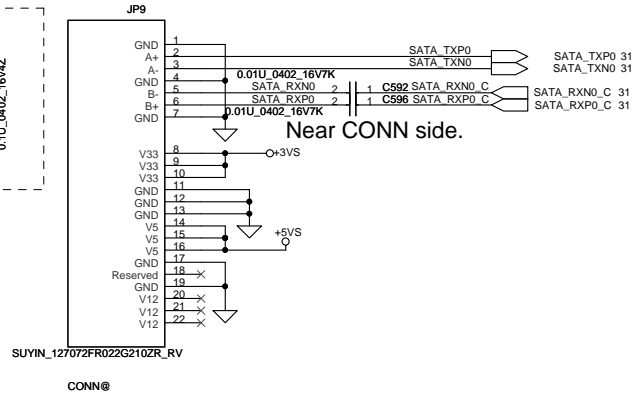
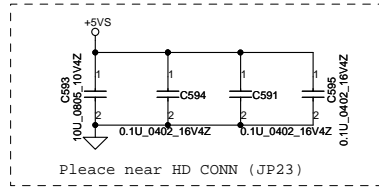
SB700 HAS 15K INTERNAL PU FOR PCI_AD[28:23]

	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	RESERVED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	

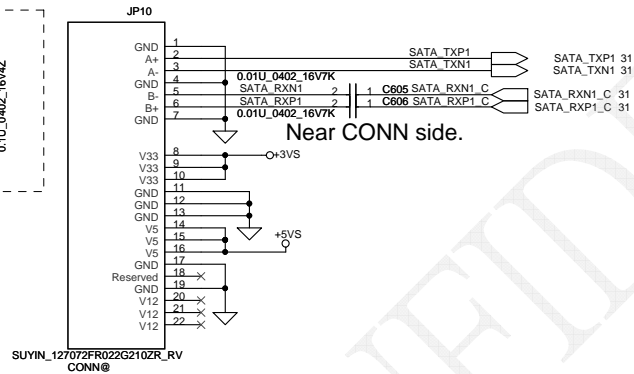
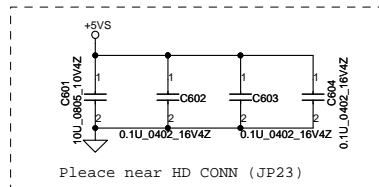


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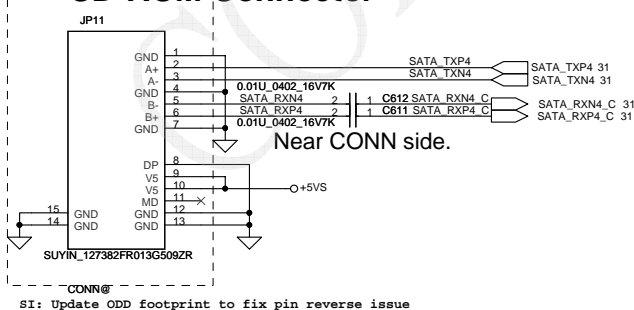
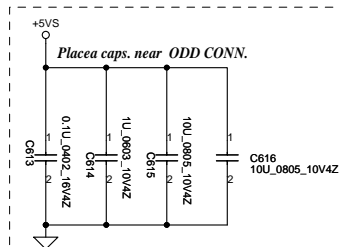
HDD Connector



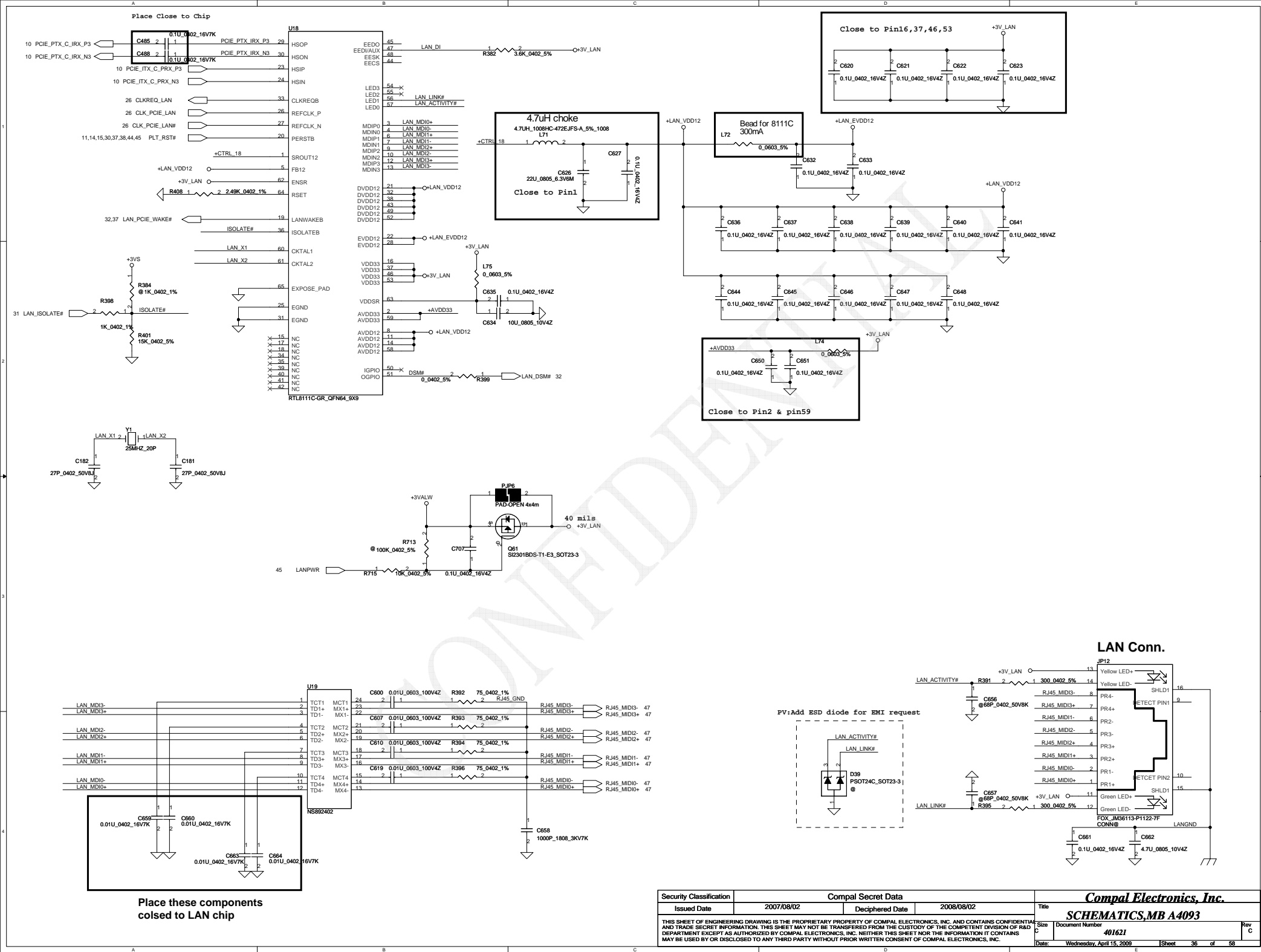
2nd HDD Connector-option



CD-ROM Connector

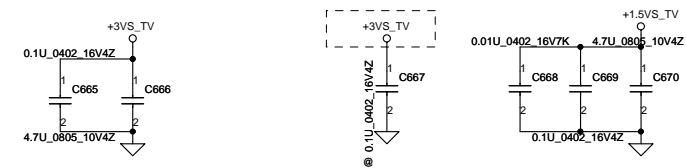


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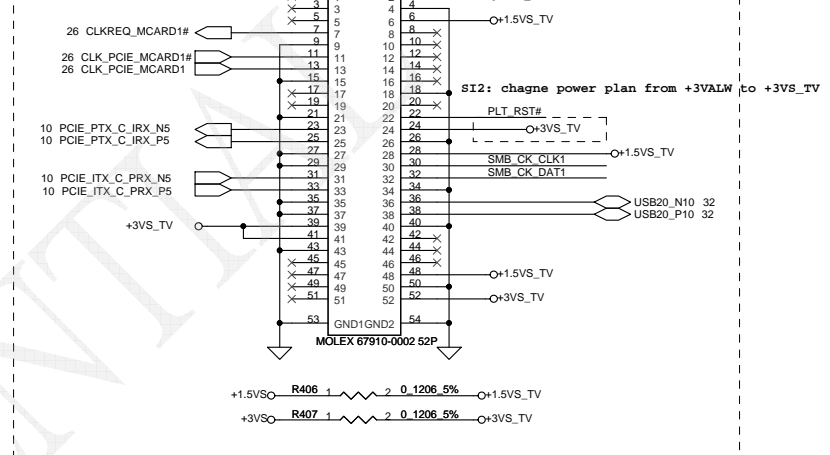


Mini Card---TV tuner

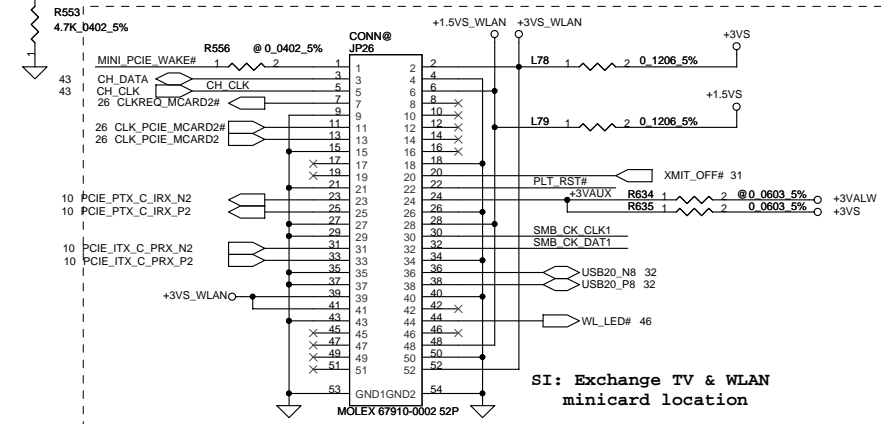
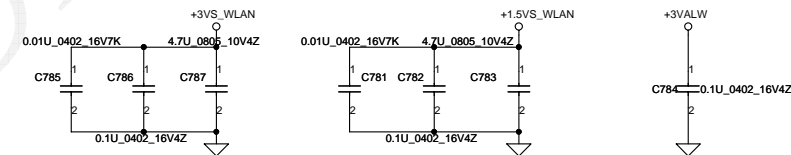
SI2: chagne power plan from +3VALW to +3VS_TV



SI: Exchange TV & WLAN minicard location

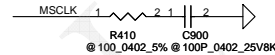
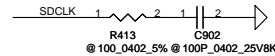
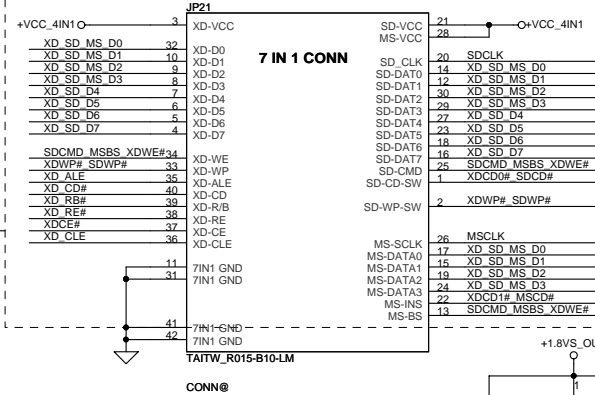


Mini-Express Card---WLAN



SI:Per ME request change
JP21 to new one

Card Reader Connector

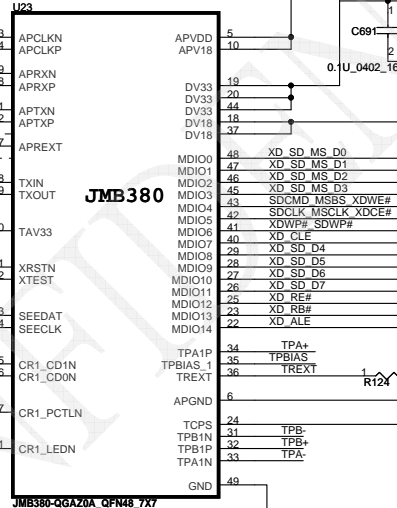


SI:Per Jmicro request change
R405 & R122 from 200K to 10K

SI2:Support D3E function

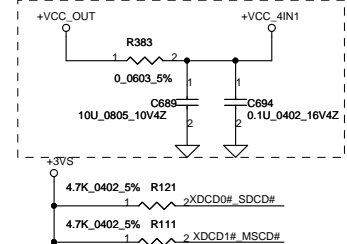
SI:Use build in Regulator
Chip unmount R387

Power Circuit



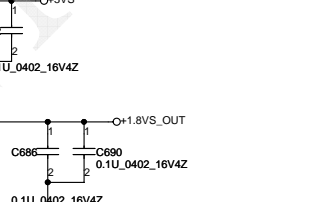
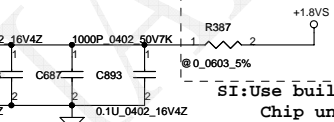
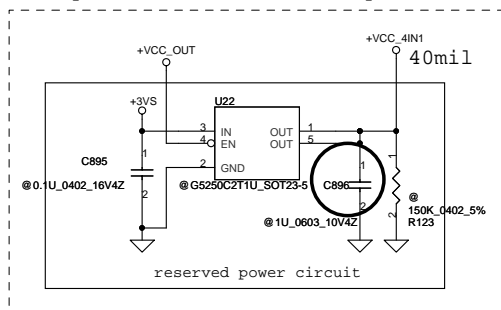
SI2: Use B version chip

SI:Use build in Regulator
Chip mount R383,C689,C694

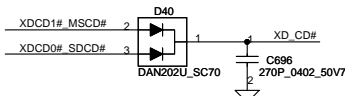
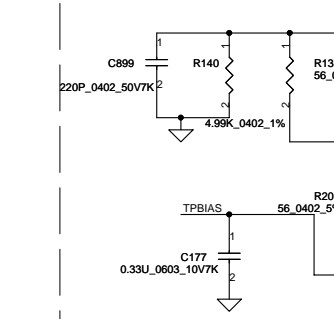


SI:Use new chip ,change to
High active control

SI:Use build in Regulator
Chip unmount U22 and relation parts

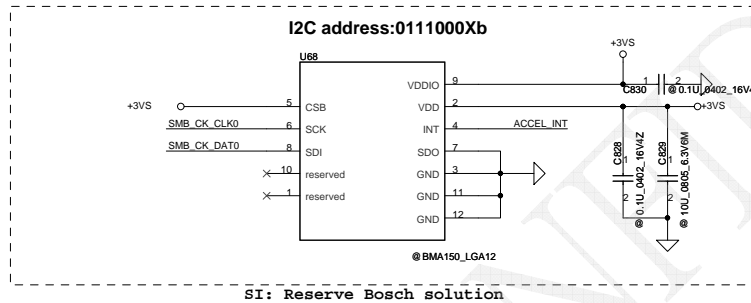
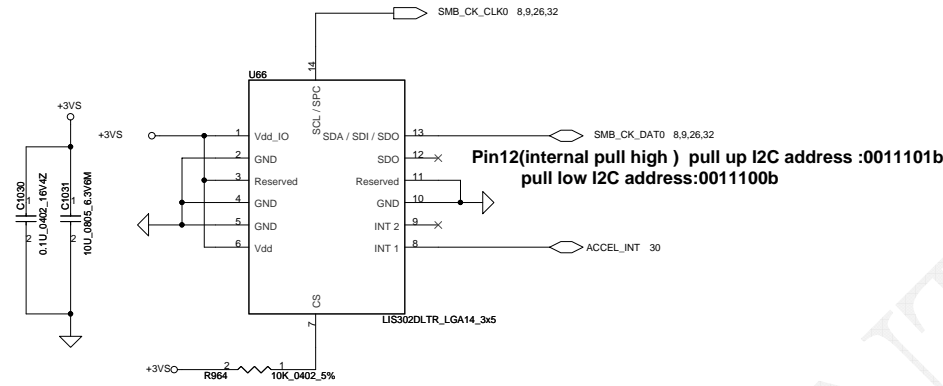


Close to Chip



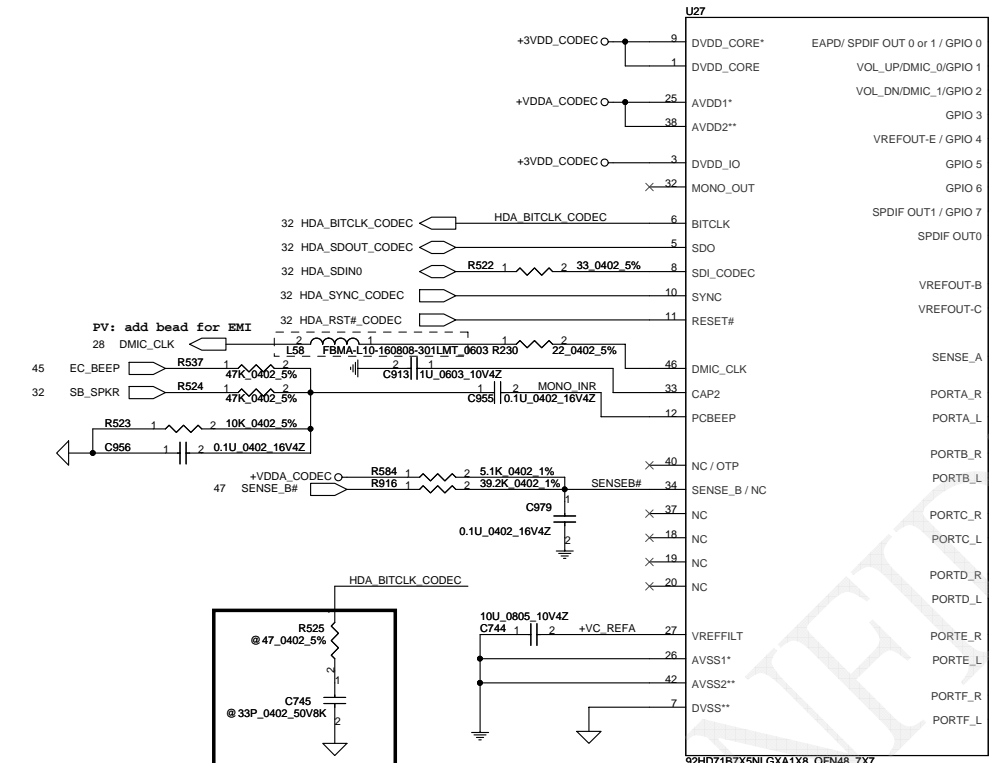
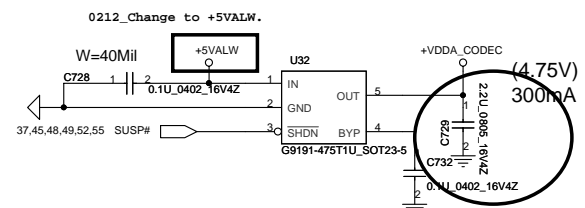
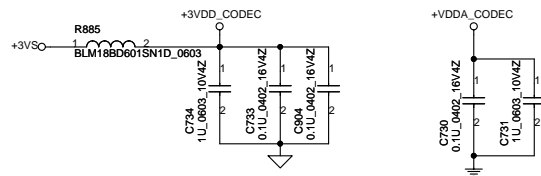
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ACCELEROMETER



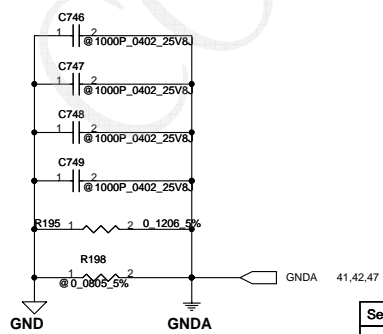
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CODEC POWER

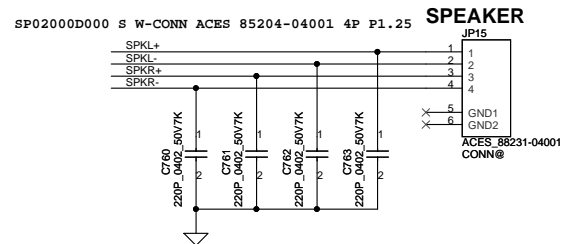
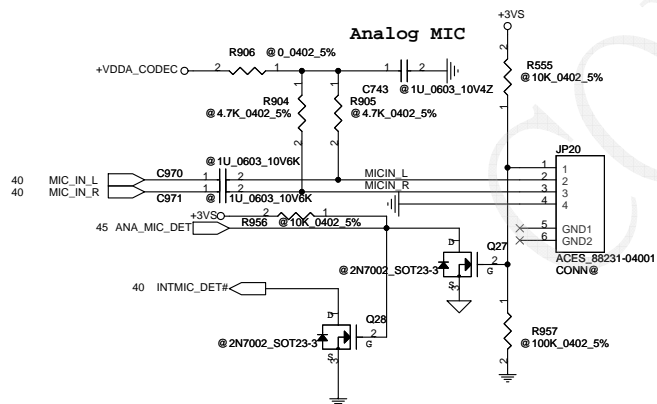
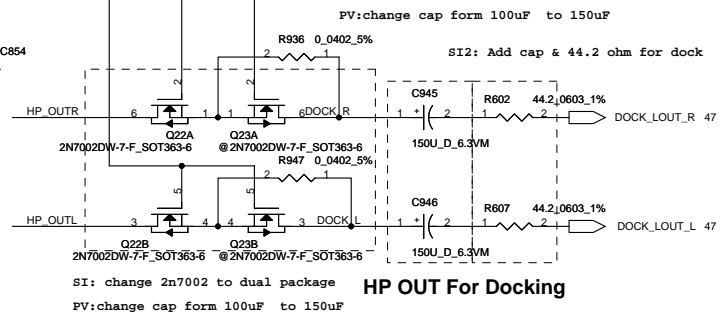
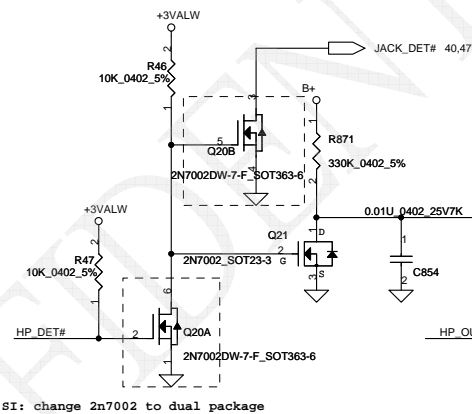
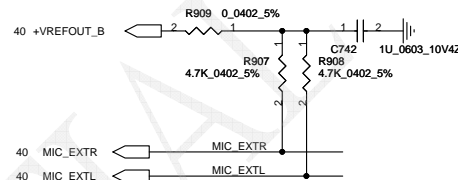
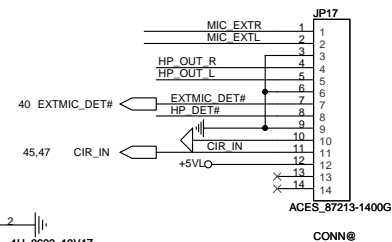
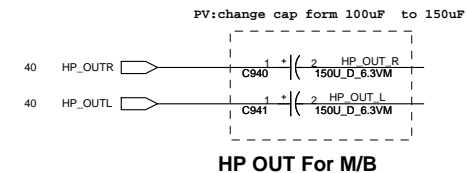
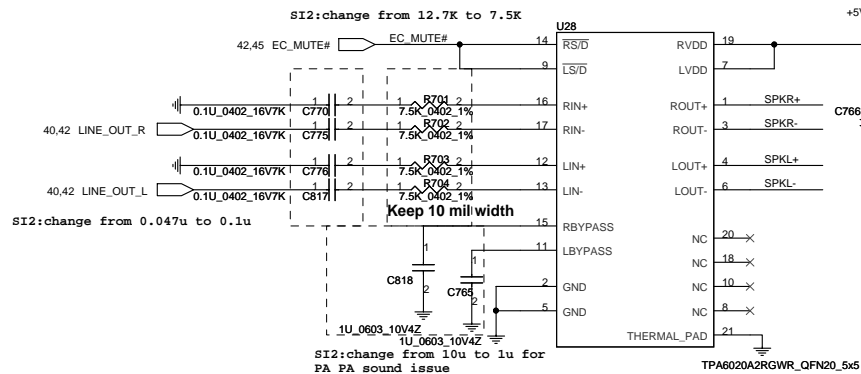


SI2: Use new version Codec

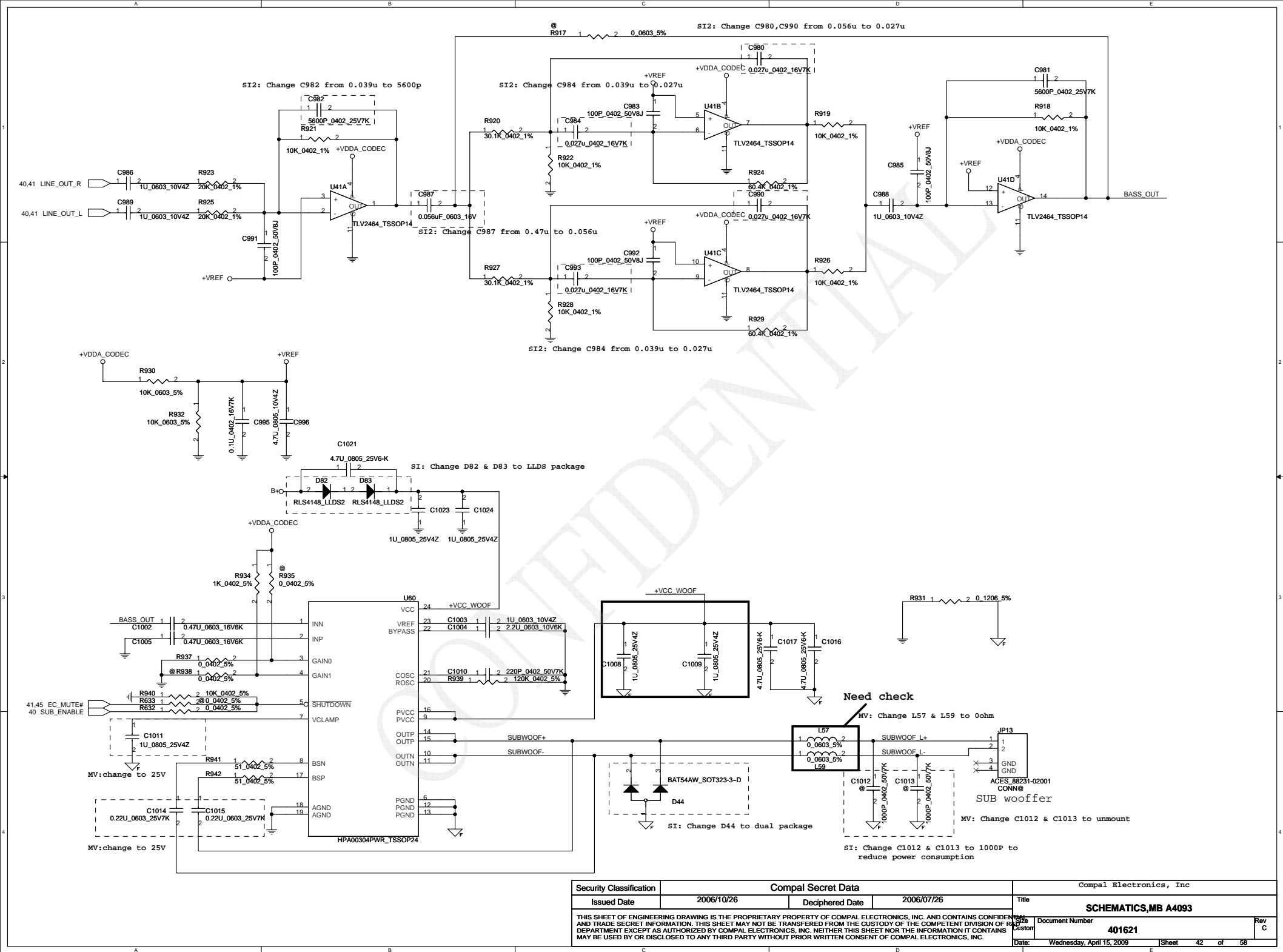
SENSE A		SENSE B	
Port	Resistor	Port	Resistor
A	39.2K	E	39.2K
B	20K	F	20K
C	10K	G	10K
D	5.11K	H	5.11K



HP_DET#	MIC_DET	LINEOUT	PORT-A <Earphone OUT>	MIC	EQ
0 (LOW)	0 (LOW)	OFF	ON	ON	Disable
0 (LOW)	NC	OFF	ON	OFF	Disable
NC	0 (LOW)	ON	OFF	ON	Enable
NC	NC	ON	OFF	OFF	Enable

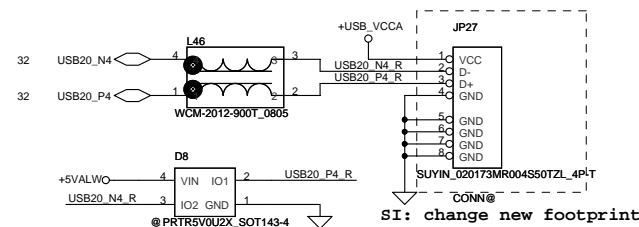
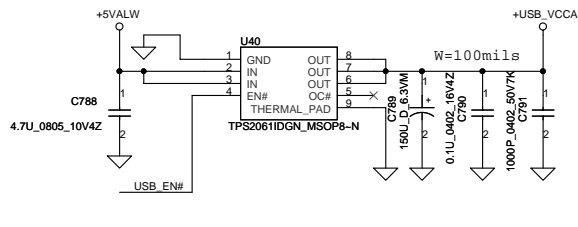


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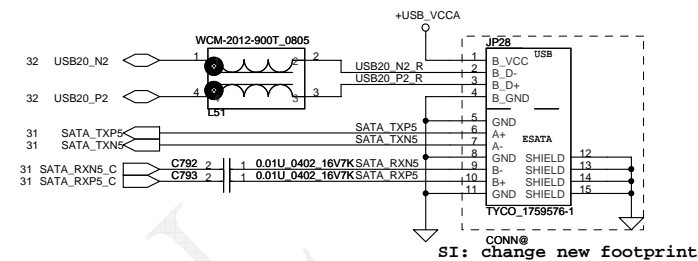


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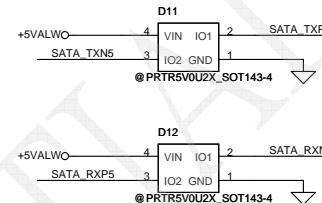
Left side USB CONNECTOR 0



Left side ESATA/USB combination Connector

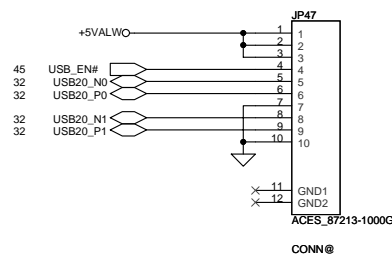


SI: change new footprint

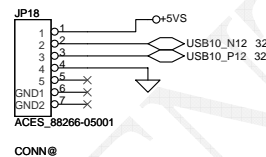


SI: new add for ESD

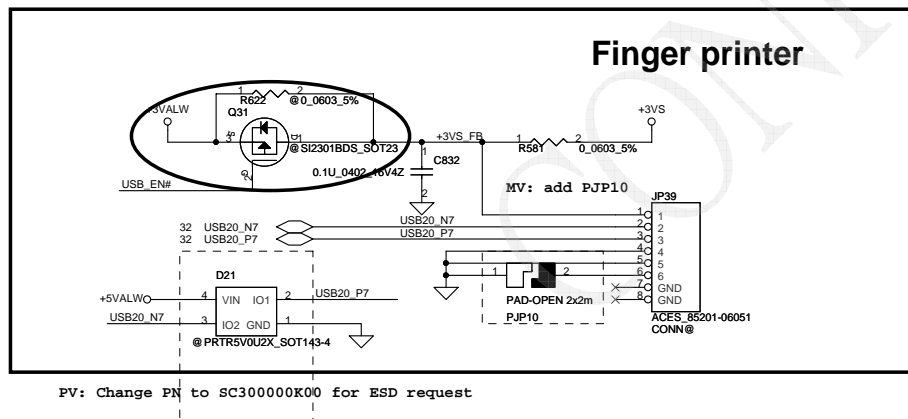
USB Board Conn



Touch screen

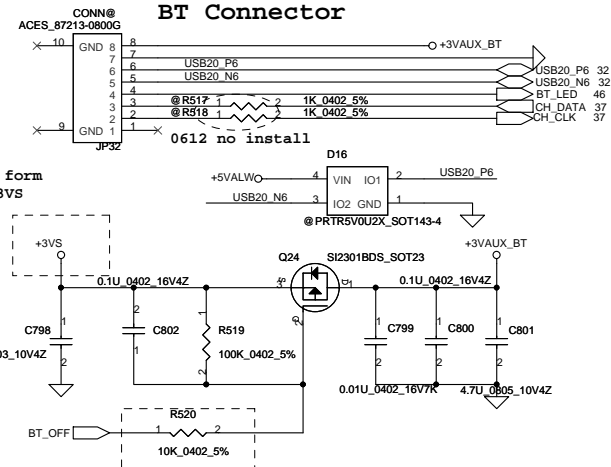


Finger printer



PV: Change PN to SC300000K00 for ESD request

BT Connector

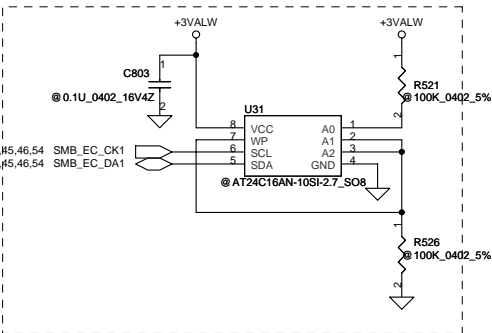


SI: change to 10K ohm to make sure MOS can turn on

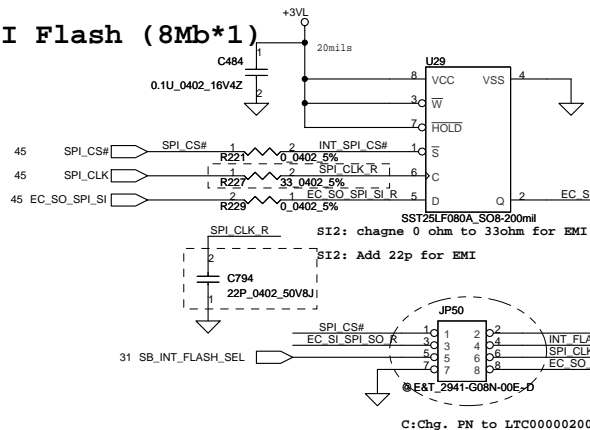
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				401621	C
				Date: Wednesday, April 15, 2009	Sheet 43 of 58

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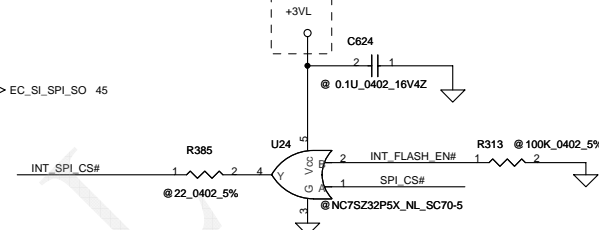
SI2: Change from +3VL to +3VALW and unmount this EEPROM



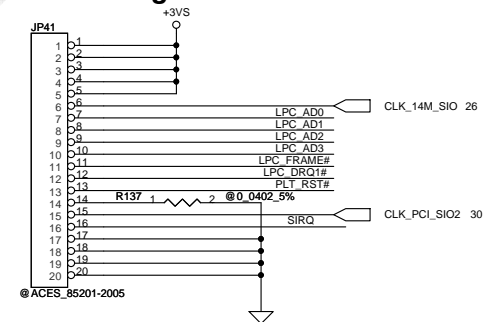
SPI Flash (8Mb*1)



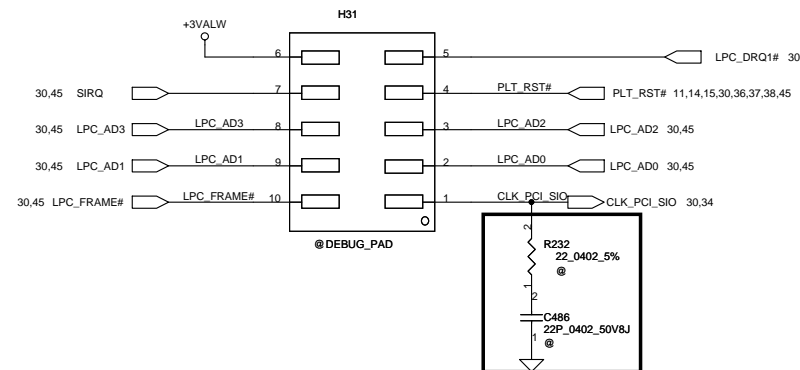
SI2: Change from +3VALW to +3VL



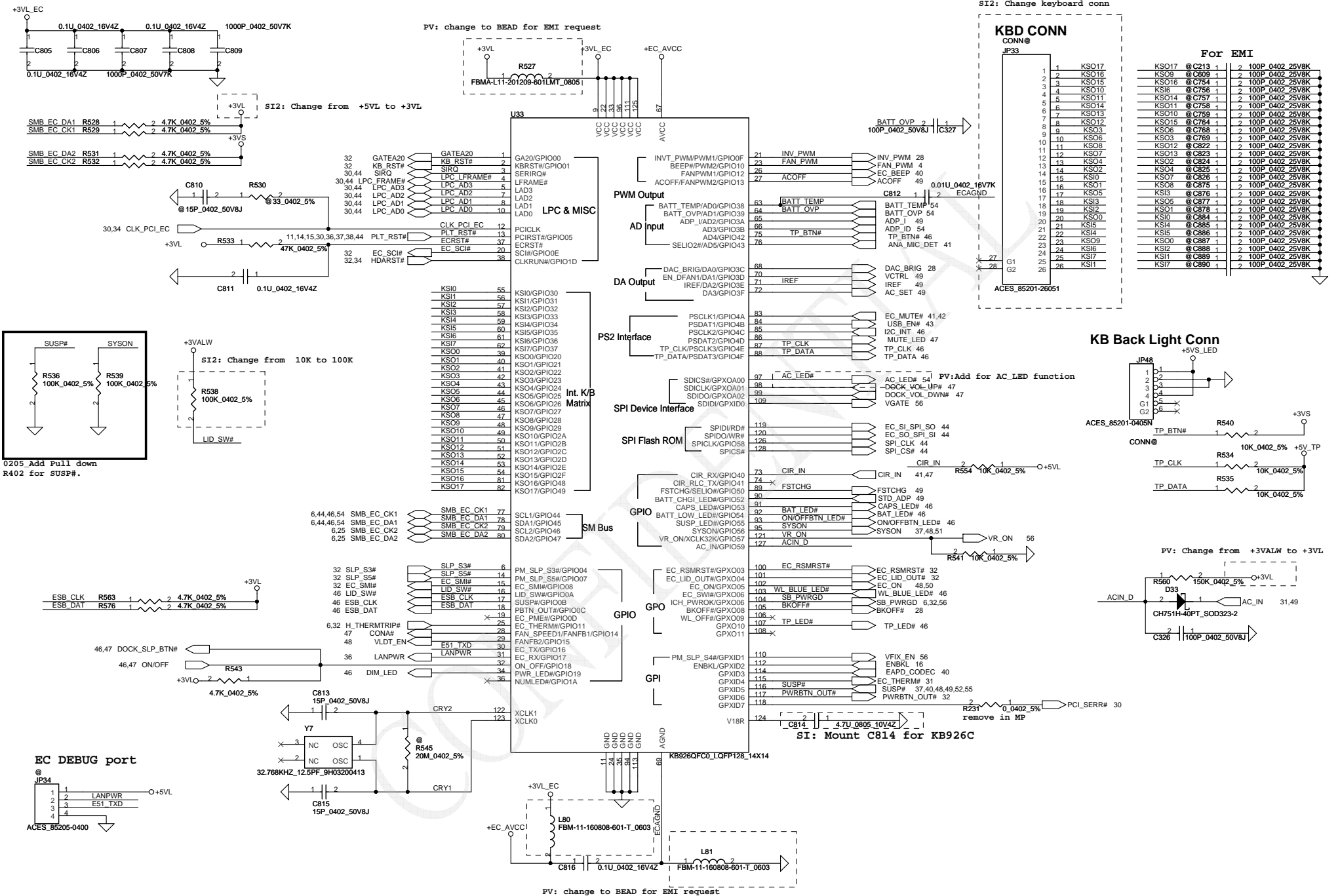
LPC Debug Port



LPC Debug Port

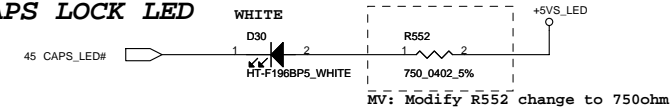
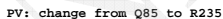
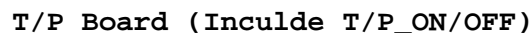
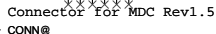


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Date: Wednesday, April 15, 2009				Sheet 44 of 58	

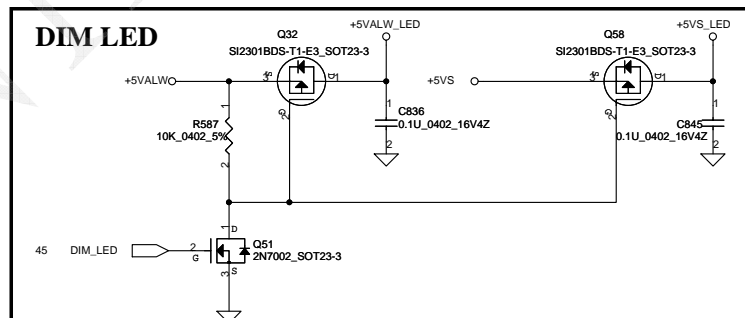


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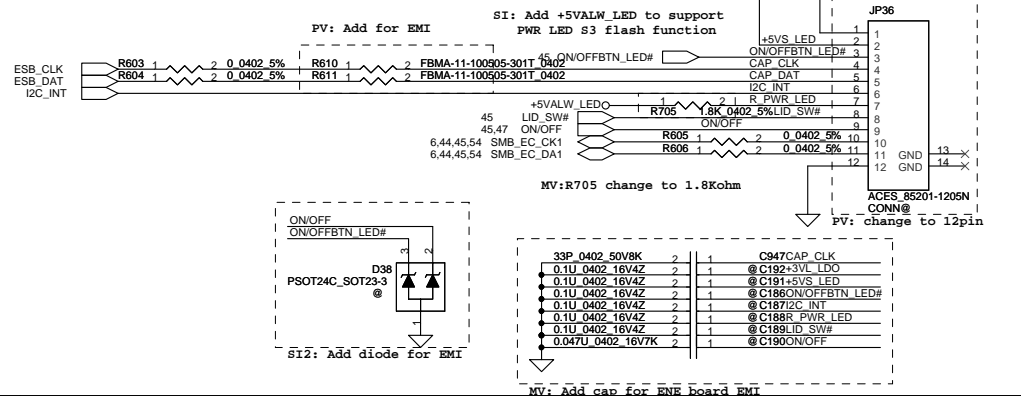
Change type 4/25



HDD LED(Left 3)

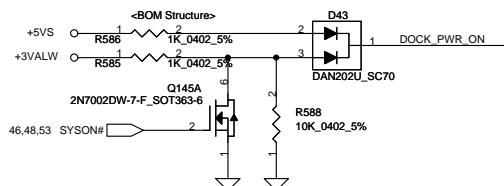


MV: Add cap for ENE board EMI



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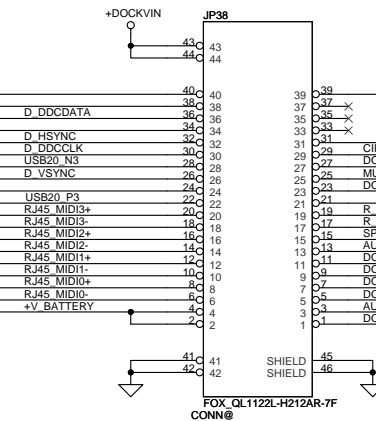
Atlas/ Saturn Dock



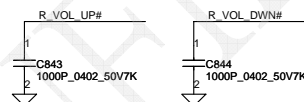
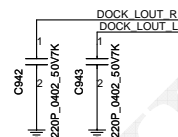
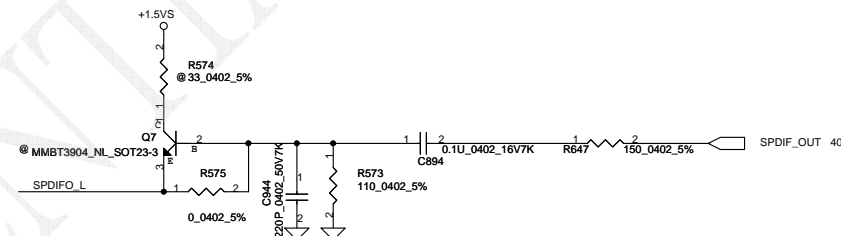
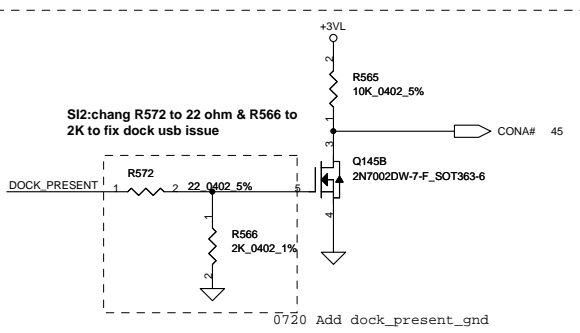
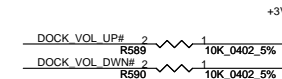
DOCK_PWR_ON Spec

0V = Notebook S4/S5, Dock off
2.5V = Notebook S3, Dock on
4V = Notebook S0, Dock on

27 GREEN_L
27 RED_L
27 BLUE_L
27 D_HSYNC
27 D_DDCLK
32 USB20_N3
27 D_VSYNC
32 USB20_P3
36 RJ45_MIDI3+
36 RJ45_MIDI3-
36 RJ45_MIDI2+
36 RJ45_MIDI2-
36 RJ45_MIDI1+
36 RJ45_MIDI1-
36 RJ45_MIDI0+
36 RJ45_MIDI0-
PJP5
@ PAD-OPEN 2x2m

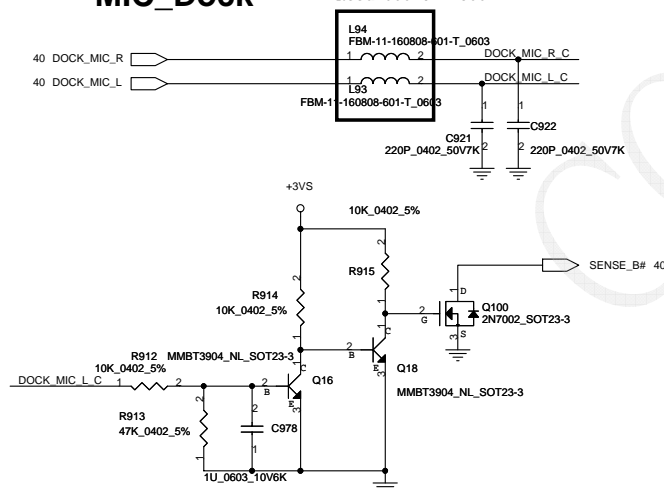


need change to reverse type connector



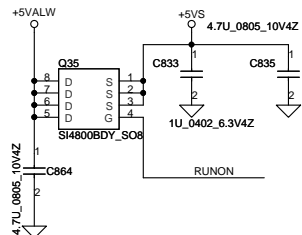
MIC_Dock

Need 600 Ohm 500 mA

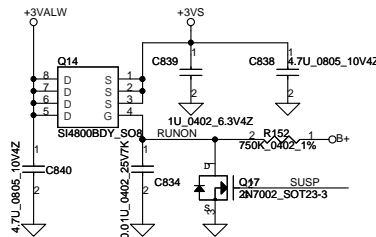


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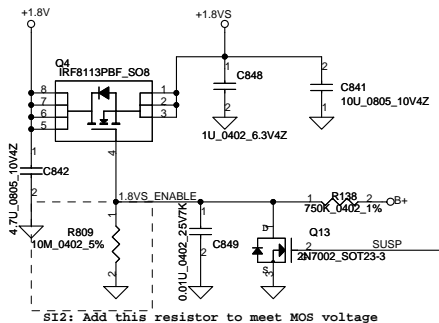
+5VALW TO +5VS



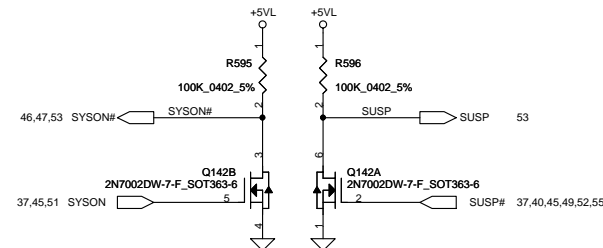
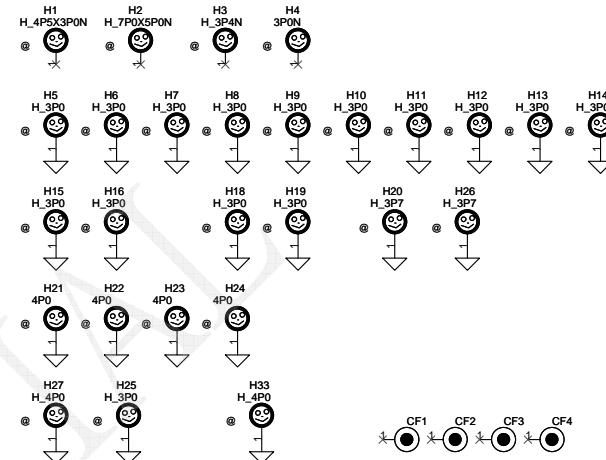
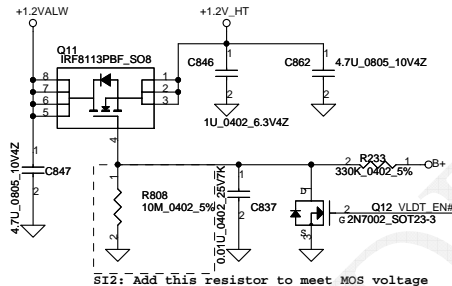
+3VALW TO +3VS



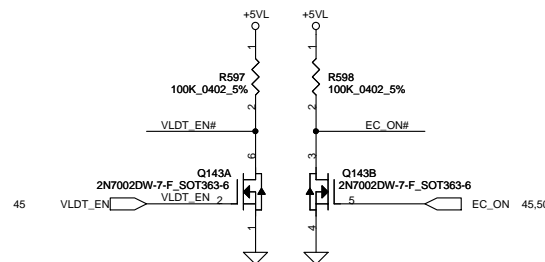
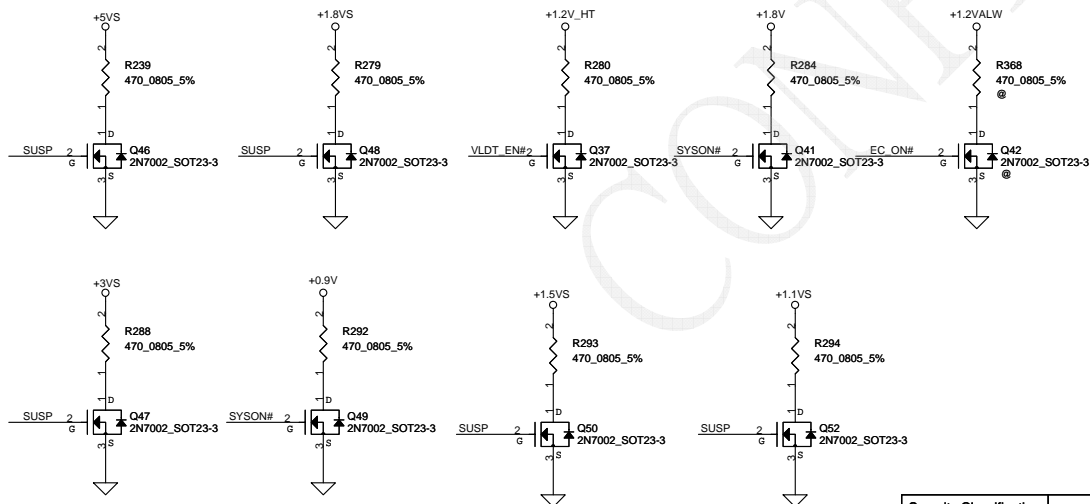
+1.8V TO +1.8VS



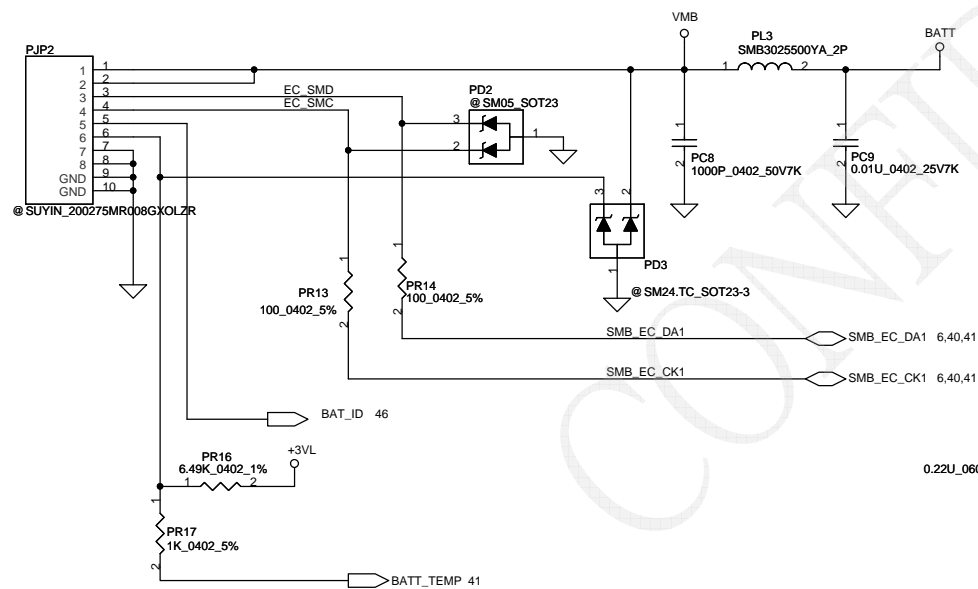
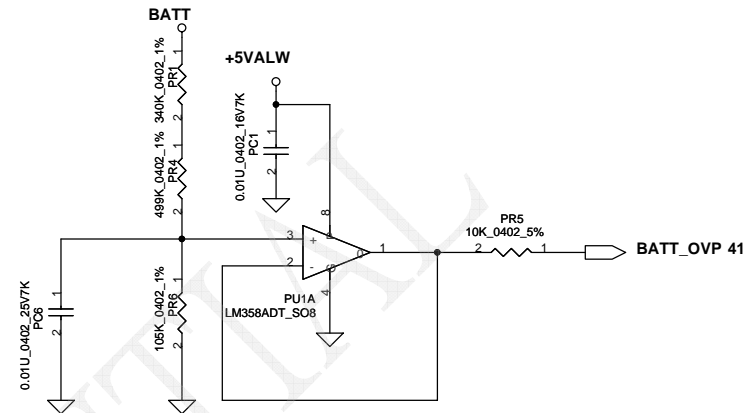
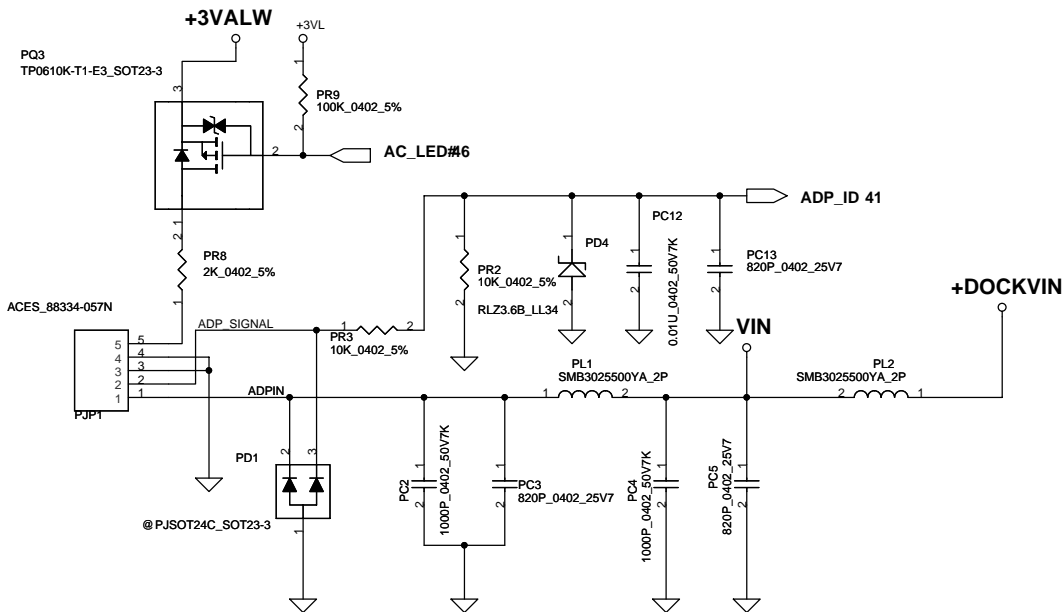
+1.2VALW TO +1.2V_HT



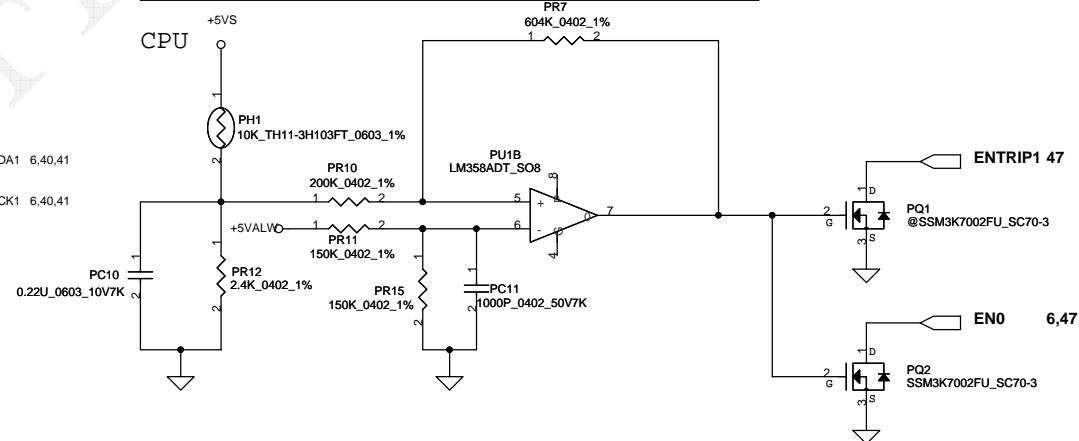
Discharge circuit



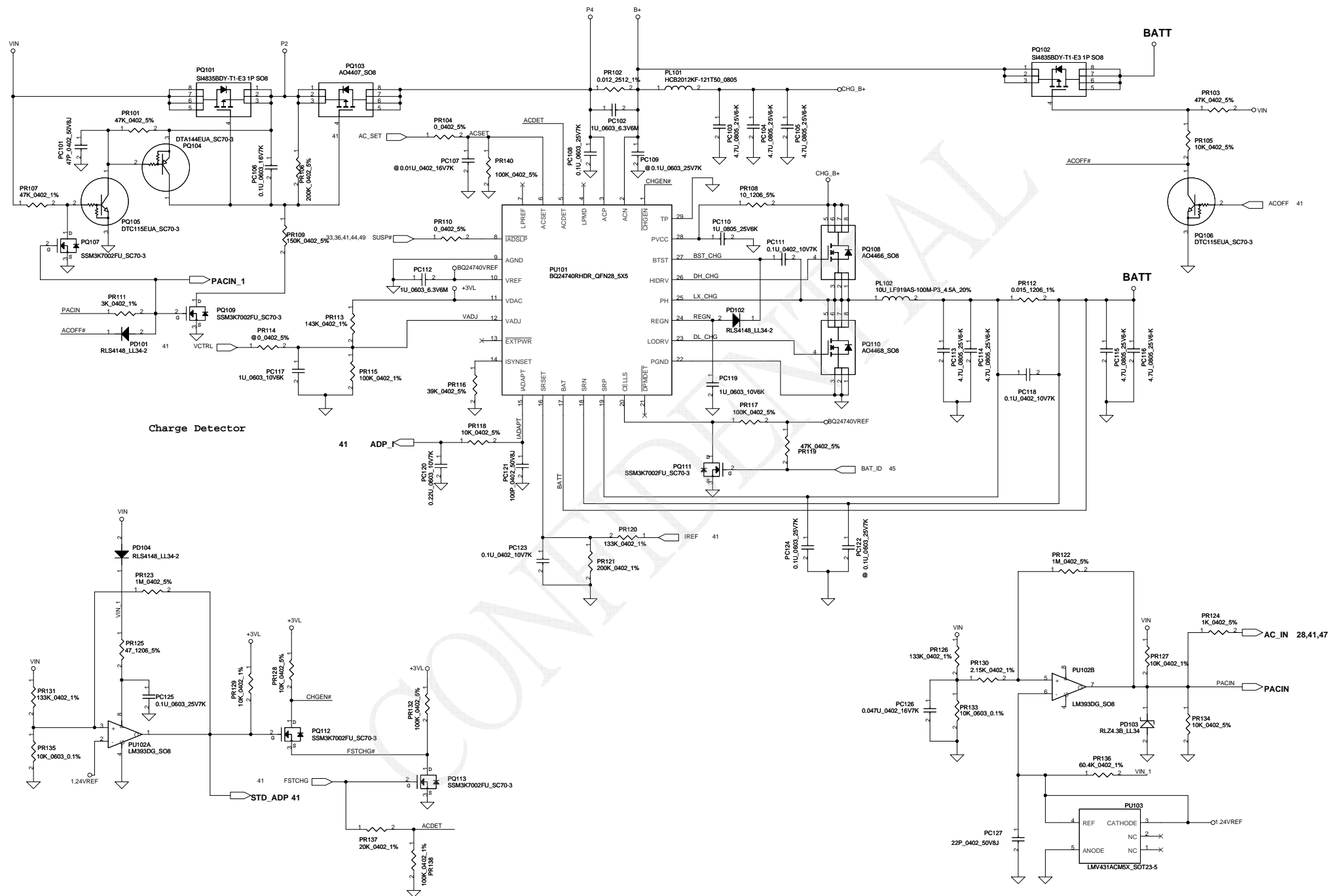
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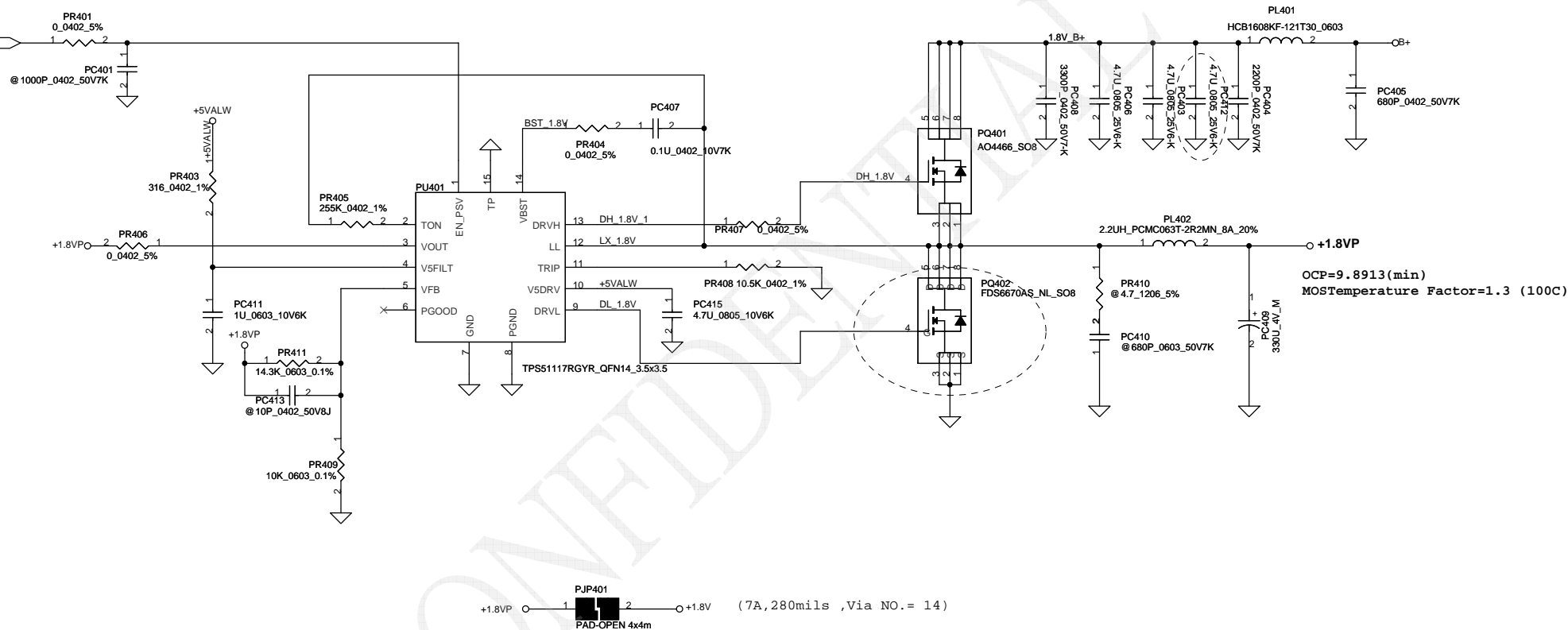
PH1 under CPU bottom side :
CPU thermal protection at 90 +-3 degree C



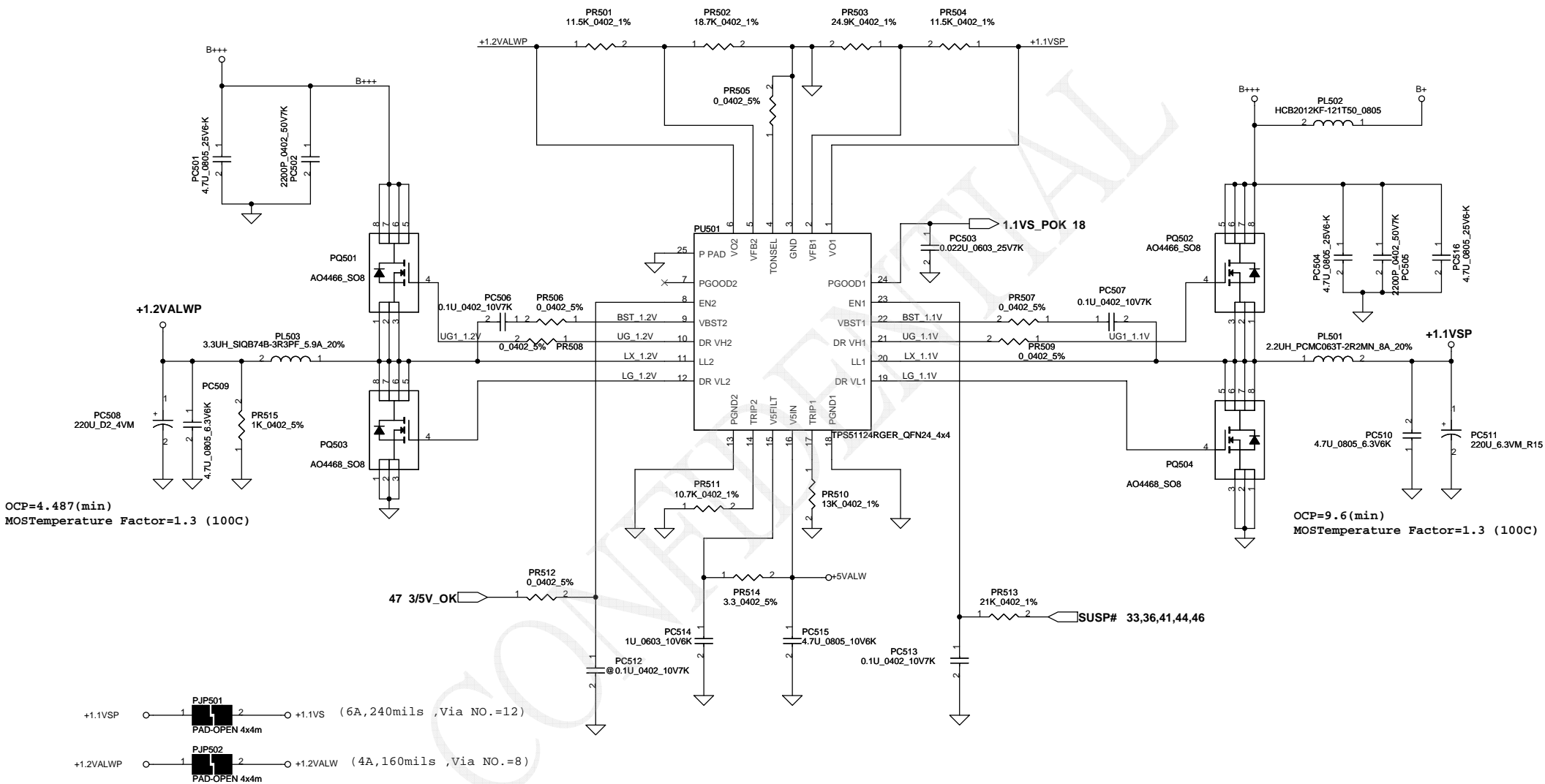
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33,41,42,44 SYSON



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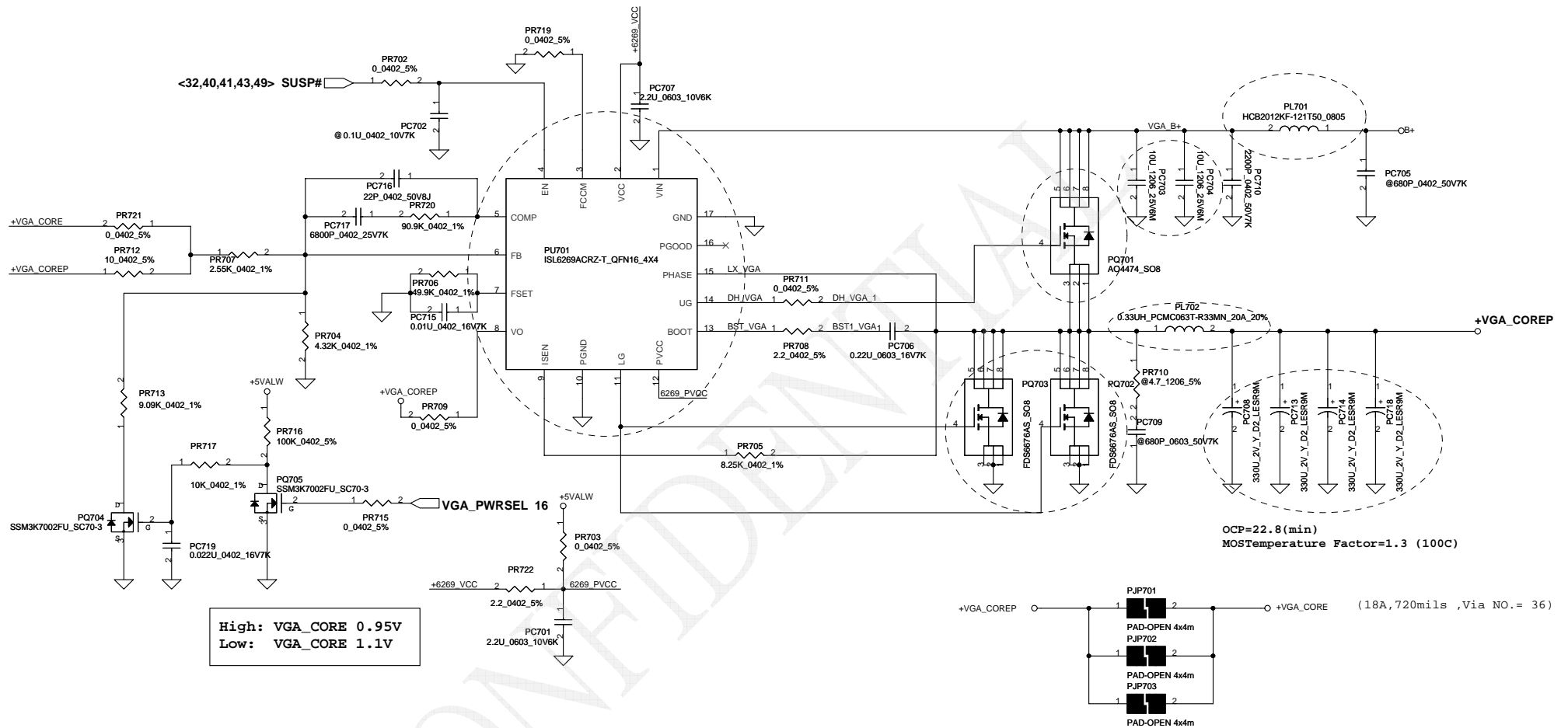


OCP=4.487(min)
MOSTemperature Factor=1.3 (100C)

OCP=9.6(min)
MOSTemperature Factor=1.3 (100C)

+1.1VSP 1 PJP501 2 +1.1VS (6A,240mils ,Via NO.=12)
PAD-OPEN 4x4m
+1.2VALWP 1 PJP502 2 +1.2VALW (4A,160mils ,Via NO.=8)
PAD-OPEN 4x4m

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Item	Modify List	PAGE	Fixed Issue and change item	M.B. Ver
1	Remove Q23 (2N7002)	41	HP OUT For Docking	1.1
2	ADD R936 R937 (0 ohm)	41	HP OUT For Docking	1.1
3	Remove U5 (M82-S)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
4	Remove U54	15,16,17 18,19,20,21	change M82S TO M86M	1.1
5	Remove Q8(S12310HDS)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
6	Remove Q9(2N7002)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
	Remove R149 (240 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
8	Remove L32,L33 (0 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
9	Remove R42,R425,R630 (0 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
10	Remove R189 (100 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
11	Remove R131,R142 (1K ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
12	Remove R120,R130,R190,R197,R243,R244,R245,R246(10K ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
13	Remove R151 (10Kohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
14	Remove R145,R146,R147,R241,R242,R403 (4.7Kohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
15	Remove R159,R160,R165,R166(56 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
16	Remove R143,R144,R148,R150 (100 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
17	Remove R109 (1.27K ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
18	Remove R112 (150 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
19	Remove R108 (2K ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
18	Remove R1136 (249 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
20	Remove R119,R132 (499 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
21	Remove R155,R156,R157,R158,R161,R162,R163,R164 (4.99Kohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
22	Remove R135 (715 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
23	Remove R177 (75 ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
24	Remove C254,C255,C256,C257,C258,C259,C260,C261,C262,C263,C264,C265,C266,C267,C268,C269,C270,C271,C272,C273,C274,C275,C276,C277,C278,C279,C280,C281,C282,C283,C284,C285 (0.1U)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
25	Remove C478 (220U)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
26	Remove L23,L24,L25,L26,L27,L28,L29,L30,L31,L34,L35,L36,L37,L38,L39,L40,L41,R110,R115 (RLM8P0210HDI)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
27	Remove C294,C295,C296,C297,C298,C299,C300,C301,C302,C303,C304,C305,C306,C307,C308,C309,C310,C311,C312,C313,C314,C315,C316,C317,C318,C319,C320,C321,C322,C323,C324,C325,C326,C327,C328,C329,C330,C331,C332,C333,C334,C335,C336,C337,C338,C339,C340,C341,C342,C343,C344,C345,C346,C347,C348,C349,C350,C351,C352,C353,C354,C355,C356,C357,C358,C359,C360,C361,C362,C363,C364,C365,C366,C367,C368,C369,C370,C371,C372,C373,C374,C375,C376,C377,C378,C379,C380,C381,C382,C383,C384,C385,C386,C387,C388,C389,C390,C391,C392,C393,C394,C395,C396,C397,C398,C399,C400,C401,C402,C403,C404,C405,C406,C407,C408,C409,C410,C411,C412,C413,C414,C415,C416,C417,C418,C419,C420,C421,C422,C423,C424,C425,C426,C427,C428,C429,C430,C431,C432,C433,C434,C435,C436,C437,C438,C439,C440,C441,C442,C443,C444,C445,C446,C447,C448,C449,C450,C451,C452,C453,C454,C455,C456,C457,C458,C459,C460,C461,C462,C463,C464,C465,C466,C467,C468,C469,C470,C471,C472,C473,C474,C475,C476,C477,C478,C479,C480,C481,C482,C483,C484,C485,C486,C487,C488,C489,C490,C491,C492,C493,C494,C495,C496,C497,C498,C499,C500,C501,C502,C503,C504,C505,C506,C507,C508,C509,C510,C511,C512,C513,C514,C515,C516,C517,C518,C519,C520,C521,C522,C523,C524,C525,C526,C527,C528,C529,C530,C531,C532,C533,C534,C535,C536,C537,C538,C539,C540,C541,C542,C543,C544,C545,C546,C547,C548,C549,C550,C551,C552,C553,C554,C555,C556,C557,C558,C559,C560,C561,C562,C563,C564,C565,C566,C567,C568,C569,C570,C571,C572,C573,C574,C575,C576,C577,C578,C579,C580,C581,C582,C583,C584,C585,C586,C587,C588,C589,C590,C591,C592,C593,C594,C595,C596,C597,C598,C599,C600,C601,C602,C603,C604,C605,C606,C607,C608,C609,C610,C611,C612,C613,C614,C615,C616,C617,C618,C619,C620,C621,C622,C623,C624,C625,C626,C627,C628,C629,C630,C631,C632,C633,C634,C635,C636,C637,C638,C639,C640,C641,C642,C643,C644,C645,C646,C647,C648,C649,C650,C651,C652,C653,C654,C655,C656,C657,C658,C659,C660,C661,C662,C663,C664,C665,C666,C667,C668,C669,C670,C671,C672,C673,C674,C675,C676,C677,C678,C679,C680,C681,C682,C683,C684,C685,C686,C687,C688,C689,C690,C691,C692,C693,C694,C695,C696,C697,C698,C699,C700,C701,C702,C703,C704,C705,C706,C707,C708,C709,C710,C711,C712,C713,C714,C715,C716,C717,C718,C719,C720,C721,C722,C723,C724,C725,C726,C727,C728,C729,C730,C731,C732,C733,C734,C735,C736,C737,C738,C739,C740,C741,C742,C743,C744,C745,C746,C747,C748,C749,C750,C751,C752,C753,C754,C755,C756,C757,C758,C759,C760,C761,C762,C763,C764,C765,C766,C767,C768,C769,C770,C771,C772,C773,C774,C775,C776,C777,C778,C779,C780,C781,C782,C783,C784,C785,C786,C787,C788,C789,C790,C791,C792,C793,C794,C795,C796,C797,C798,C799,C800,C801,C802,C803,C804,C805,C806,C807,C808,C809,C810,C811,C812,C813,C814,C815,C816,C817,C818,C819,C820,C821,C822,C823,C824,C825,C826,C827,C828,C829,C830,C831,C832,C833,C834,C835,C836,C837,C838,C839,C840,C841,C842,C843,C844,C845,C846,C847,C848,C849,C850,C851,C852,C853,C854,C855,C856,C857,C858,C859,C860,C861,C862,C863,C864,C865,C866,C867,C868,C869,C870,C871,C872,C873,C874,C875,C876,C877,C878,C879,C880,C881,C882,C883,C884,C885,C886,C887,C888,C889,C890,C891,C892,C893,C894,C895,C896,C897,C898,C899,C900,C901,C902,C903,C904,C905,C906,C907,C908,C909,C910,C911,C912,C913,C914,C915,C916,C917,C918,C919,C920,C921,C922,C923,C924,C925,C926,C927,C928,C929,C930,C931,C932,C933,C934,C935,C936,C937,C938,C939,C940,C941,C942,C943,C944,C945,C946,C947,C948,C949,C950,C951,C952,C953,C954,C955,C956,C957,C958,C959,C960,C961,C962,C963,C964,C965,C966,C967,C968,C969,C970,C971,C972,C973,C974,C975,C976,C977,C978,C979,C980,C981,C982,C983,C984,C985,C986,C987,C988,C989,C990,C991,C992,C993,C994,C995,C996,C997,C998,C999	15,16,17 18,19,20,21	change M82S TO M86M	1.1
28	Remove C298,C299,C300,C301,C302,C303,C304,C305,C306,C307,C308,C309,C310,C311,C312,C313,C314,C315,C316,C317,C318,C319,C320,C321,C322,C323,C324,C325,C326,C327,C328,C329,C330,C331,C332,C333,C334,C335,C336,C337,C338,C339,C340,C341,C342,C343,C344,C345,C346,C347,C348,C349,C350,C351,C352,C353,C354,C355,C356,C357,C358,C359,C360,C361,C362,C363,C364,C365,C366,C367,C368,C369,C370,C371,C372,C373,C374,C375,C376,C377,C378,C379,C380,C381,C382,C383,C384,C385,C386,C387,C388,C389,C390,C391,C392,C393,C394,C395,C396,C397,C398,C399,C400,C401,C402,C403,C404,C405,C406,C407,C408,C409,C410,C411,C412,C413,C414,C415,C416,C417,C418,C419,C420,C421,C422,C423,C424,C425,C426,C427,C428,C429,C430,C431,C432,C433,C434,C435,C436,C437,C438,C439,C440,C441,C442,C443,C444,C445,C446,C447,C448,C449,C450,C451,C452,C453,C454,C455,C456,C457,C458,C459,C460,C461,C462,C463,C464,C465,C466,C467,C468,C469,C470,C471,C472,C473,C474,C475,C476,C477,C478,C479,C480,C481,C482,C483,C484,C485,C486,C487,C488,C489,C490,C491,C492,C493,C494,C495,C496,C497,C498,C499,C500,C501,C502,C503,C504,C505,C506,C507,C508,C509,C510,C511,C512,C513,C514,C515,C516,C517,C518,C519,C520,C521,C522,C523,C524,C525,C526,C527,C528,C529,C530,C531,C532,C533,C534,C535,C536,C537,C538,C539,C540,C541,C542,C543,C544,C545,C546,C547,C548,C549,C550,C551,C552,C553,C554,C555,C556,C557,C558,C559,C560,C561,C562,C563,C564,C565,C566,C567,C568,C569,C570,C571,C572,C573,C574,C575,C576,C577,C578,C579,C580,C581,C582,C583,C584,C585,C586,C587,C588,C589,C590,C591,C592,C593,C594,C595,C596,C597,C598,C599,C600,C601,C602,C603,C604,C605,C606,C607,C608,C609,C610,C611,C612,C613,C614,C615,C616,C617,C618,C619,C620,C621,C622,C623,C624,C625,C626,C627,C628,C629,C630,C631,C632,C633,C634,C635,C636,C637,C638,C639,C640,C641,C642,C643,C644,C645,C646,C647,C648,C649,C650,C651,C652,C653,C654,C655,C656,C657,C658,C659,C660,C661,C662,C663,C664,C665,C666,C667,C668,C669,C670,C671,C672,C673,C674,C675,C676,C677,C678,C679,C680,C681,C682,C683,C684,C685,C686,C687,C688,C689,C690,C691,C692,C693,C694,C695,C696,C697,C698,C699,C700,C701,C702,C703,C704,C705,C706,C707,C708,C709,C710,C711,C712,C713,C714,C715,C716,C717,C718,C719,C720,C721,C722,C723,C724,C725,C726,C727,C728,C729,C730,C731,C732,C733,C734,C735,C736,C737,C738,C739,C740,C741,C742,C743,C744,C745,C746,C747,C748,C749,C750,C751,C752,C753,C754,C755,C756,C757,C758,C759,C760,C761,C762,C763,C764,C765,C766,C767,C768,C769,C770,C771,C772,C773,C774,C775,C776,C777,C778,C779,C780,C781,C782,C783,C784,C785,C786,C787,C788,C789,C790,C791,C792,C793,C794,C795,C796,C797,C798,C799,C800,C801,C802,C803,C804,C805,C806,C807,C808,C809,C810,C811,C812,C813,C814,C815,C816,C817,C818,C819,C820,C821,C822,C823,C824,C825,C826,C827,C828,C829,C830,C831,C832,C833,C834,C835,C836,C837,C838,C839,C840,C841,C842,C843,C844,C845,C846,C847,C848,C849,C850,C851,C852,C853,C854,C855,C856,C857,C858,C859,C860,C861,C862,C863,C864,C865,C866,C867,C868,C869,C870,C871,C872,C873,C874,C875,C876,C877,C878,C879,C880,C881,C882,C883,C884,C885,C886,C887,C888,C889,C890,C891,C892,C893,C894,C895,C896,C897,C898,C899,C900,C901,C902,C903,C904,C905,C906,C907,C908,C909,C910,C911,C912,C913,C914,C915,C916,C917,C918,C919,C920,C921,C922,C923,C924,C925,C926,C927,C928,C929,C930,C931,C932,C933,C934,C935,C936,C937,C938,C939,C940,C941,C942,C943,C944,C945,C946,C947,C948,C949,C950,C951,C952,C953,C954,C955,C956,C957,C958,C959,C960,C961,C962,C963,C964,C965,C966,C967,C968,C969,C970,C971,C972,C973,C974,C975,C976,C977,C978,C979,C980,C981,C982,C983,C984,C985,C986,C987,C988,C989,C990,C991,C992,C993,C994,C995,C996,C997,C998,C999	15,16,17 18,19,20,21	change M82S TO M86M	1.1
29	Remove C298,C299,C300,C301,C302,C303,C304,C305,C306,C307,C308,C309,C310,C311,C312,C313,C314,C315,C316,C317,C318,C319,C320,C321,C322,C323,C324,C325,C326,C327,C328,C329,C330,C331,C332,C333,C334,C335,C336,C337,C338,C339,C340,C341,C342,C343,C344,C345,C346,C347,C348,C349,C350,C351,C352,C353,C354,C355,C356,C357,C358,C359,C360,C361,C362,C363,C364,C365,C366,C367,C368,C369,C370,C371,C372,C373,C374,C375,C376,C377,C378,C379,C380,C381,C382,C383,C384,C385,C386,C387,C388,C389,C390,C391,C392,C393,C394,C395,C396,C397,C398,C399,C400,C401,C402,C403,C404,C405,C406,C407,C408,C409,C410,C411,C412,C413,C414,C415,C416,C417,C418,C419,C420,C421,C422,C423,C424,C425,C426,C427,C428,C429,C430,C431,C432,C433,C434,C435,C436,C437,C438,C439,C440,C441,C442,C443,C444,C445,C446,C447,C448,C449,C450,C451,C452,C453,C454,C455,C456,C457,C458,C459,C460,C461,C462,C463,C464,C465,C466,C467,C468,C469,C470,C471,C472,C473,C474,C475,C476,C477,C478,C479,C480,C481,C482,C483,C484,C485,C486,C487,C488,C489,C490,C491,C492,C493,C494,C495,C496,C497,C498,C499,C500,C501,C502,C503,C504,C505,C506,C507,C508,C509,C510,C511,C512,C513,C514,C515,C516,C517,C518,C519,C520,C521,C522,C523,C524,C525,C526,C527,C528,C529,C530,C531,C532,C533,C534,C535,C536,C537,C538,C539,C540,C541,C542,C543,C544,C545,C546,C547,C548,C549,C550,C551,C552,C553,C554,C555,C556,C557,C558,C559,C560,C561,C562,C563,C564,C565,C566,C567,C568,C569,C570,C571,C572,C573,C574,C575,C576,C577,C578,C579,C580,C581,C582,C583,C584,C585,C586,C587,C588,C589,C590,C591,C592,C593,C594,C595,C596,C597,C598,C599,C600,C601,C602,C603,C604,C605,C606,C607,C608,C609,C610,C611,C612,C613,C614,C615,C616,C617,C618,C619,C620,C621,C622,C623,C624,C625,C626,C627,C628,C629,C630,C631,C632,C633,C634,C635,C636,C637,C638,C639,C640,C641,C642,C643,C644,C645,C646,C647,C648,C649,C650,C651,C652,C653,C654,C655,C656,C657,C658,C659,C660,C661,C662,C663,C664,C665,C666,C667,C668,C669,C670,C671,C672,C673,C674,C675,C676,C677,C678,C679,C680,C681,C682,C683,C684,C685,C686,C687,C688,C689,C690,C691,C692,C693,C694,C695,C696,C697,C698,C699,C700,C701,C702,C703,C704,C705,C706,C707,C708,C709,C710,C711,C712,C713,C714,C715,C716,C717,C718,C719,C720,C721,C722,C723,C724,C725,C726,C727,C728,C729,C730,C731,C732,C733,C734,C735,C736,C737,C738,C739,C740,C741,C742,C743,C744,C745,C746,C747,C748,C749,C750,C751,C752,C753,C754,C755,C756,C757,C758,C759,C760,C761,C762,C763,C764,C765,C766,C767,C768,C769,C770,C771,C772,C773,C774,C775,C776,C777,C778,C779,C780,C781,C782,C783,C784,C785,C786,C787,C788,C789,C790,C791,C792,C793,C794,C795,C796,C797,C798,C799,C800,C801,C802,C803,C804,C805,C806,C807,C808,C809,C810,C811,C812,C813,C814,C815,C816,C817,C818,C819,C820,C821,C822,C823,C824,C825,C826,C827,C828,C829,C830,C831,C832,C833,C834,C835,C836,C837,C838,C839,C840,C841,C842,C843,C844,C845,C846,C847,C848,C849,C850,C851,C852,C853,C854,C855,C856,C857,C858,C859,C860,C861,C862,C863,C864,C865,C866,C867,C868,C869,C870,C871,C872,C873,C874,C875,C876,C877,C878,C879,C880,C881,C882,C883,C884,C885,C886,C887,C888,C889,C890,C891,C892,C893,C894,C895,C896,C897,C898,C899,C900,C901,C902,C903,C904,C905,C906,C907,C908,C909,C910,C911,C912,C913,C914,C915,C916,C917,C918,C919,C920,C921,C922,C923,C924,C925,C926,C927,C928,C929,C930,C931,C932,C933,C934,C935,C936,C937,C938,C939,C940,C941,C942,C943,C944,C945,C946,C947,C948,C949,C950,C951,C952,C953,C954,C955,C956,C957,C958,C959,C960,C961,C962,C963,C964,C965,C966,C967,C968,C969,C970,C971,C972,C973,C974,C975,C976,C977,C978,C979,C980,C981,C982,C983,C984,C985,C986,C987,C988,C989,C990,C991,C992,C993,C994,C995,C996,C997,C998,C999	15,16,17 18,19,20,21	change M82S TO M86M	1.1
30	Remove U17 (2N4339A)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
31	Remove R330 (4.7K ohm)	15,16,17 18,19,20,21	change M82S TO M86M	1.1
32	Remove C465 (0.1U)	38	Fix CR_LED	1.1
33	Remove C467 (2200P)	38	Fix CR_LED	1.1
34	Remove Q51(2N7002)	38	Fix XD_LED	1.1
35	ADD R933(0 ohm)			
36	ADD R127(10Kohm)			

Security Classification	Compal Secret Data			<div>Compal Electronics, Inc.</div>		
Issued Date	2007/08/02	Deciphered Date	2008/08/02			
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Item	Modify List	PAGE	Fixed Issue and change item	M.B. Ver.
1	ADD *I06,IY7,U7,UV8,U8,UV9,U9,UV10 *(H06H07)	P15-P25	change M82S TO M86M	1.1
2	ADDQ9 (2N7002)	P15-P25	change M82S TO M86M	1.1
3	ADD CV3(S12301M0S)	P15-P25	change M82S TO M86M	1.1
4	ADD RV47(240 ohm)	P15-P25	change M82S TO M86M	1.1
5	ADD R42(0 ohm)	P15-P25	change M82S TO M86M	1.1
6	ADD RV19,RV44 (1K ohm)	P15-P25	change M82S TO M86M	1.1
7	ADD RV96,RV100,RV72,RV73,RV74,RV75,RV76,RV77,RV78,RV79,RV92,RV93,RV94,RV95,RV97,RV99,RV101,RV107,RV108,RV7,RV9,RV10,RV11,RV12,RV13,RV14,RV16,RV17,RV18,RV33,RV98,RV102(10K ohm)	P15-P25	change M82S TO M86M	1.1
8	ADD RV30,RV34(100K ohm)	P15-P25	change M82S TO M86M	1.1
9	ADD RV103,RV104,RV45,RV46,R403(4.7K ohm)	P15-P25	change M82S TO M86M	1.1
10	ADD RV36,RV37,RV38,RV39,RV40,RV41,RV42,RV43(100 ohm)	P15-P25	change M82S TO M86M	1.1
11	ADD RV6 (1.27Kohm)	P15-P25	change M82S TO M86M	1.1
12	ADD RV25,RV26,RV27,RV8(150 ohm)	P15-P25	change M82S TO M86M	1.1
13	ADD RV5(2K ohm)	P15-P25	change M82S TO M86M	1.1
14	ADD RV23,RV15,RV22(499 ohm)	P15-P25	change M82S TO M86M	1.1
15	ADD RV60,RV61,RV62,RV63,RV66,RV67,RV68,RV69,RV54,RV55,RV56,RV57,RV48,RV49,RV50,RV51(4.99K ohm)	P15-P25	change M82S TO M86M	1.1
16	ADD RV24(715 ohm)	P15-P25	change M82S TO M86M	1.1
17	ADD RV28(75 ohm)	P15-P25	change M82S TO M86M	1.1
18	ADD TV1(CRYSTAL 27MHz)	P15-P25	change M82S TO M86M	1.1
19	ADD RV103,RV104,RV45,RV46,R403(4.7K ohm)	P15-P25	change M82S TO M86M	1.1
20	ADD LV1, LV2, LV3, LV4, LV5, LV6, LV7, LV8, LV9, LV10, LV11, LV12, LV13, LV14, LV15, LV16, LV17, LV18, LV19, LV21, LV22, LV23, LV24, LV25 (M0RATA R4M18P01218N10)	P15-P25	change M82S TO M86M	1.1
21	ADD CV226,CV227,CV234,CV235,CV249,CV250,CV257,CV258,CV203,CV204,CV211,CV212,CV180,CV181,CV188,CV189,CV42,CV45,CV48,CV52,CV53,CV56,CV59,CV64,CV67,CV68,CV71,CV76,CV84,CV85,CV90,CV92,CV95,CV97,CV99,CV101,CV108,CV117,CV119,CV131,CV132,CV136,CV140,CV145,CV149,CV157,CV165,CV166,CV169,CV173(10u)	P15-P25	change M82S TO M86M	1.1
22	ADD CV80,CV44,CV228,CV230,CV231,CV232,CV237,CV238,CV239,CV240,CV242,CV246,CV247,CV252,CV253,CV254,CV255,CV260,CV261,CV262,CV263,CV265,CV267,CV269,CV270,CV206,CV207,CV208,CV209,CV214,CV215,CV216,CV217,CV219,CV221,CV223,CV224,CV196,CV183,CV184,CV185,CV186,CV191,CV192,CV193,CV194,CV198,CV200,CV201,CV44,CV46,CV49,CV50,CV55,CV58,CV60,CV62,CV66,CV69,CV72,CV75,CV79,CV81,CV87,CV88,CV106,CV114,CV120,CV125,CV159,CV168,CV171,CV172,CV175,CV176,CV177,CV178,CV179,CV272(10.1u)	P15-P25	change M82S TO M86M	1.1
23	ADD CV77,CV78(22P)	P15-P25	change M82S TO M86M	1.1
24	ADD CV273 (2200P)	P15-P25	change M82S TO M86M	1.1
25	ADD CV233,CV241,CV256,CV264,CV210,CV218,CV187,CV195(0.01U)	P15-P25	change M82S TO M86M	1.1
26	ADD CV45,CV28,CV36,CV243,CV251,CV259,CV266,CV268,CV205,CV213,CV220,CV222,CV187,CV182,CV189,CV199,CV41,CV41,CV47,CV51,CV57,CV63,CV65,CV70,CV73,CV74,CV82,CV86,CV89,CV93,CV94,CV98,CV100,CV103,CV104,CV105,CV107,CV109,CV111,CV113,CV118,CV121,CV135,CV139,CV144,CV162,CV163,CV164,CV54,CV61,CV94,CV102,CV110,CV112,CV115,CV116,CV123,CV124,CV126,CV127,CV129,CV130,CV133,CV134,CV137,CV138,CV141,CV142,CV146,CV147,CV150,CV151,CV153,CV154,CV155,CV156,CV158,CV160,CV161,CV167,CV170,CV174(75 ohm) ADD RV28(10)	P15-P25	change M82S TO M86M	1.1
27	ADD CV9,CV10,CV11,CV12,CV13,CV14,CV15,CV16,CV17,CV18,CV19,CV20,CV21,CV22,CV23,CV24,CV25,CV26,CV27,CV28,CV29,CV30,CV31,CV32,CV33,CV34,CV35,CV36,CV37,CV38,CV39,CV40 (0.1U)	P15-P25	change M82S TO M86M	1.1
28	ADD CV122,CV91,CV128(330U)	P15-P25	change M82S TO M86M	1.1
29				
30				
31	Change RV10 100K to 100 ohm	P16	About GNU 27MHz	
32	Change RV702 4.7K to 0 ohm	P55	About GNU IV_delay time	
33	del PC702 0.1 U	P55	About GNU IV_delay time	
34	change_INT DMA44 TO US F3 Pin	P22	about GNU A channel VRAM	
35	change_INT DMA45 TO US B3 Pin	P22	about GNU A channel VRAM	
36	Change INT DMA4 ORA4 TO US F7_E8 PIN	P22	about GNU A channel VRAM	
37	Change_INT DMA5 ORA5TO US B7_A8 PIN	P22	about GNU A channel VRAM	
38	change_INT DMA6 TO US F3 Pin	P22	about GNU A channel VRAM	
39	change_INT DMA6 TO US B3 Pin	P22	about GNU A channel VRAM	
40	Change_INT ORA7 ORA7TO US F7_E8 PIN	P22	about GNU A channel VRAM	
41	Change_INT DMA6 ORA6 TO US B7_A8 PIN	P22	about GNU A channel VRAM	
42	Change_V12 ROM SAQ10320120 to SAQ10320110	P25	about GNU Thermal Sensor	
43	Modify R237,R238,R176,R209 PULL +3V2 TO +3V5_DELAY	P27 P29	Change power plan	
44	ADD LV20,CV152,CV148,CV143	P18	ADD M86M +VDC05 POWER	

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