

# Compal Confidential

## Schematics Document

AUBURNDALE/CLARKSFIELD with  
Intel IBEX PEAK-M core logic

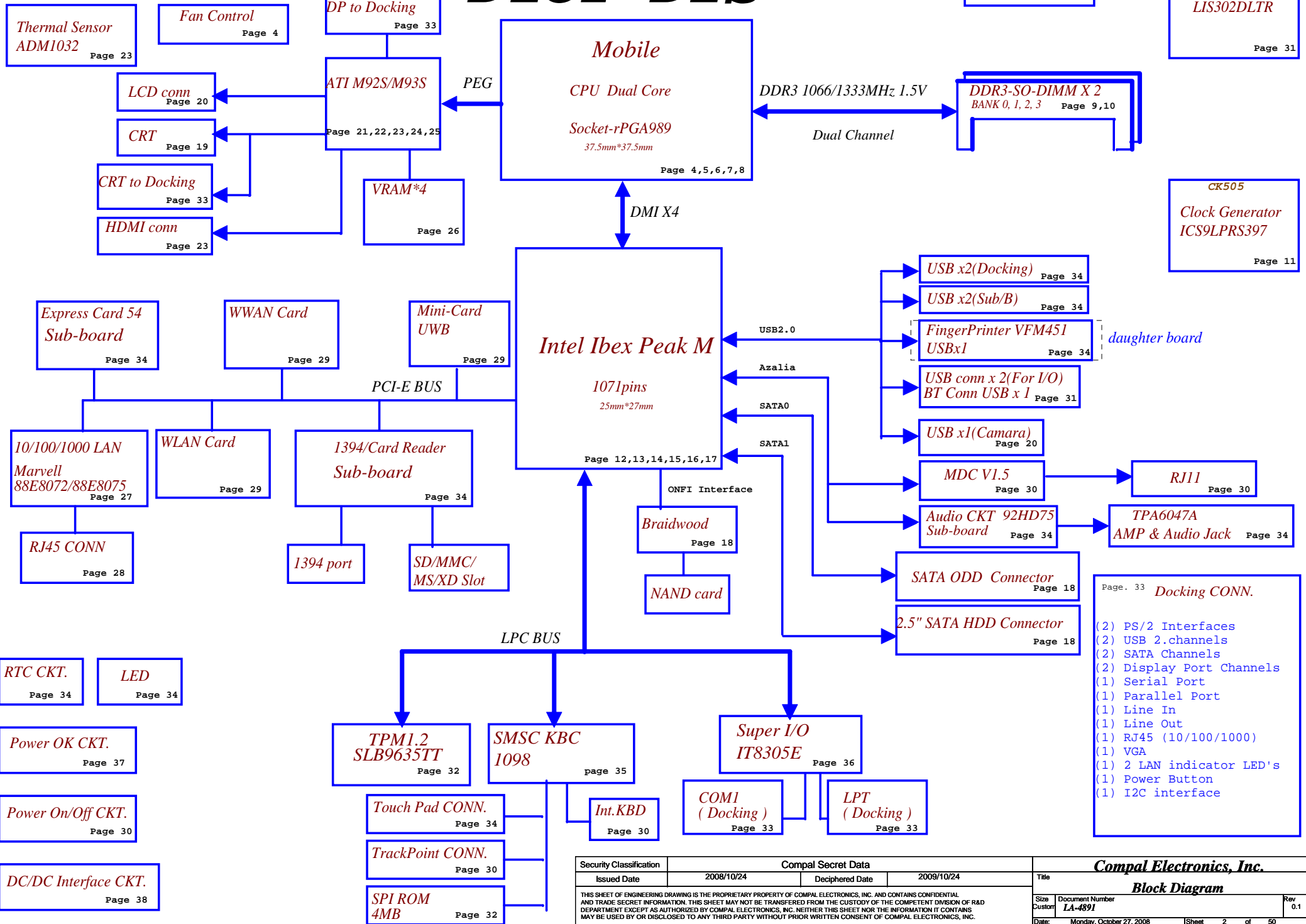
# Dior DIS

2008-10-30

REV:0.1

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	產出人員	
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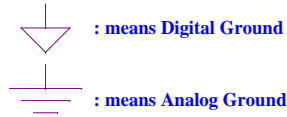
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Issued Date	2008/10/24	Deciphered Date	2009/10/24	Size Custom		
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				Date: Thursday, October 30, 2008		Sheet 1 of 50



## Voltage Rails ( O MEANS ON X MEANS OFF )

power plane State	+RTCVCC	+B +3VL	+5VALW +3VALW	+1.5V +0.75V	+5VS +3VS +1.5VS +NVVDD +VCCP +CPU_CORE +1.05VS +1.8VS
S0	O	O	O	O	O
S1	O	O	O	O	O
S3	O	O	O	O	X
S5 S4/AC	O	O	O	X	X
S5 S4/ Battery only	O	O	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X

## Symbol Note :



## Install below 43 level BOM structure for ver. 0.1

DEBUG@ : means just build when PCIE port 80 CARD function enable. *Remove before MP*

M92@ : Install for M92 Graphic controller

8072@ : Install for 8072 NIC controller

1098@ : Install for 1098 KBC controller

CK32@ : Install for 32 pin CLOCK GEN

## Install below 45 level BOM structure for ver. 0.1

45@ : means just put it in the BOM of 45 level.

## Reserve below BOM structure for ver. 0.1

@ : means just reserve , no build

CONN@ : means ME part.

M93@ : Install for M93 Graphic controller

8075@ : Install for 8075 NIC controller

1091@ : Install for 1091 KBC controller

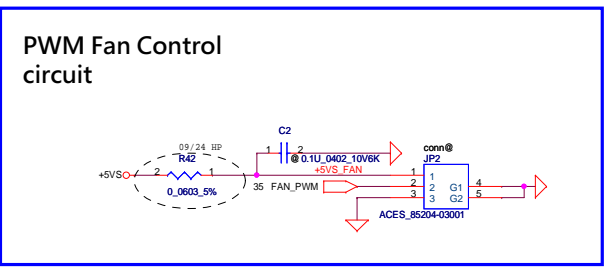
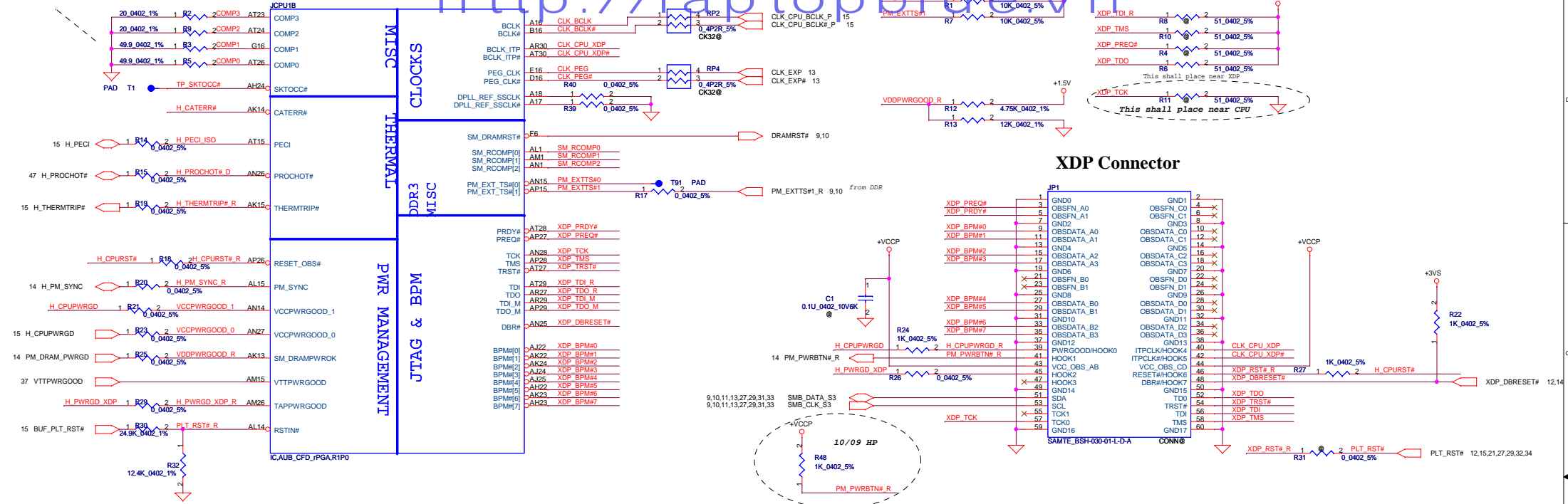
CK72@ : Install for 72 pin CLOCK GEN

SMBUS Control Table

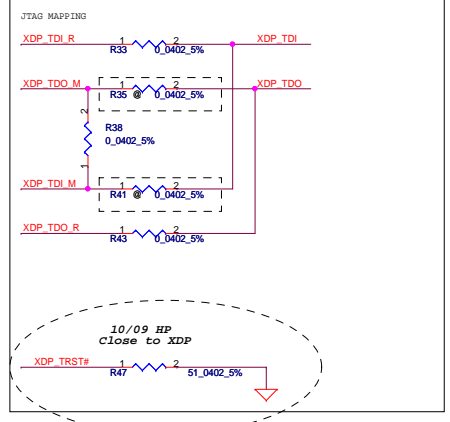
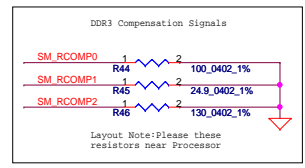
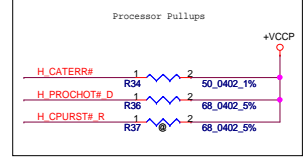
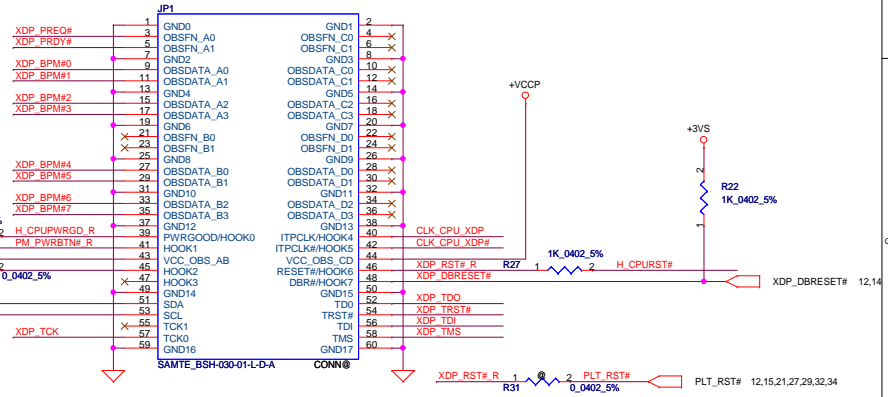
	SOURCE	BATT	XDP	SODIMM	CLK CHIP	MINI CARD	DOCK	NIC	THERMAL SENSOR	G-SENSOR
SMB_EC_CK1 SMB_EC_DAI	SMSC1098	V	X	X	X	X	X	X	X	X
SMBCLK SMBDATA	Calpella	X	V	V	V	V	V	V	X	V
SML0CLK SML0DATA	Calpella	X	X	X	X	X	X	X	X	X
SML1CLK SML1DATA	Calpella	X	X	X	X	X	X	X	V	X

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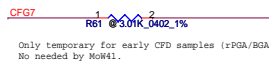
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length < 0.5", spacing 20mil



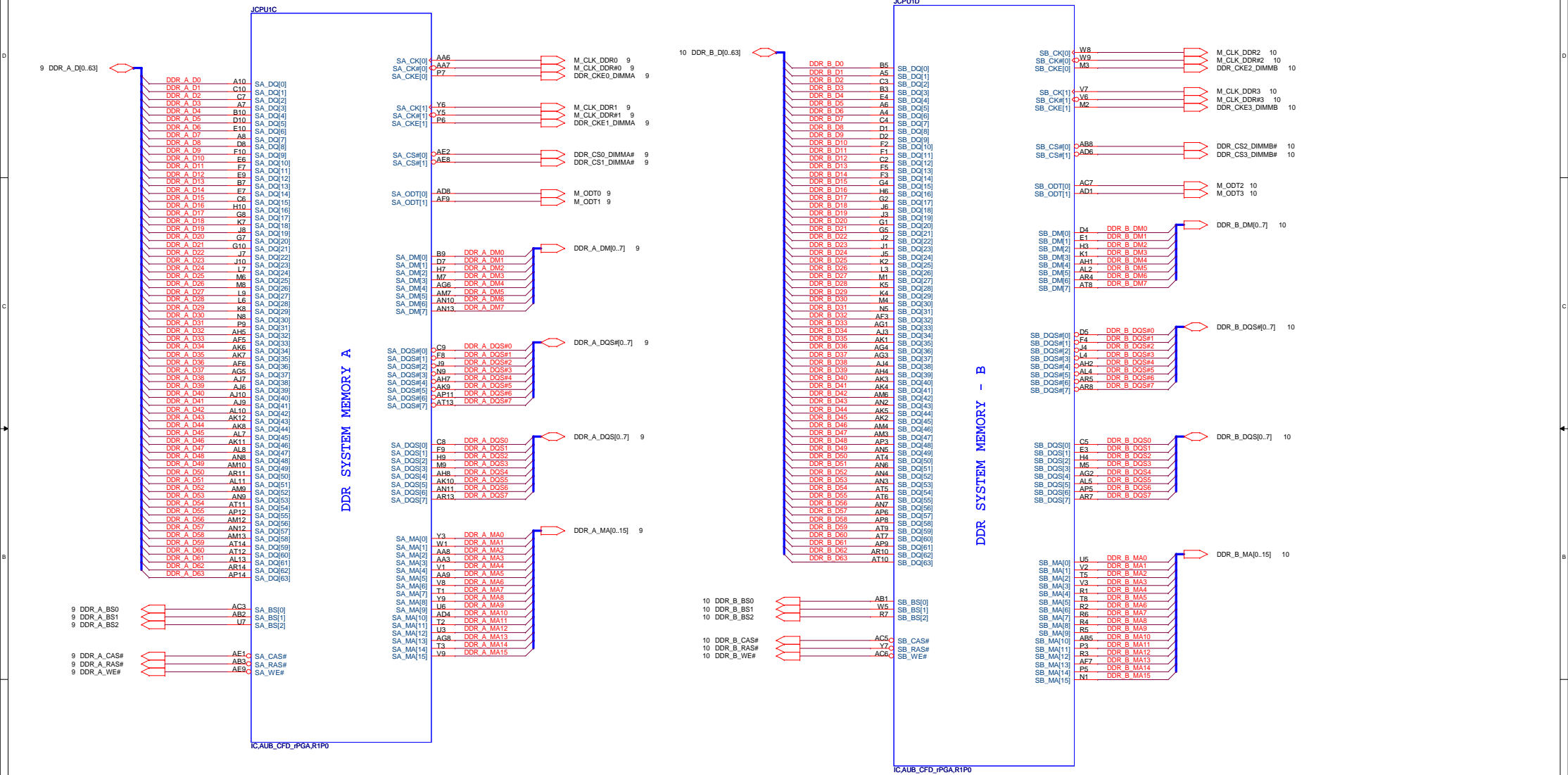
XDP Connector



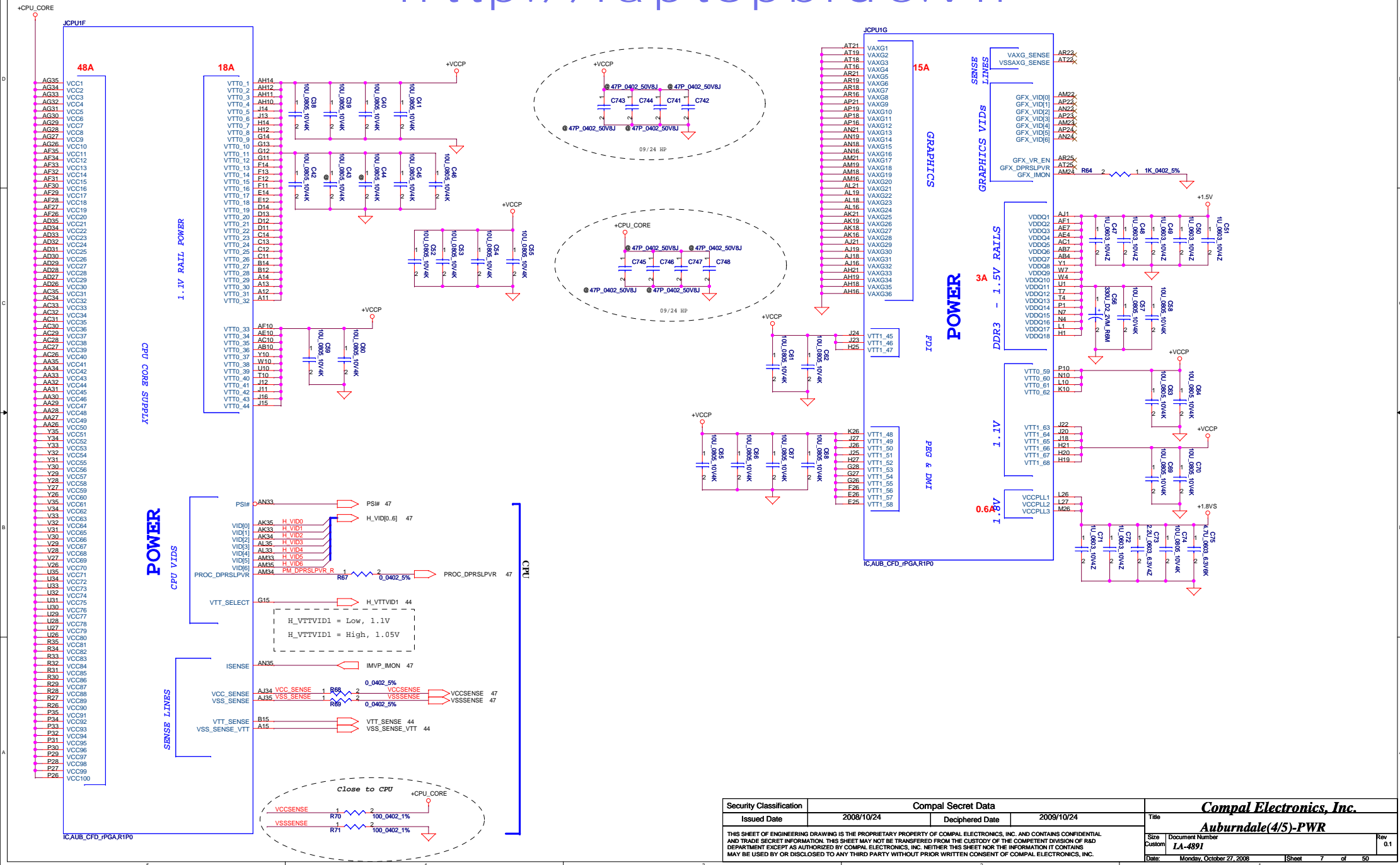
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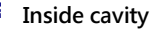
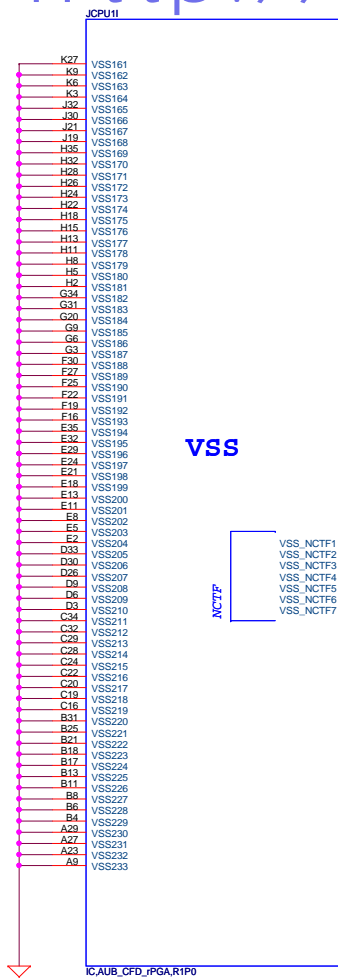


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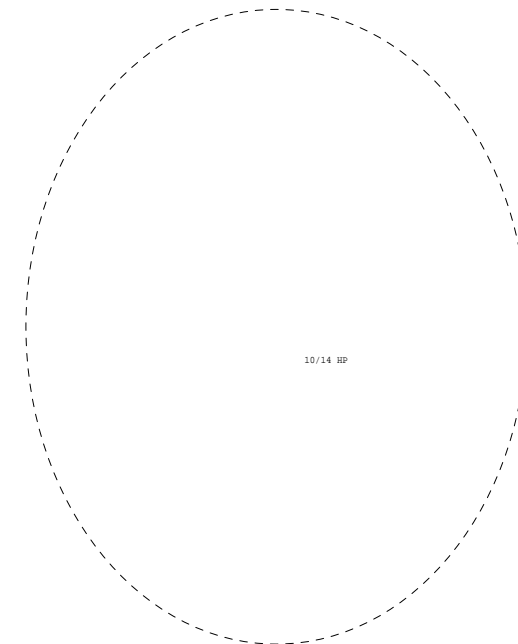
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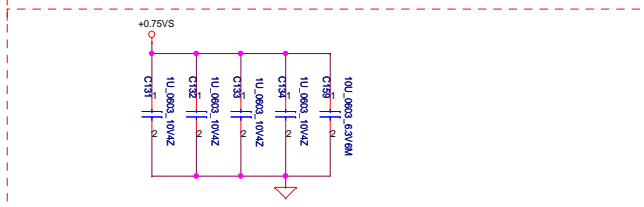
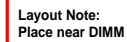
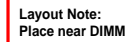
between Inductor and socket

Under cavity



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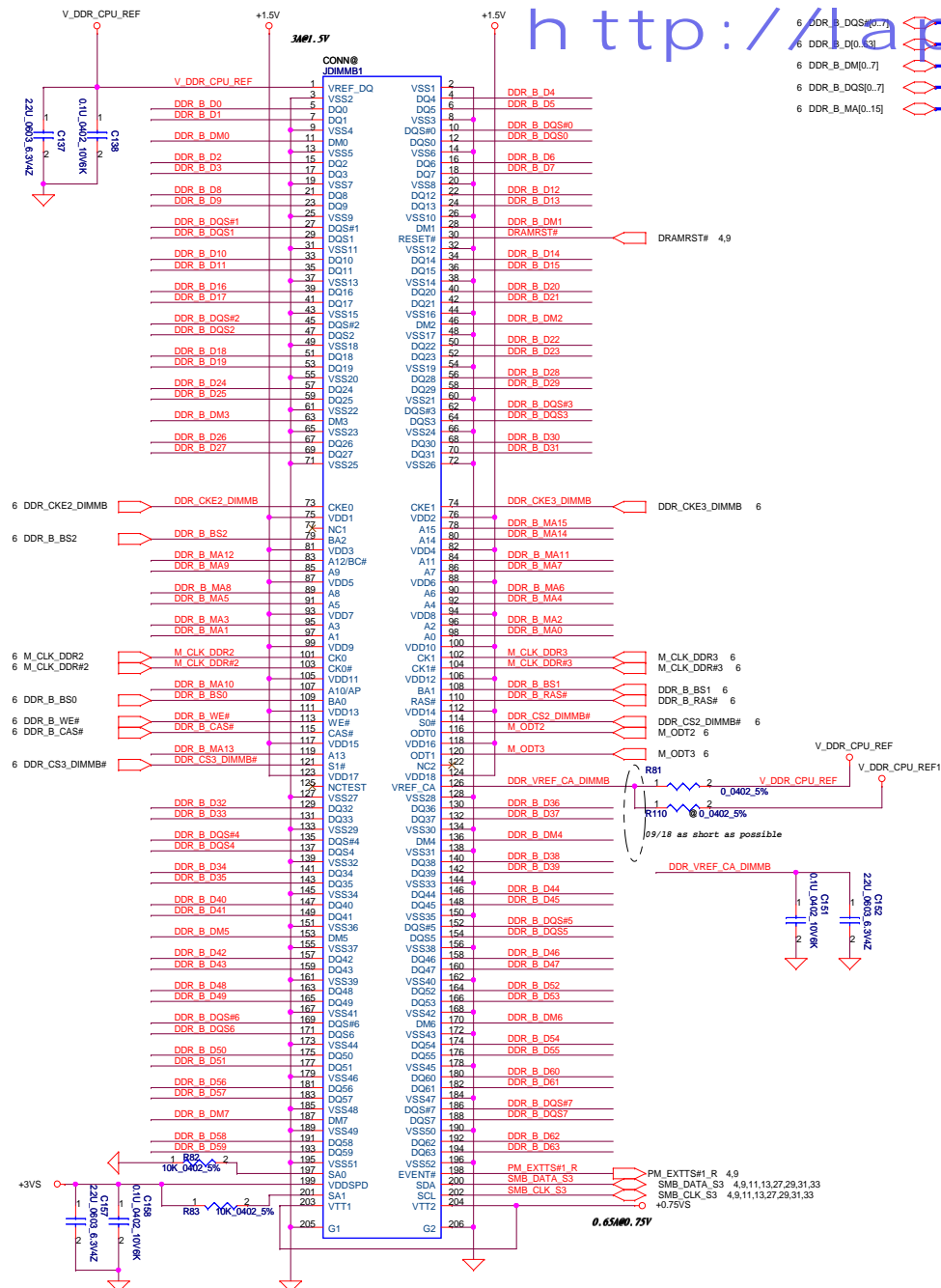




VREFDQ		
M1	V_DDR_CPU_REF	R76, R77, R78, R81,
M3	V_DDR_CPU_REF0	R104, R110
	V_DDR_CPU_REF1	

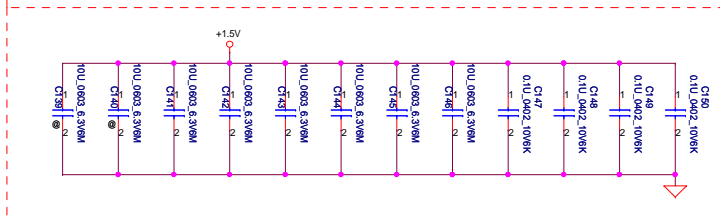
***TOP SLOT***

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**BOT SLOT**

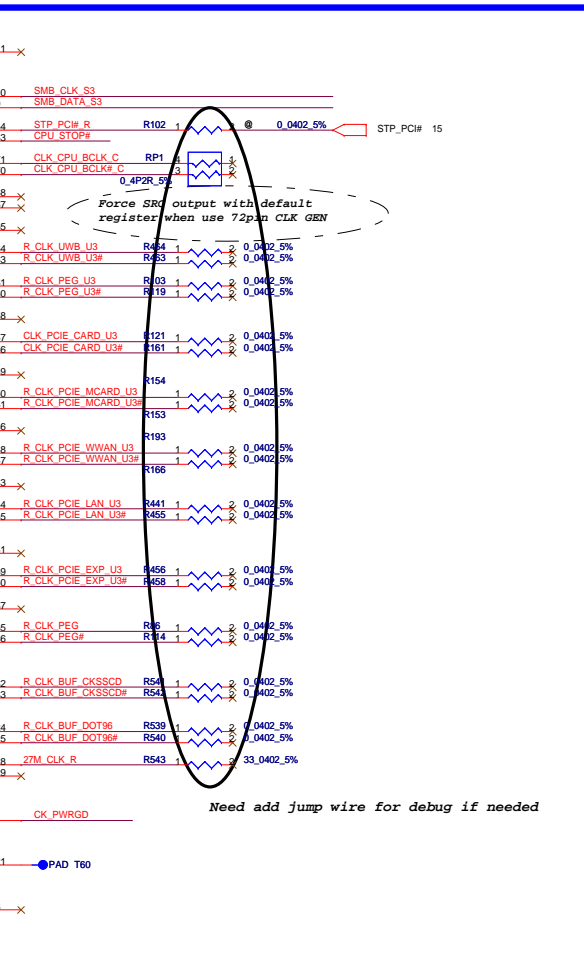
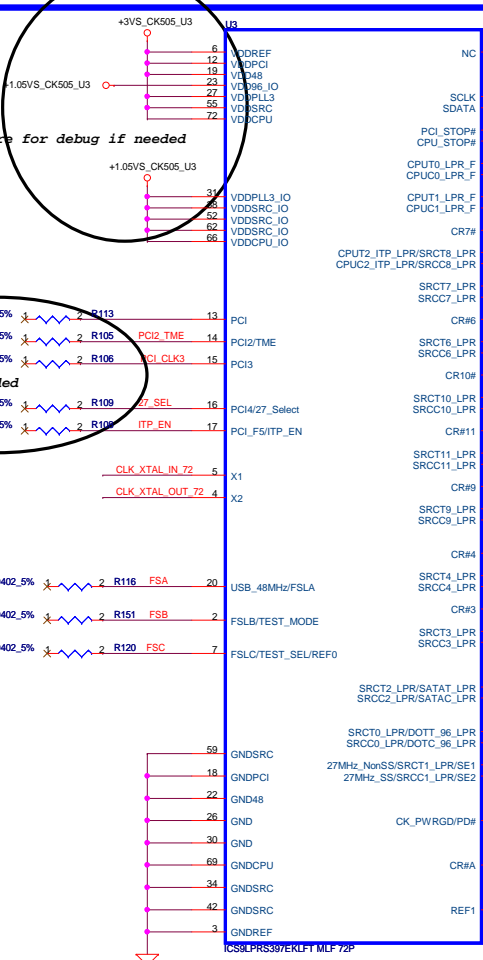
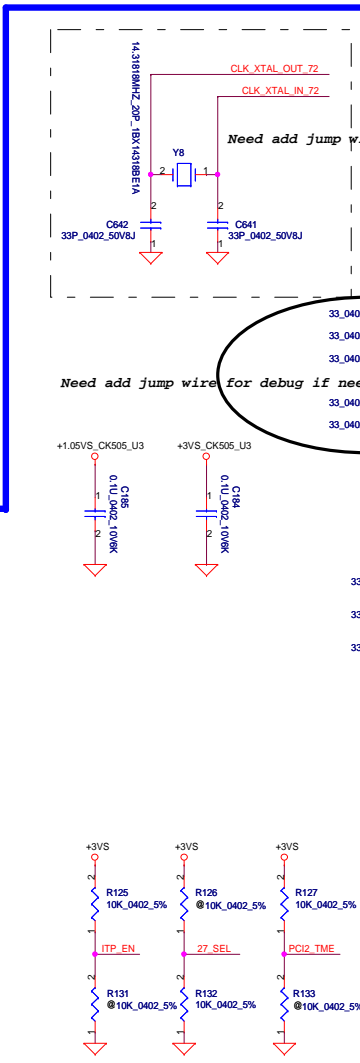
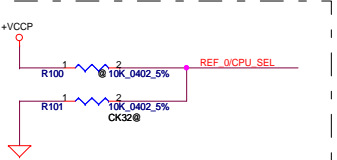
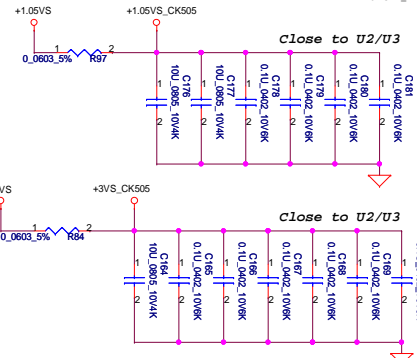
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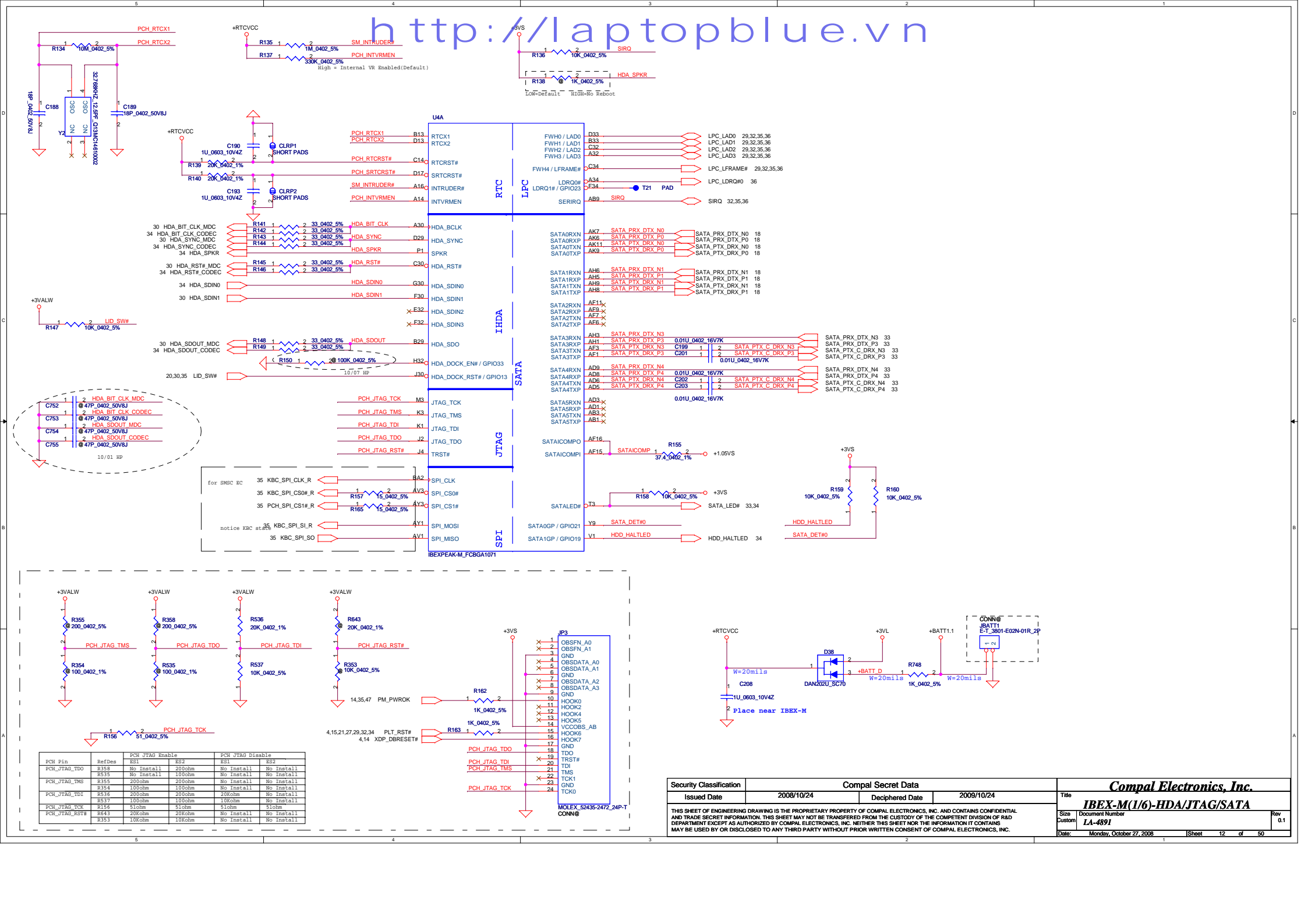
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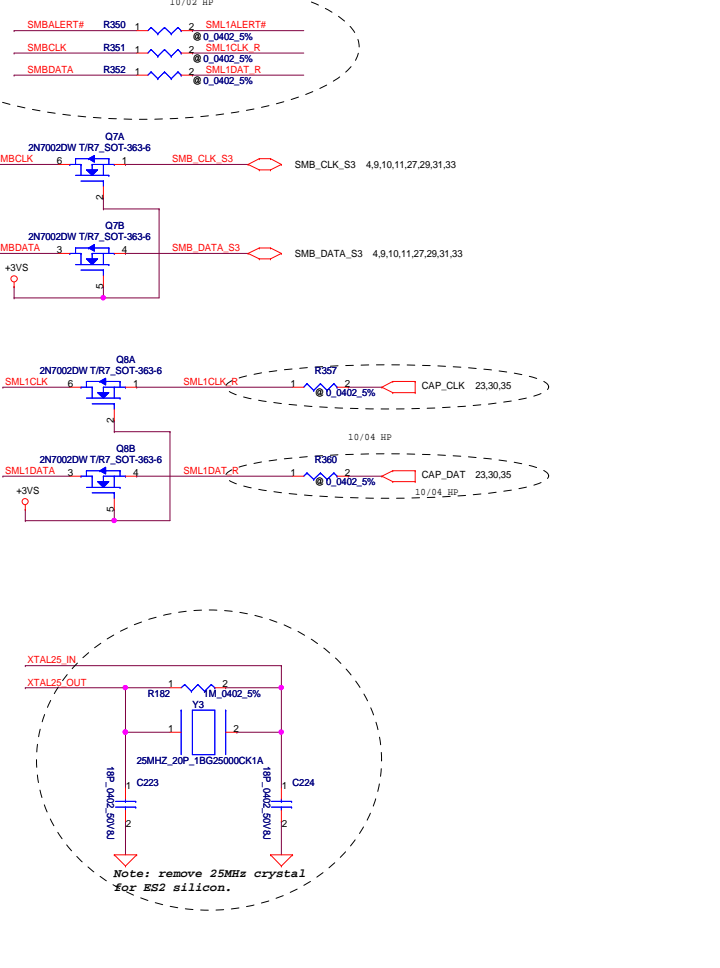
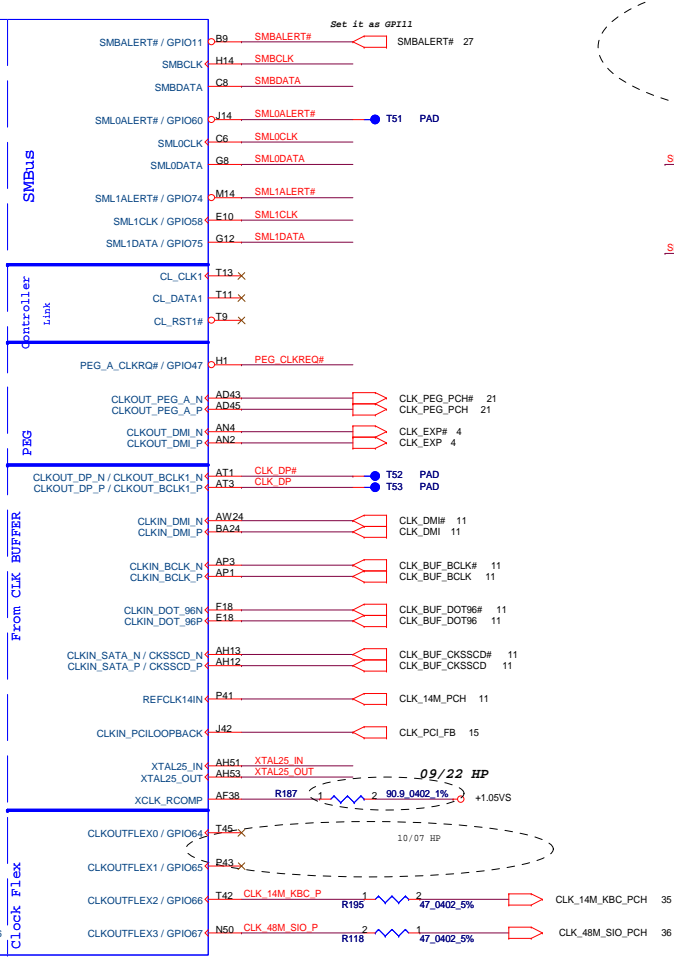
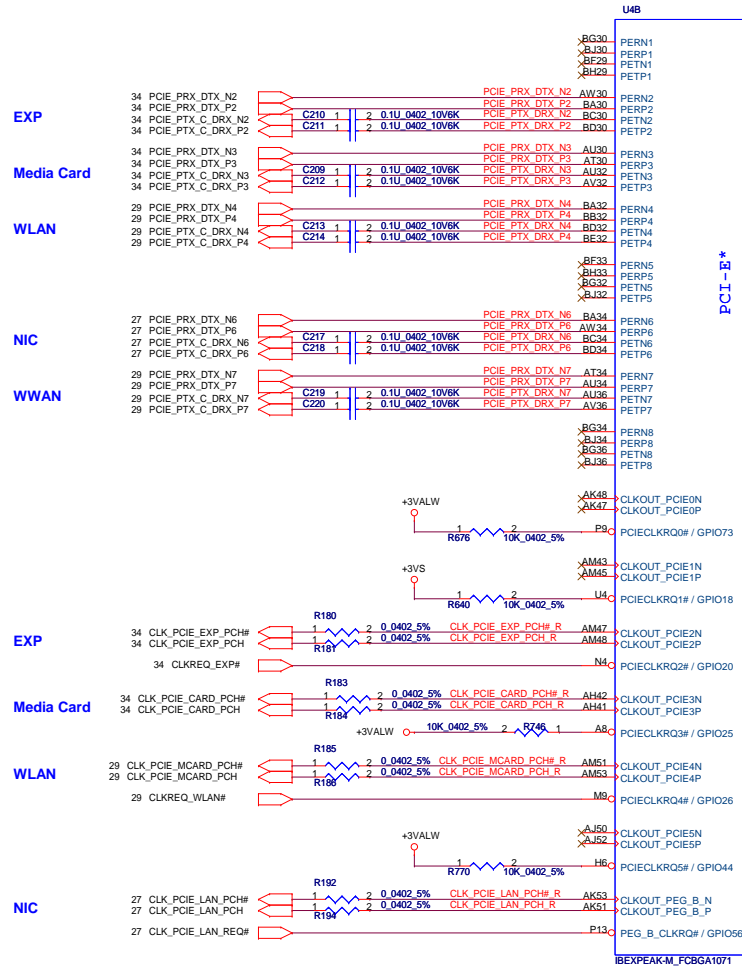


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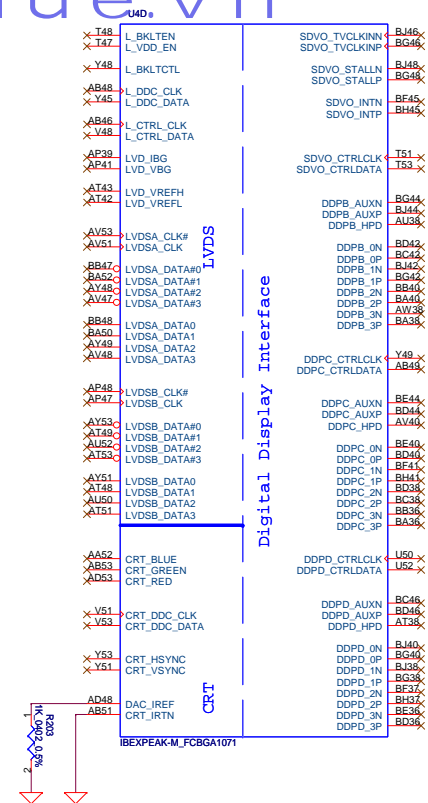
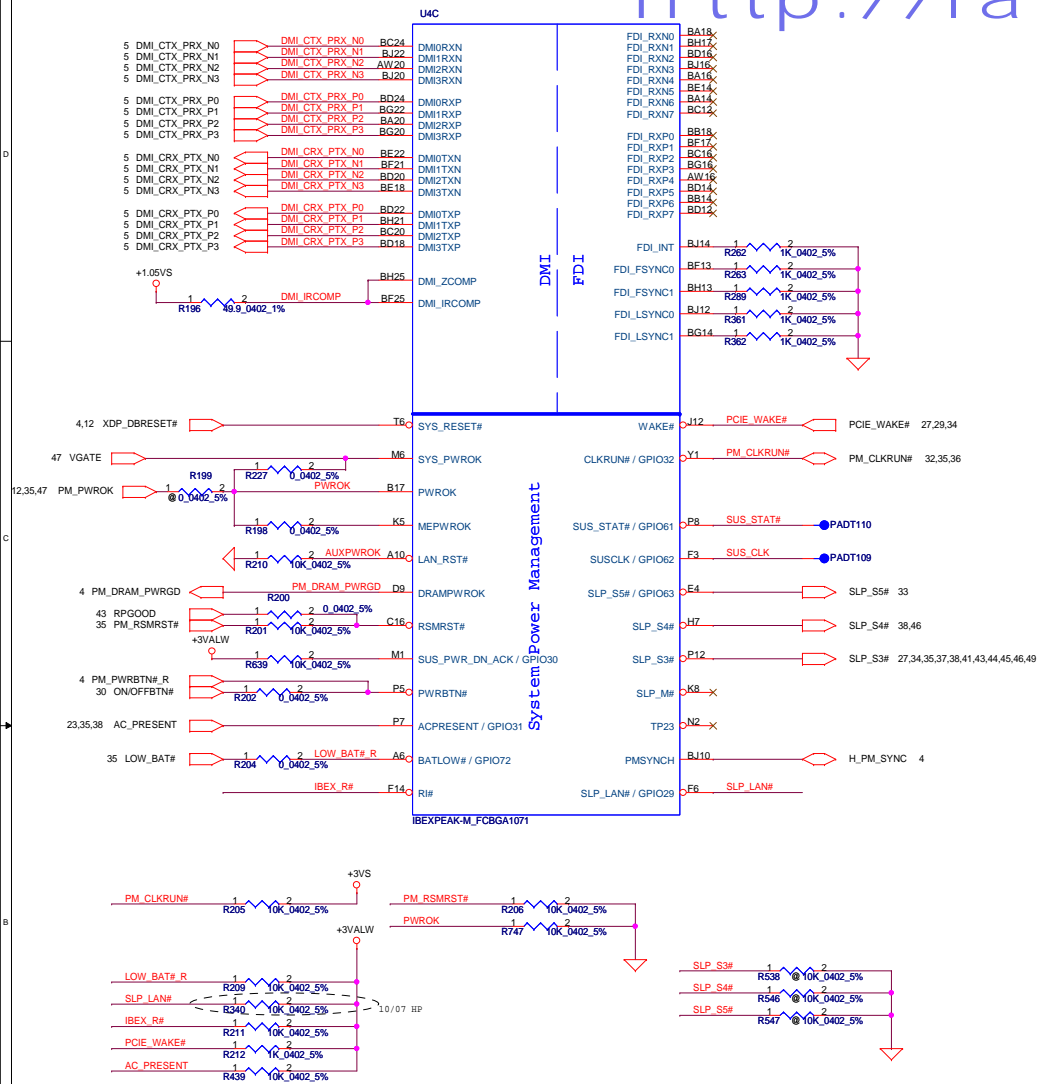


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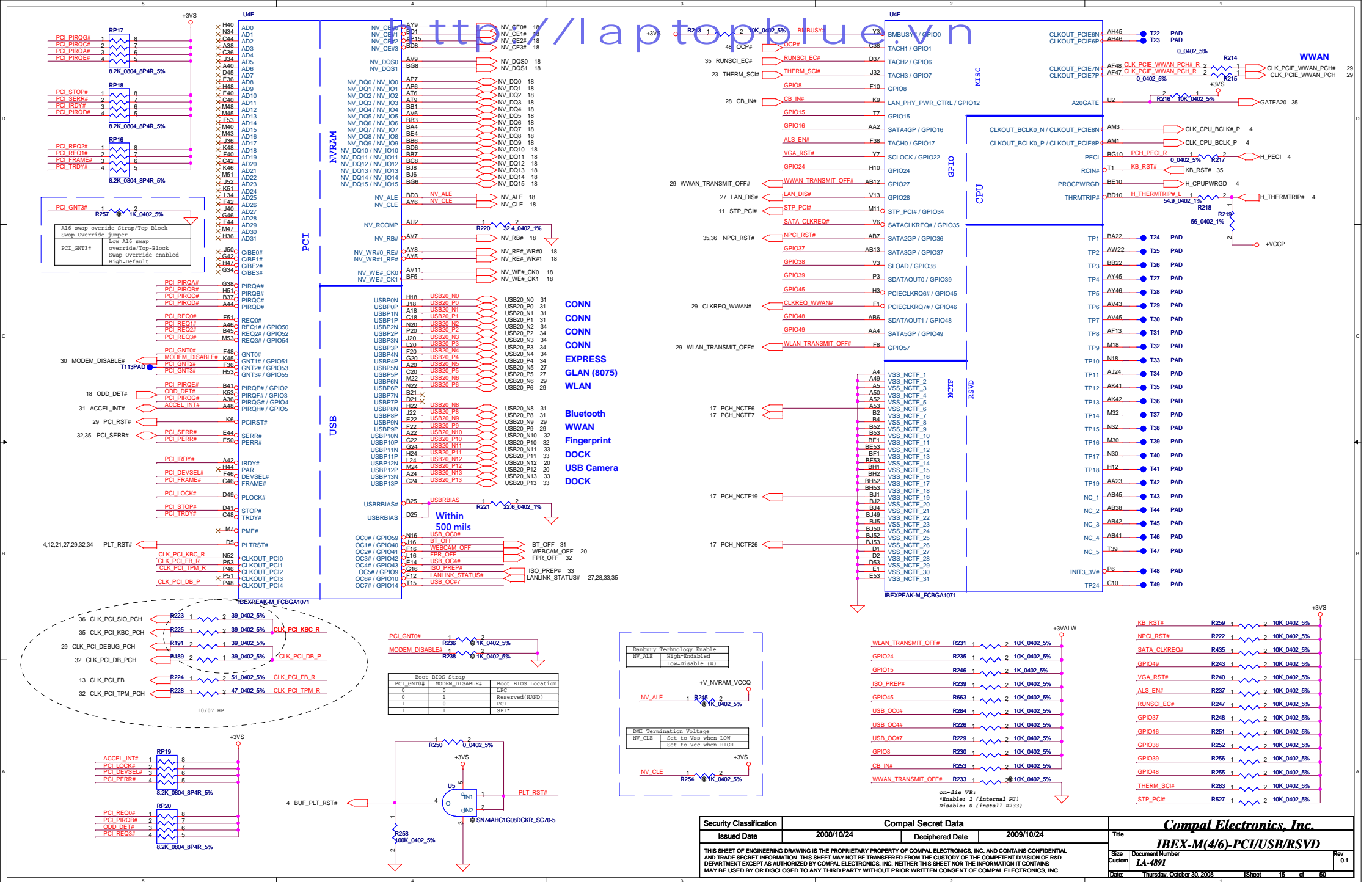
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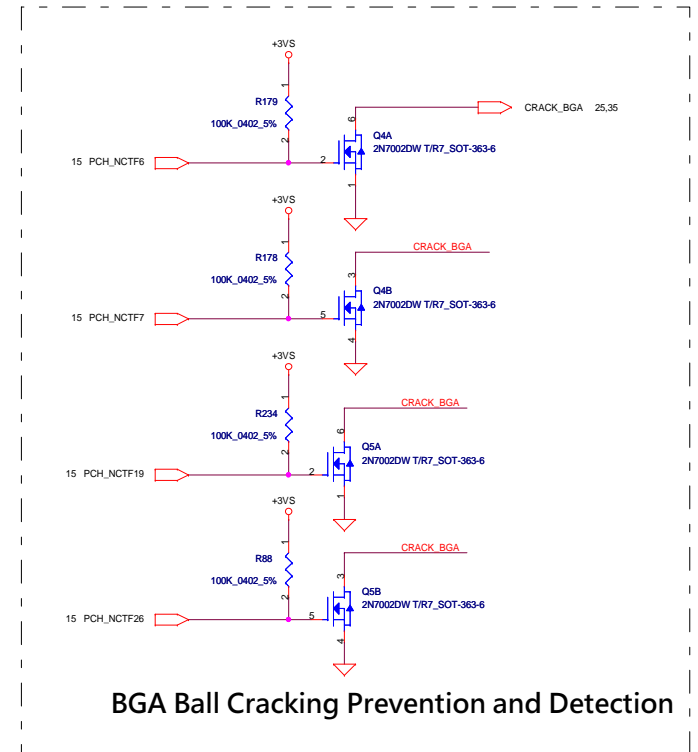
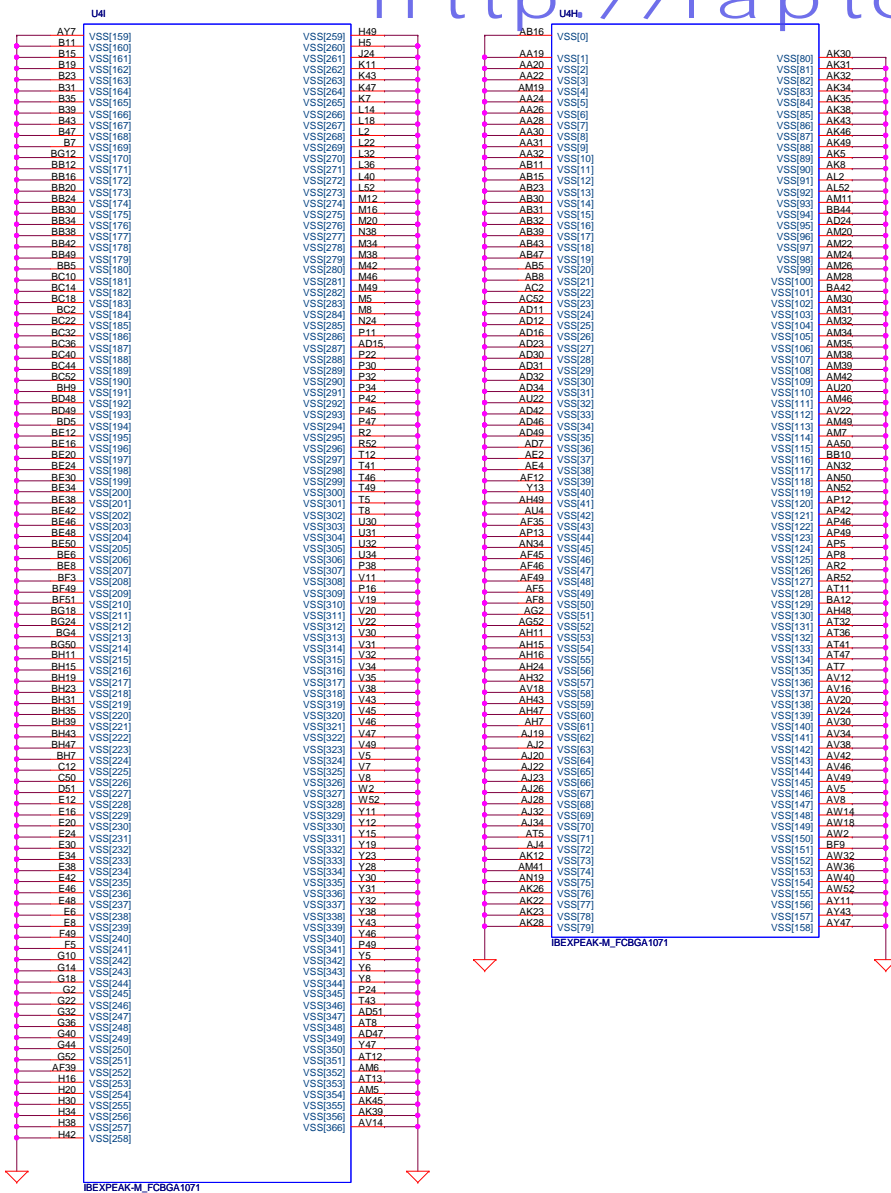














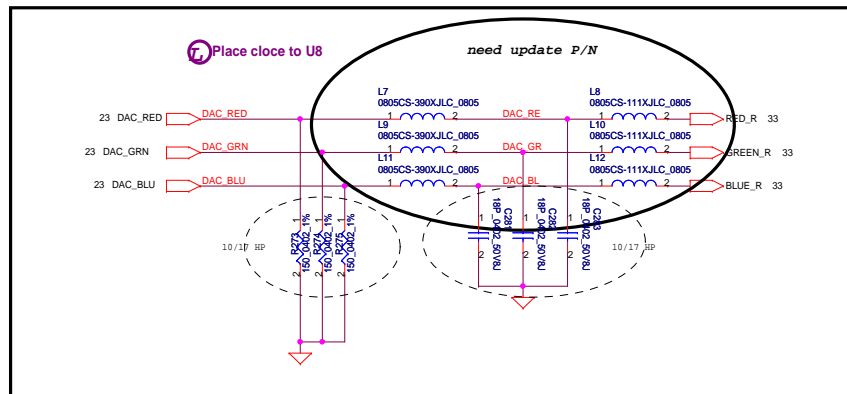
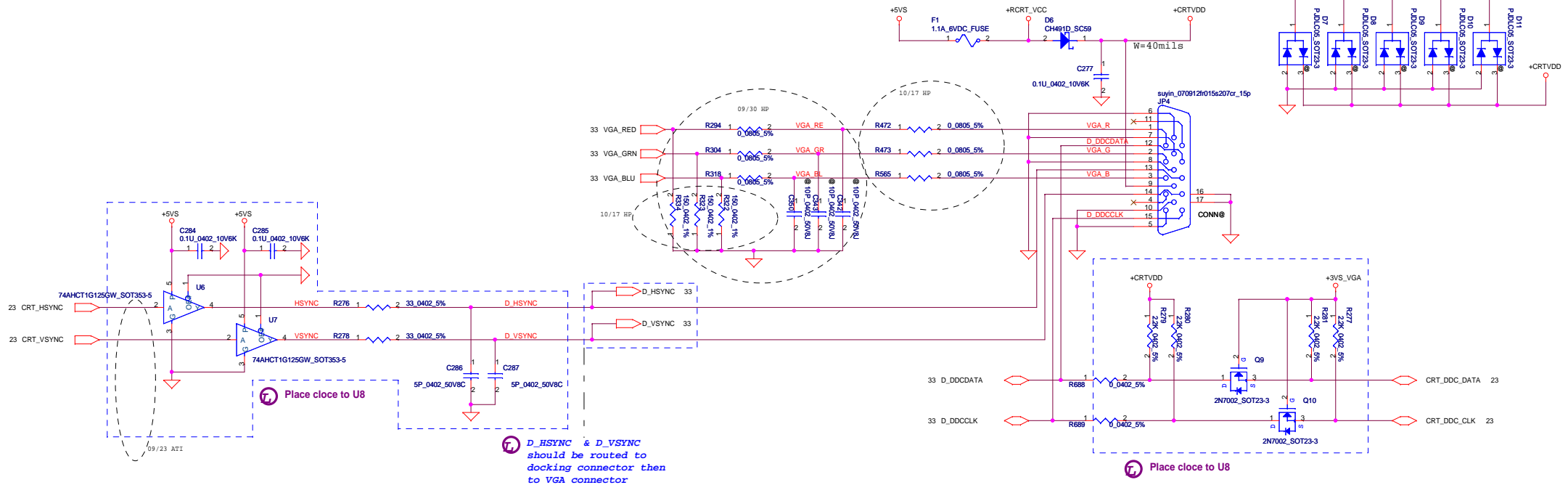
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**IBEX-M(1/6)-HDA/JTAG/SATA**

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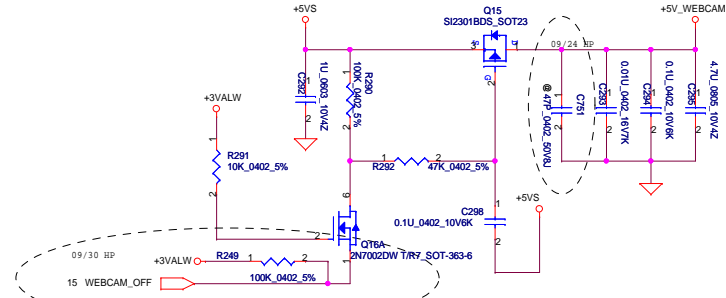
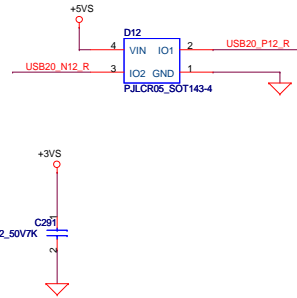
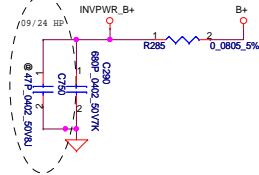
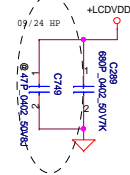
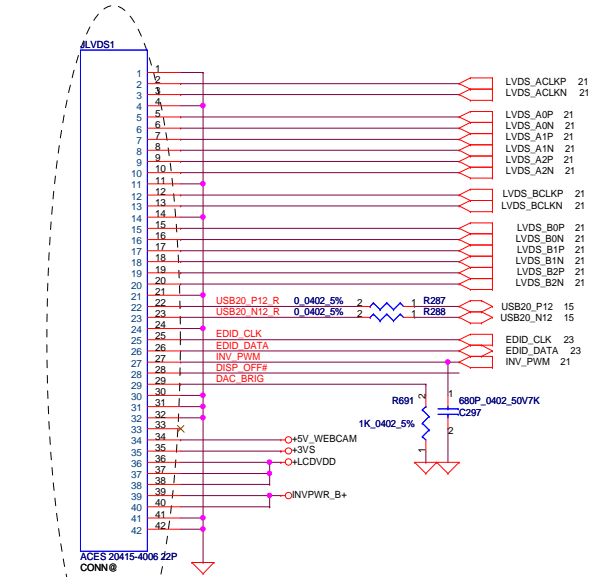
# CRT Connector

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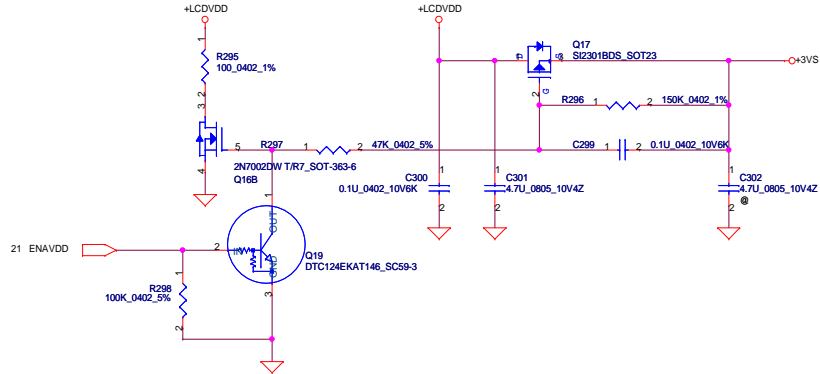


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# LCD/PANEL BD. CONN.



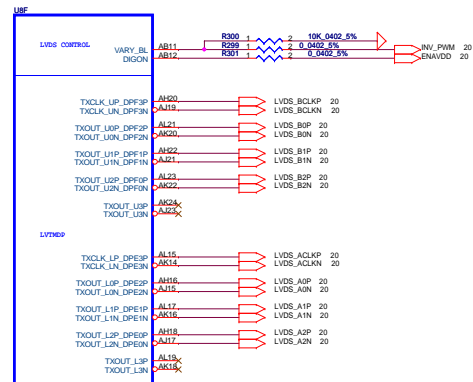
## LCD POWER CIRCUIT



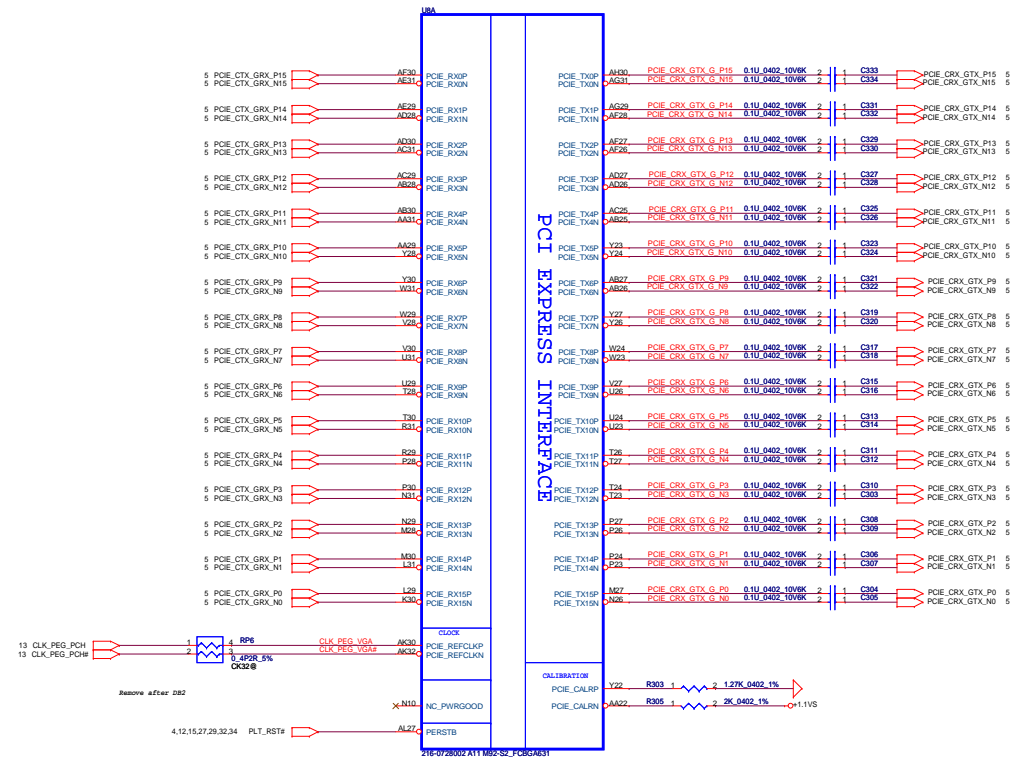
Security Classification	Compal Secret Data		Title	
Issued Date	2008/10/24	Deciphered Date	2009/10/24	LCD CONN & Q-Switch & GPIO Ext.
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				Sheet 20 of 50

## PEG Interface


## LVDS Interface

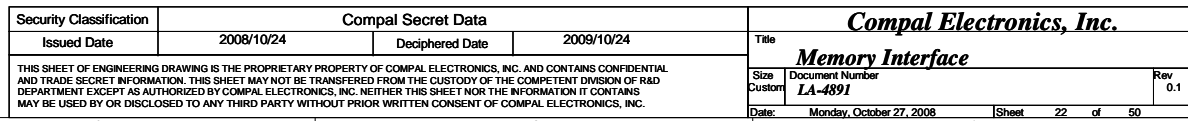


216-0728002 A11 M92-S2\_FCBGA631

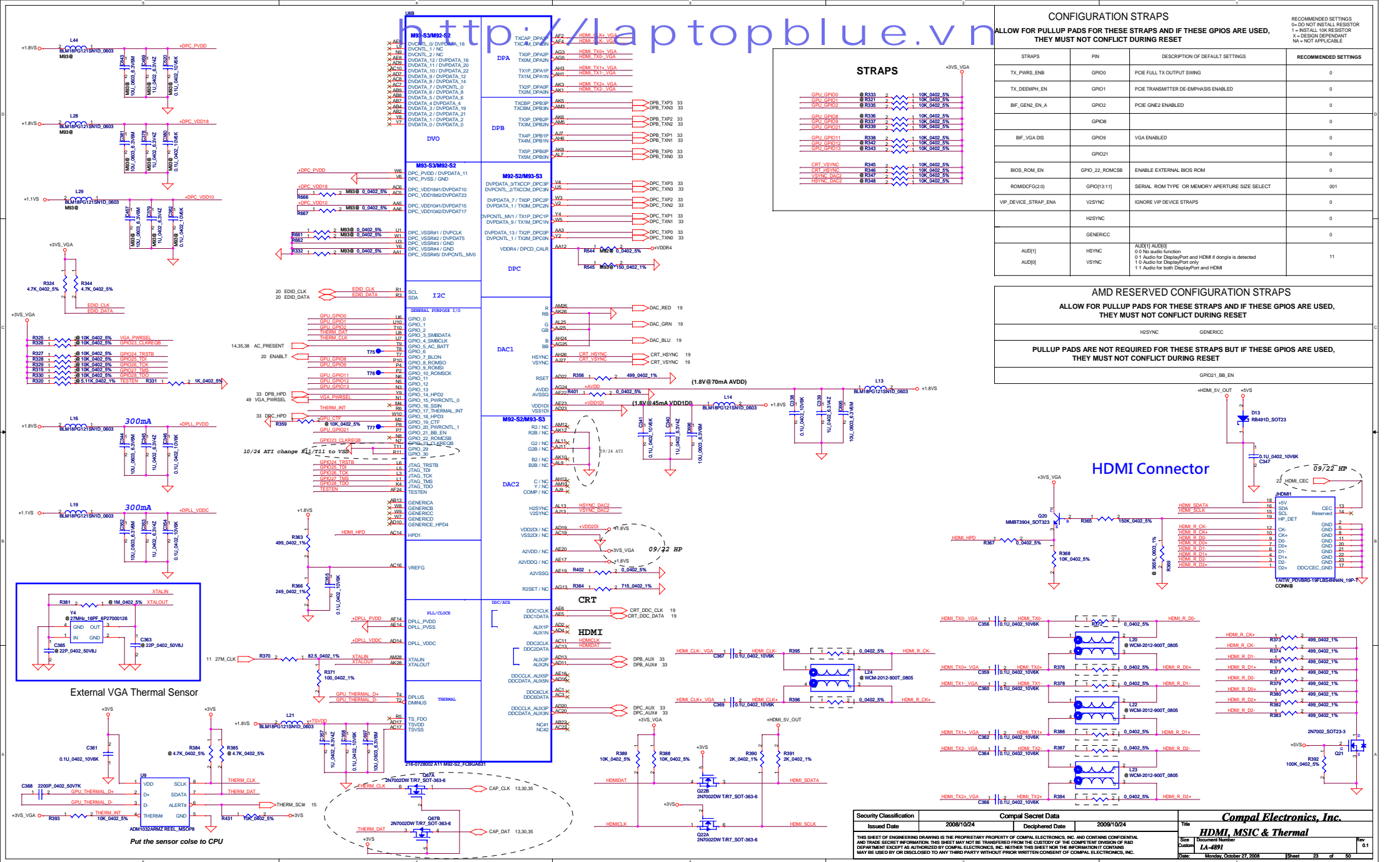


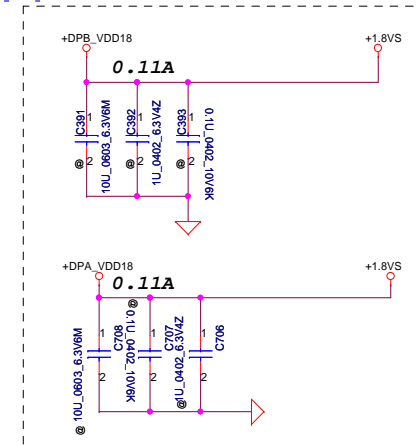
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			Date <b>11-09-08</b>	Rev <b>6.1</b>
			Issue Date <b>11-09-08</b>	Issue Count <b>91</b> of <b>80</b>









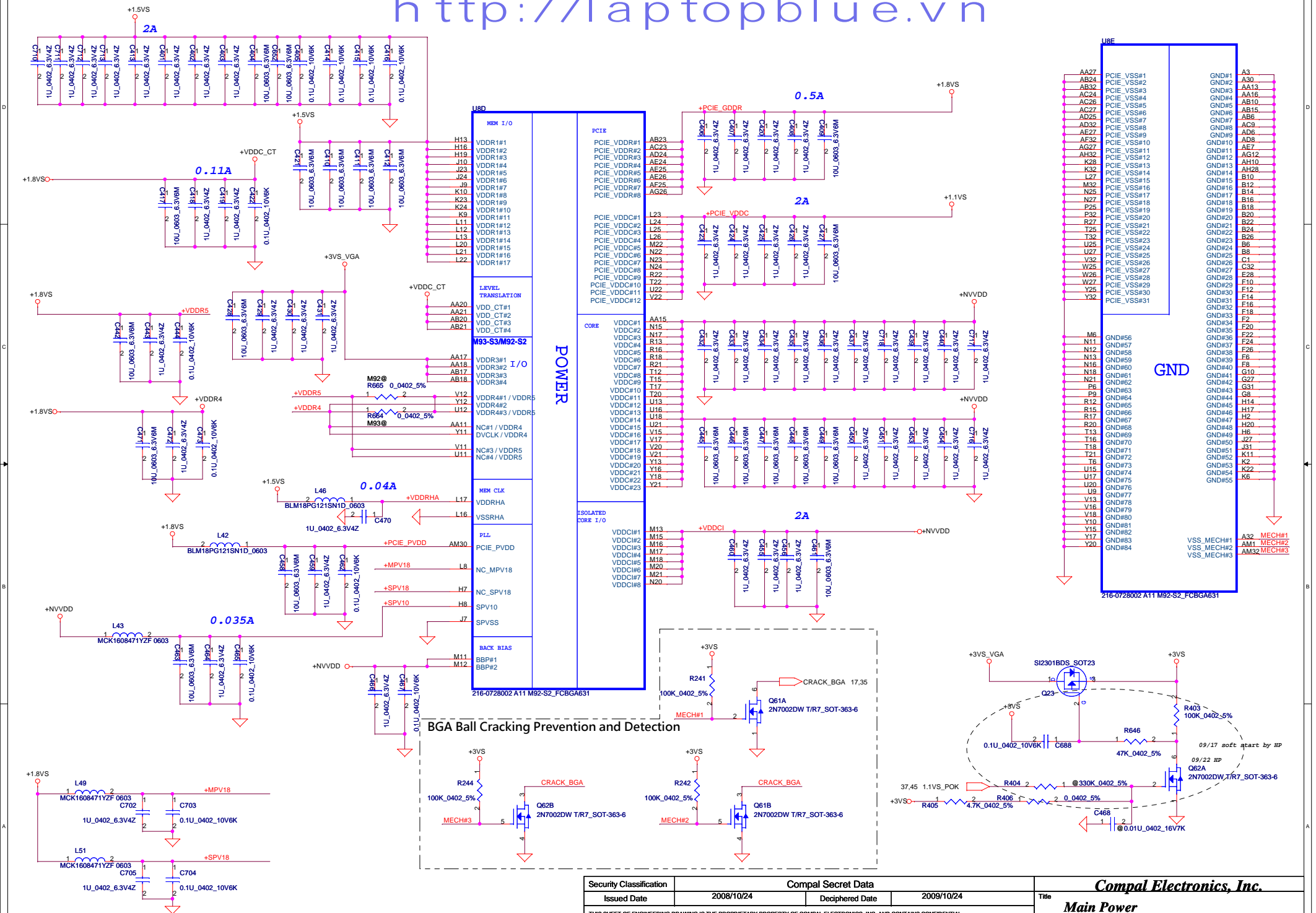
***Compal Electronics, Inc.***

***DPX POWER***

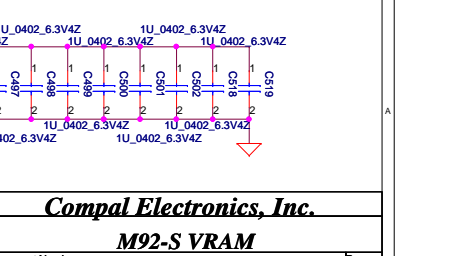
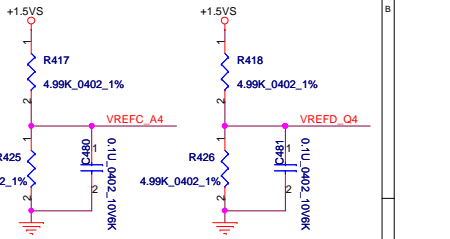
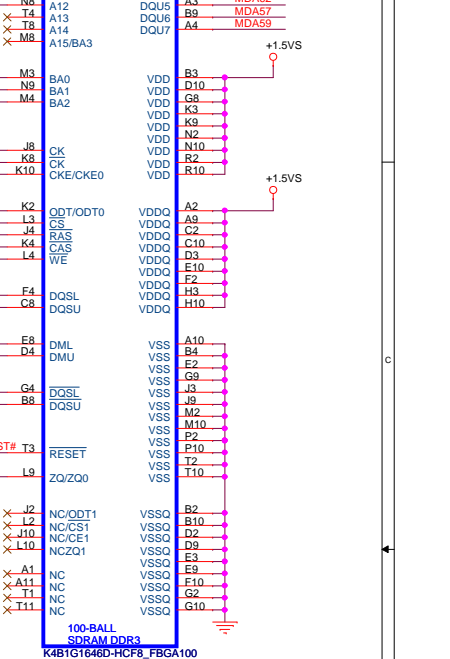
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**LA-4891**

Rev  
0.1

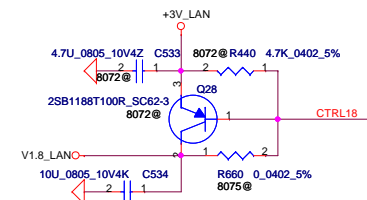
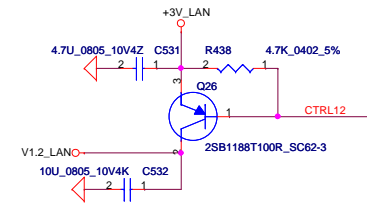
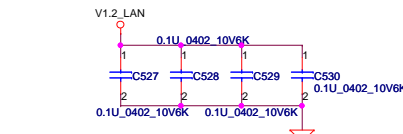
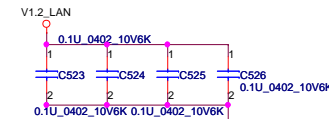
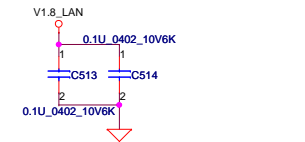
Date:	Monday, October 27, 2008	Sheet	24	of	50
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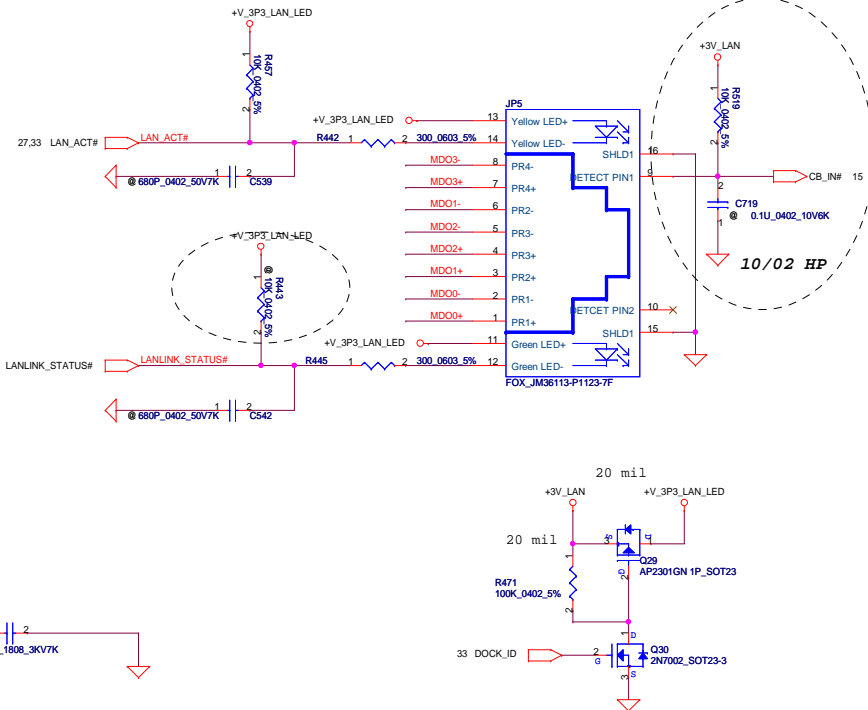
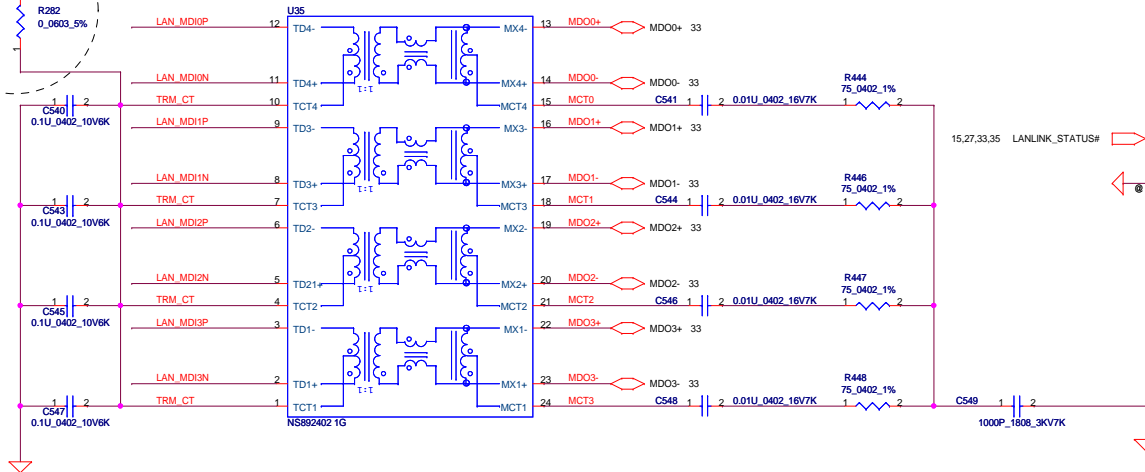
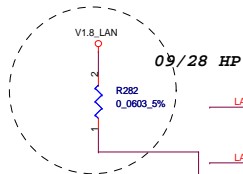
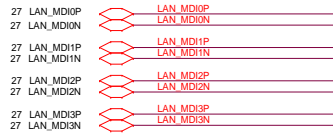


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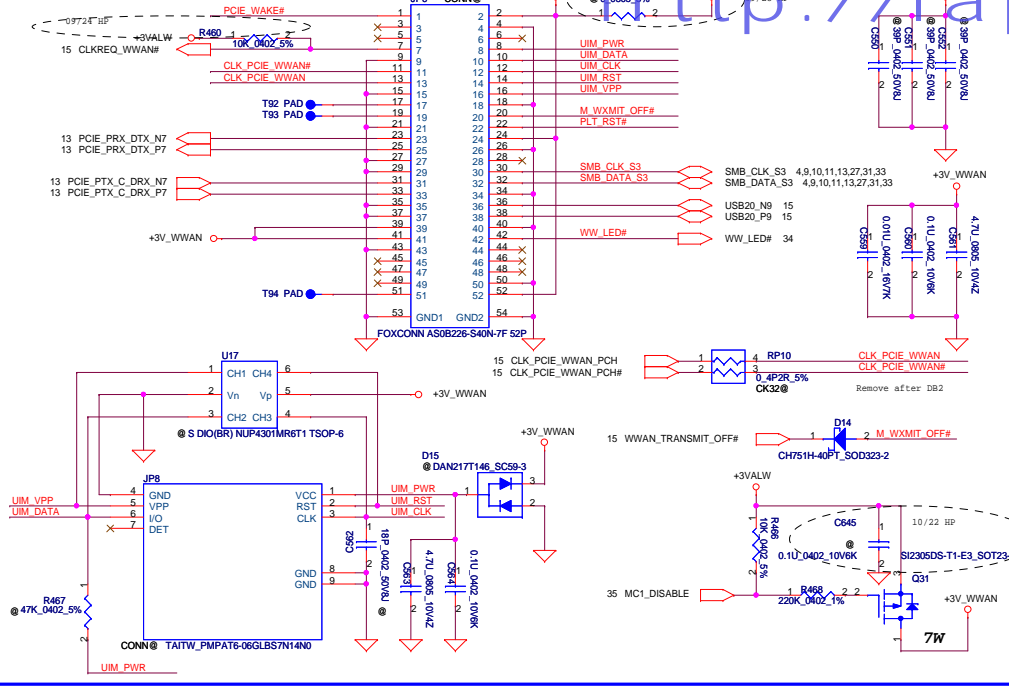


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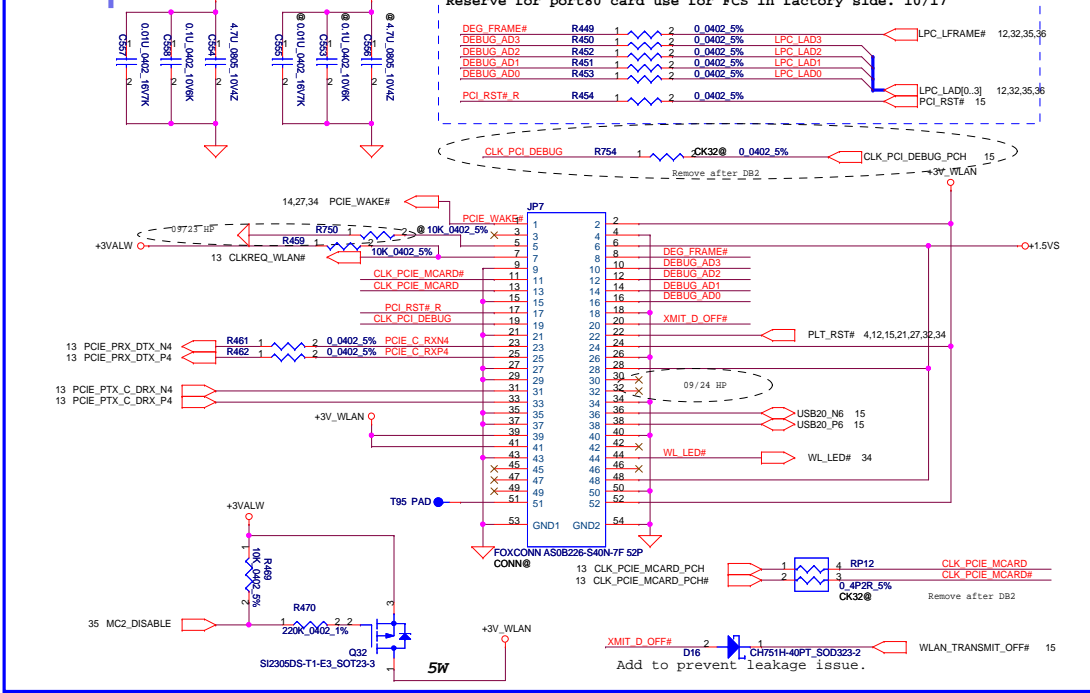
10/15 HP



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**WWAN**

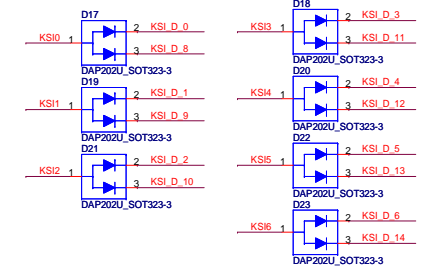
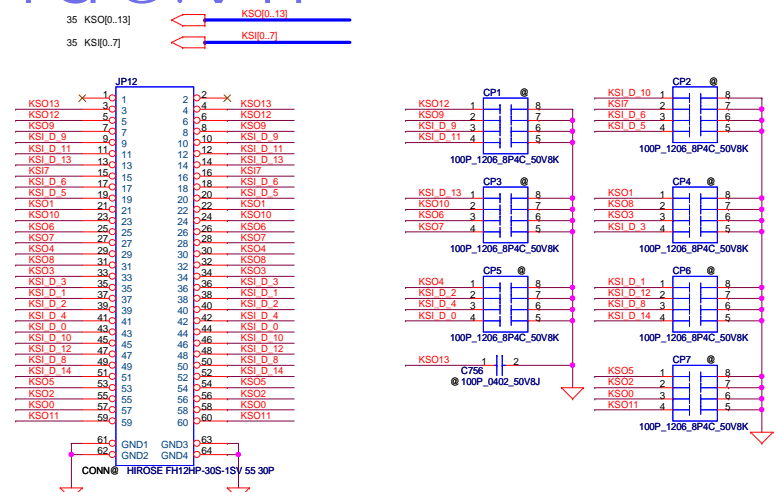
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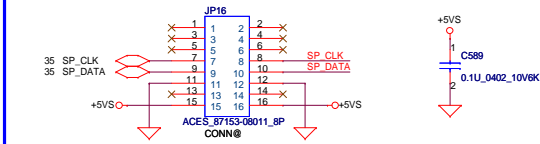
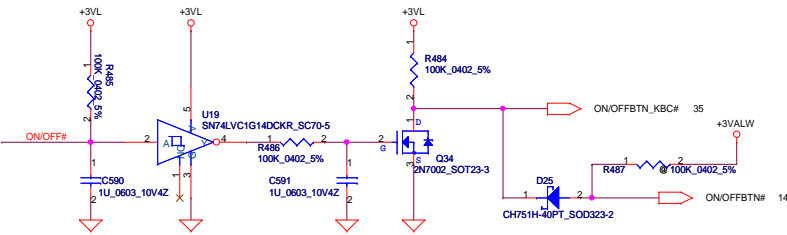
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Date: <b>Wednesday, October 29, 2008</b>				Sheet <b>29</b> of <b>50</b>	



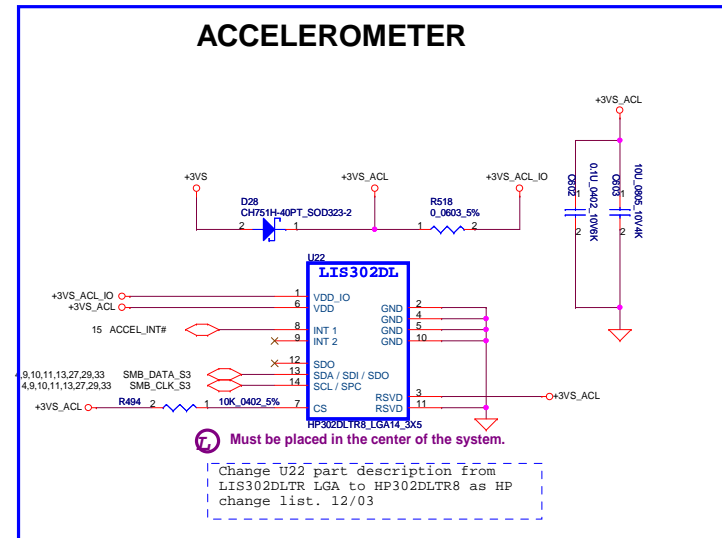
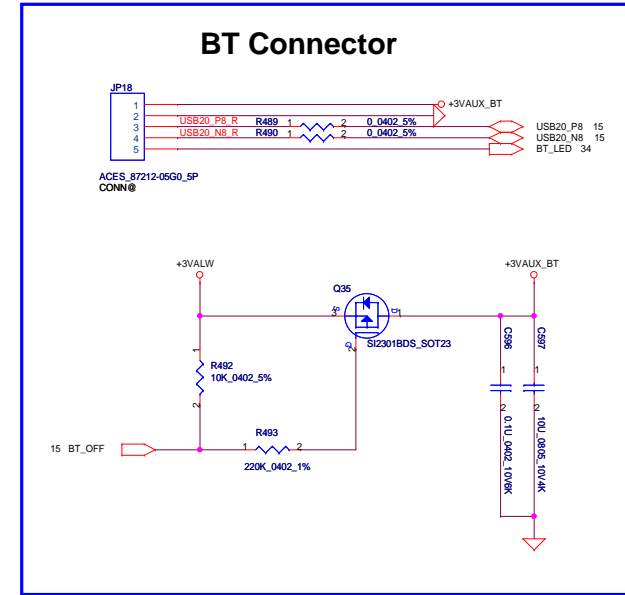
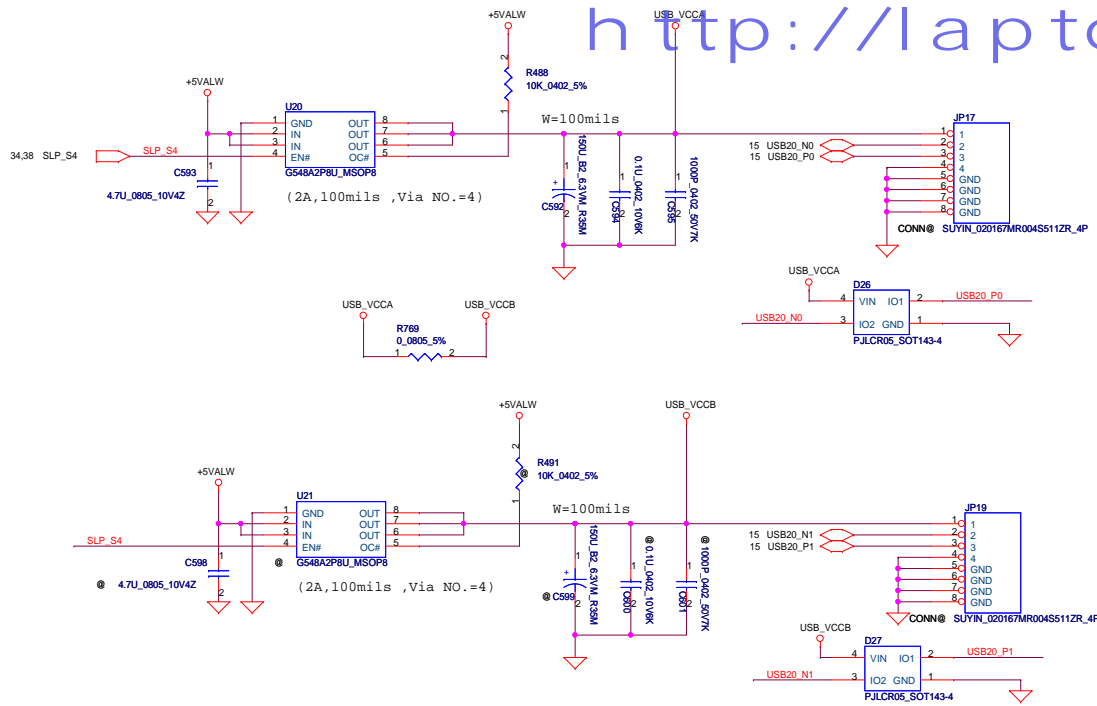
http://laptopblue.vn

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## TrackPoint CONN.

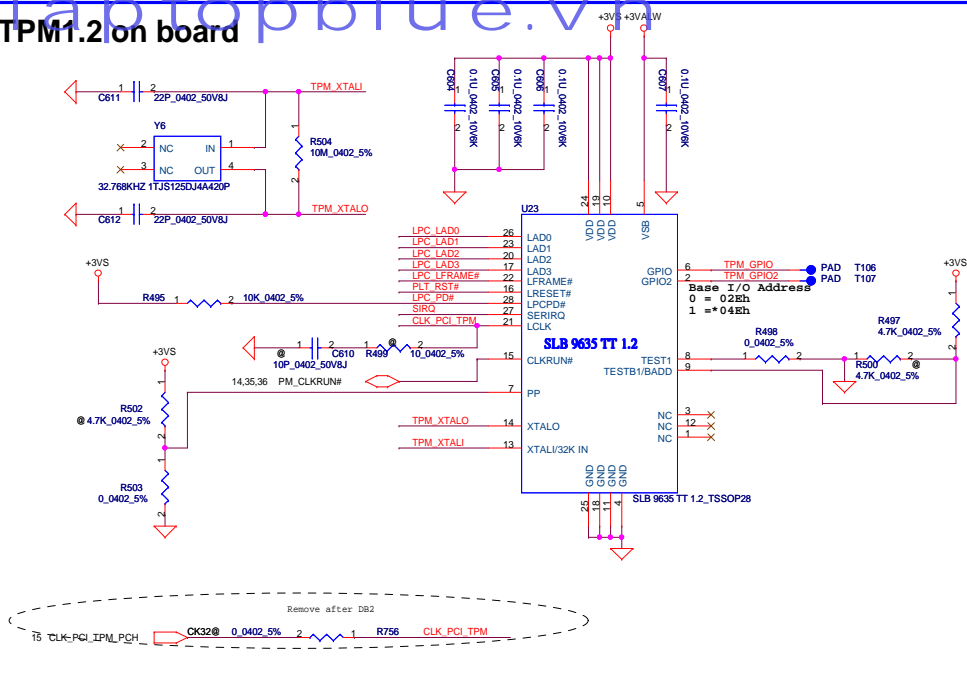
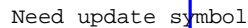


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				50

TPM1.2 on board



# LPC Debug Port

10k 14 RP

8051 RECOVER#

1229,35,36 LPC\_LFRAME#

12,35,36 PLT\_SIRQ

4,12,15,21,27,29,34 PCI\_SERR#

15,35 PCI\_SERR#

12,29,35,36 LPC\_LAD0

12,29,35,36 LPC\_LAD1

12,29,35,36 LPC\_LAD2

12,29,35,36 LPC\_LAD3

35 8051TX

35 8051RX

35 8051\_RECOVER#

43 DEBUG\_KBCRST

35 KBC\_SPI\_CS#\_R

CLK\_PCI\_DB

SIQ

8051\_RECOVER#

SPI\_CLK\_JP

SPI\_CS0#\_JP

SPI\_SI\_JP

SPI\_SO\_JP

SPI\_HOLD#\_0

SPI\_HOLD#

J21

1 Ground

2 LPC\_PCI\_CLK

3 Ground

4 LPC\_FRAME#

5 +V3S

6 LPC\_RESET#

7 +V3S

8 LPC\_AD0

9 LPC\_AD1

10 LPC\_AD2

11 LPC\_AD3

12 VCC\_3VA

13 PWR\_LED#

14 CAPS\_LED#

15 NUM\_LED#

16 VCC1\_PWRGRD

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21 SPI\_HOLD#

22 Reserved

23 Reserved

24 Reserved

ACES\_87216-2404\_24P CONN@

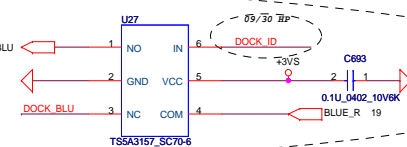
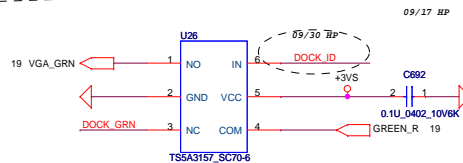
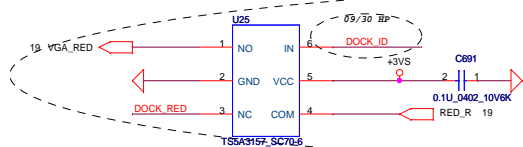
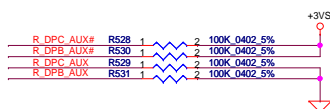
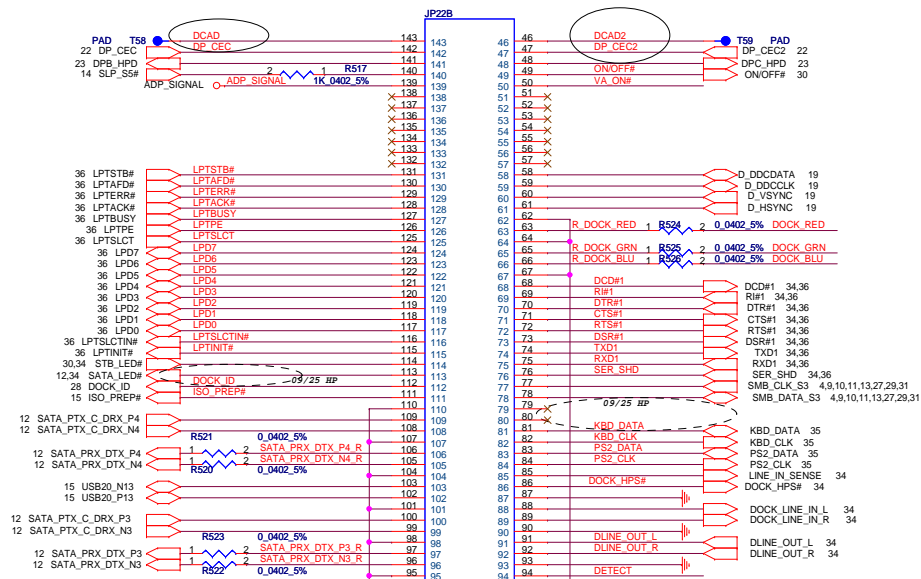
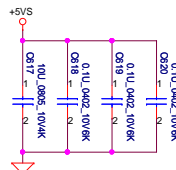
Remove after D82

CK32@ 0\_0402\_5% 2 2 1 R758 CLK\_PCI\_DB

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ONN 190PIN: **DOCKING CONNECTOR**

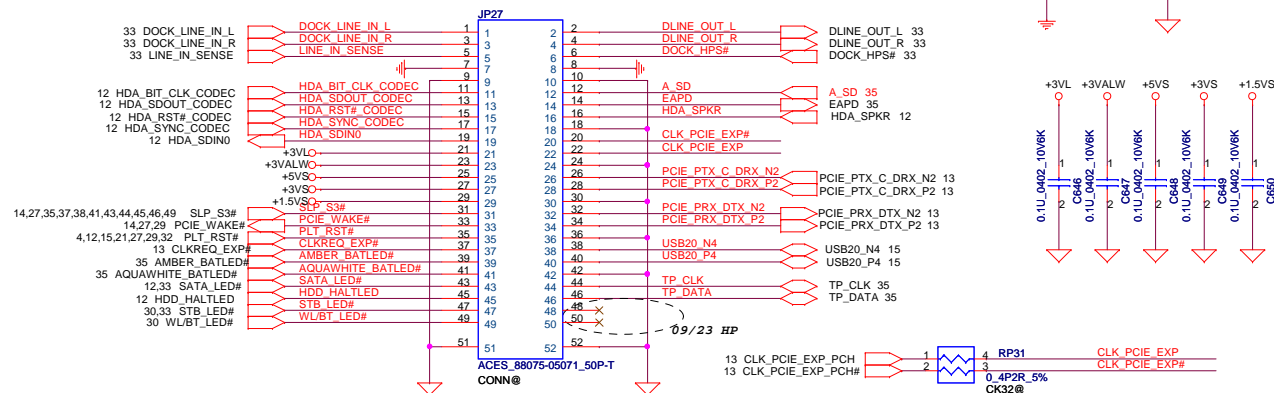
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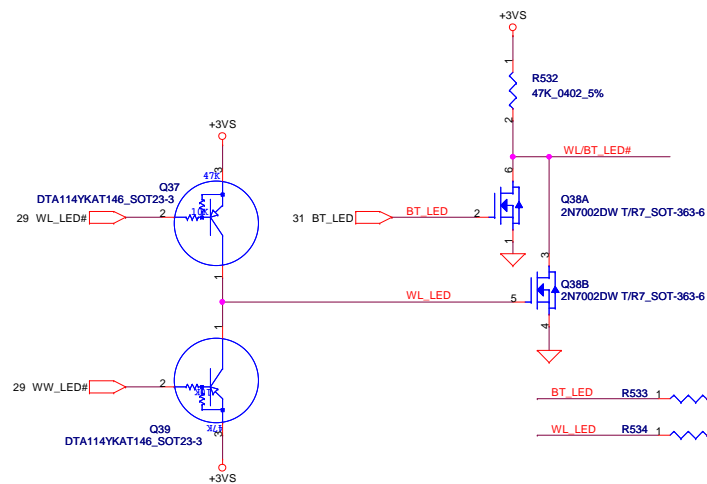
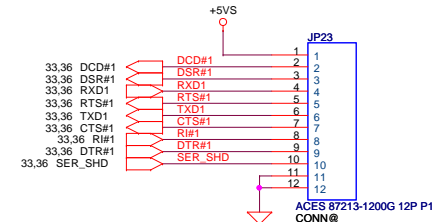
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# Audio/Express Card/TP/Leds Connector

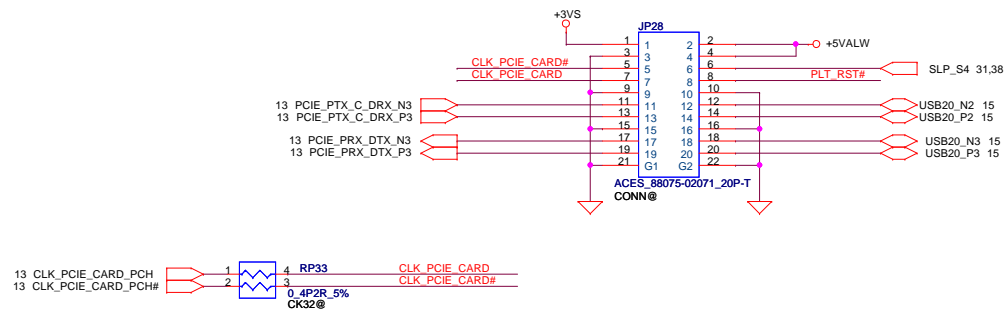
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## Serial Port CONN



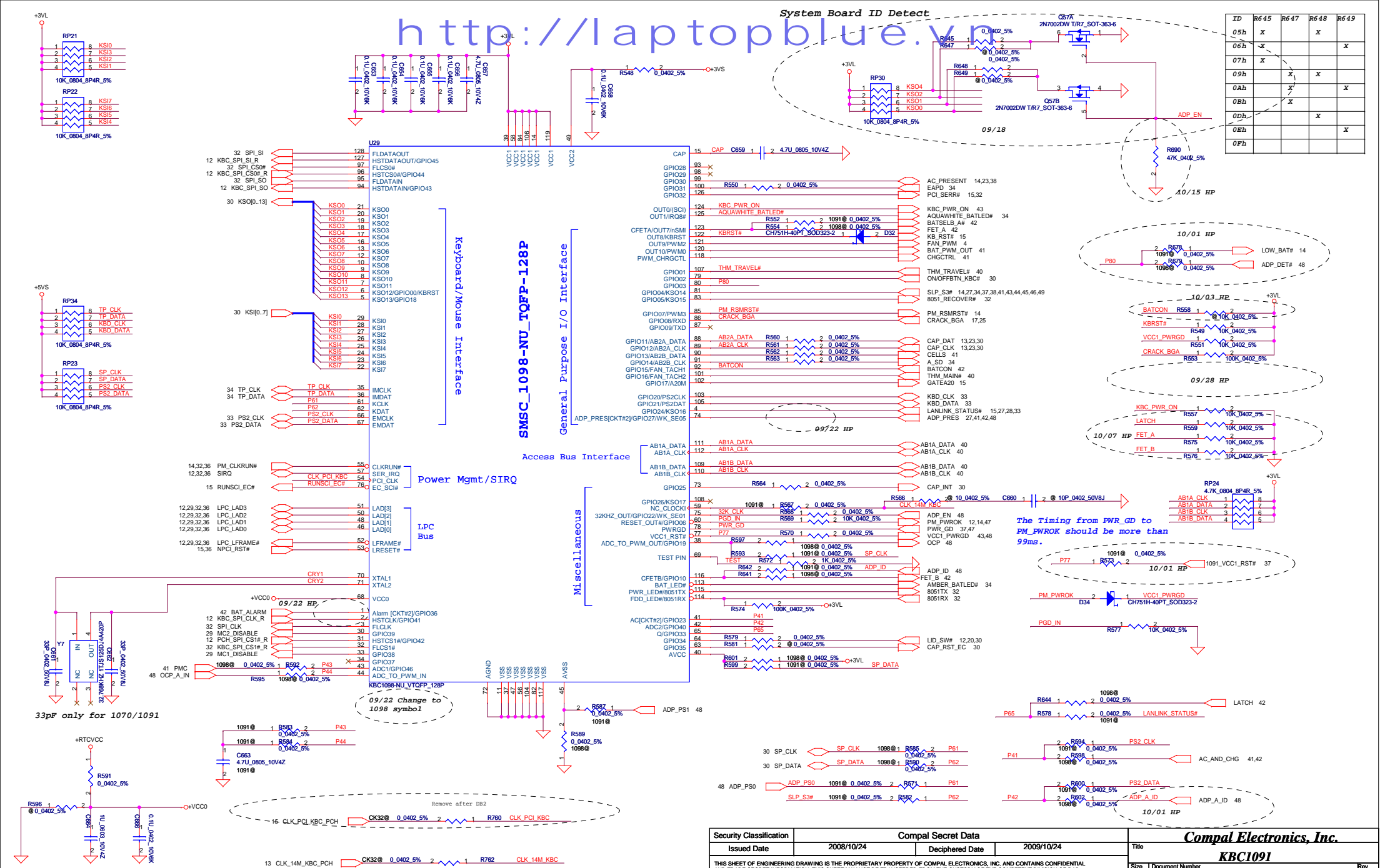
## Card Reader 7 in 1 + 2 Port USB Connector



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CR&LEDSPW&Audio&Exp Conn

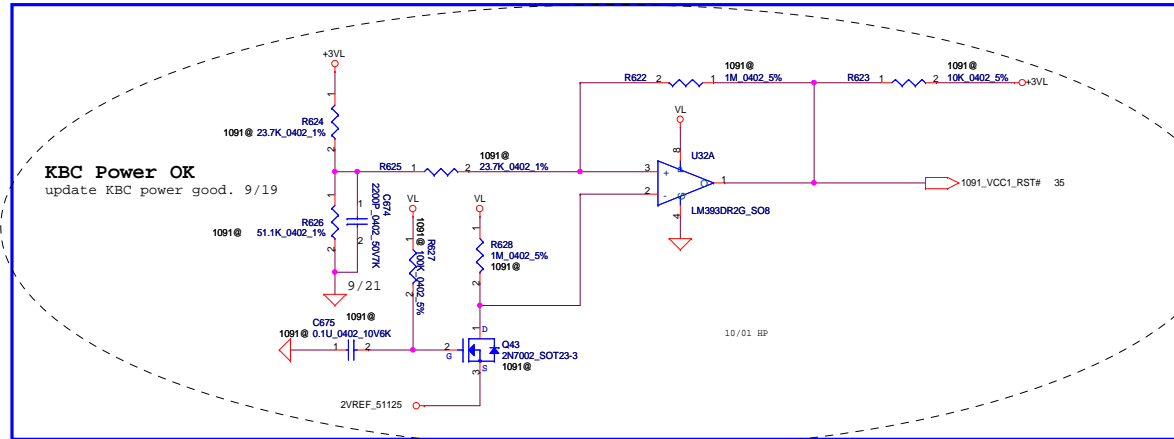
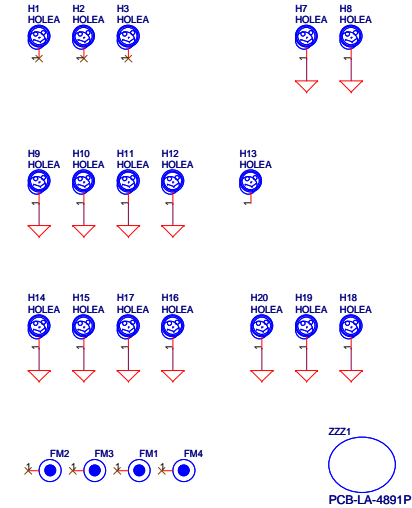
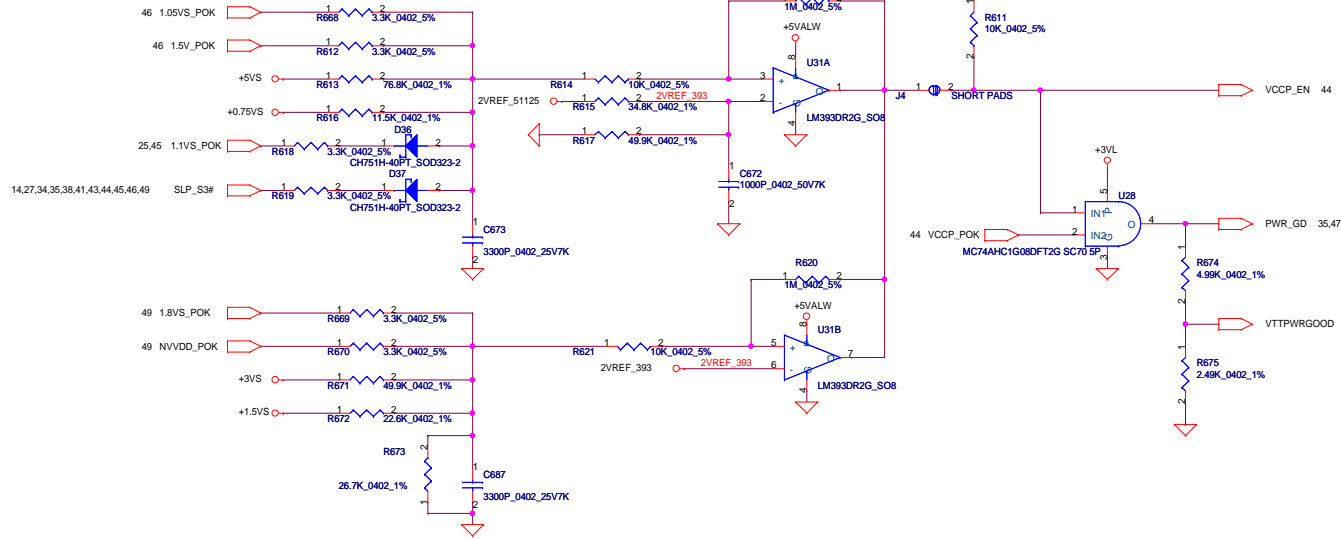
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0Bh		X		
0Dh			X	
0Eh				X
0Fh				



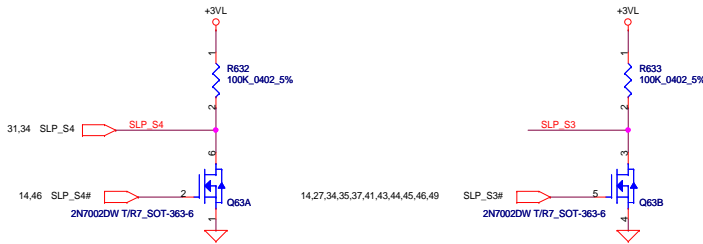
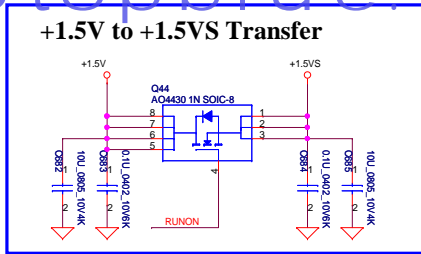
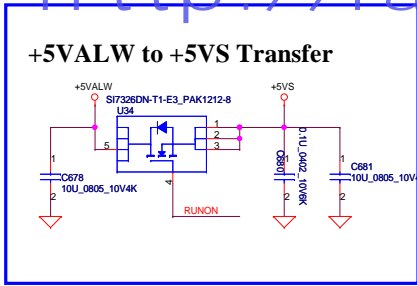
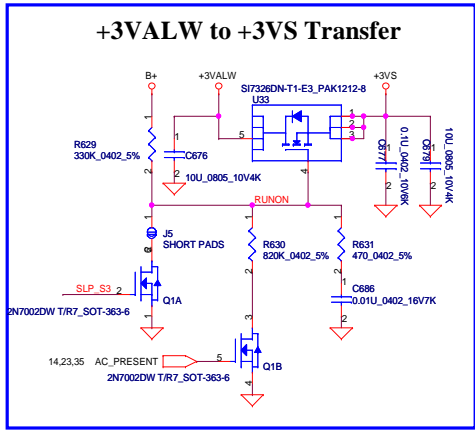
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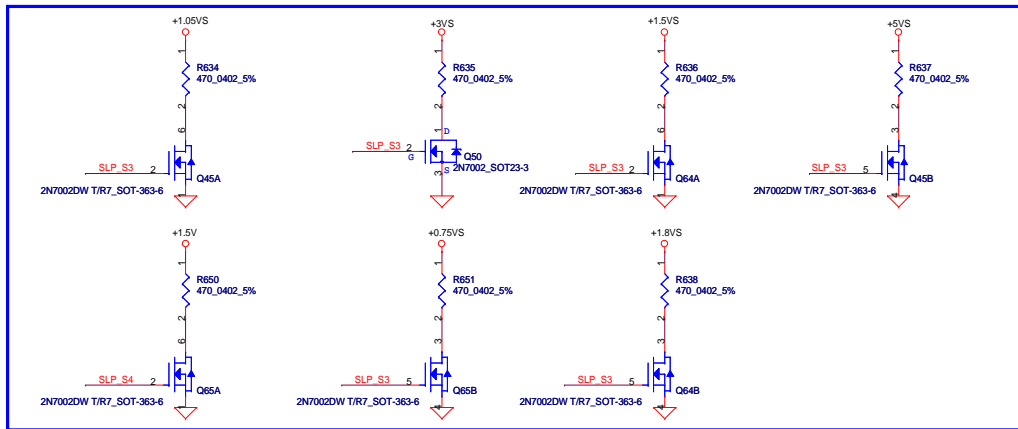




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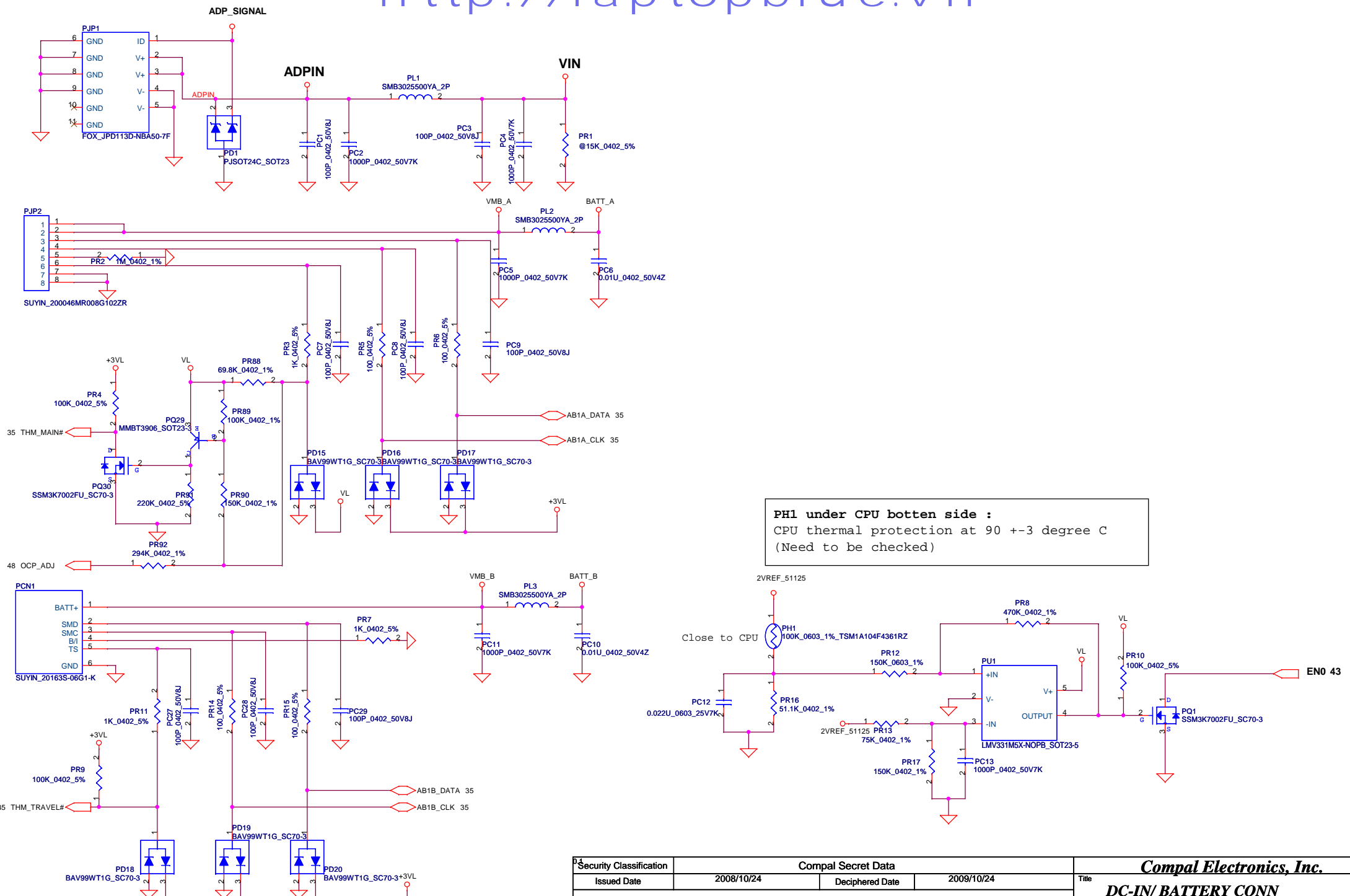
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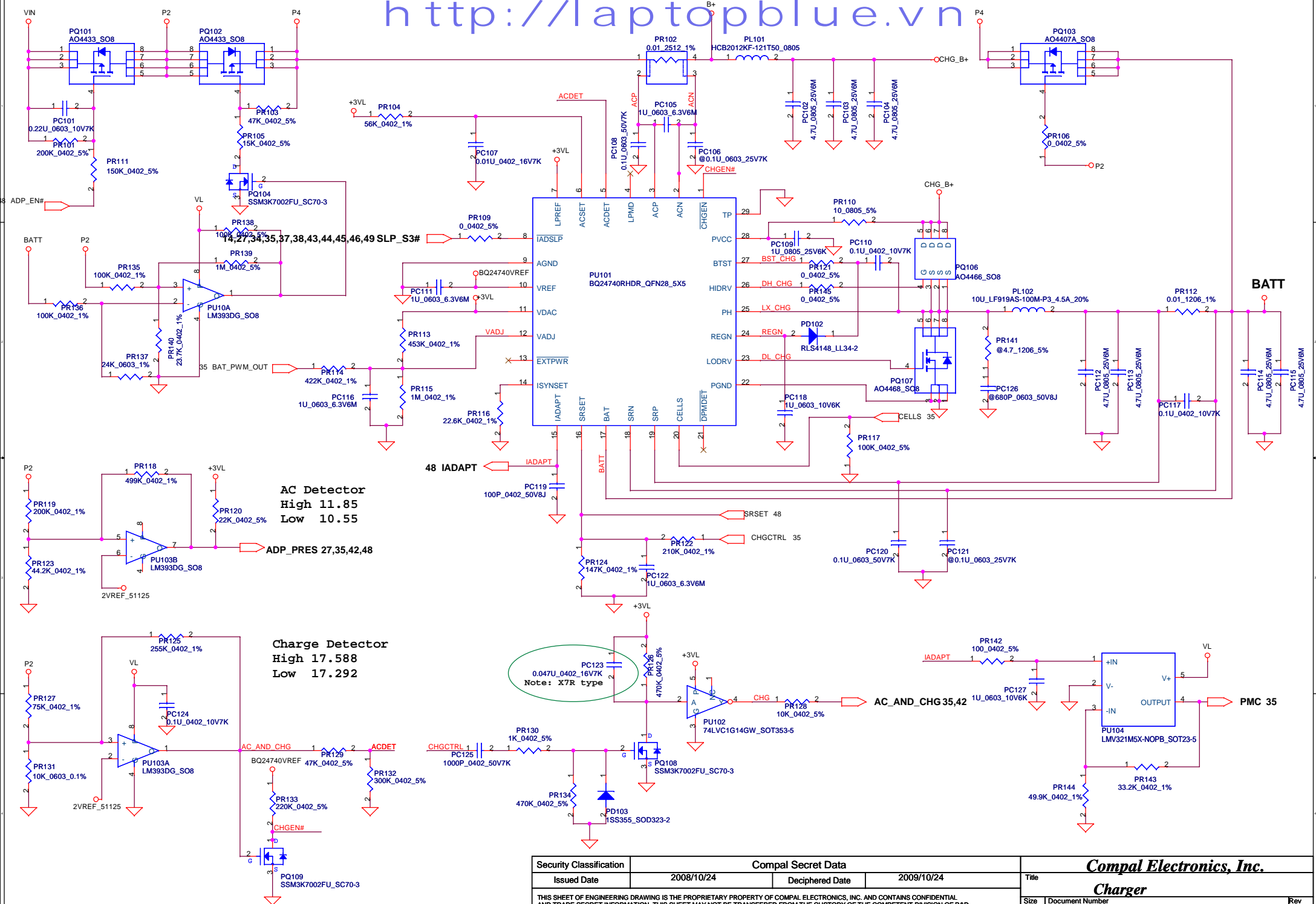
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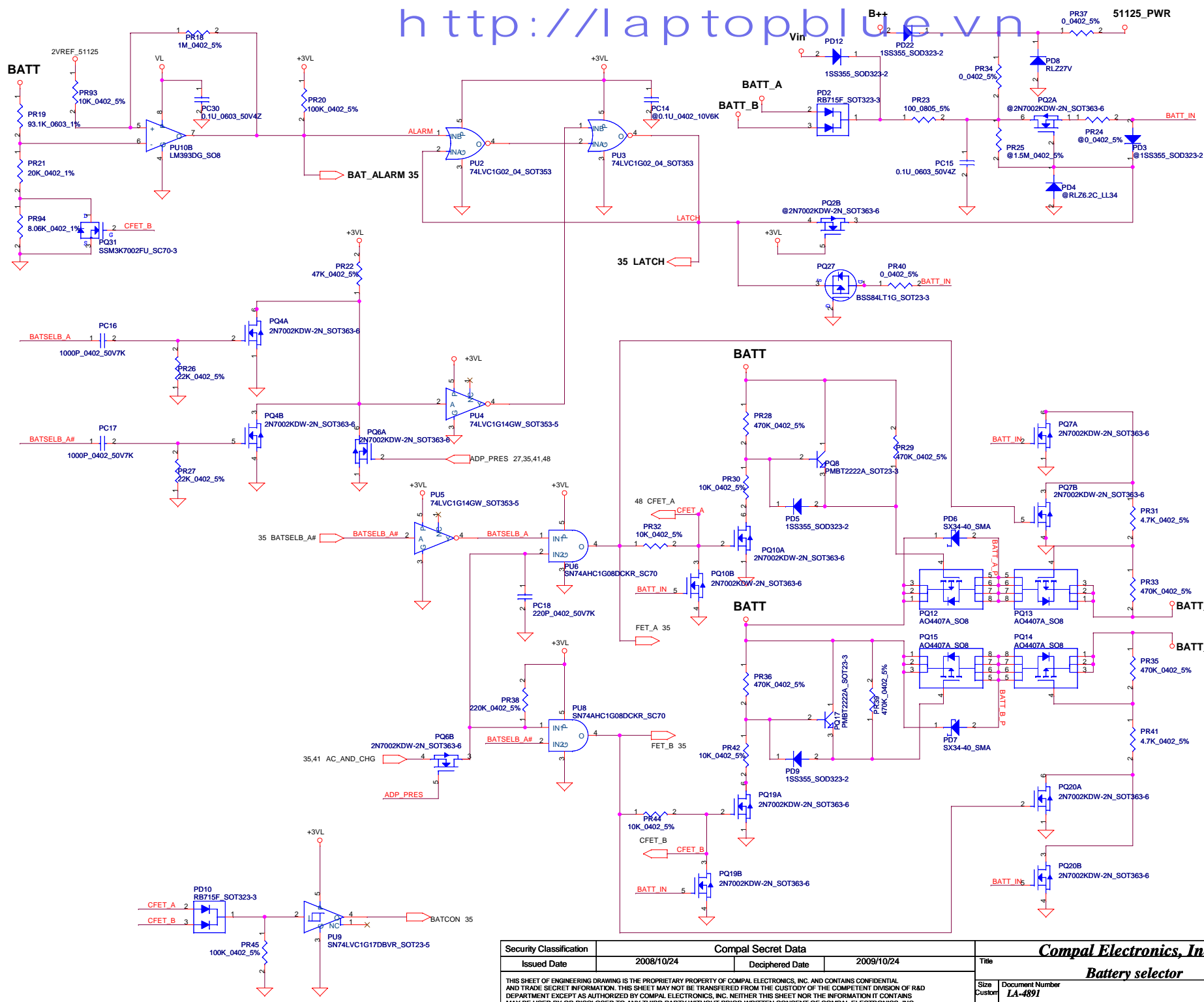


Title			
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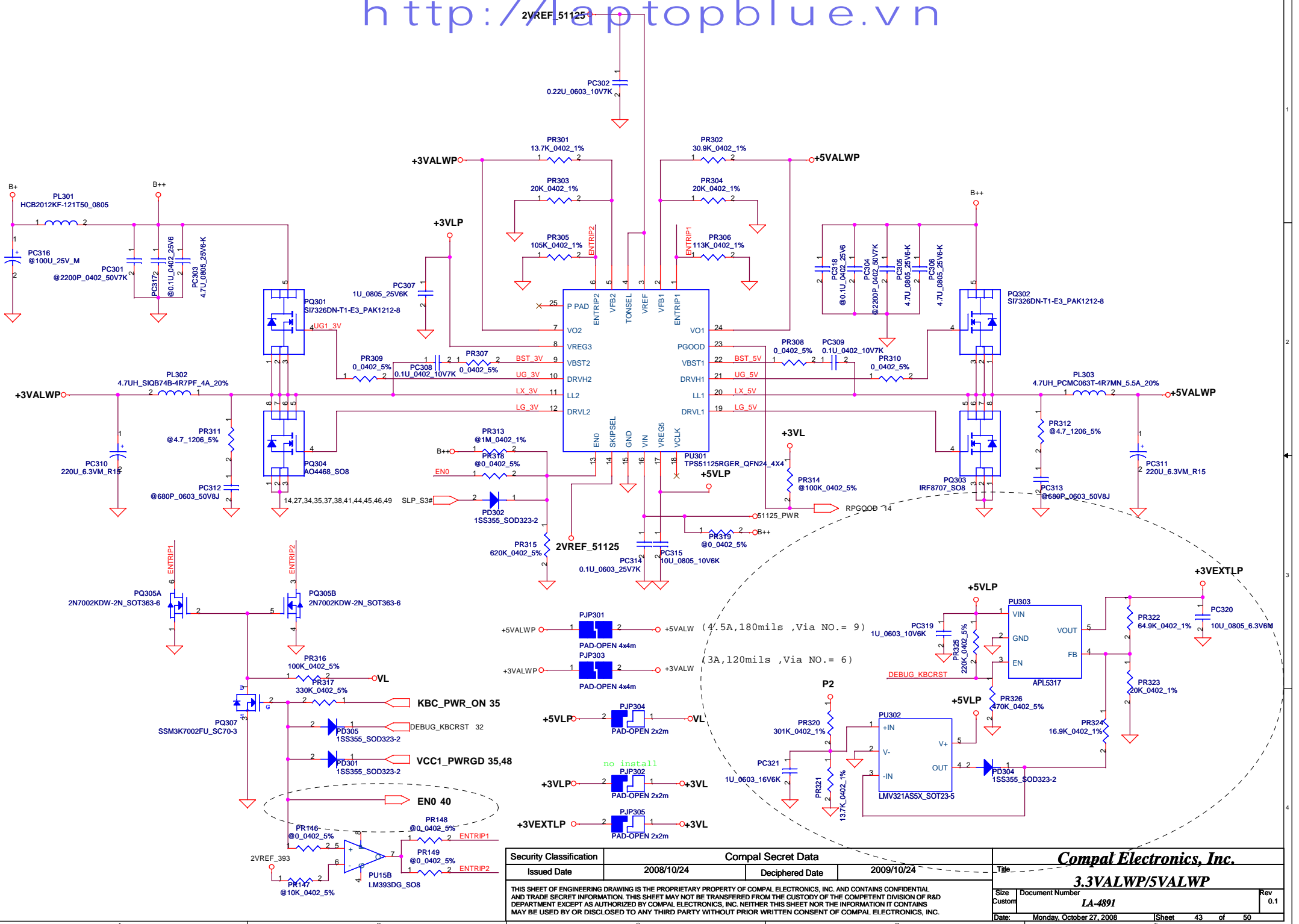


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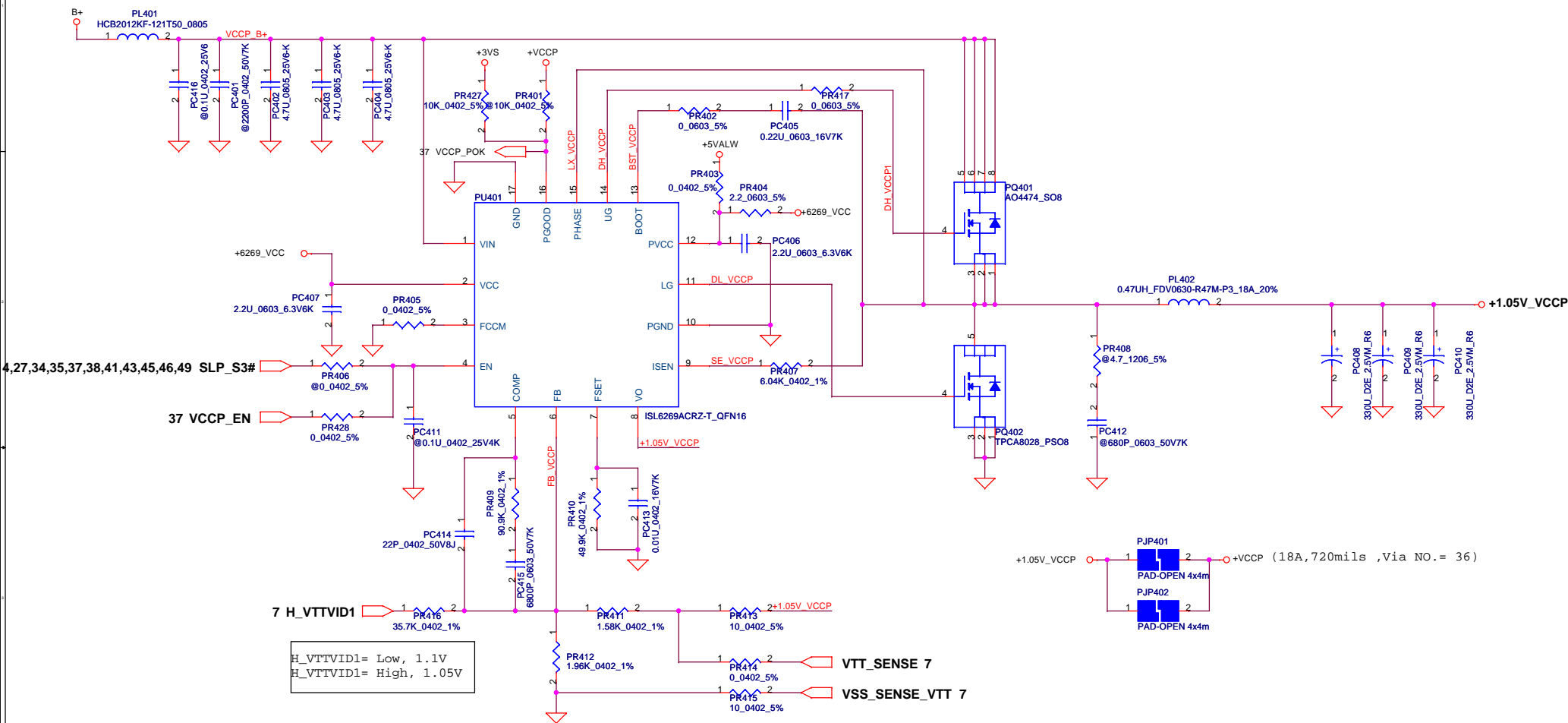
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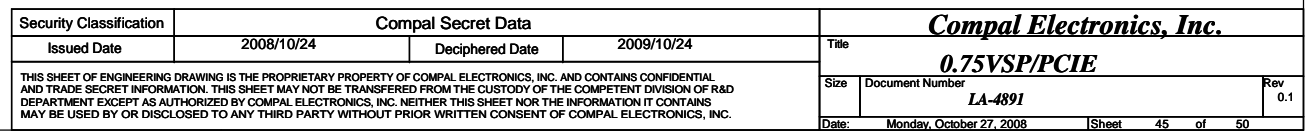
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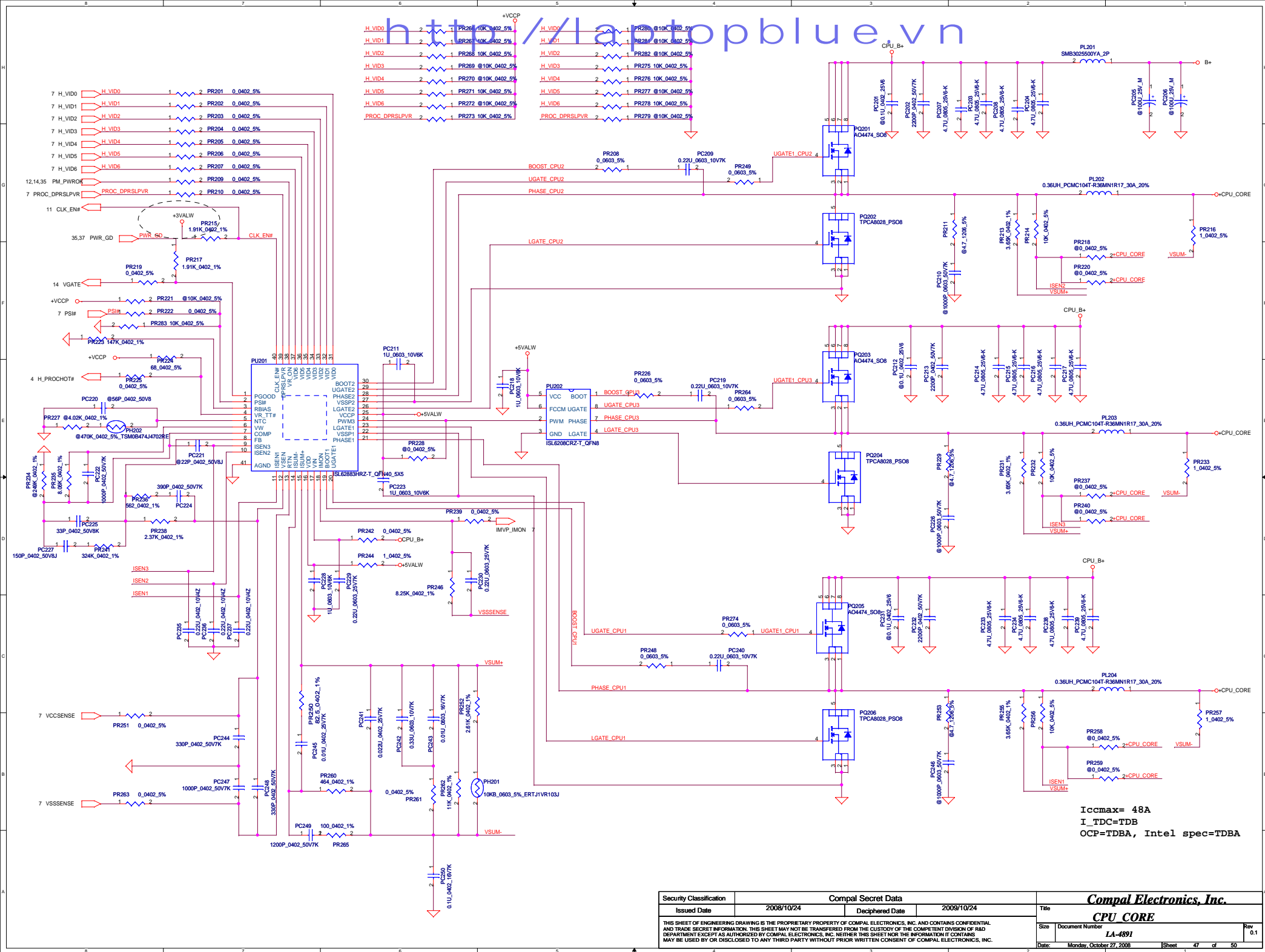
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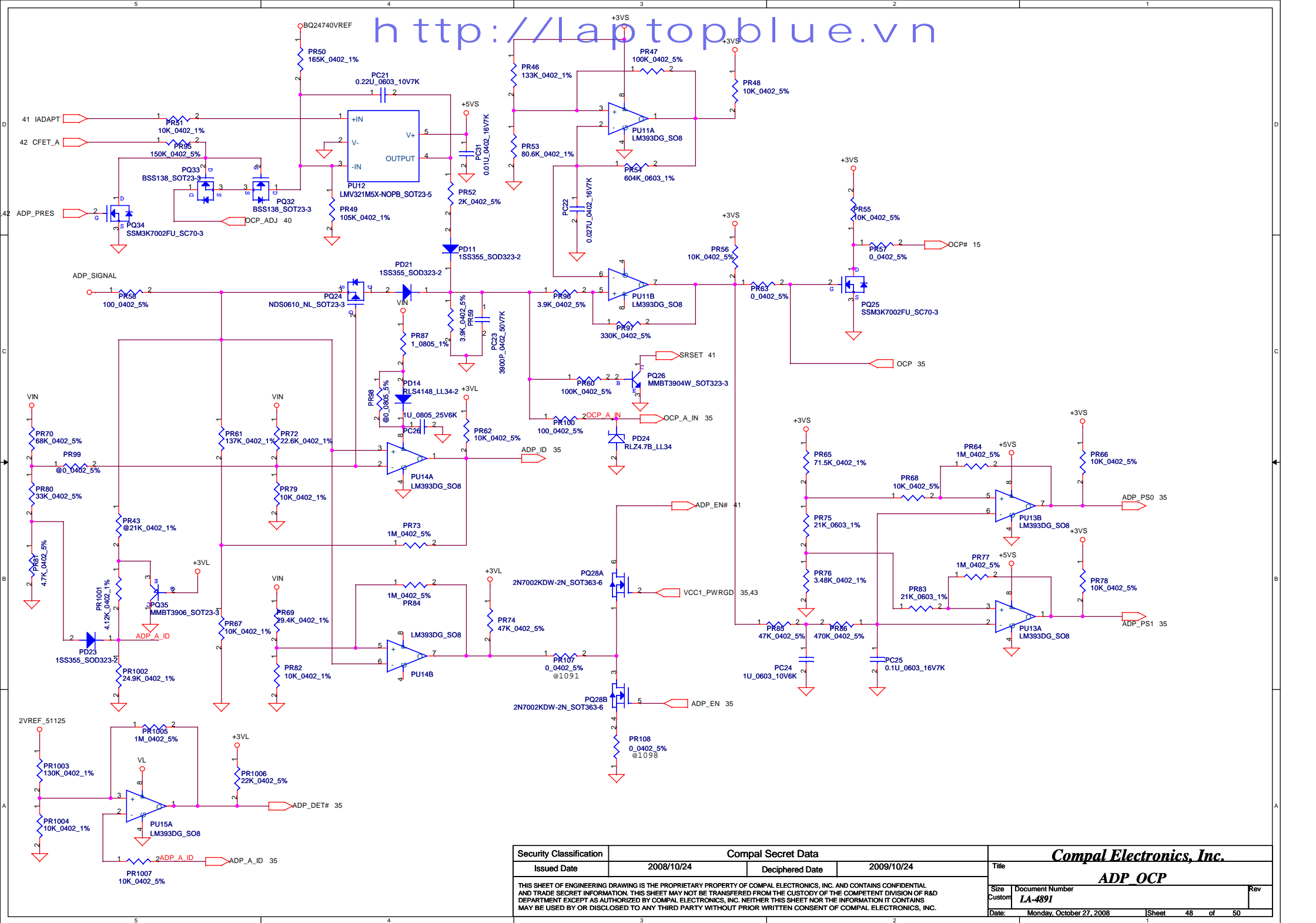


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