

Compal Confidential

Schematics Document

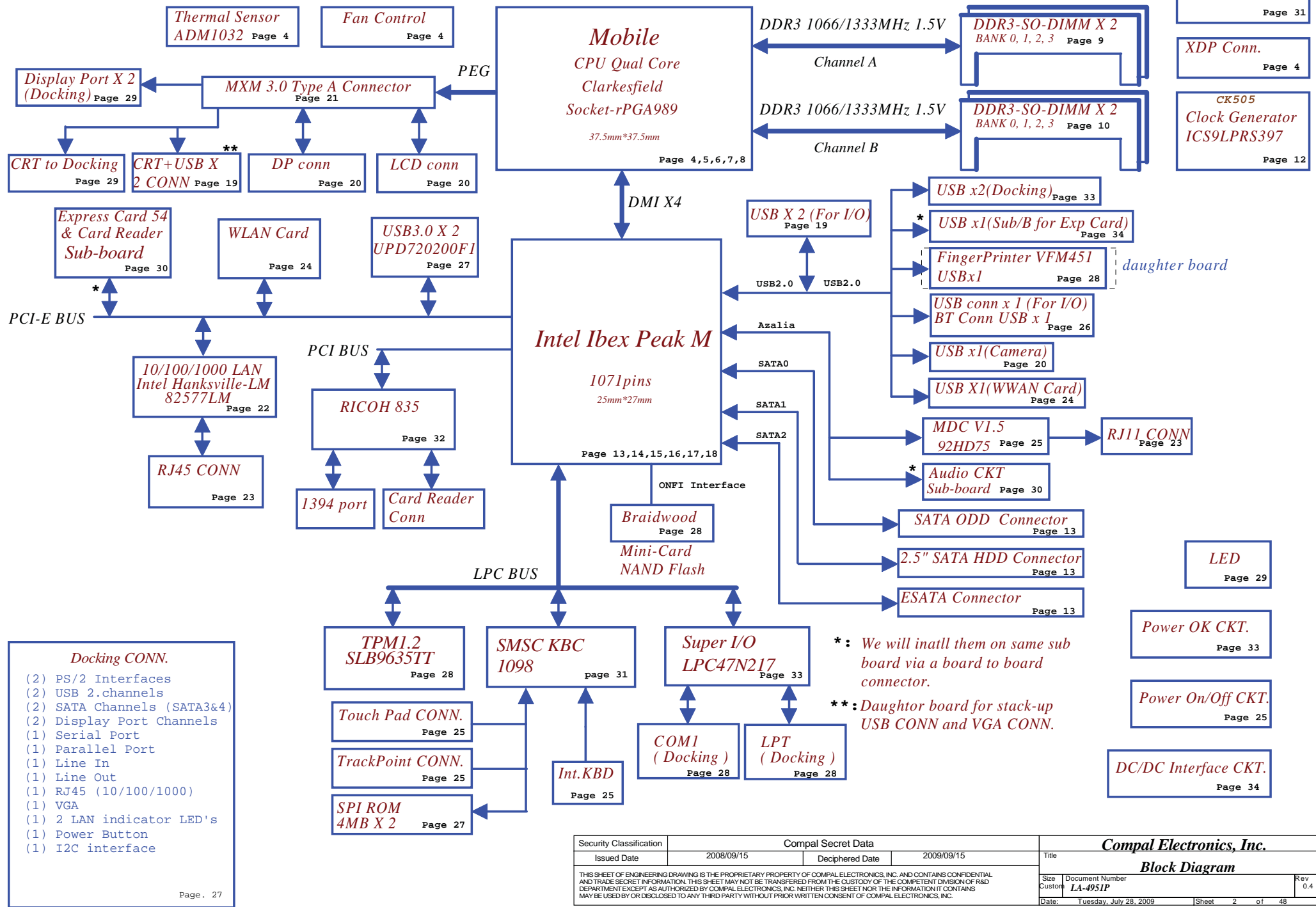
AUBURNDALE/CLARKSFIELD with
Intel IBEX PEAK-M core logic
Versace

2009-07-24

REV:0.4



Security Classification	Compal Secret Data			Title Compal Electronics, Inc.		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Cover Sheet		
<small>THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.</small>				Size	Document Number	Rev
				Custom	LA-4951P	0.4
Date: Tuesday, July 28, 2009				Sheet 1 of 48		

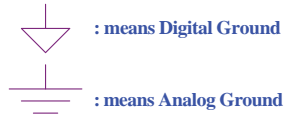


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-4951P
				Date	Rev
				Tuesday, July 28, 2009	0.4
				Sheet	2 of 48

Voltage Rails (O MEANS ON X MEANS OFF)

power plane State	+RTCVCC	+B +3VL	+5VALW +3VALW	+1.5V +0.75V	+5VS +3VS +1.5VS +NVVDD +VCCP +CPU_CORE +1.05VS +1.8VS
S0	O	O	O	O	O
S1	O	O	O	O	O
S3	O	O	O	O	X
S5 S4/AC	O	O	O	X	X
S5 S4/ Battery only	O	O	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X

Symbol Note :



Install below 43 level BOM structure for ver. 0.1

DEBUG@ : means just build when PCIE port 80 CARD function enable. *Remove before MP*

M92@ : Install for M92 Graphic controller

8072@ : Install for 8072 NIC controller

1098@ : Install for 1098 KBC controller

CK32@ : Install for 32 pin CLOCK GEN

Install below 45 level BOM structure for ver. 0.1

45@ : means just put it in the BOM of 45 level.

Reserve below BOM structure for ver. 0.1

@ : means just reserve , no build

CONN@ : means ME part.

M93@ : Install for M93 Graphic controller

8075@ : Install for 8075 NIC controller

1091@ : Install for 1091 KBC controller

CK72@ : Install for 72 pin CLOCK GEN

SMBUS Control Table

	SOURCE	BATT	XDP	SODIMM	CLK CHIP	MINI CARD	DOCK	NIC	THERMAL SENSOR	G-SENSOR
SMB_EC_CK1 SMB_EC_DA1	SMSC1098	V	X	X	X	X	X	X	X	X
SMBCLK SMBDATA	Calpella	X	V	V	V	V	V	X	X	X
SML0CLK SML0DATA	Calpella	X	X	X	X	X	X	V	X	X
SML1CLK SML1DATA	Calpella	X	X	X	X	X	X	X	V	V

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	Notes List
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number LA-495IP
				Date:	Tuesday, July 28, 2009
				Sheet	3 of 48
				Rev	0.4

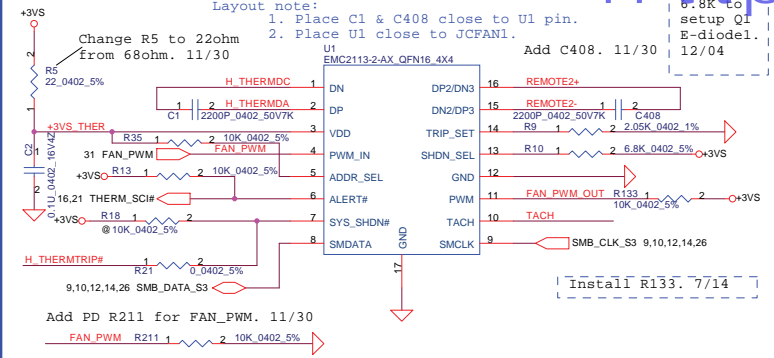
Thermal Sensor EMC2113 with CPU PWM FAN

Layout note:

1. Place C1 & C408 close to U1 pin.
2. Place U1 close to JCFAN1.

Add C408. 11/30

Change R5 to 22ohm
from 68ohm. 11/30



Change Q24.2 connect from +3VS to PWR_GD_12/13/34

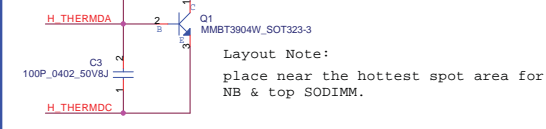
REMOTE thermal sensor

Layout note:

1. Place Q1 close to bottom DDR DIMM.



Layout Note:

place near the hottest spot area for
NB & top SODIMM.



Intel S3

Change from +1.5V.

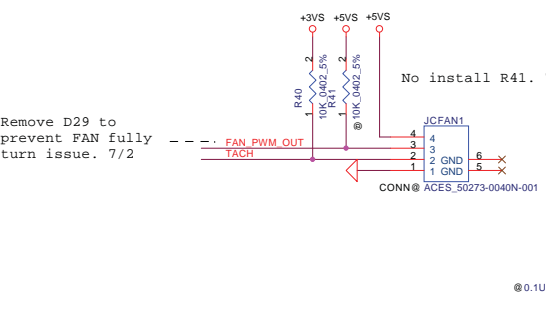
11 VCCP_1.5VSPWRGD  R33 2 1 1.5K 0402 1%  R34 2 1 750 0402 1% VDD1

Change R33, R34 value. 7/10

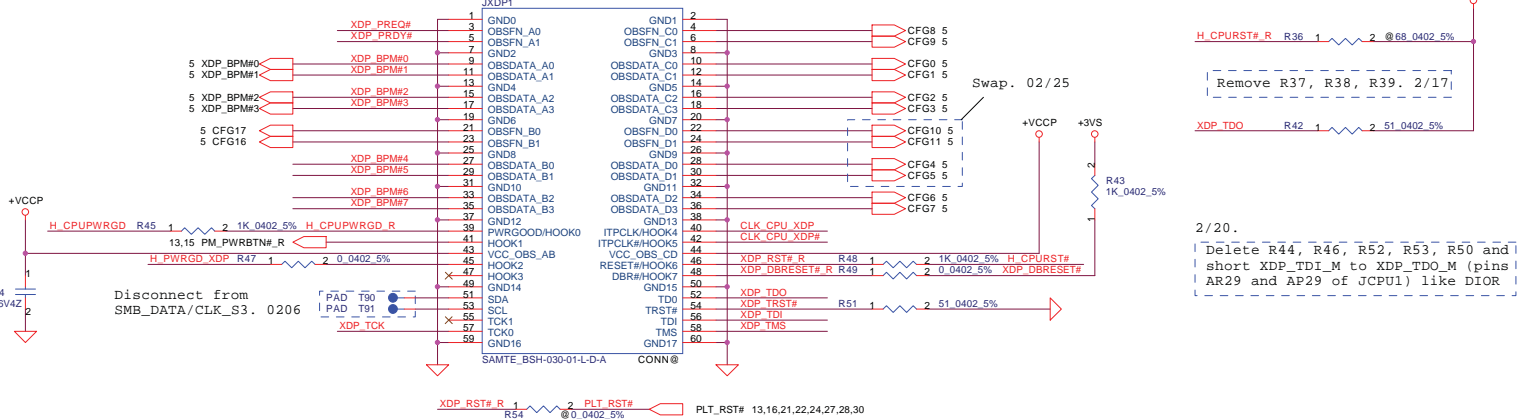
Intel S3

VDDPWGOOD_R R751 2 1 @ 1.1K 0402 1% +1.5VS_CPU_VDDQ

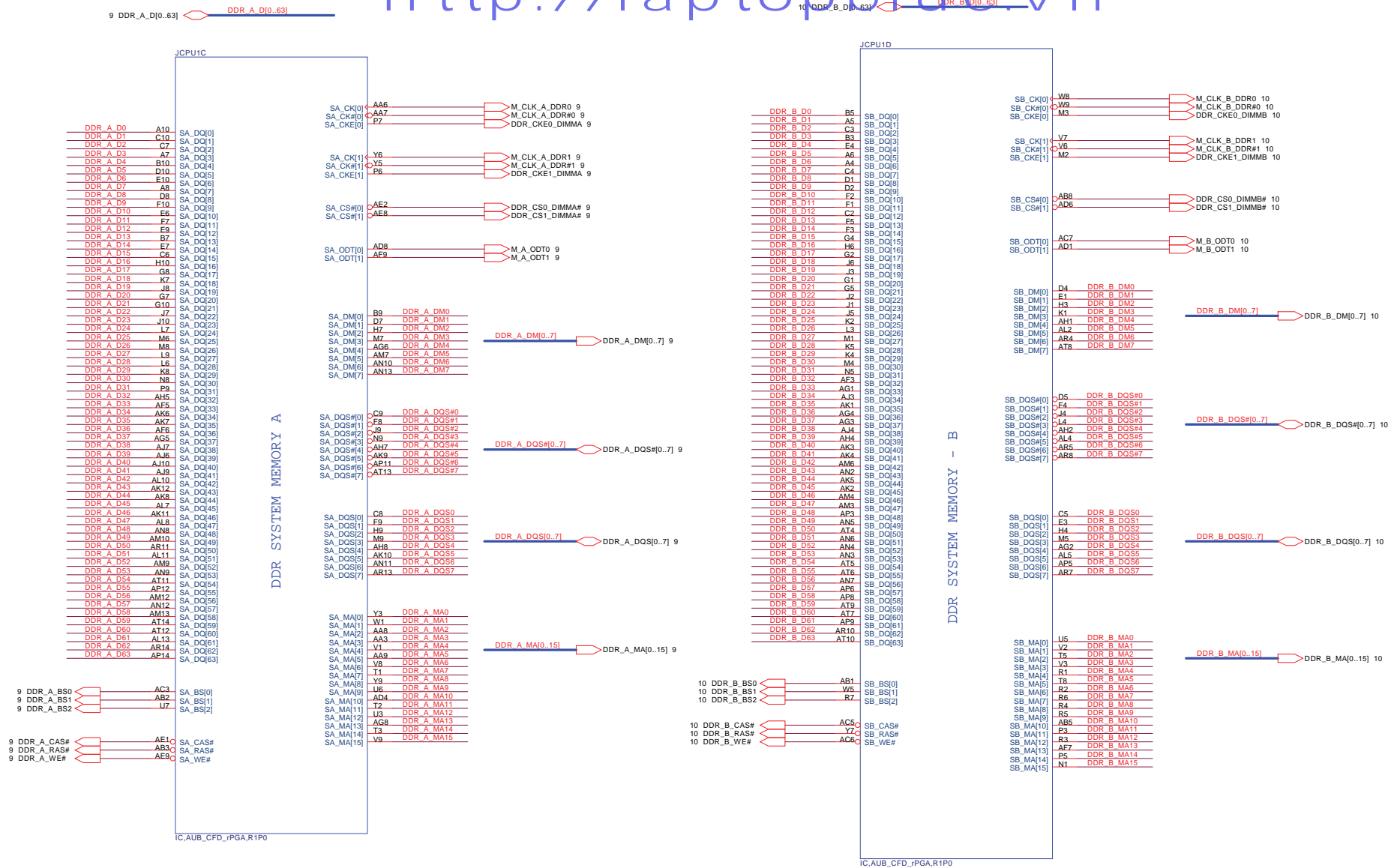
FAN Connector



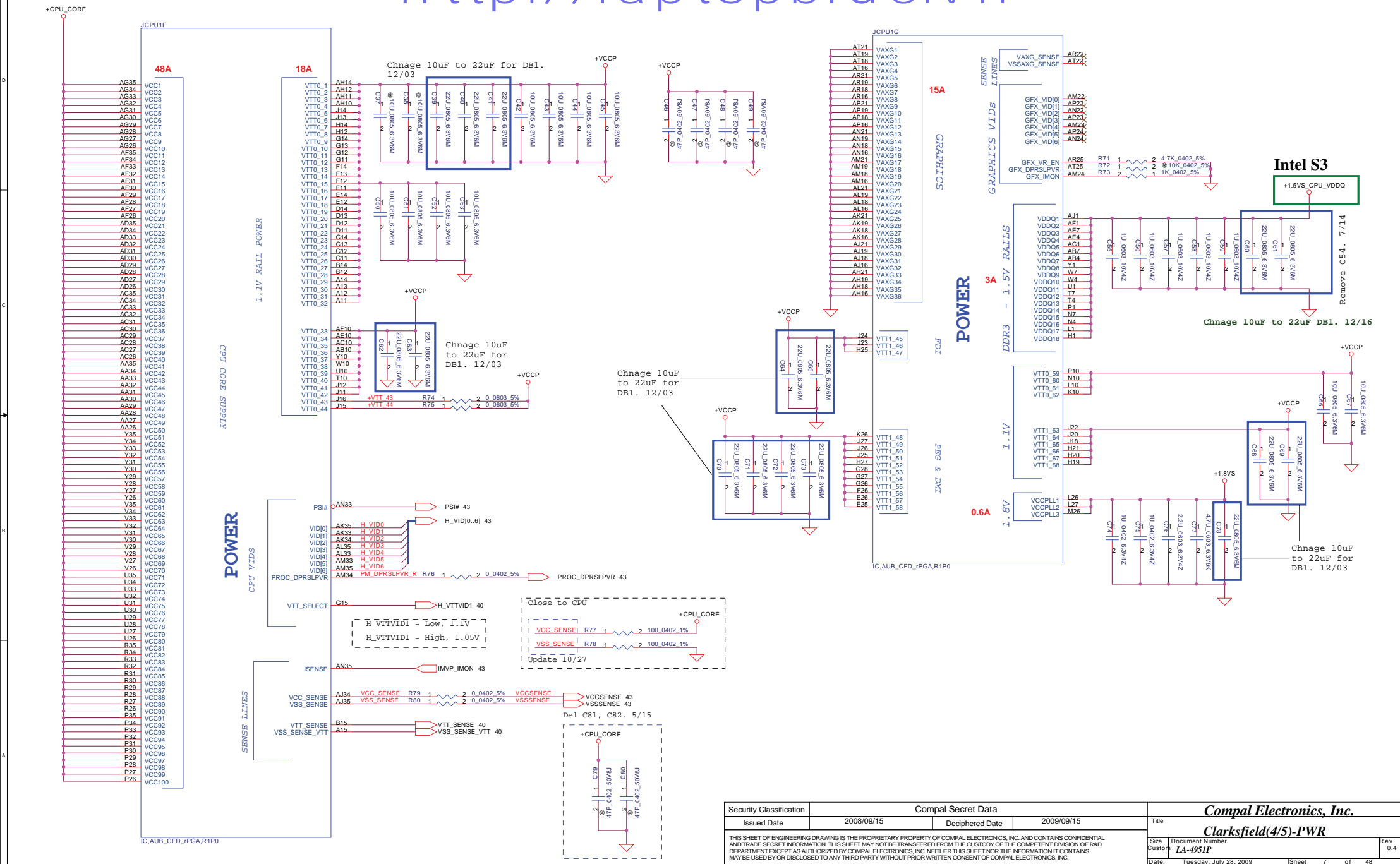
XDP Connector

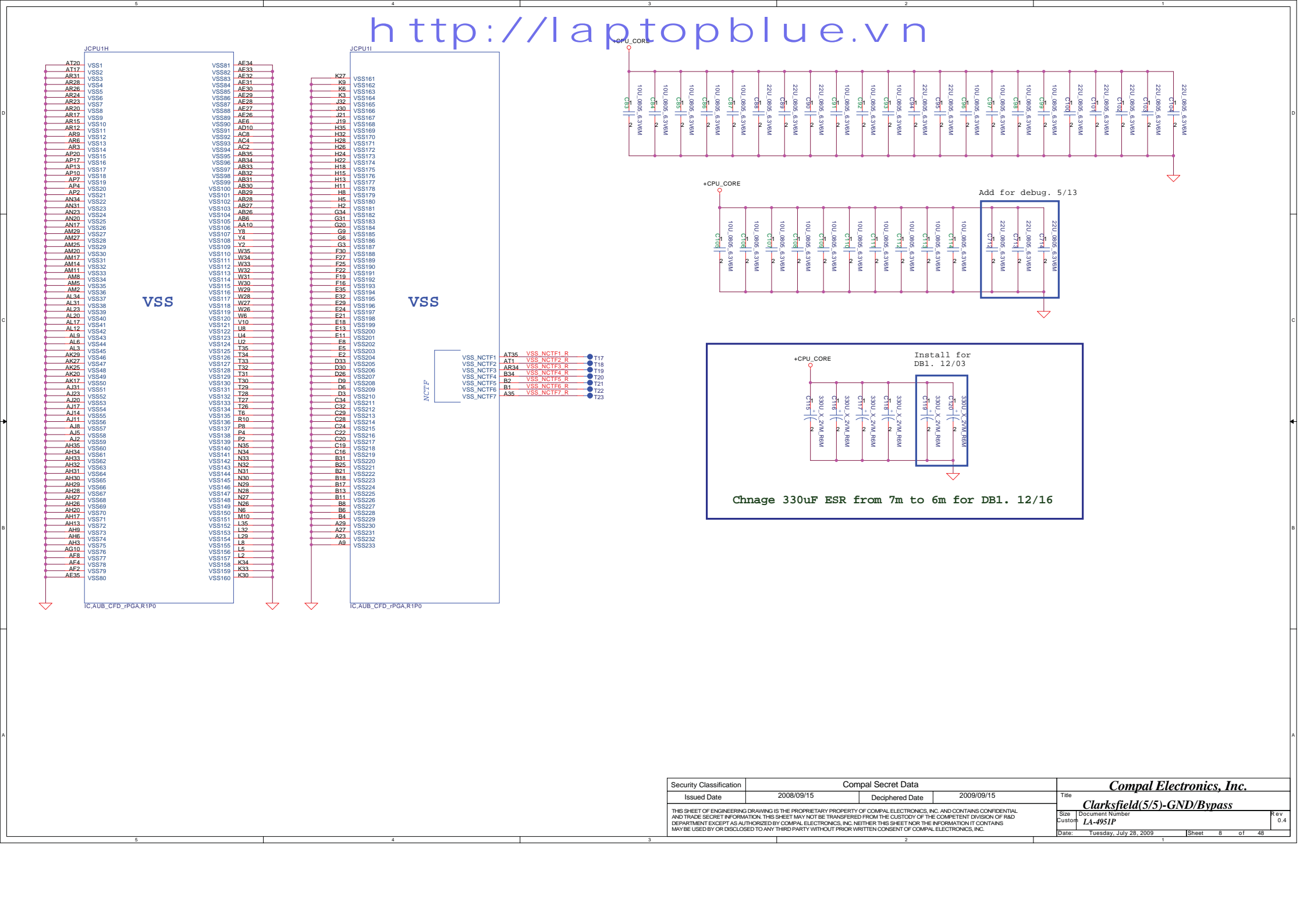
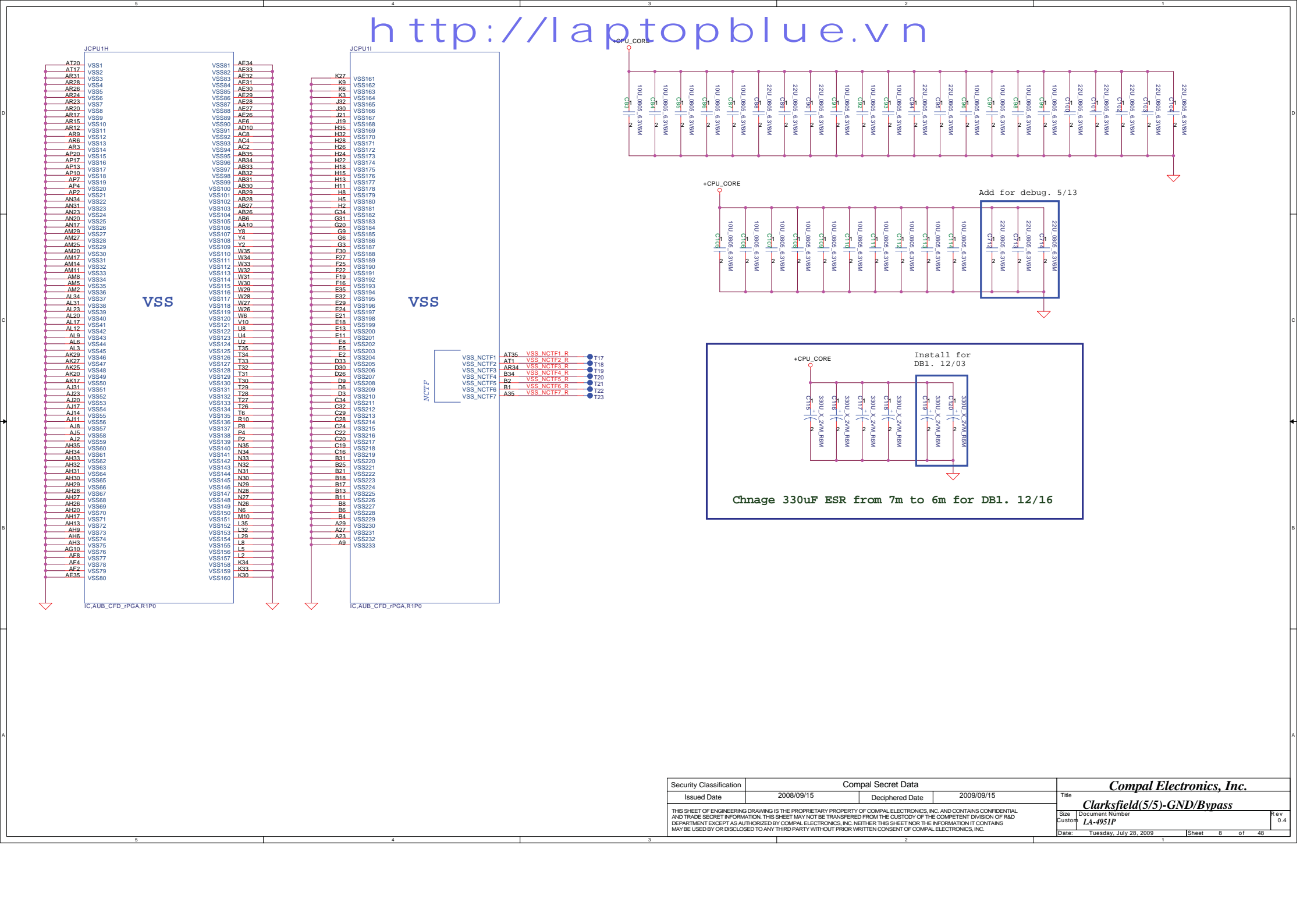
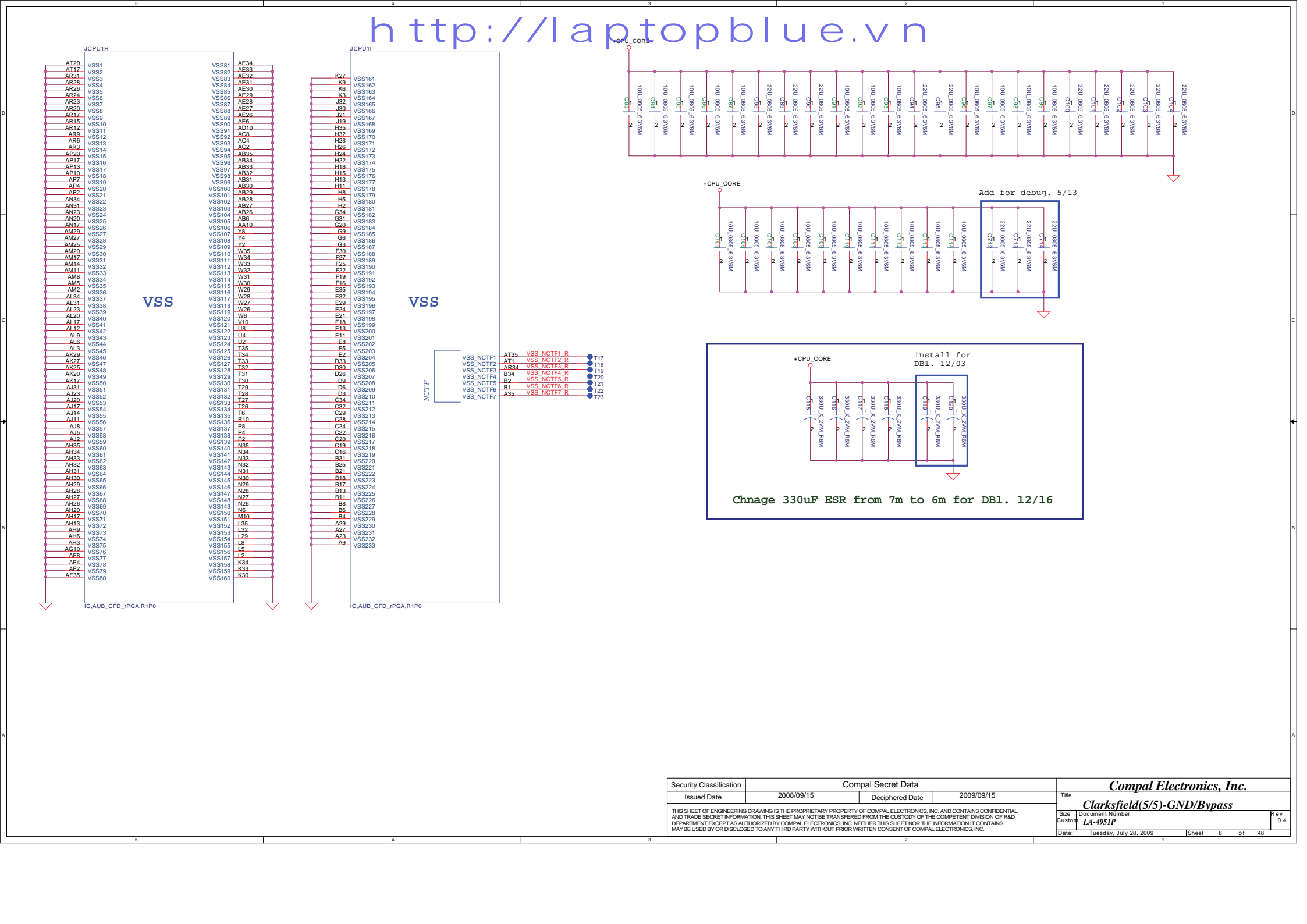


Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	Clarksfield(1/5)-Thermal/XDP	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custom	LA-4951P	0.4
Date:		Tuesday, July 28, 2009		Sheet	4 of 48	



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	Clarksfield(2/6)-DDR3 A/B CH
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number LA-4951P
				Date	Tuesday, July 28, 2009
				Sheet	6 of 48
				Rev	0.4





http://laptopblue.vn

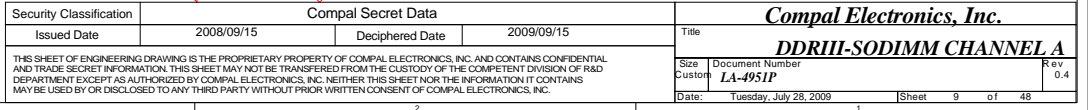
The diagram shows a detailed PCB layout for a laptop motherboard, divided into three main sections: CPU1H, CPU1I, and CPU CORE. Each section contains numerous components, primarily capacitors, and is connected to a common ground plane labeled VSS. The CPU1H and CPU1I sections also feature NCTF components. The CPU CORE section includes a CPU CORE component and a DB1 component. The layout is annotated with various labels and notes, including 'Add for debug. 5/13' and 'Chnage 330uF ESR from 7m to 6m for DB1. 12/16'. A legend on the right side of the diagram lists components and their locations, such as AT35, VSS, NCTF1, R, T17, and T23.

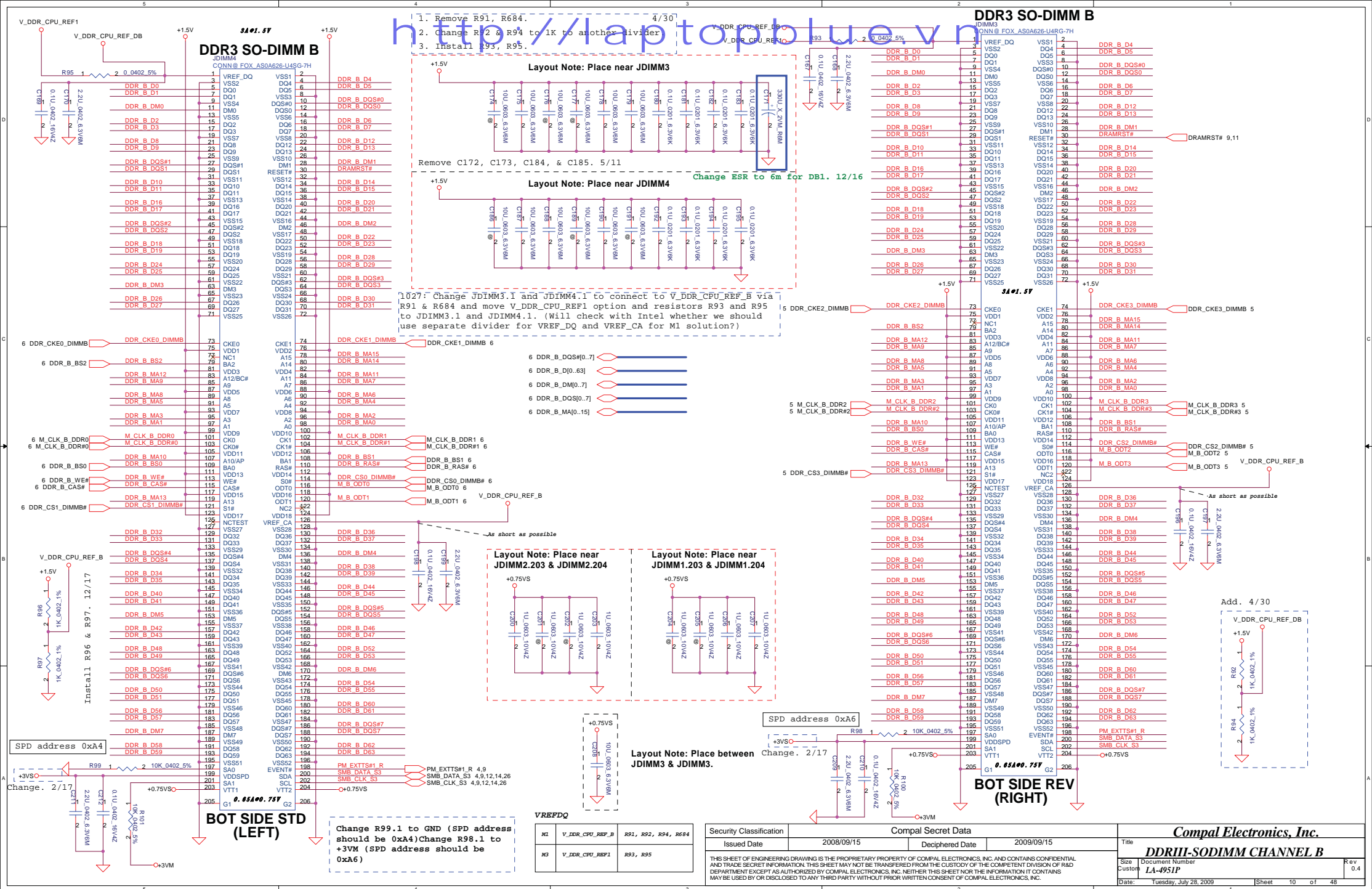
Legend:

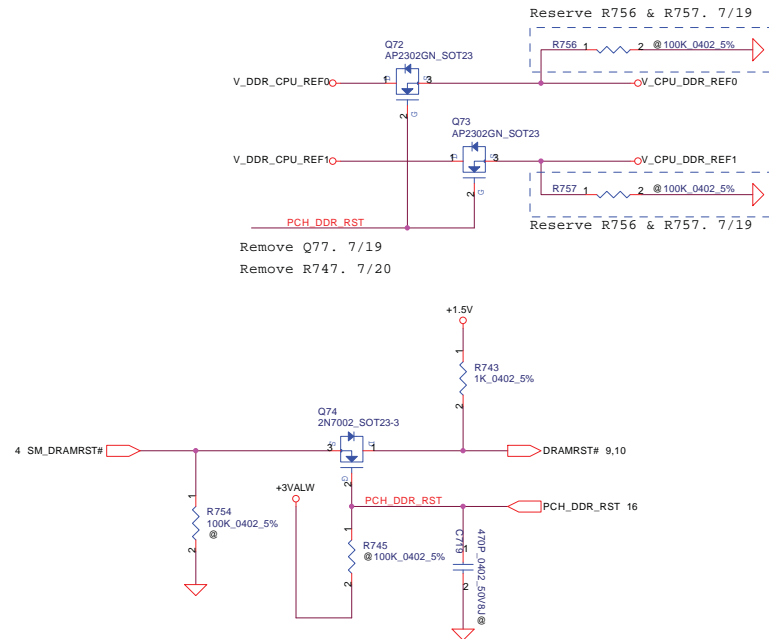
Component	Location
AT35	VSS NCTF1 R
AT1	VSS NCTF2 R
AR34	VSS NCTF3 R
B34	VSS NCTF4 R
B2	VSS NCTF5 R
B1	VSS NCTF6 R
A35	VSS NCTF7 R
T17	
T18	
T19	
T20	
T21	
T22	
T23	

Notes:

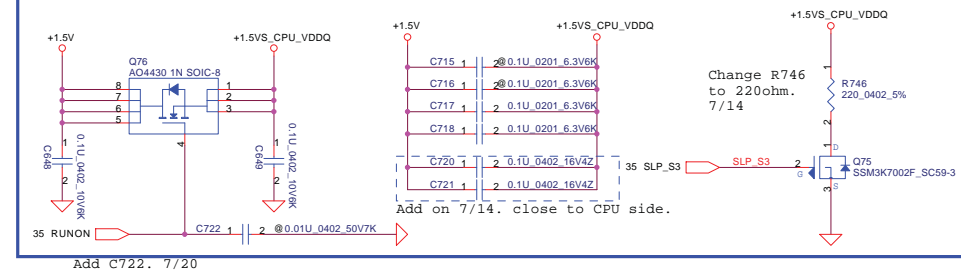
- Add for debug. 5/13
- Chnage 330uF ESR from 7m to 6m for DB1. 12/16



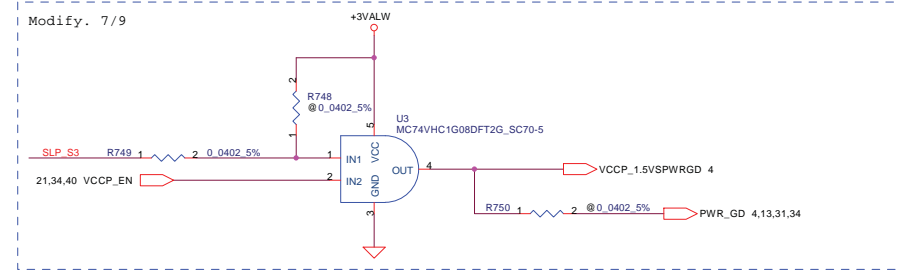




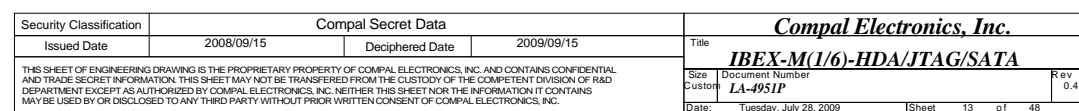
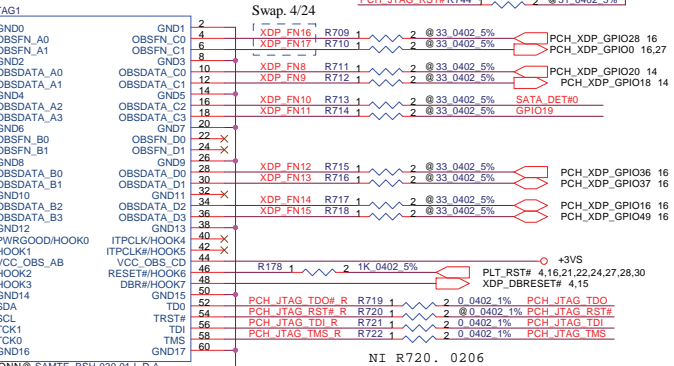
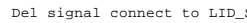
+1.5V to +1.5VS_CPU_VDDQ Transfer

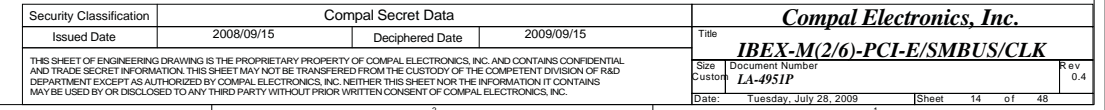


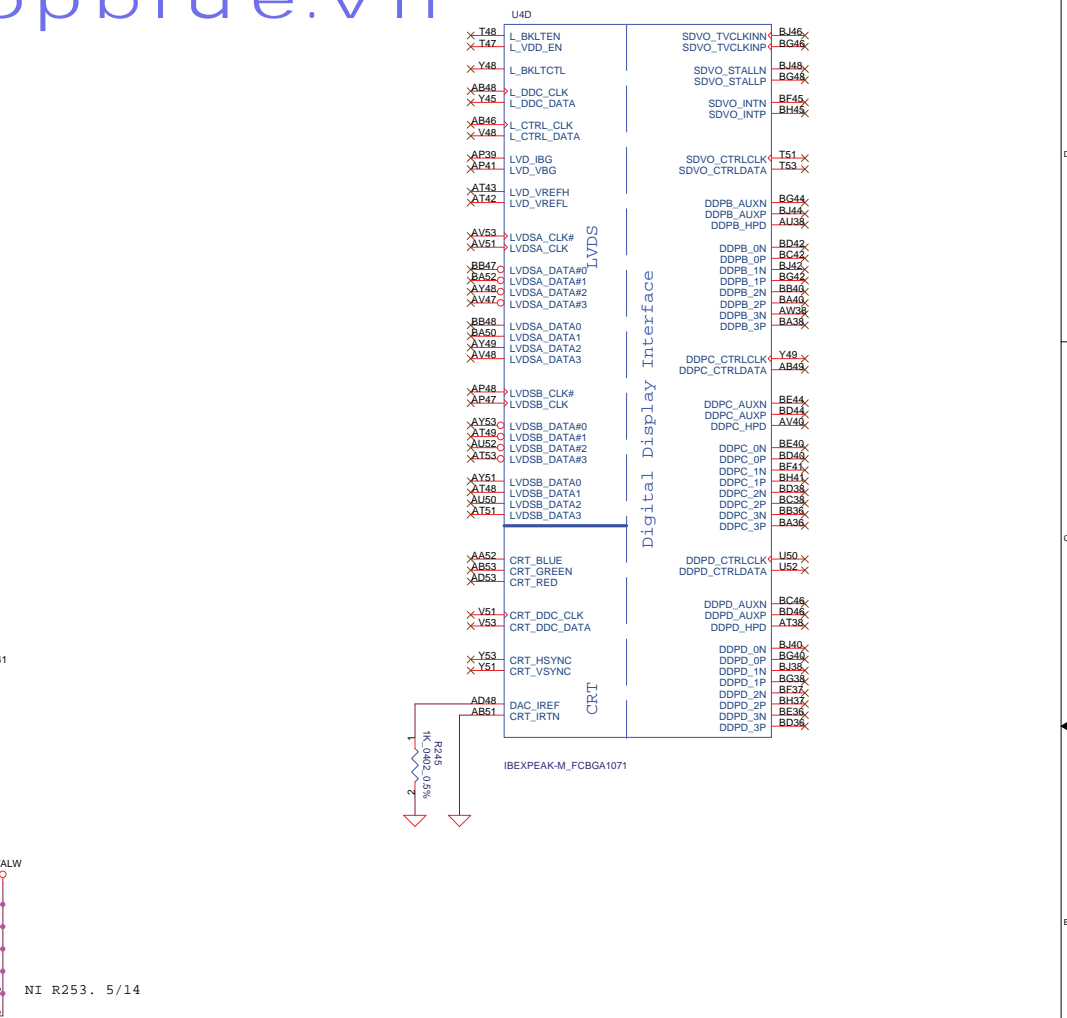
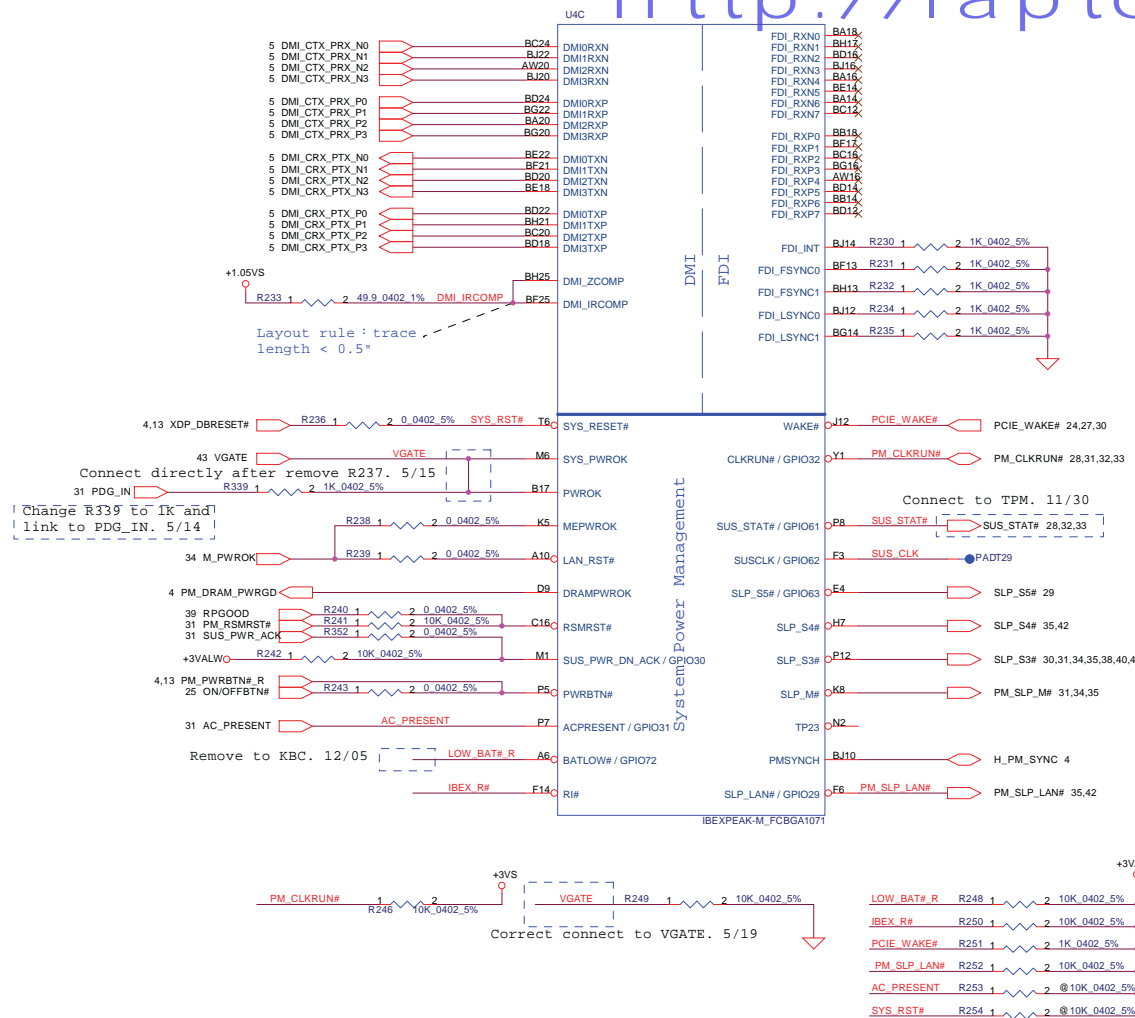
Modify. 7/9

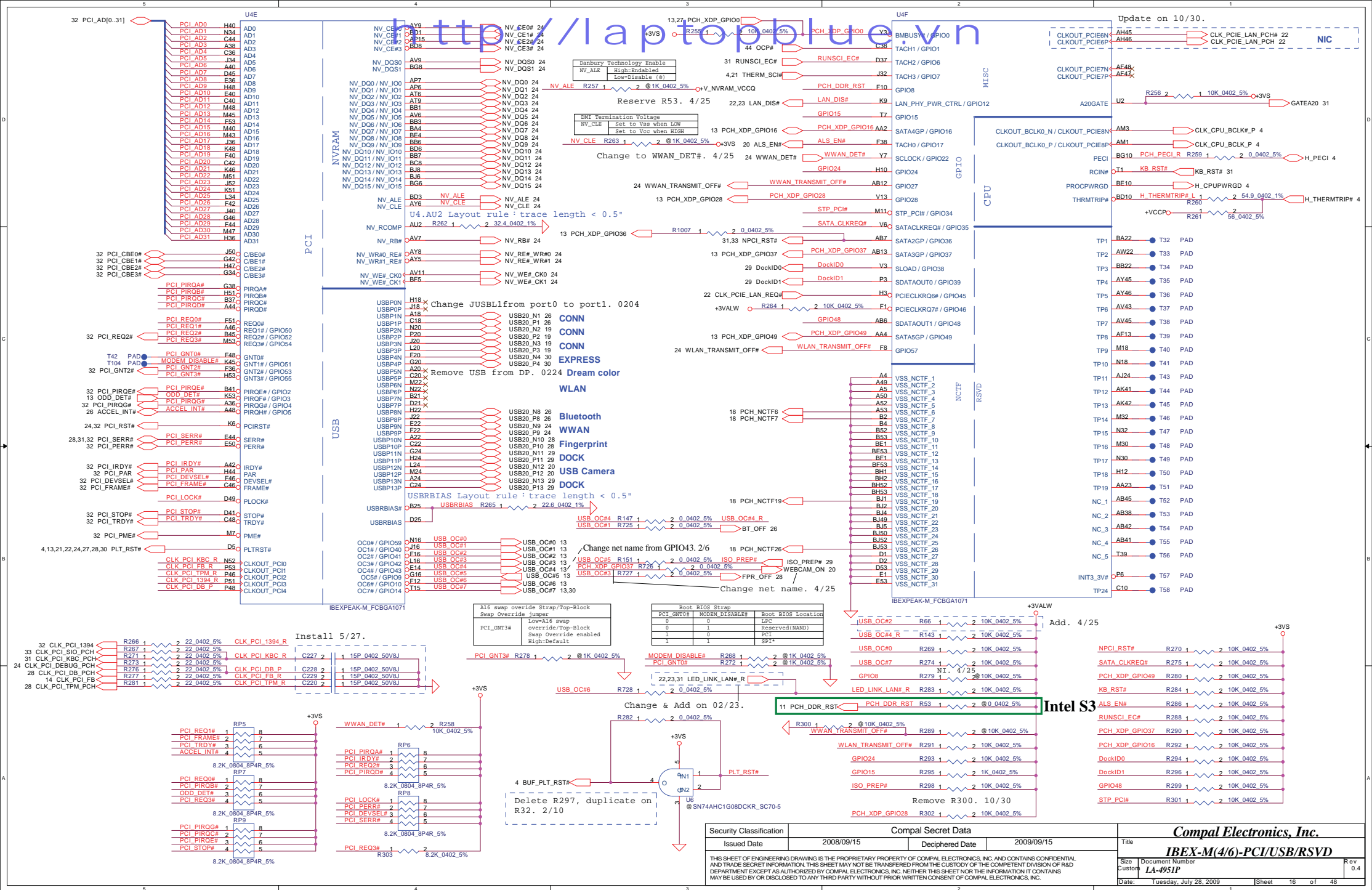


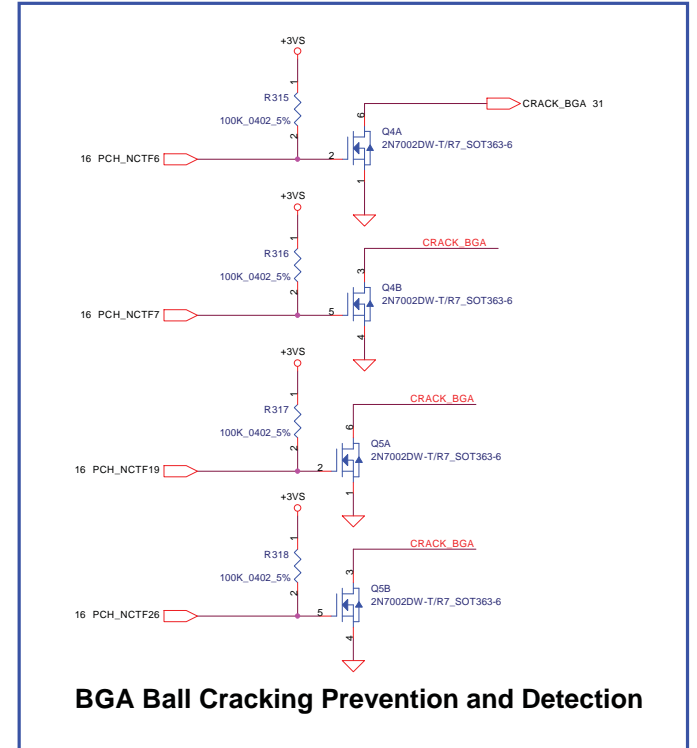
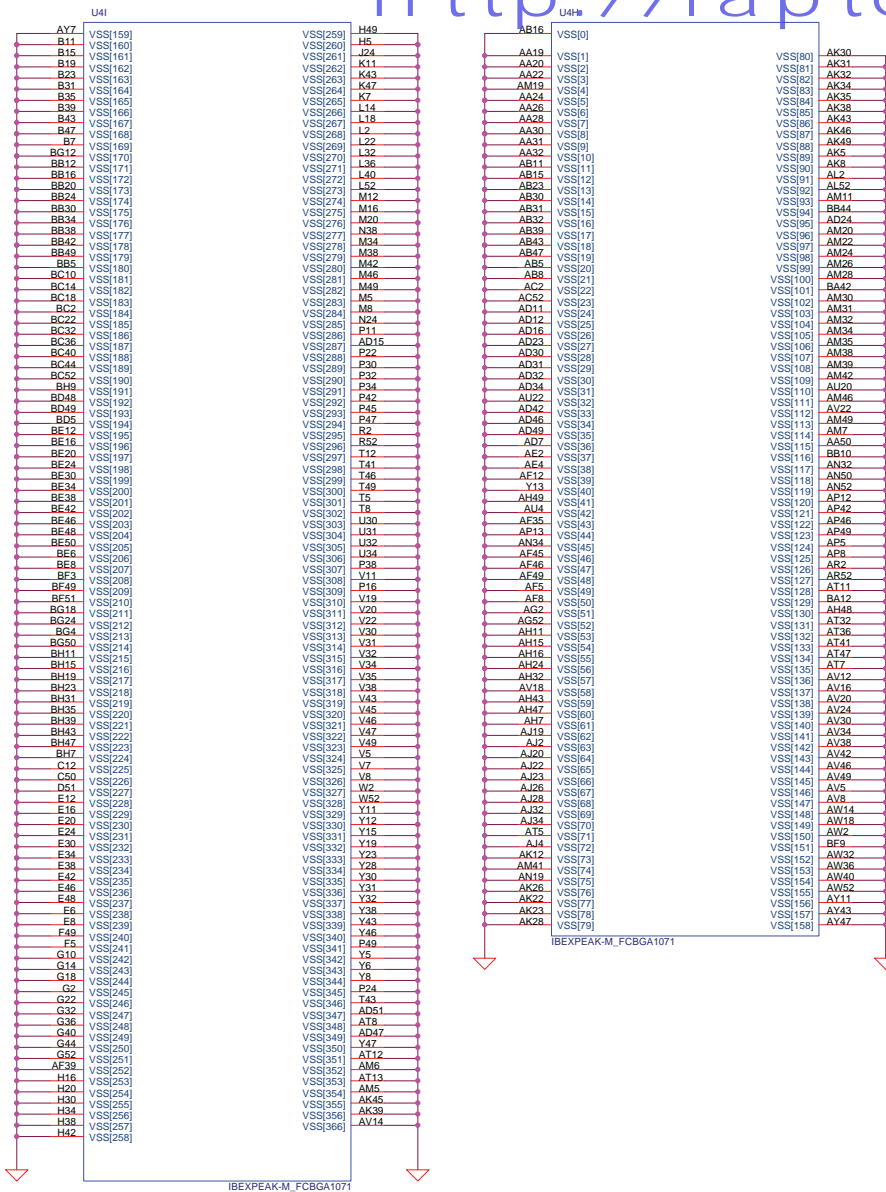
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Intel S3 power saving	
				Size Custom	Document Number LA-4951P
				Date: Tuesday, July 28, 2009	Rev 0.4
				Sheet 11 of 48	



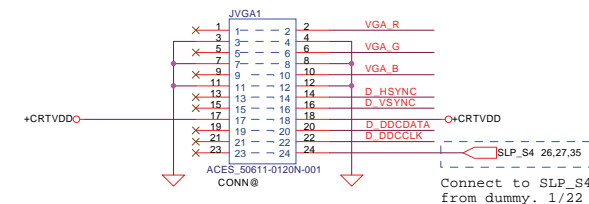
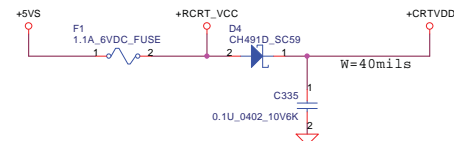




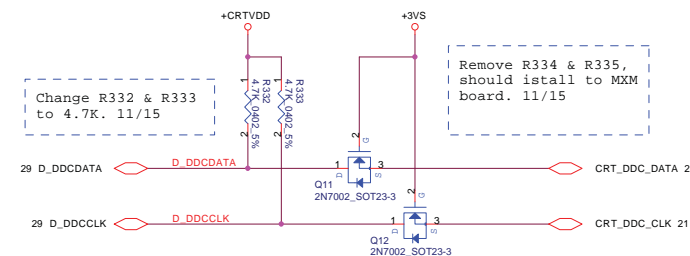
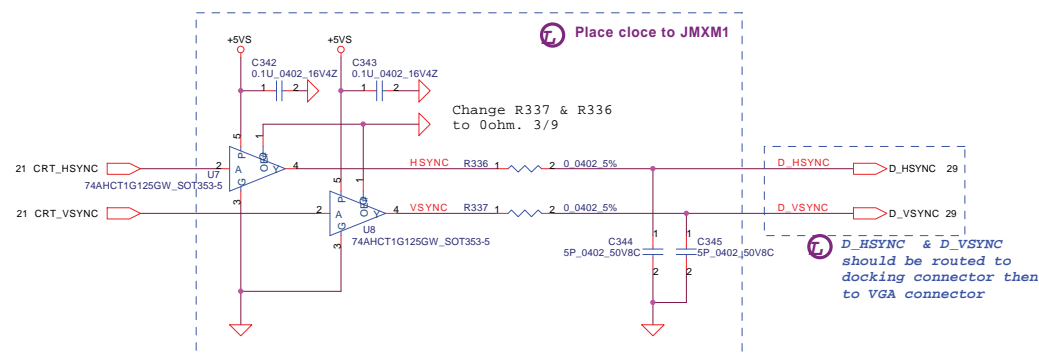
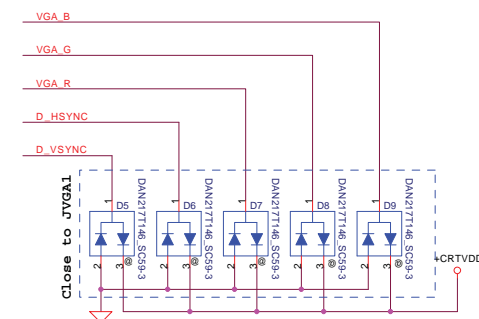
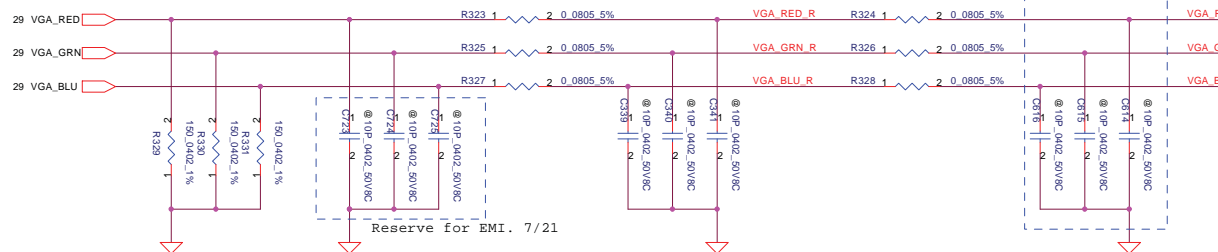




Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	IBEX-M(6/6)-GND
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number LA-4951P
				Date	Tuesday, July 28, 2009
				Sheet	18 of 48



Add C614~C616 for Nvidia request.
11/15

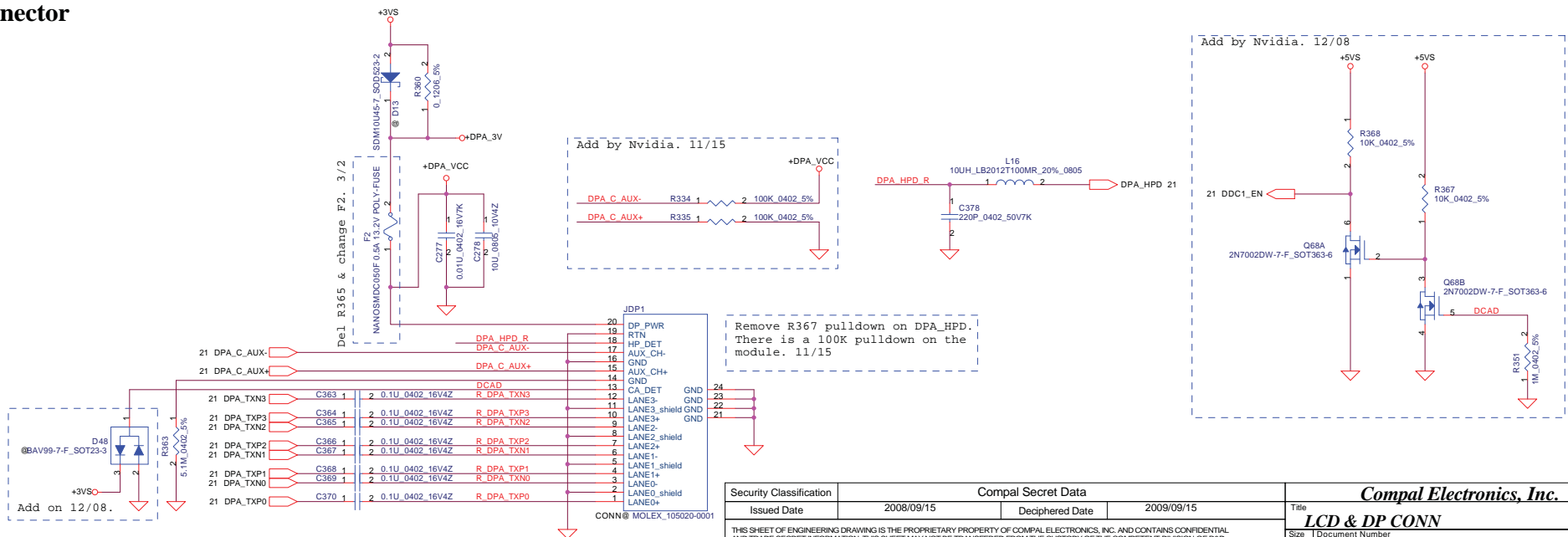


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	CRT & USB Connector	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev. 0.
				LA-4951P		
Date: Tuesday, July 28, 2009				Sheet	19	of 48

http://laptopblue.vn



Security Classification	Compal Secret Data			<i>Compal Electronics, Inc.</i>		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	<i>LCD & DP CONN</i>	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number <i>LA-495P</i>	Rev 0.4
				Date:	Tuesday, July 28, 2009	Sheet 20 of 48



Remove Q19 & R376. 2/24
Remove C377. 7/10

Remove Q19 & R376. 2/24
Remove C377. 7/10

31,35,38,44 ADP_PRES 2 Q69
2N7002 SOT23-5

31,35,38,44 ADP_PRES 2 Q69
2N7002 SOT23-5

Dream Color Panel

Docking

Change to dummy. 12/2

Remove R387. 02/04 PEG_CLK

TH_OVERT# R391 1 2 10K_0402_5%

ADMI_CEC	R394	1	2	10K 0402 5%
PRST_L#	R396	1	2	100K 0402 5%
PRST_P#	R397	1	2	100K 0402 5%

Change R387, R391, R393, and R394 to 10K. 11/11

NI R387, because have PR120 PU to +3VL. 12/05

Change R394 to NI and install R381 & R382. 12/0

Deciphered Date	2006/03/10	Title
-----------------	------------	-------

Size	Document Number
	LA-4951P

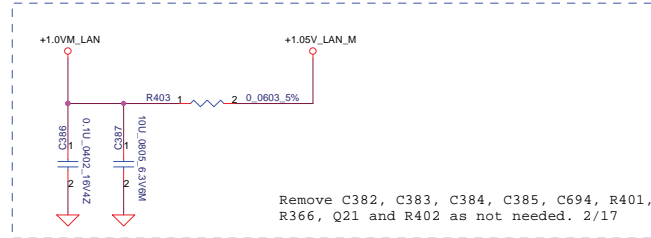
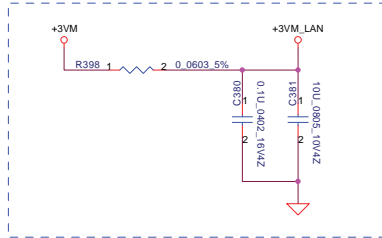
Date: Tuesday, Jul	
2	

Compal Electronics, Inc.

AM-III CONT

day, July 28, 2009

Update on 10/27.



Remove C382, C383, C384, C385, C694, R401, R366, Q21 and R402 as not needed. 2/17

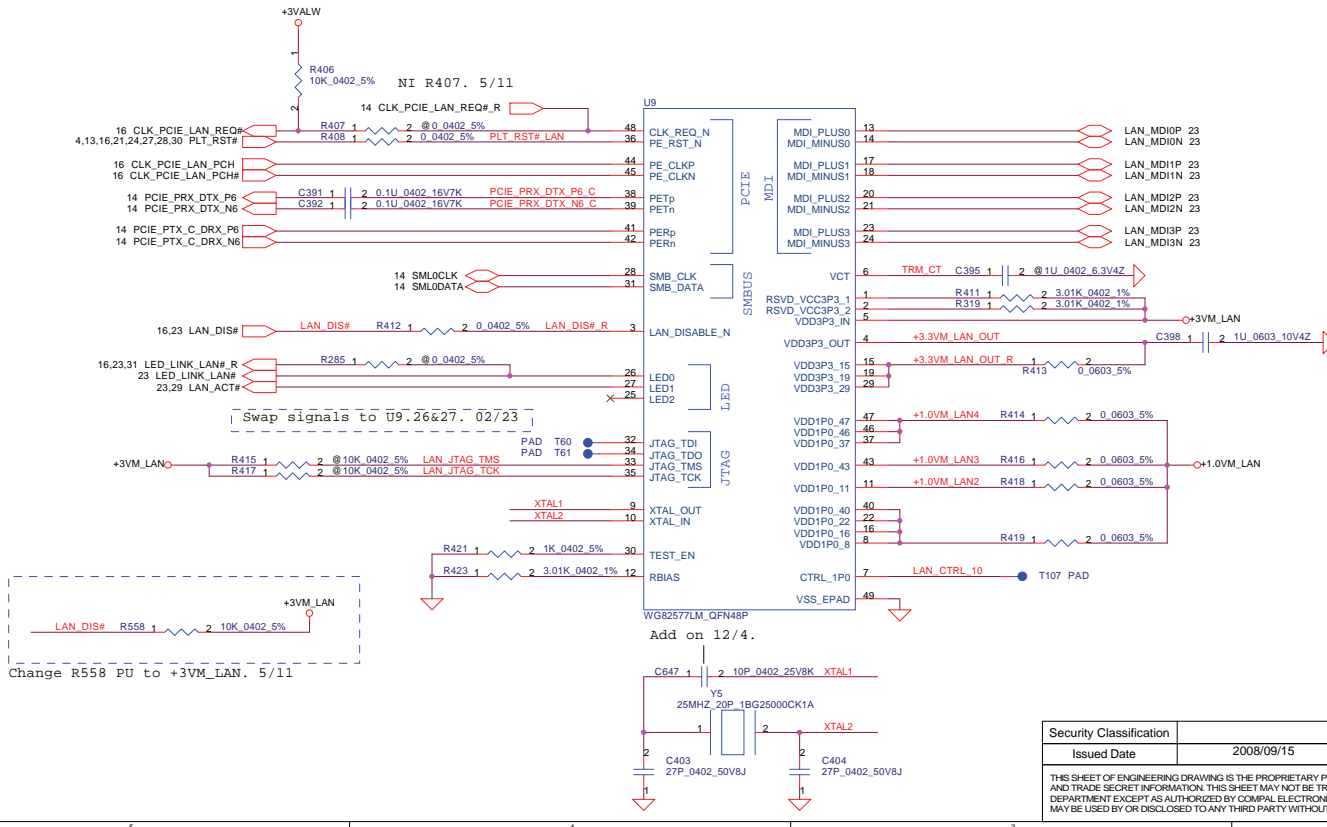
Remove note. 0205

Remove C388, C389, C390, & C393 on +1.0VM_LAN; Remove C394, C396 & C397 on +3.3VM_LAN_OUT_R; Remove C399 & C400 on +1.0VM_LAN4; Remove C401 on +1.0VM_LAN3; and Remove C402 on +1.0VM_LAN2 as done in Intel's RedFort CRB

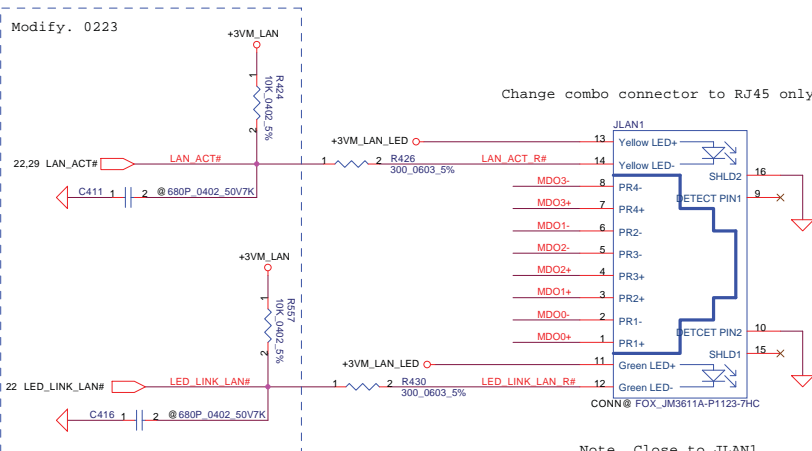
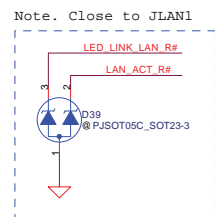
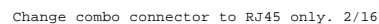
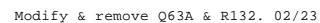
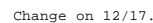
Remove Q17, R124, and R405 (Intel confirmed isolation not required for Hanksville)

2/23

Remove R354, R355, Q22A, Q22B; and connect SML0CLK and SML0DATA directly to LAN_SM_CLK and LAN_SM_DAT. 2/23

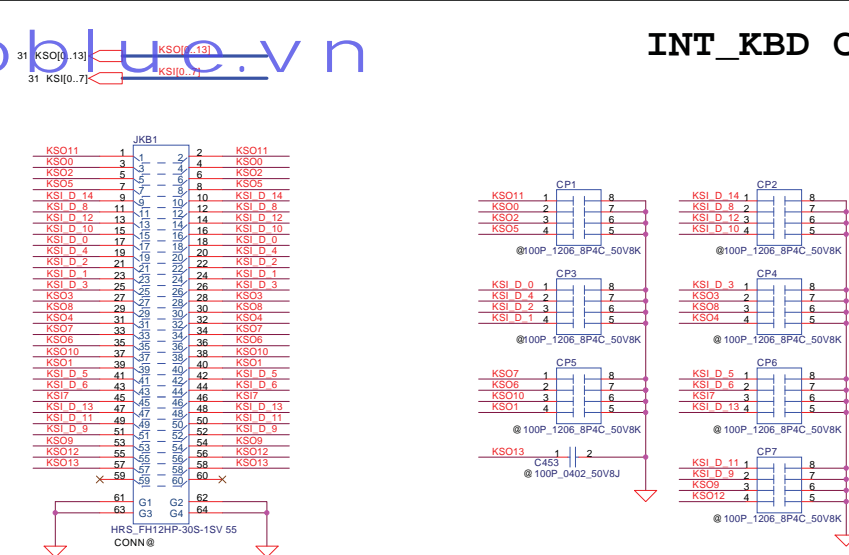


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/12/31	Title	Intel 82578 Hanksville-LM
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					LA-4951P
				Date	Tuesday, July 28, 2009
				Sheet	22 of 48
				Rev	0.4

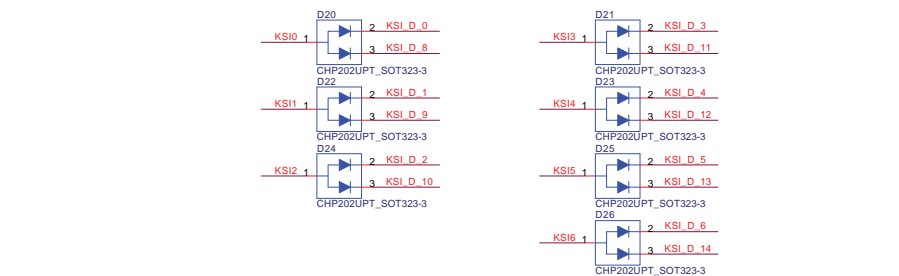


http://laptopblue.vn

INT_KBD_CONN.

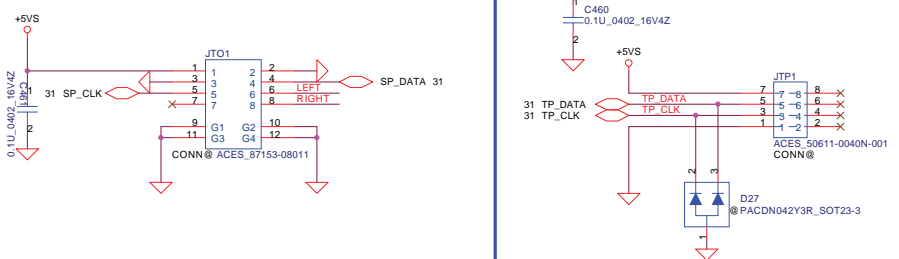


MDC 1.5 Conn.



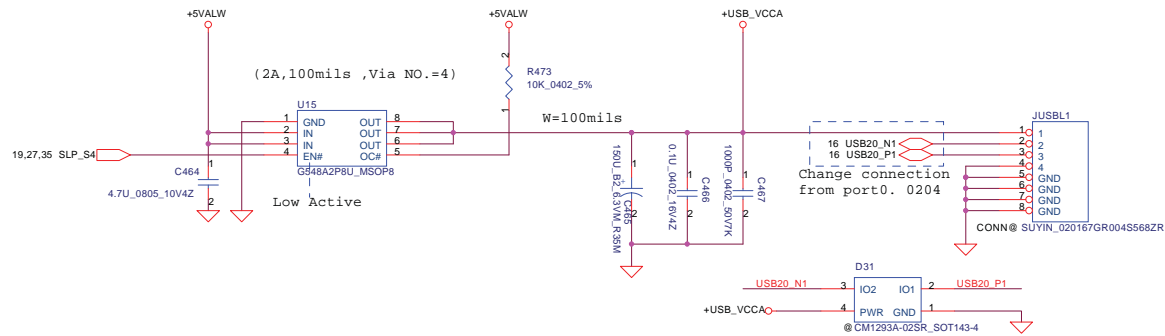
Track Point CONN.

Touch Pad CONN.

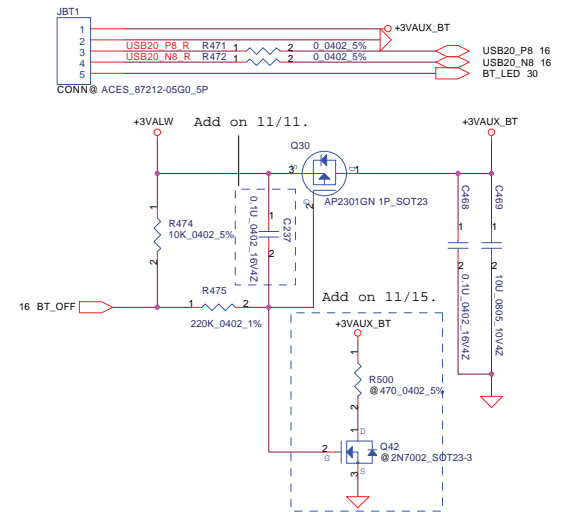


Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title MDC/KBD/ON OFF/LID		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number LA-4915P	Rev 0.
				Date:	Tuesday, July 28, 2009	Sheet 26 of 48

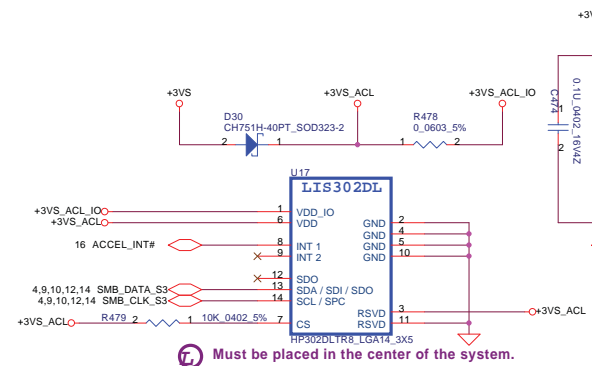
<http://laptopblue.vn>



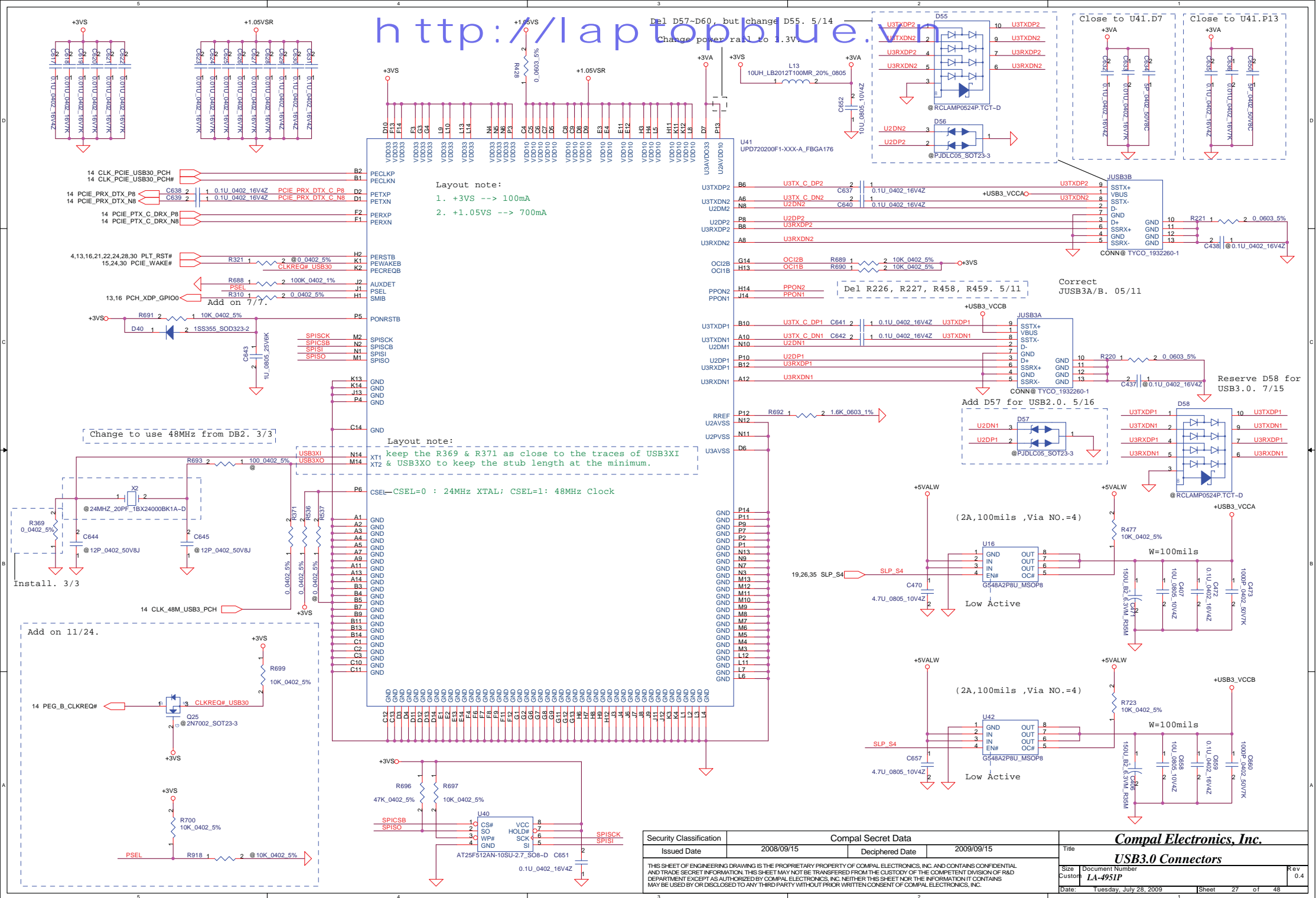
BT Connector



ACCELEROMETER

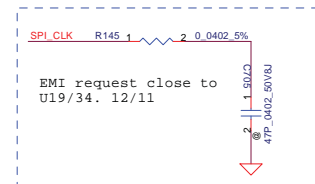


Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	USB & BT Connector & Acclerometer	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev 0.4
				LA-4951P		
				Date:	Tuesday, July 28, 2009	Sheet 26 of 48



Security Classification	Compal Secret Data			<i>Compal Electronics, Inc.</i>		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title <i>USB3.0 Connectors</i>		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
				Custody	LA-4951P	0.4
Date:				tuesdav. July 28, 2009	Sheet	27 of 48

http://laptopblue.vn TPM 1.2 on board



LPC Debug Port

The diagram illustrates the LPC Debug Port connections. On the left, the LPC1114 pins are shown with their functions and values. On the right, the JDBG1 module pins are listed. Connections are made between the two modules, with some pins marked as 'X' indicating no connection. A red triangle points to the connection for U3.10/27.

LPC1114 Pins:

- +3VL
- R492 100K_0402_5%
- 8051_RECOVER#
- 16 CLK_PCI_DB_PCH
- 13,24,31,33 LPC_LFRAME#
- 13,31,32,33 SIRO
- 4,13,16,21,22,24,27,30 PLT_RST#
- 13,24,31,33 LPC_LAD0
- 13,24,31,33 LPC_LAD1
- 13,24,31,33 LPC_LAD2
- 13,24,31,33 LPC_LAD3
- 31 8051TX
- 31 8051RX
- 31 SPI_CS1#

JDBG1 Pins:

- 1 GND
- 2 LPC_PCI_CLK
- 3 GND
- 4 LPC_FRAME#
- 5 +3VS
- 6 LPC_RESET#
- 7 +3VS
- 8 LPC_AD0
- 9 LPC_AD1
- 10 LPC_AD2
- 11 LPC_AD3
- 12 VCC_3VA
- 13 PWR_LED1
- 14 CAPS_LED#
- 15 NUM_LED#
- 16 VCC1_PWRGD
- 17 SPI_CLK
- 18 SPI_CS#
- 19 SPI_SI
- 20 SPI_SO
- 21 SPI_HOLD#
- 22 RSV
- 23 RSV
- 24 RSV
- 25 GND
- 26 GND

Connections:

- 8051_RECOVER# to 16 CLK_PCI_DB_PCH
- 13,24,31,33 LPC_LFRAME# to 4 LPC_FRAME#
- 13,31,32,33 SIRO to 5 +3VS
- 4,13,16,21,22,24,27,30 PLT_RST# to 6 LPC_RESET#
- 13,24,31,33 LPC_LAD0 to 8 LPC_AD0
- 13,24,31,33 LPC_LAD1 to 9 LPC_AD1
- 13,24,31,33 LPC_LAD2 to 10 LPC_AD2
- 13,24,31,33 LPC_LAD3 to 11 LPC_AD3
- 31 8051TX to 12 VCC_3VA
- 31 8051RX to 13 PWR_LED1
- 31 SPI_CS1# to 18 SPI_CS#
- 31 SPI_CLK_JP to 17 SPI_CLK
- 31 SPI_CS0#_JP to 18 SPI_CS#
- 31 SPI_SI_JP to 19 SPI_SI
- 31 SPI_SO_JP to 20 SPI_SO
- 31 SPI_HOLD#_0 to 21 SPI_HOLD#

Removed Connections:

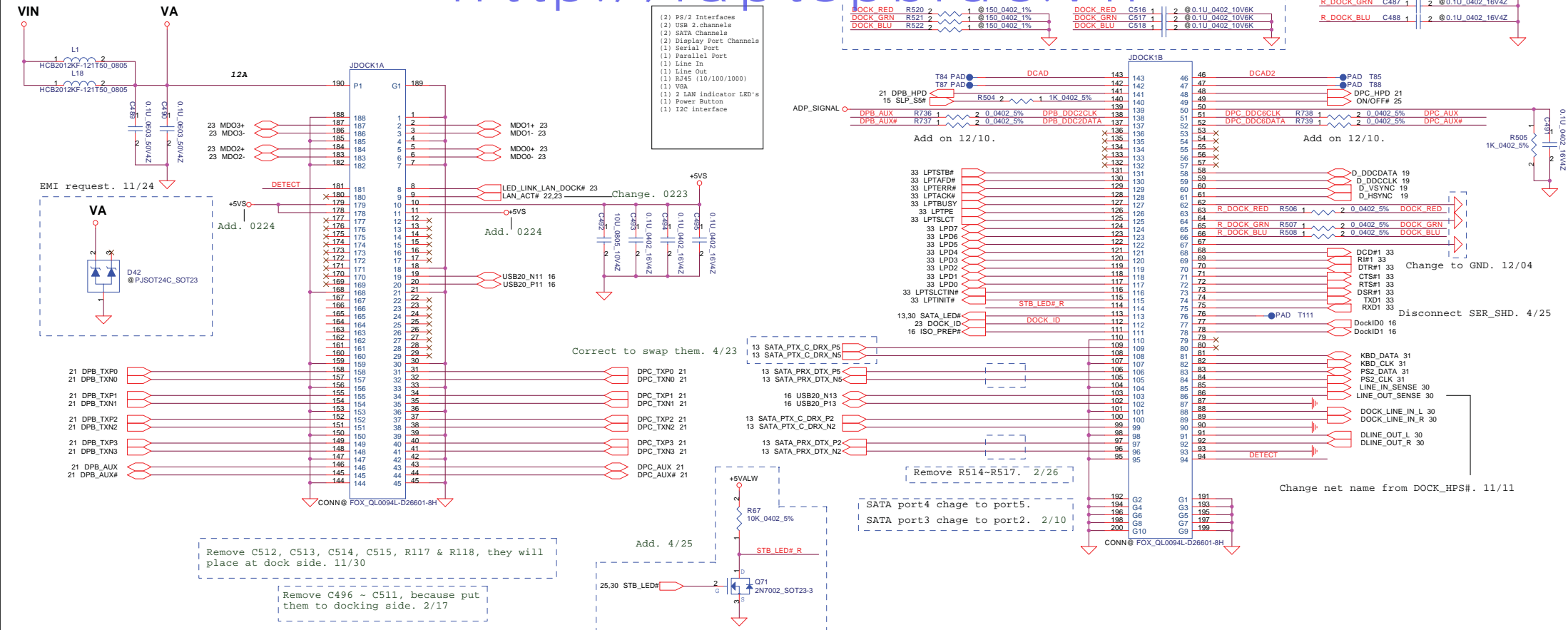
- 31 SPI_CS1# to 18 SPI_CS#
- 31 SPI_CLK_JP to 17 SPI_CLK
- 31 SPI_CS0#_JP to 18 SPI_CS#
- 31 SPI_SI_JP to 19 SPI_SI
- 31 SPI_SO_JP to 20 SPI_SO
- 31 SPI_HOLD#_0 to 21 SPI_HOLD#

Notes:

- Change from B+. 1209
- Connect pin3 & 23. 11/11
- Removed R500, R501 connect to U3. 10/27

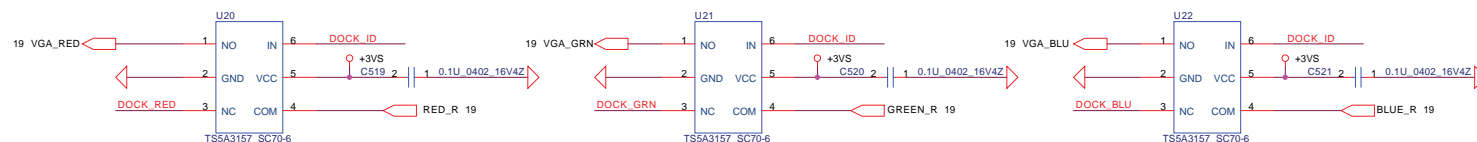
Security Classification		Compal Secret Data		Compal Electronics, Inc. TCG/BIOS ROM/PS2/SW LPC DEBUG	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				LA-495P	
				Date:	Tuesday, July 28, 2009
				Sheet	28 of 48

R_DOCK_RED C486 1 2 @0.1U_0402 16V4Z
R_DOCK_GRN C487 1 2 @0.1U_0402 16V4Z
R_DOCK_BLU C488 1 2 @0.1U_0402 16V4Z

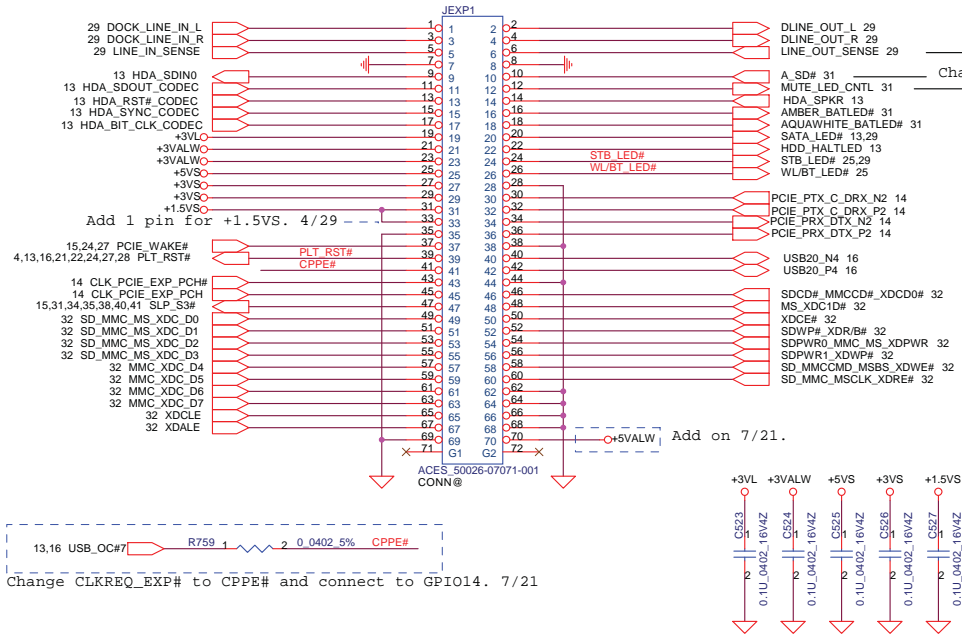


RGB Q-Switch

IN	NC<-->COM	NO<-->COM
L	ON	OFF
H	OFF	ON

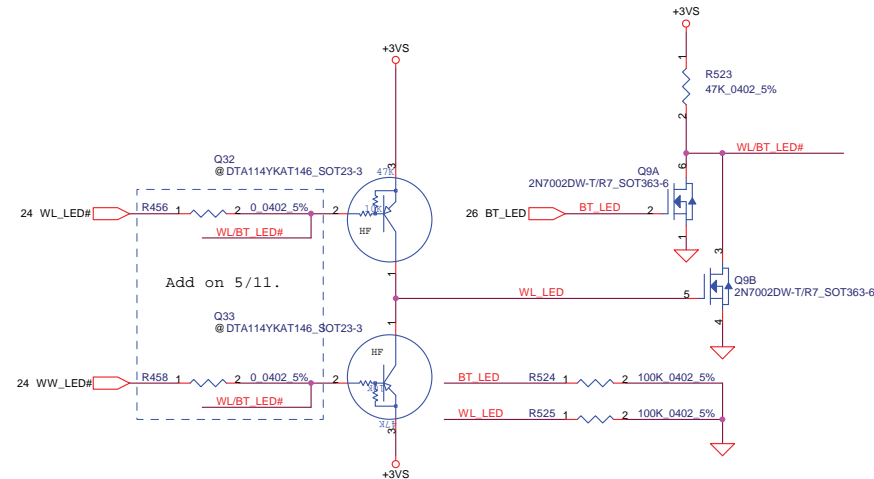
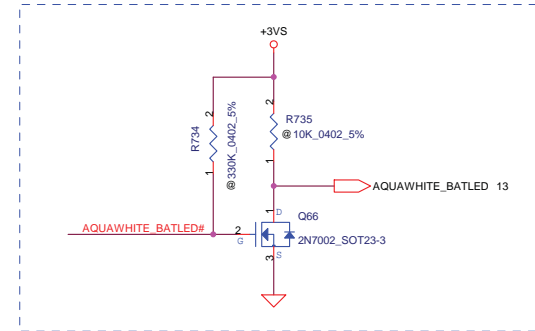


Security Classification	Compal Secret Data		Title	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Document Number
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev 0.4
Date: Tuesday, July 28, 2009				Sheet 29 of 48

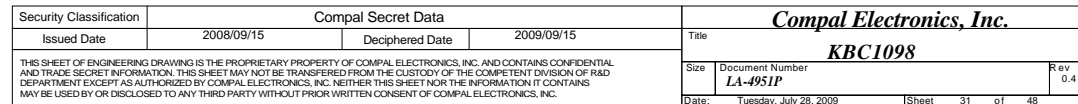
**Power Button Connector**

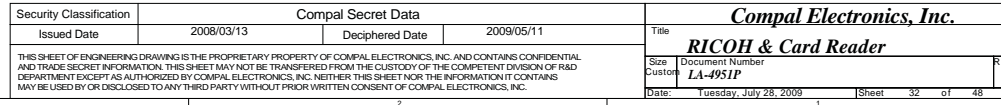
Removed Power button JPB1 & C522, combin with JSWITCH1. 11/10

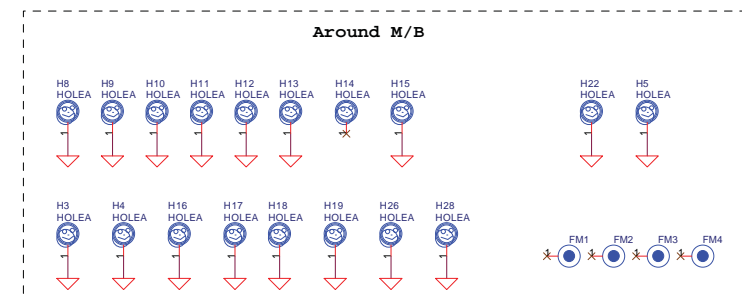
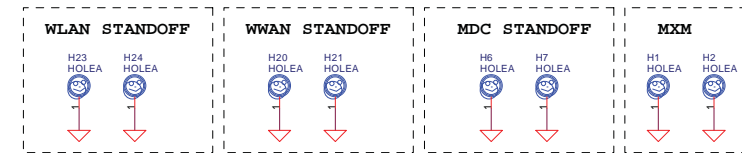
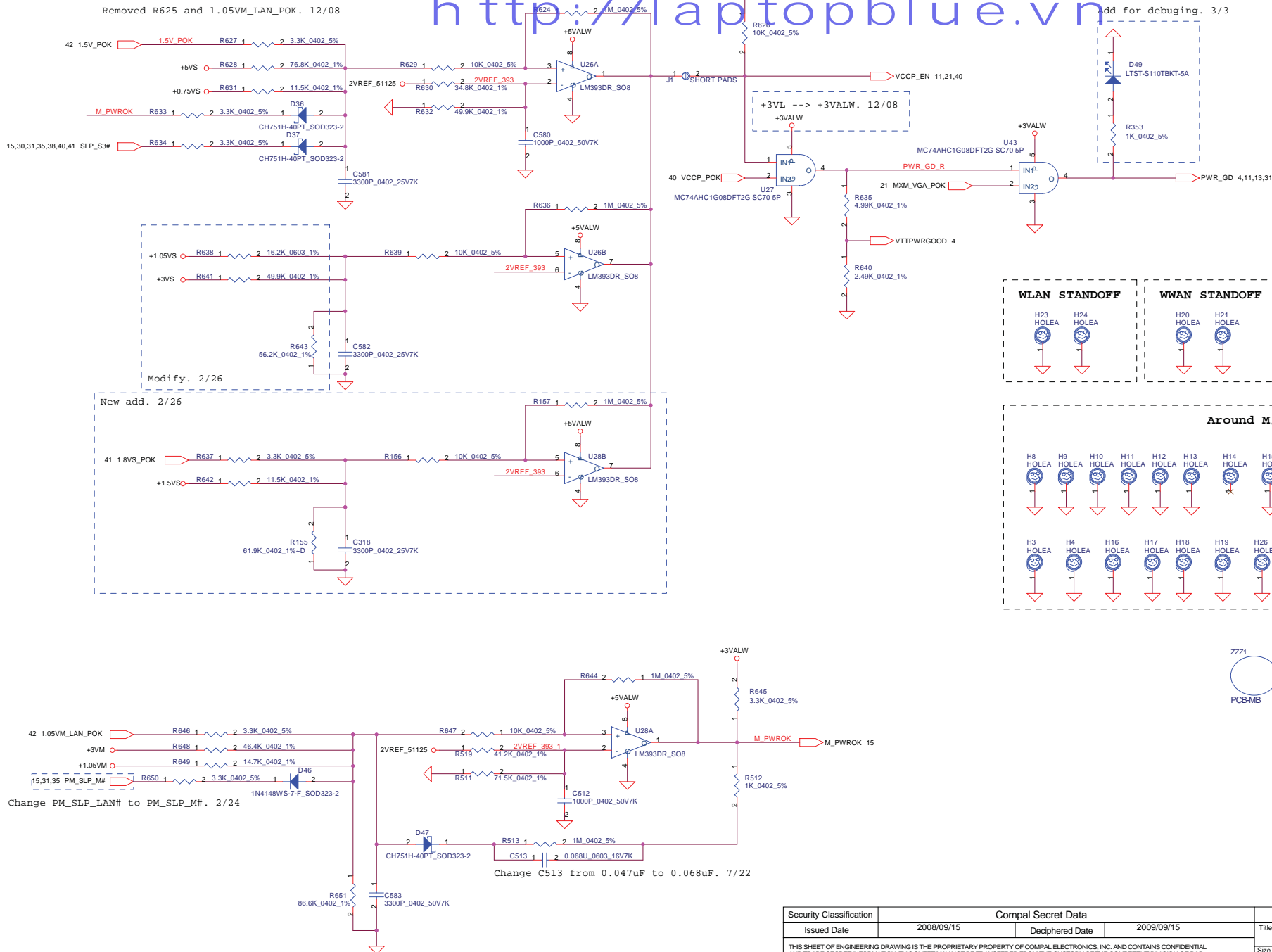
Change R734 & R735 to NI. 02/06



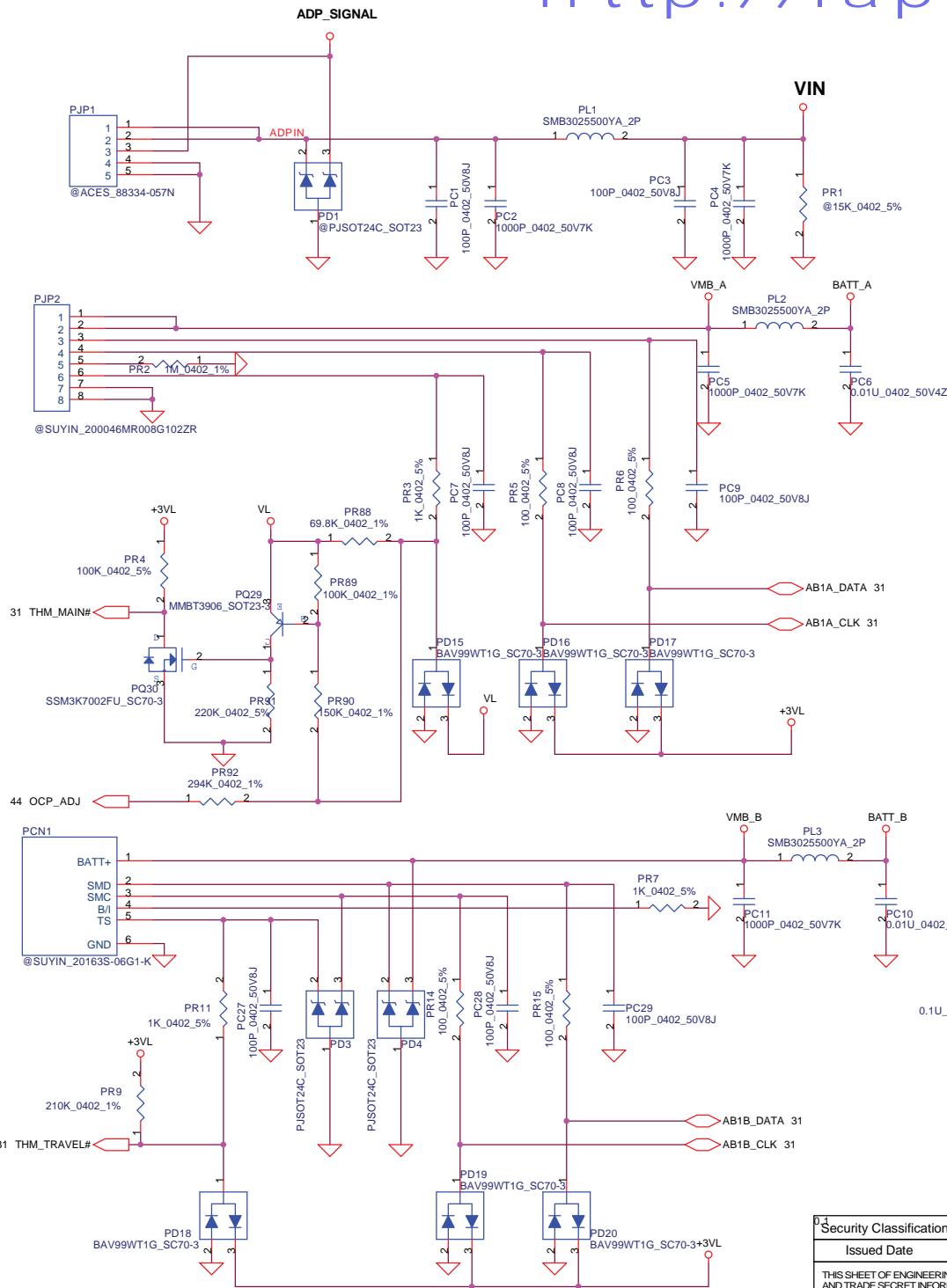
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/01	Deciphered Date	2010/09/01	Title	CR&LEDs&PW&Audio&Exp Conn
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size Custom	Document Number LA-4951P
				Date	Rev
				Tuesday, July 28, 2009	0.4
				Sheet	30 of 48



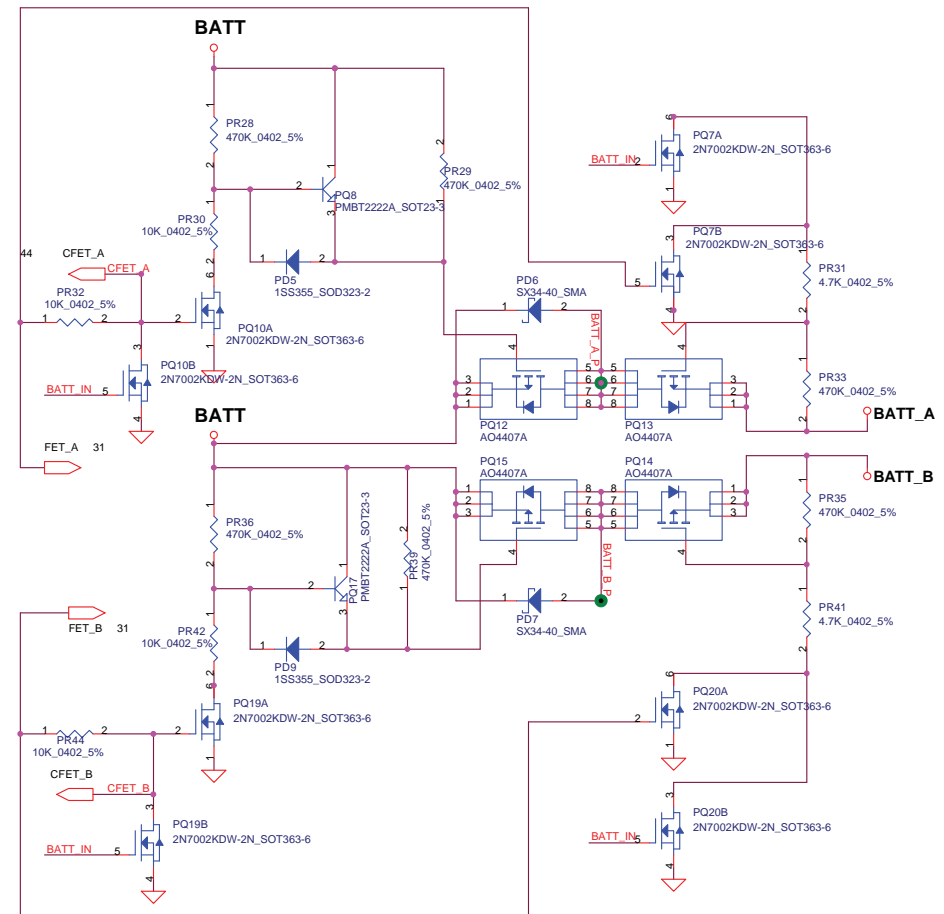




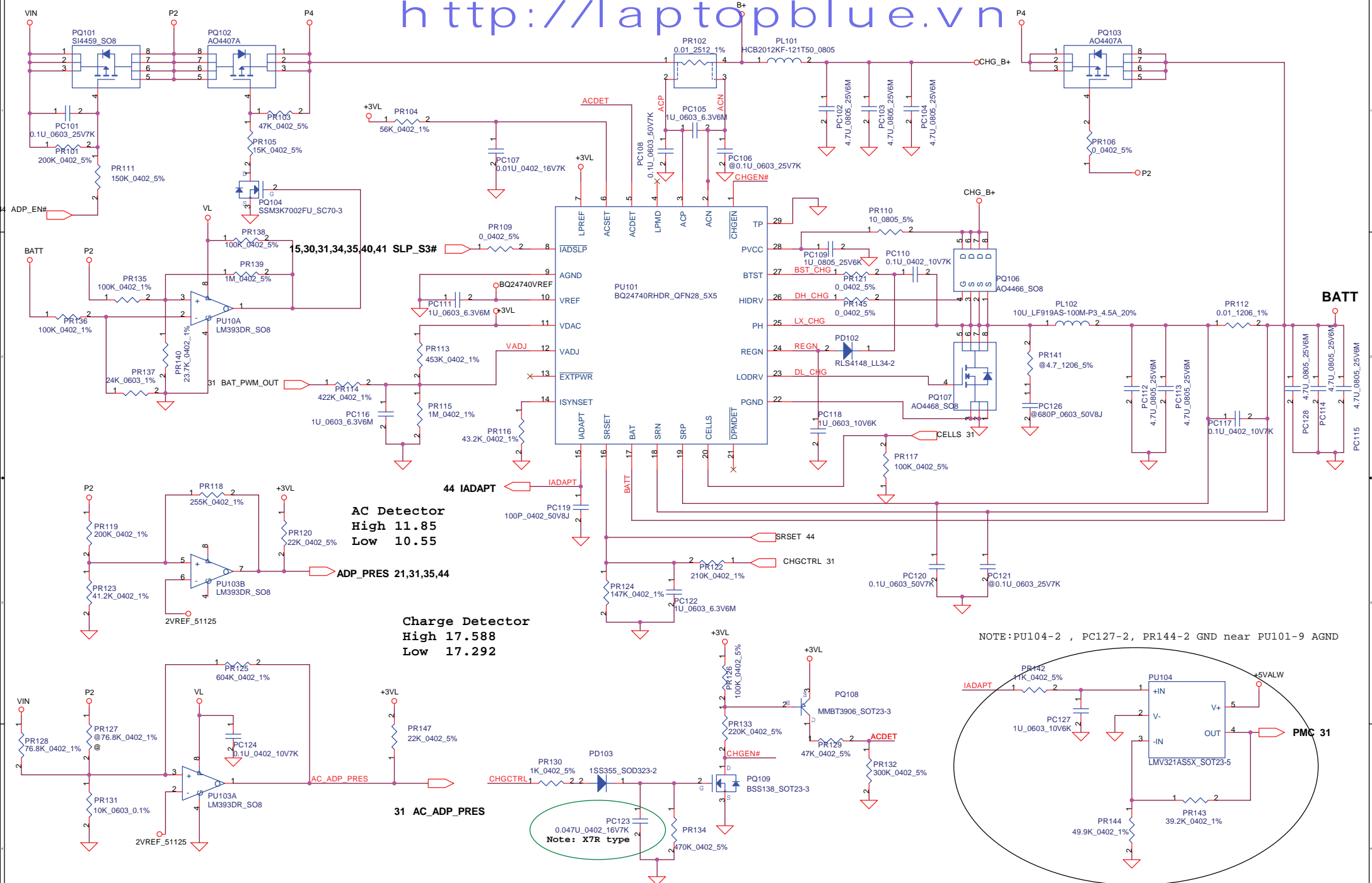
Security Classification	Compal Secret Data			Title		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Size		Document Number	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				LA-495IP		Rev 0.4	
				Date: Tuesday, July 28, 2009		Sheet 34 of 48	



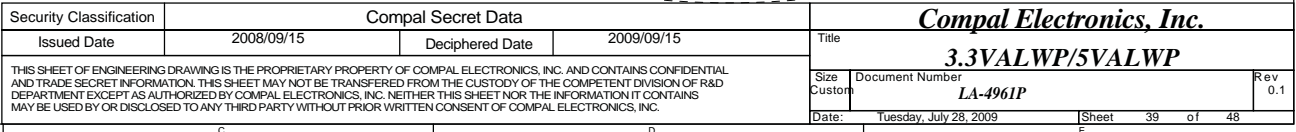
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	DC-IN/ BATTERY CONN
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	LA-4891
				Date:	Tuesday, July 28, 2009
				Sheet	36 of 48

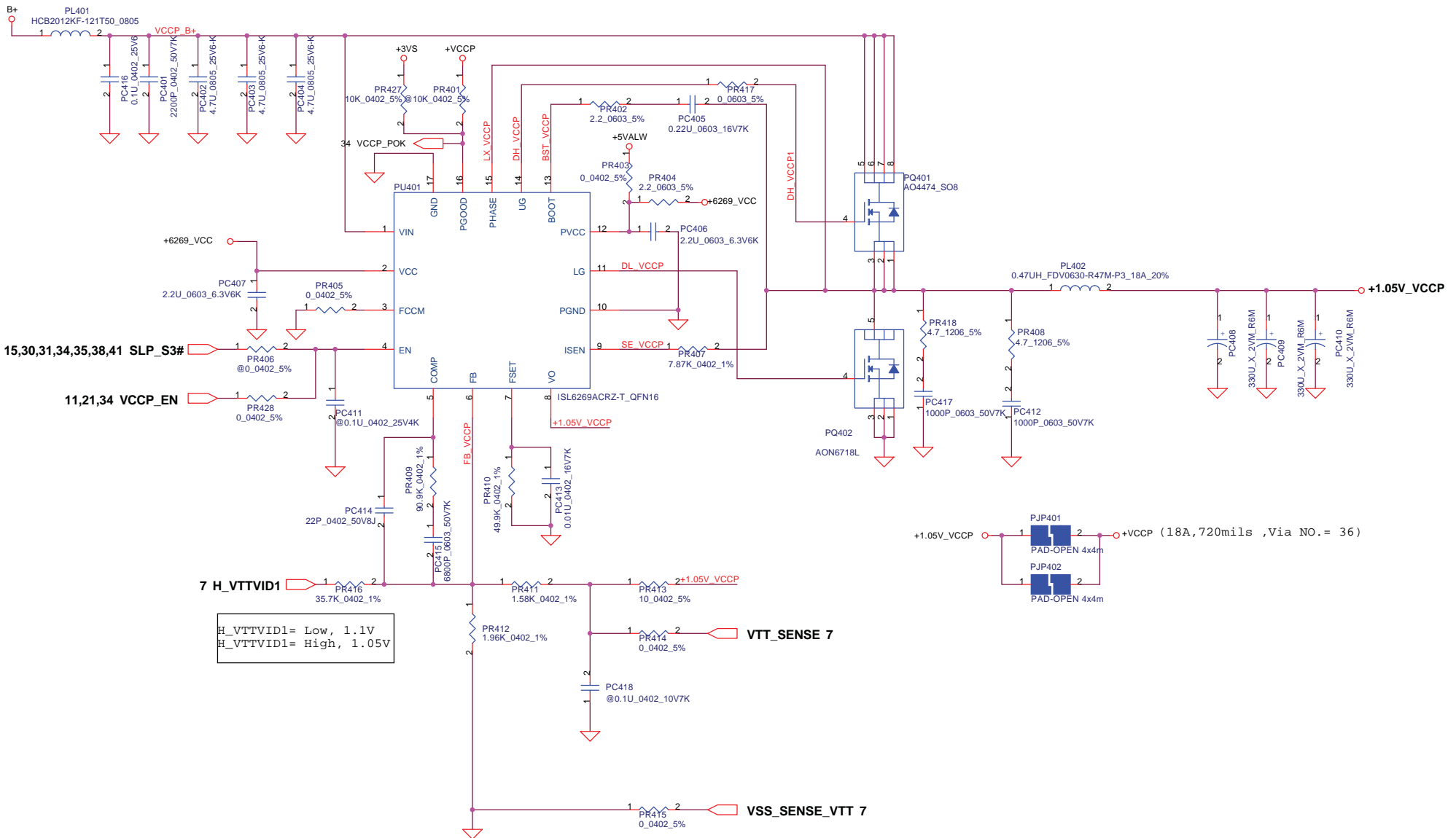


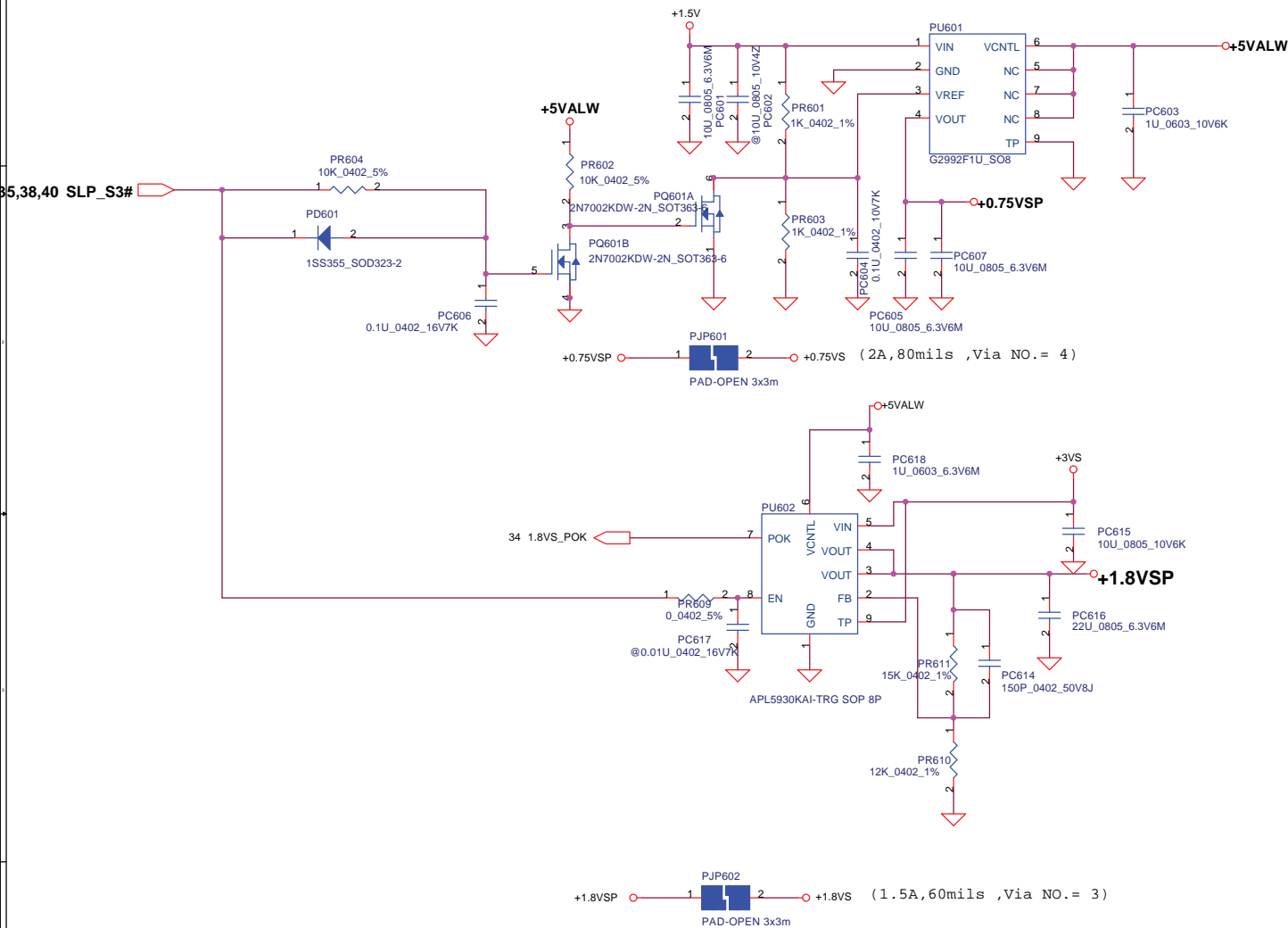
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Battery selector	
				Size	Document Number
				Custom	LA-4891
Date:		Tuesday, July 28, 2009		Sheet	37 of 48



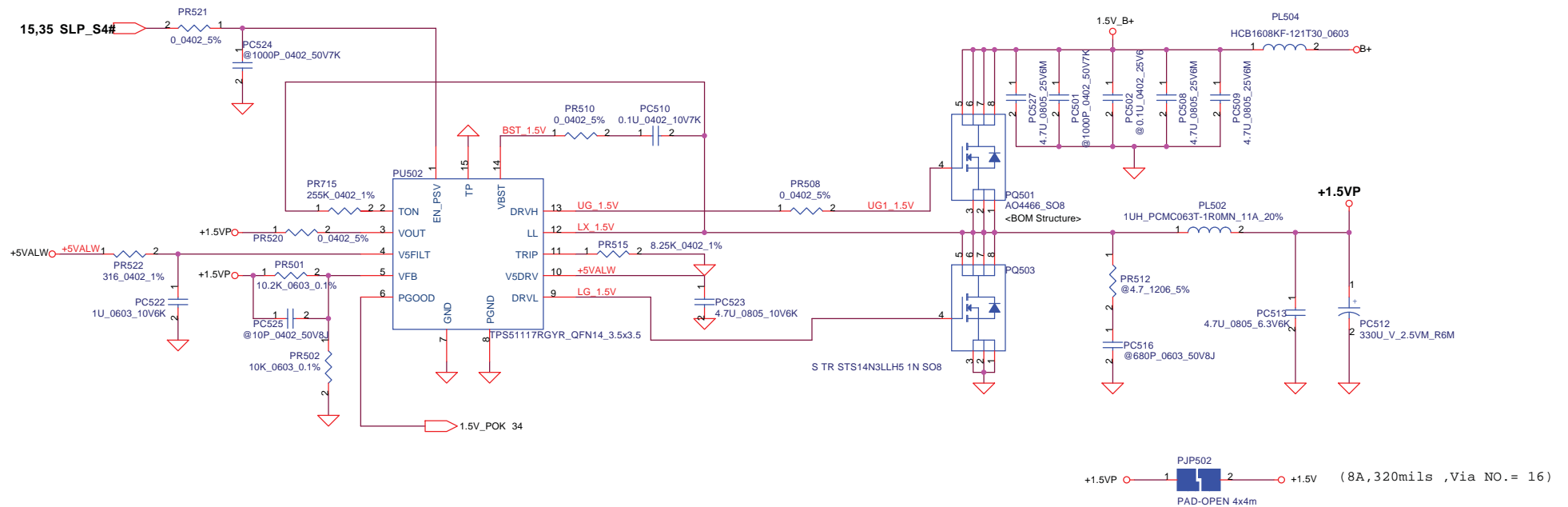
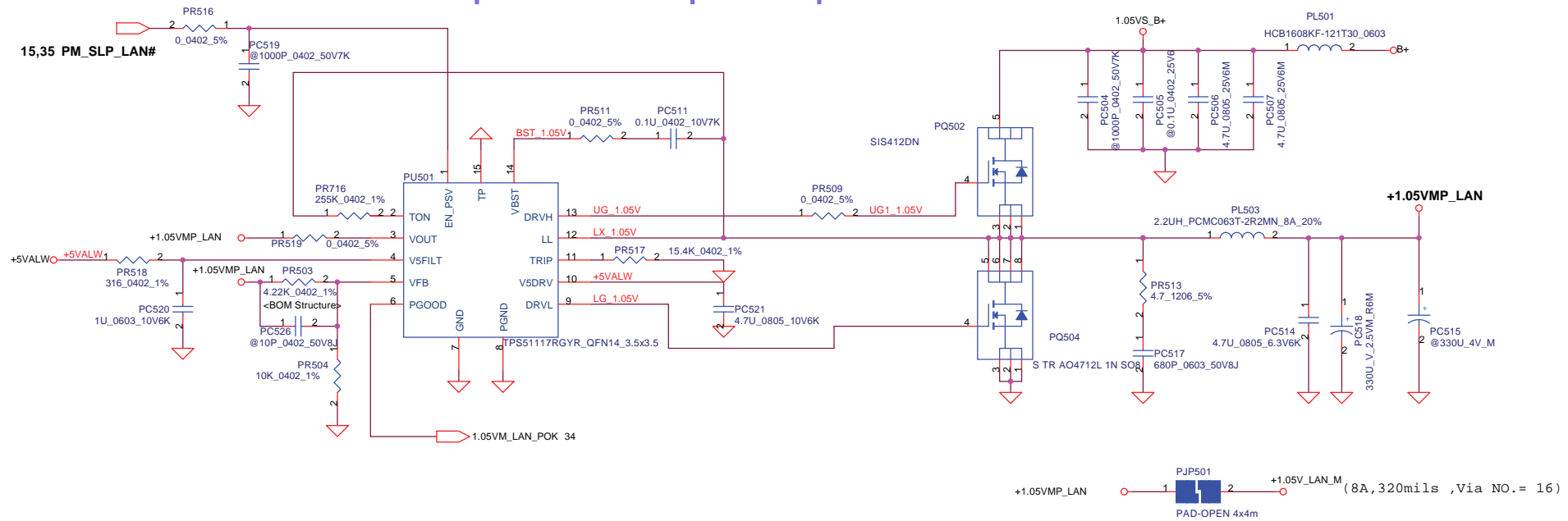
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	Charger
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				LA-4891	Rev 0.1
				Date: Tuesday, July 28, 2009	Sheet 38 of 48



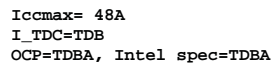




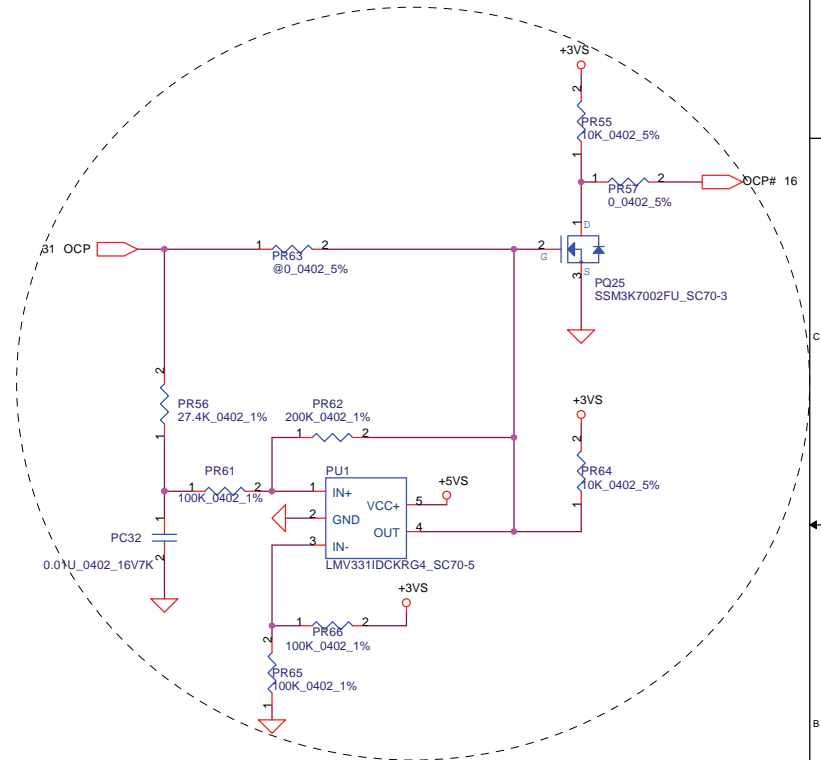
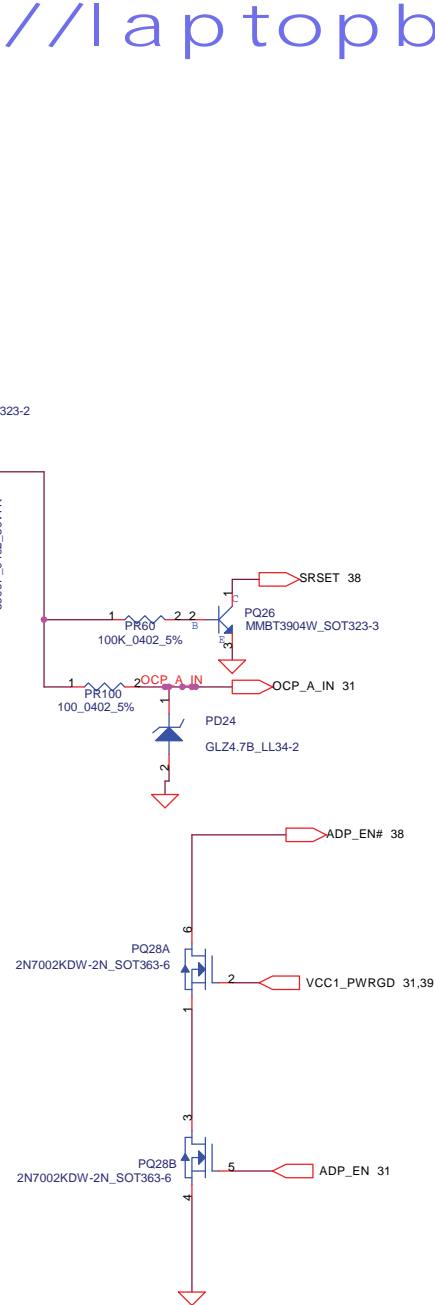
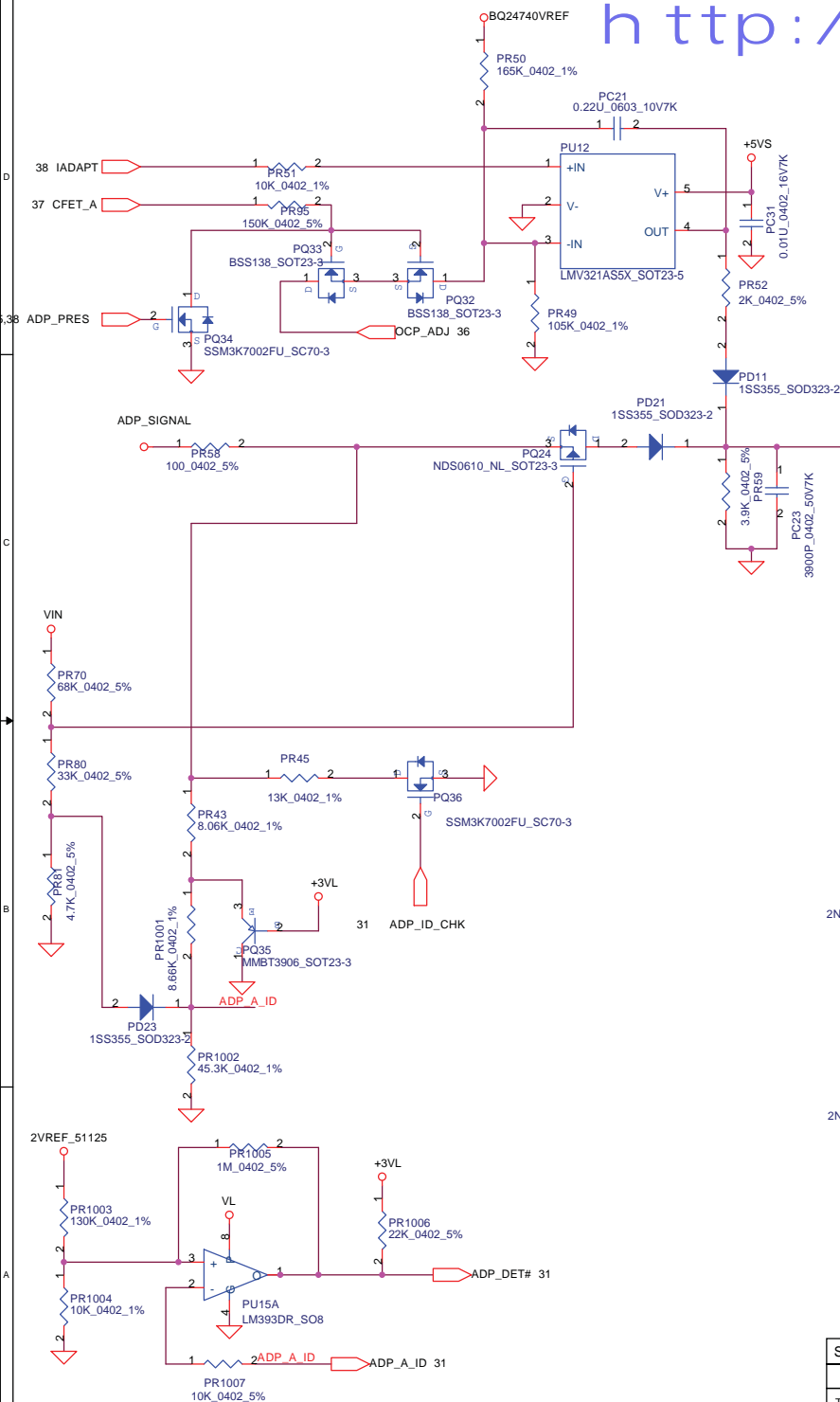
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					LA-4891
				Date:	Tuesday, July 28, 2009
				Sheet	41 of 48
				Rev	0.1



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				1.5VP/1.05VSP	
Size	Document Number	Rev		0.1	
Date:		Tuesday, July 28, 2009		Sheet 42 of 48	



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	CPU CORE	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
					LA-4891	0.1
Date: Tuesday, July 28, 2009				Sheet	43	of 48



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/09/15	Deciphered Date	2009/09/15	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Custom	LA-4891
				Date:	Tuesday, July 28, 2009
				Sheet	44 of 48

[illegible][illegible]

Version Change List (P. I. R. List) for HW Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	21	MXM PEG Bus	01/19	Compal	Can't detect GPU of MXM board	Need reverse TX & RX bus of PEG.	0.2
2	21	MXM LVDS Bus	01/19	Compal	No display of LVDS panel	Need reverse LVDS low&high bit BUS.	0.2
3	13	RTC	01/19	Compal	RTC no function	Reverse signals of RTC connector.	0.2
4	29	JVGAFFC1	01/20	Compal	JVGAFFC1 pin23&24 was dummy, USB cacn't work	Connect JVGAFFC1 pin23&24 to SLP_S4.	0.2
5	20	DISP_OFF#	02/02	Compal	Use wrong power rail for this signal.	Currently panel spec is +3VS.	0.2
6	20	Left USB port	02/02	Compal	It's wrong port for debug	Change connection from port0 to port1 for debug.	0.2
7	21	PWR_LEVEL	02/04	Compal	Need isolation circit per HP request.	Add Q69, Q70, R397, R388, R118.	0.2
8	4	CPU FAN CONN	02/06	Compal	No GND pin of FAN connector.	Change Connector to 4 pin with GND pin.	0.2
9	4,13,24	SM BUS	02/06	HP	HP request to remove SM bus to XDP, JTAG & WLAN	Leave TP and remove signals.	0.2
10	22	Intel LAN	02/06	HP	Intel change design to remove some caps.	Remove these caps as Intel CRB design.	0.2
11	11	DDR M2 support	02/08	Compal	Data & CLK signals are reverse of U36.	Reverse CLK & DATA.	0.2
12	30	LED CTL circuit	02/06	HP	HP request to update LED control circuit.	No install R734 & R735.	0.2
13	15	Duplicate	02/06	HP	Remove R247 because of R541existing.	Remove R247.	0.2
14	13	PCH_JTAG_RST#	02/06	HP	Change R720 to NO INSTALL.	No install R720.	0.2
15	13	GPIO	02/06	HP	Change GPIO43_R to USB_OC#4 & reserve 33 ohm serial.	Add R134 but no install.	0.2
16	13	LED control	02/06	HP	Remove GND & change connection of R176.	Change R176 to 1K with install it.	0.2
17	14	CLKREQ_EXP#	02/06	HP	Change R687 PU for CLKREQ_EXP# to INSTALL.	Install R687.	0.2
18	17	PCH Power Rail	02/06	HP	Some power rail of PCH are no use.	Remove these power and add TP.	0.2
19	32	Card Reader	02/06	HP	It will need find tune value of R731 & C661.	Change R731 to 100K & C661 to 1uF X5R as Cartier first.	0.2
20	9	Debug port	02/10	HP	HP request to add debug port for IAMT.	Add JIAMT1.	0.2
21	16	WWAN CONN	02/10	HP	WOW# to WWAN connector is no longer supported.	Remove R117 and signal.	0.2
22	13	SATA port	02/10	HP	Change SATA assignments to support PM.	Port 4 --> 5, Port2 --> 4, Port3 -->2.	0.2
23	31	KBC1091	02/10	HP	Remove ADP_DET# on U23 GPIO9.	Add R320 PD on GPIO9.	0.2
24	24	WLAN	02/10	HP	Remove R441 and connection to JWLAN1.5	Remove R441.	0.2
25	24	USB port6	02/10	HP	Remove USB signals to JWLAN1.36 and 38.	Remove them.	0.2
26	20	WLAN	02/10	HP	Add 680P on DISP_OFF# close JLCD1 and change C359.	Change C359 to 0.1uF and add C439.	0.2
27	22	Intel 82578	02/10	HP	Can remove Q17, R405 & R124 if no leakage.	Reserve Q17, R405 and R124 in DB2.	0.2
28	4, 16	Intel Change	02/10	HP	414044 DG update 1.11	Change R31/R32 to 1.5K/750ohm and R297 to 100K.	0.2
29	31	System ID control	02/17	HP	Common Design with other project.	Del D49, R149 and Q154 and Add U44.	0.2
30	20	Webcam	02/17	HP	WEBCAM_OFF is active high, so need change design.	Add R287 to turn on Gate.	0.2
31	15	PWR_GD	02/17	HP	HP request to change design.	Add R399 and install R237.	0.2
32	15	NAND Flash	02/17	HP	HP request to change design.	Add NAND_DETECT# form U4.Y7 to JNAND1.17.	0.2
33	4	XDP PU	02/17	HP	HP request to change design.	Del R37, R38, R39.	0.2
34	22	LAN Power	02/17	HP	Remove power switch from 3V to 1V and related parts.	Remove C382, C383, C384, C385, C694, R401, R366, Q21 and R402.	0.2
35	29	Docking	02/17	HP	HP agree to remove caps for DP from MB to DOCKING.	Remove C496 ~ C511 from MB.	0.2
36	33	Super I/O	02/17	Compal	Common design change to SMSC		0.2

Security Classification

Issued Date

2006/02/28

Compal Secret Data

Deciphered Date

2007/02/28

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Title

Compal Electronics, Inc.

Size

Document Number

LA-4951P

Rev

0.4

Date:

Tuesday, July 28, 2009

Sheet

46

of

48

Version Change List (P. I. R. List) for HW Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	25	ESD DIODE	04/23	Compal	Wrong connection of D53 & D54, system can't boot.	Swap +5VS & GND of D53 & D54 pin 2 & 5.	0.3
2	29	SATA Port 5	04/23	Compal	It have wrong connection of JDOCK1 pin108 & pin109.	Swap SATA_PTX_C_DRX_P5/N5 of connector side.	0.3
3	28	SPI_CS0#	04/23	HP	HP request to add pull up resistor close to SPI ROM.	Reserve R6 close to U19.	0.3
4	14	SM BUS	04/23	HP	Intel request to change PU to 2.2K.	Change R199, R200 from 4.7K to 2.2K	0.3
5	23	LAN Transfermer	04/23	HP	Intel request to add 1uF cap between TRM caps to GND	Add C264 to GND.	0.3
6	13	JTAG Port	04/24	HP	HP review need swap JTAG1 pin4 & pin6.	Swap XDP_FN16 & XDP_FN17 at JTAG1 side.	0.3
7	17	PCH +1.05VS	04/25	HP	HP review will no need connect resistor between them.	Remove R310.	0.3
8	14	PCH 25MHz Crystall	04/25	HP	No need 25MHz for PCH as common design.	No install R229, C281, C282, Y4 but add R52.	0.3
9	31	KBC1098 VCC0	04/25	HP	The VCC0 will never connect to GND.	Remove R587 from schematic.	0.3
10	16	LAN_DIS#	04/25	HP	GPIO12 of PCH have internal PU.	No install R279 and reserve R53 PD.	0.3
11	21	DPA AUX	04/25	HP	Need 0.1uF cap for Q20, Q67 gate pin.	Add C265.	0.3
12	30	WWAN_DET#	04/25	HP	Design Change PCH GPIO22 to WWAN_DET#.	Change GPIO of PCH to WWAN_DET#.	0.3
13	16	Webcam control	04/25	HP	Chnage the control pin from GPIO47 to GPIO22 of PCH	Modify WEBCAM_OFF to WEBCAM_ON, add R66 PU for GPIO47.	0.3
14	20	Webcam control	04/25	HP	Chnage the control pin from GPIO47 to GPIO22 of PCH	Del R350.	0.3
15	13	DOCK LED	04/25	HP	Need inverter for docking power LED signal.	Add Q71 & R67.	0.3
16	33	SER_SHD	04/25	HP	HP request to remove SER_SHD from SIO to docking.	Disconnect SER_SHD at docking side, add R7 PU to +3VS.	0.3
17	9, 10	DDR3 M1 & M3	04/30	HP	Need implement M1, M3 but reserve M2 for SI1.	Remove R91, R682~R684 and add divider for V_DDR_CPU_REF0/1.	0.3
18	17	PCH Power Rail		HP	Some power rail of PCH are no use.	Remove these power and add TP.	0.3
19	32	Card Reader		HP	It will need find tune value of R731 & C661.	Change R731 to 100K & C661 to 1uF X5R as Cartier first.	0.3
20	9	Debug port		HP	HP request to add debug port for IAMT.	Add JIAMT1.	0.3
21	16	WWAN CONN		HP	WOW# to WWAN connector is no longer supported.	Remove R117 and signal.	0.3
22	13	SATA port		HP	Change SATA assignments to support PM.	Port 4 --> 5, Port2 --> 4, Port3 --> 2.	0.3
23	31	KBC1091		HP	Remove ADP_DET# on U23 GPIO9.	Add R320 PD on GPIO9.	0.3
24	24	WLAN		HP	Remove R441 and connection to JWLAN1.5	Remove R441.	0.3
25	24	USB port6		HP	Remove USB signals to JWLAN1.36 and 38.	Remove them.	0.3
26	20	WLAN		HP	Add 680P on DISP_OFF# close JLCD1 and change C359.	Change C359 to 0.1uF and add C439.	0.3
27	22	Intel 82578		HP	Can remove Q17, R405 & R124 if no leakage.	Reserve Q17, R405 and R124 in DB2.	0.3
28	4, 16	Intel Change		HP	414044 DG update 1.11	Change R31/R32 to 1.5K/750ohm and R297 to 100K.	0.3
29	31	System ID control		HP	Common Design with other project.	Del D49, R149 and Q154 and Add U44.	0.3
30	20	Webcam		HP	WEBCAM_OFF is active high, so need change design.	Add R287 to turn on Gate.	0.3
31	15	PWR_GD		HP	HP request to change design.	Add R399 and install R237.	0.3
32	15	NAND Flash		HP	HP request to change design.	Add NAND_DETECT# form U4.Y7 to JNAND1.17.	0.3
33	4	XDP PU		HP	HP request to change design.	Del R37, R38, R39.	0.3
34	22	LAN Power		HP	Remove power switch from 3V to 1V and related parts.	Remove C382, C383, C384, C385, C694, R401, R366, Q21 and R402.	0.3
35	29	Docking		HP	HP agree to remove caps for DP from MB to DOCKING.	Remove C496 ~ C511 from MB.	0.3
36	33	Super I/O		Compal	Common design change to SMSC		0.3

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/02/28	Deciphered Date	2007/02/28	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	
				Document Number	
				LA-495IP	
				Rev	
				0.4	
				Date	
				Tuesday, July 28, 2009	
				Sheet	
				47 of 48	

Version Change List (P. I. R. List) for HW Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	4	FAN CTRL Circuit	07/2	HP	FAN always 100% turn after power on	Del D29 & NI R133 to prevent this issue.	0.4
2	29	SATA Port 5	04/23	Compal	It have wrong connection of JDCK1 pin108 & pin109.	Swap SATA_PTX_C_DRX_P5/N5 of connector side.	0.3
3	28	SPI_CS0#	04/23	HP	HP request to add pull up resistor close to SPI ROM.	Reserve R6 close to U19.	0.3
4	14	SM BUS	04/23	HP	Intel request to change PU to 2.2K.	Change R199, R200 from 4.7K to 2.2K	0.3
5	23	LAN Transfermer	04/23	HP	Intel request to add 1uF cap between TRM caps to GND	Add C264 to GND.	0.3
6	13	JTAG Port	04/24	HP	HP review need swap JTAG1 pin4 & pin6.	Swap XDP_FN16 & XDP_FN17 at JTAG1 side.	0.3
7	17	PCH +1.05VS	04/25	HP	HP review will no need connect resistor between them.	Remove R310.	0.3
8	14	PCH 25MHz Crystall	04/25	HP	No need 25MHz for PCH as common design.	No install R229, C281, C282, Y4 but add R52.	0.3
9	31	KBC1098 VCC0	04/25	HP	The VCC0 will never connect to GND.	Remove R587 from schematic.	0.3
10	16	LAN_DIS#	04/25	HP	GPIO12 of PCH have internal PU.	No install R279 and reserve R53 PD.	0.3
11	21	DPA AUX	04/25	HP	Need 0.1uF cap for Q20, Q67 gate pin.	Add C265.	0.3
12	30	WWAN_DET#	04/25	HP	Design Change PCH GPIO22 to WWAN_DET#.	Change GPIO of PCH to WWAN_DET#.	0.3
13	16	Webcam control	04/25	HP	Chnage the control pin from GPIO47 to GPIO22 of PCH	Modify WEBCAM_OFF to WEBCAM_ON, add R66 PU for GPIO47.	0.3
14	20	Webcam control	04/25	HP	Chnage the control pin from GPIO47 to GPIO22 of PCH	Del R350.	0.3
15	13	DOCK LED	04/25	HP	Need inverter for docking power LED signal.	Add Q71 & R67.	0.3
16	33	SER_SHD	04/25	HP	HP request to remove SER_SHD from SIO to docking.	Disconnect SER_SHD at docking side, add R7 PU to +3VS.	0.3
17	9, 10	DDR3 M1 & M3	04/30	HP	Need implement M1, M3 but reserve M2 for SI1.	Remove R91, R682~R684 and add divider for V_DDR_CPU_REF0/1.	0.3
18	17	PCH Power Rail		HP	Some power rail of PCH are no use.	Remove these power and add TP.	0.3
19	32	Card Reader		HP	It will need find tune value of R731 & C661.	Change R731 to 100K & C661 to 1uF X5R as Cartier first.	0.3
20	9	Debug port		HP	HP request to add debug port for IAMT.	Add JIAMT1.	0.3
21	16	WWAN CONN		HP	WOW# to WWAN connector is no longer supported.	Remove R117 and signal.	0.3
22	13	SATA port		HP	Change SATA assignments to support PM.	Port 4 --> 5, Port2 --> 4, Port3 --> 2.	0.3
23	31	KBC1091		HP	Remove ADP_DET# on U23 GPIO9.	Add R320 PD on GPIO9.	0.3
24	24	WLAN		HP	Remove R441 and connection to JWLAN1.5	Remove R441.	0.3
25	24	USB port6		HP	Remove USB signals to JWLAN1.36 and 38.	Remove them.	0.3
26	20	WLAN		HP	Add 680P on DISP_OFF# close JLCD1 and change C359.	Change C359 to 0.1uF and add C439.	0.3
27	22	Intel 82578		HP	Can remove Q17, R405 & R124 if no leakage.	Reserve Q17, R405 and R124 in DB2.	0.3
28	4, 16	Intel Change		HP	414044 DG update 1.11	Change R31/R32 to 1.5K/750ohm and R297 to 100K.	0.3
29	31	System ID control		HP	Common Design with other project.	Del D49, R149 and Q154 and Add U44.	0.3
30	20	Webcam		HP	WEBCAM_OFF is active high, so need change design.	Add R287 to turn on Gate.	0.3
31	15	PWR_GD		HP	HP request to change design.	Add R399 and install R237.	0.3
32	15	NAND Flash		HP	HP request to change design.	Add NAND_DETECT# form U4.Y7 to JNAND1.17.	0.3
33	4	XDP PU		HP	HP request to change design.	Del R37, R38, R39.	0.3
34	22	LAN Power		HP	Remove power switch from 3V to 1V and related parts.	Remove C382, C383, C384, C385, C694, R401, R366, Q21 and R402.	0.3
35	29	Docking		HP	HP agree to remove caps for DP from MB to DOCKING.	Remove C496 ~ C511 from MB.	0.3
36	33	Super I/O		Compal	Common design change to SMSC		0.4

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/02/28	Deciphered Date	2007/02/28	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	
				Document Number	
				LA-4951P	
				Rev	
				0.4	
				Date: Tuesday, July 28, 2009	
				Sheet 48 of 48	

<http://laptopblue.vn>

www.s-manuals.com