

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

SCHEMATICS, M/B LA-971

Title	SCHEMATICS, M/B LA-971		
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	35 of 37

COMPAL CONFIDENTIAL

MODEL NAME : 888J2 LA-971

REVISION HISTORY

Date: 2000/07/11 REV.: 0.1 Description: A-TEST Release Date: 2000-7-14

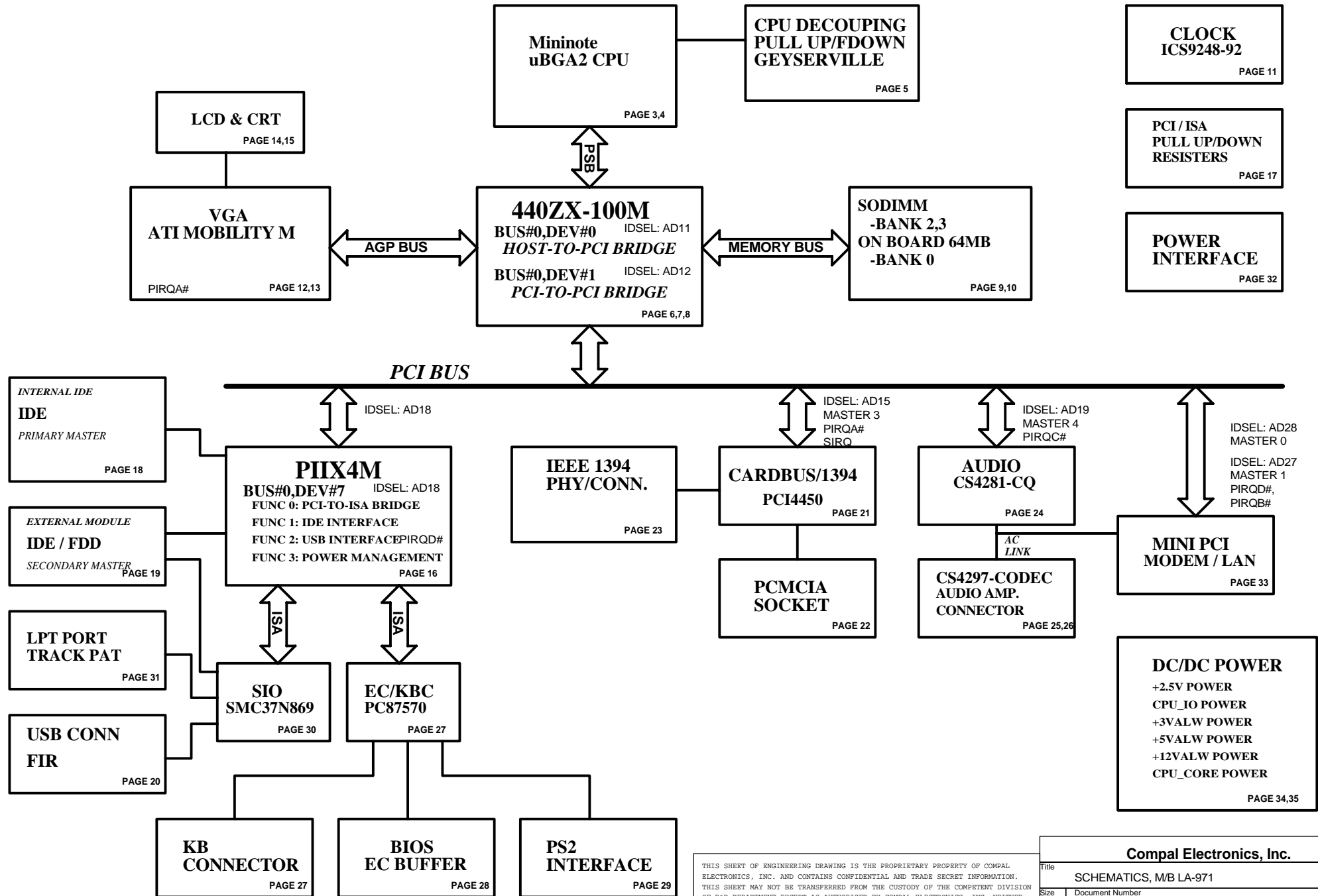
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.		
Title		
SCHEMATICS, M/B LA-971		
Size	Document Number	Rev
B	401166	1C
Date:	Friday, December 01, 2000	Sheet 2 of 37

Model Name: 888J2

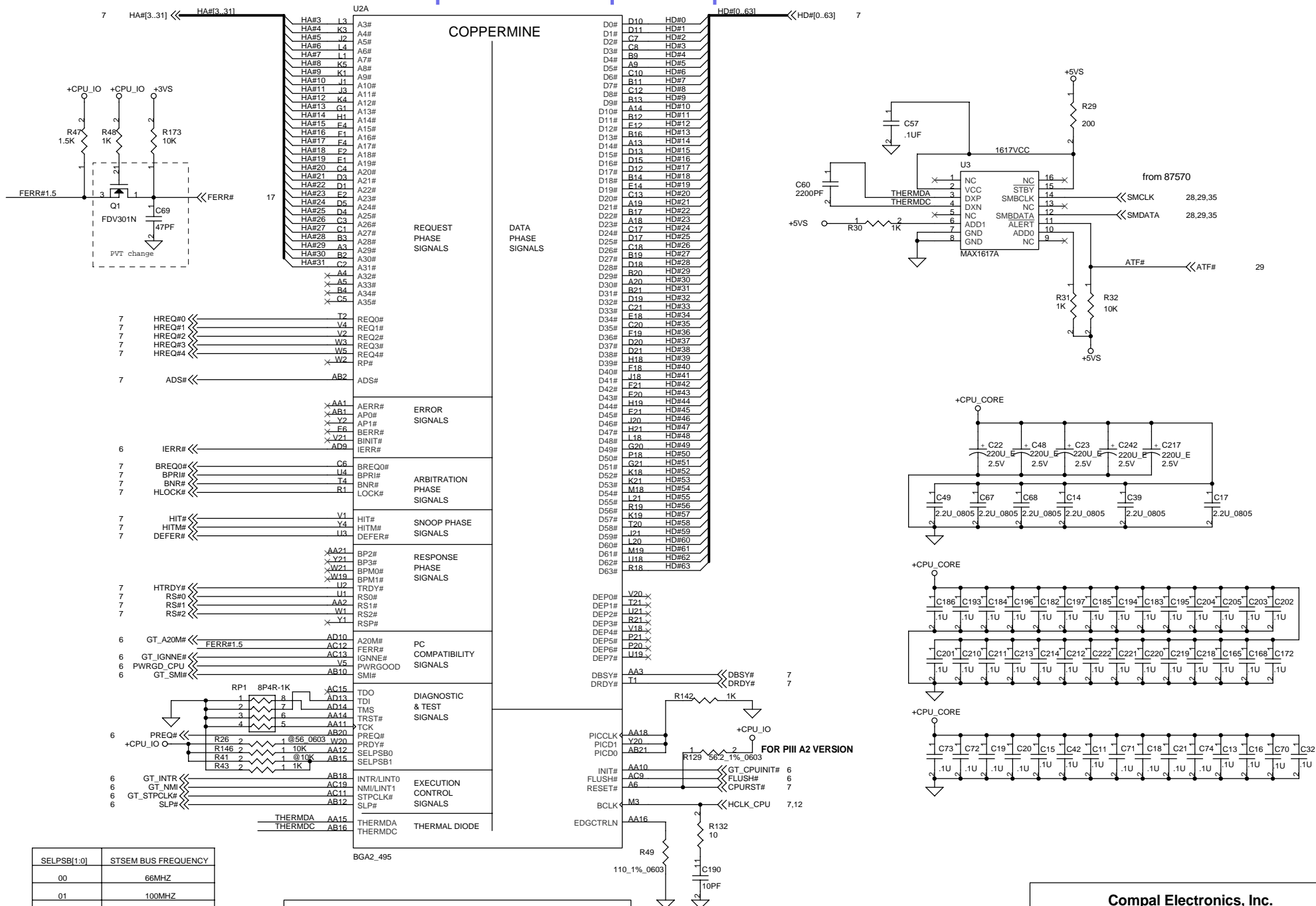
PCB No: LA971

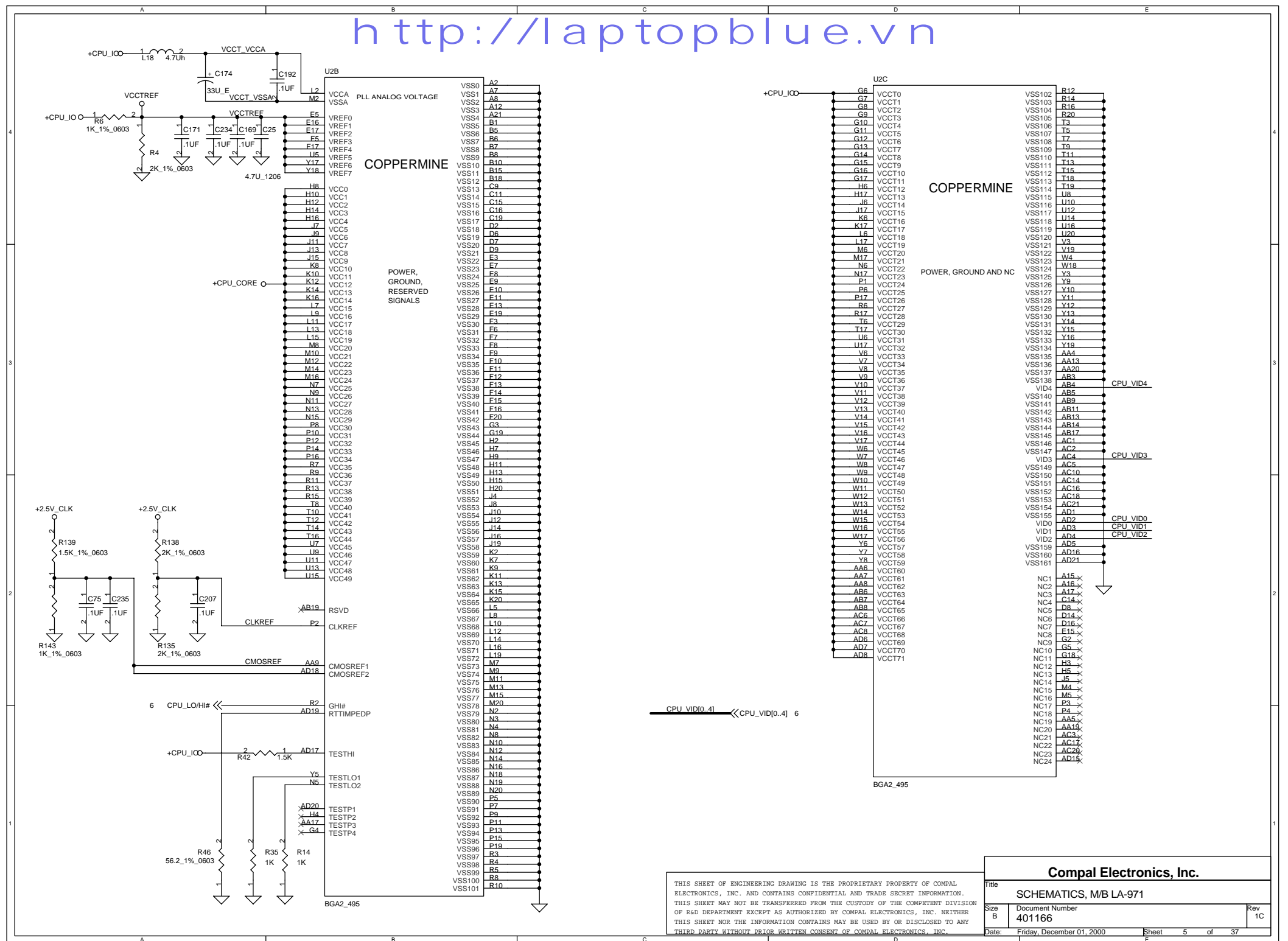
888J2 BLOCK DIAGRAM



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

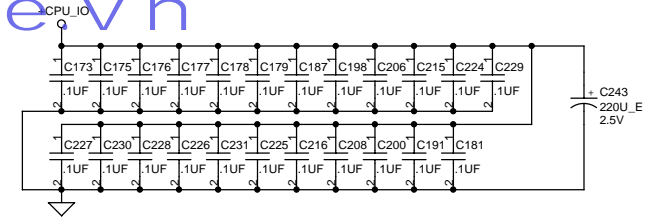
Compal Electronics, Inc.			
Title		SCHEMATICS, M/B LA-971	
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	3 of 37





THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

http://laptopblue.vn



With SpeedStep CPU Support:

Mount: U16: G_ASIC (SA658370000)

U15: 74CBT3383 (SA433830000)

R57, R184, R185, R216 R215: 0ohm
(1.6V for high performance)

Remove:

RP4: 8P4R-0

R223: 0ohm

R54: 0ohm

R232: 31.6Kohm 1%

RP50: 8P4R-0

R214: 10Kohm

RP49: 8P4R-0

R231: 100Kohm 1%

R234: 0ohm

RP56: 8P4R-4.7K

R208: 1Kohm

R214: 1Kohm

R136: 1.5Kohm

R242: 680ohm

R236: 100Kohm

R240: 4.7Kohm

R239: 1.5Kohm

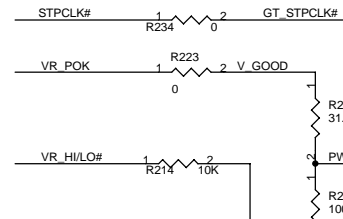
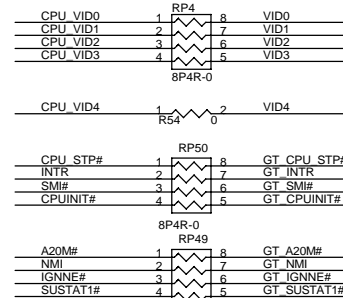
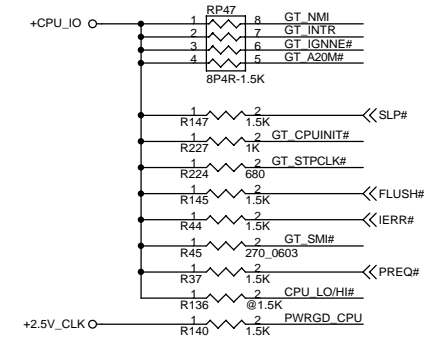
R209: 100Kohm

R207: 100Kohm

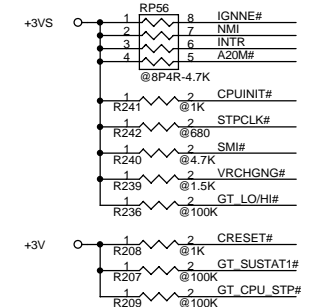
R236: 100Kohm

R140: 1.5Kohm

without Geyserville,
GHI# (CPU_LO/HI#) can
OPEN



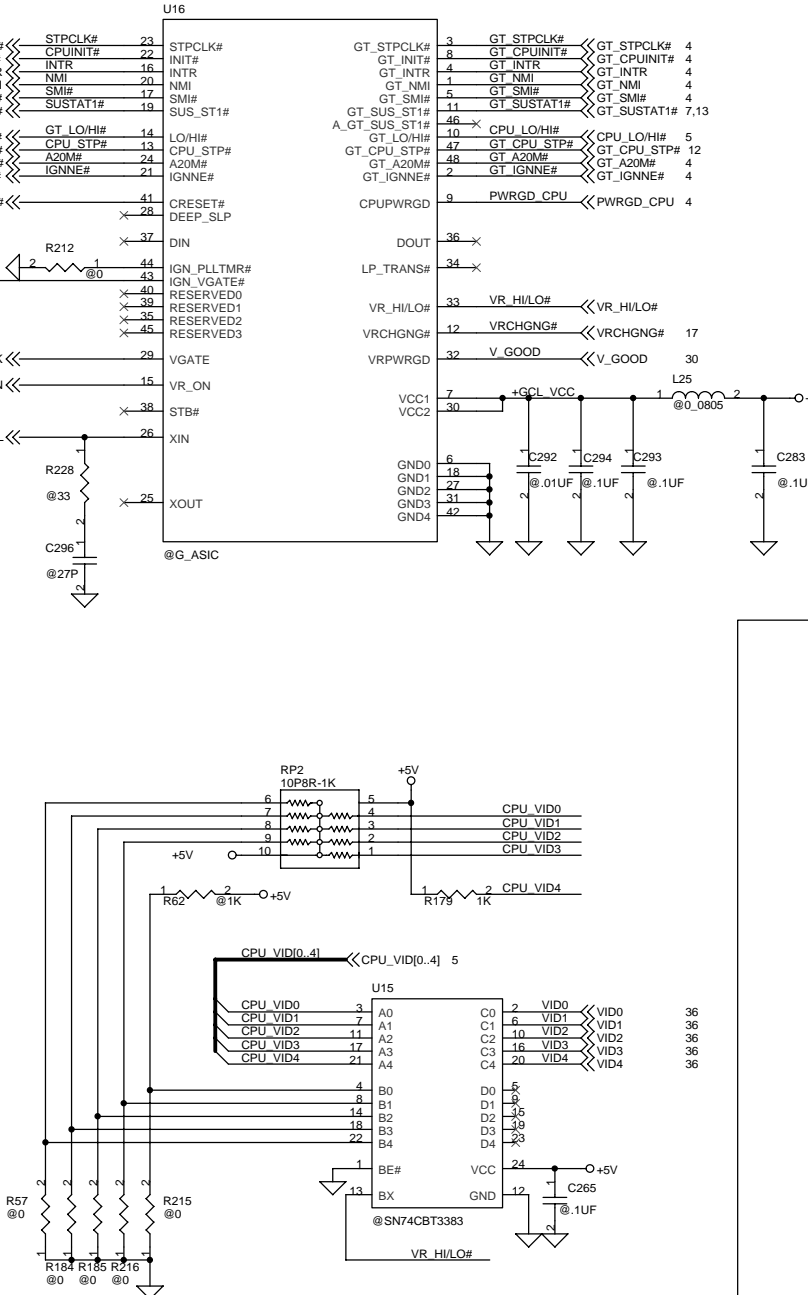
for without Geyserville

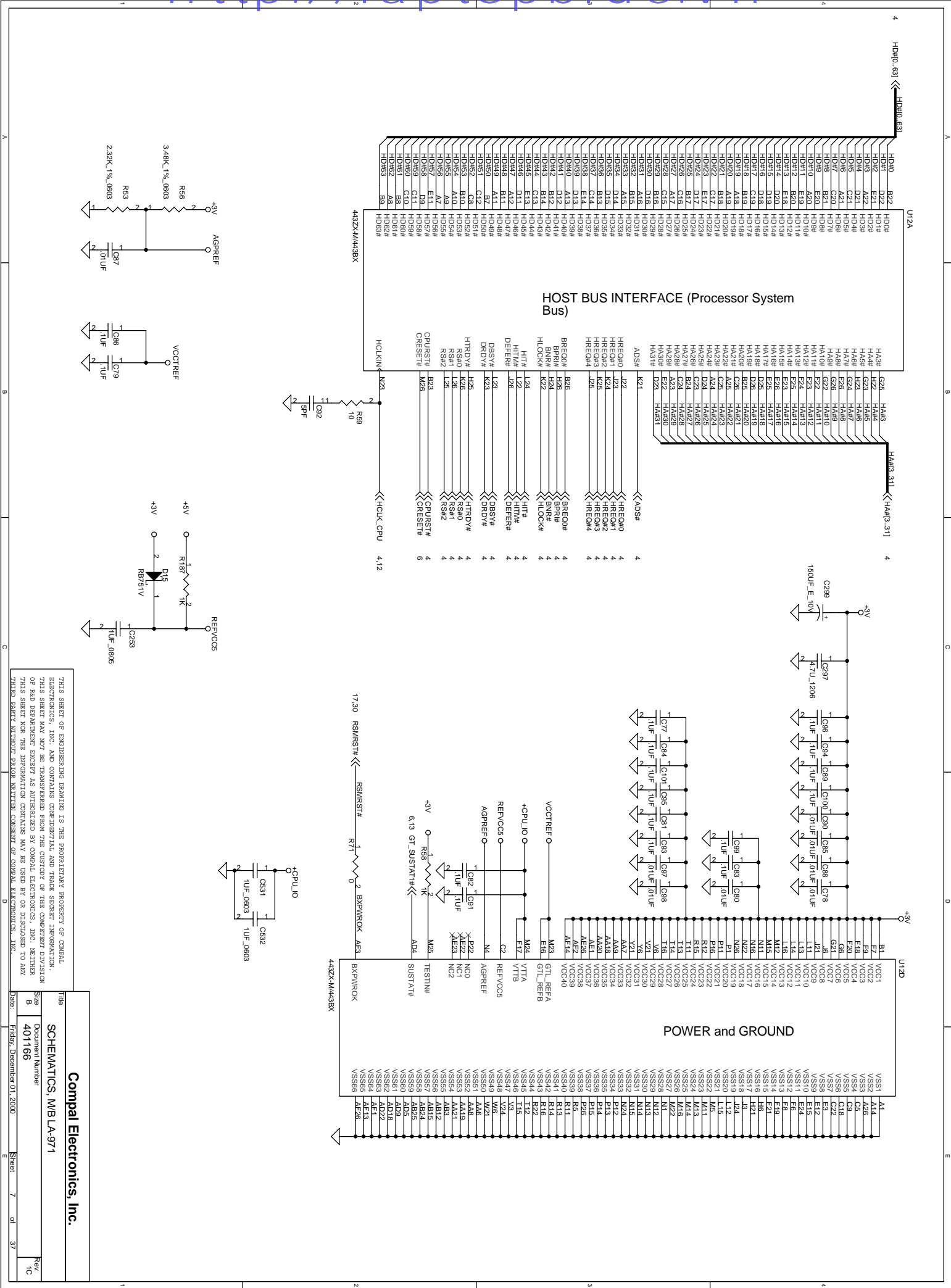


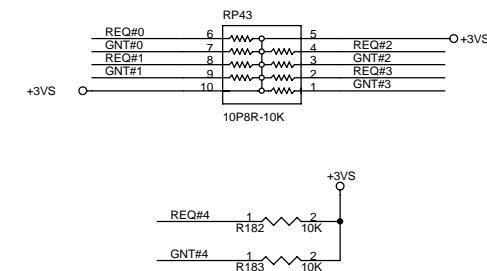
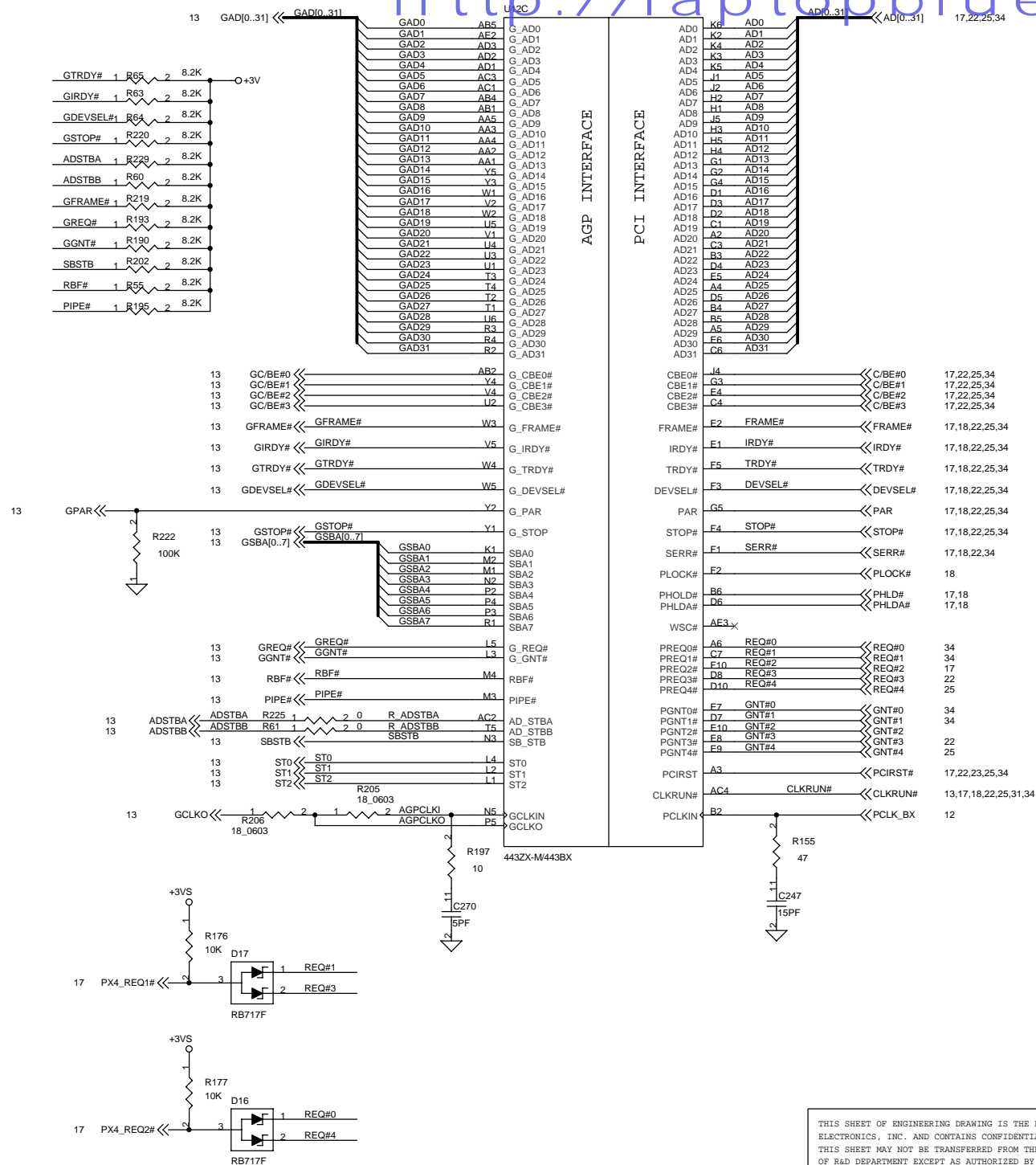
Compal Electronics, Inc.

Title			SCHEMATICS, M/B LA-971
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	6 of 37

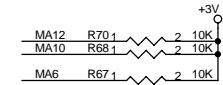
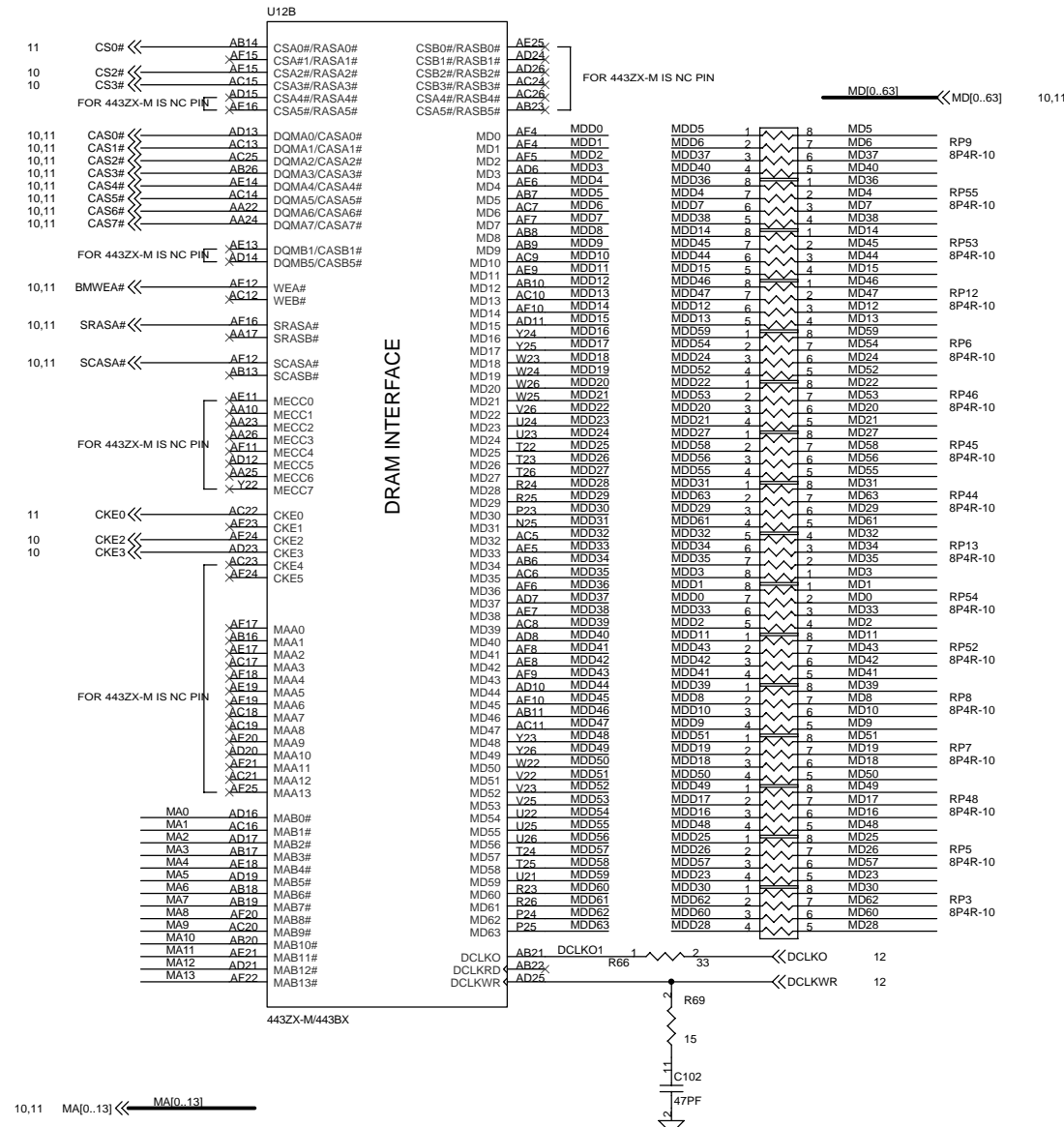
VID[4:0]	CPU VCC
00000	2.00
00001	1.95
00010	1.90
00011	1.85
00100	1.80
00101	1.75
00110	1.70
00111	1.65
01000	1.60
01001	1.55
01010	1.50
01011	1.45
01100	1.40
01101	1.35
01110	1.30
01111	NO CPU
10000	1.275
10001	1.250
10010	1.225
10011	1.200
10100	1.175
10101	1.150
10110	1.125
10111	1.100
11000	1.075
11001	1.050
11010	1.025
11011	1.000
11100	0.975
11101	0.950
11110	0.925
11111	NO CPU







PCI REQ ASSIGNMENT	
REQ#0	MiniPCI(Compal)
REQ#1	MINI PCI
REQ#2	NO USED
REQ#3	PCMCIA CONTROLLER
REQ#4	PCI AUDIO



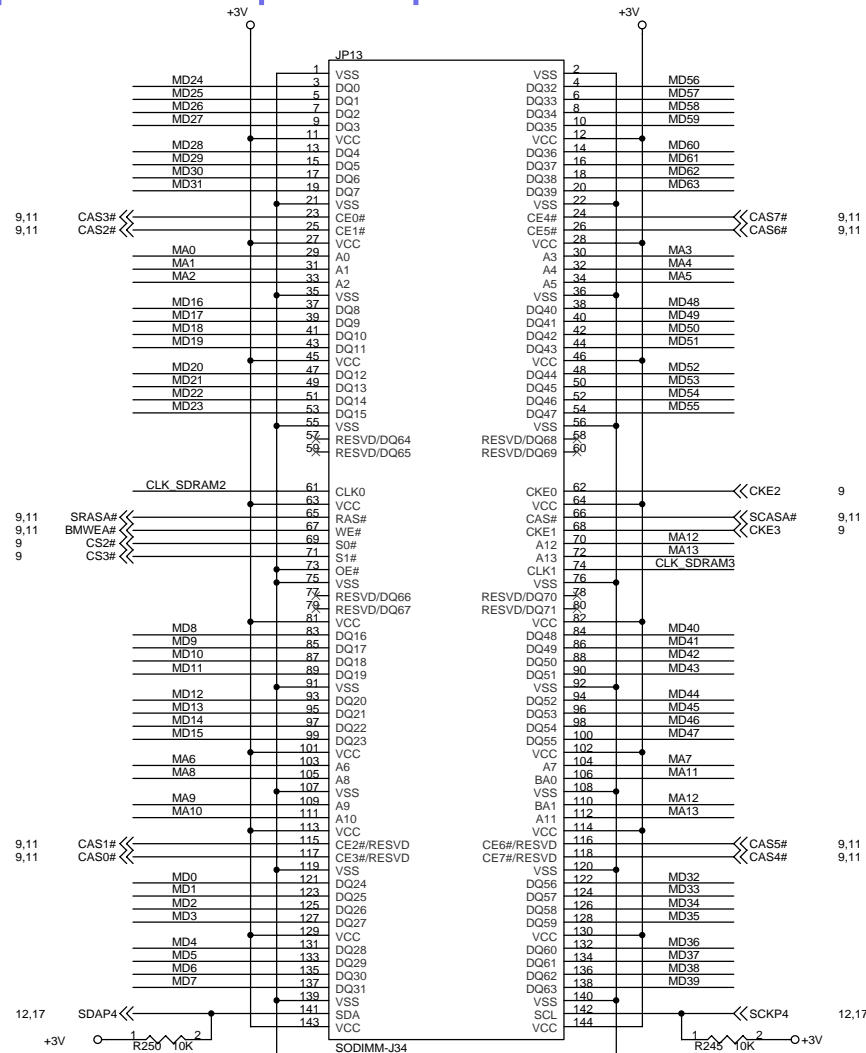
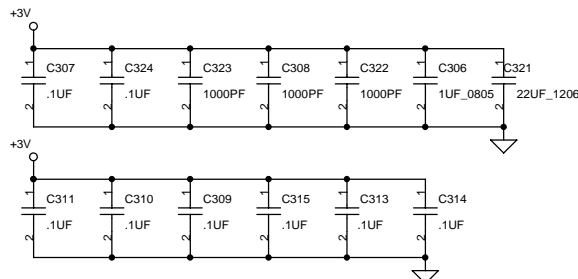
Pin Name	Function	Low	High	Internal Resistor	Status Register
MAB12#	Host Frequency Select	66MHz	100MHz	Pull-down	NBXCFG[13]
MAB10#	Quick Start Select	Stop Clock Mode	Quick Start Mode	Pull-down	PMCR[3]
MAB6#	Host Bus Buffer Mode Select	Desktop GTL+	Mobile Low Power GTL+	Pull-down	none

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

(H: 5.6mm)

PIN 60, 77, 78 Difference

BANK 2,3



SODIMM

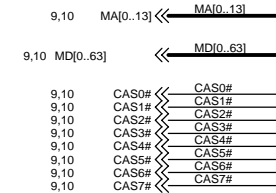
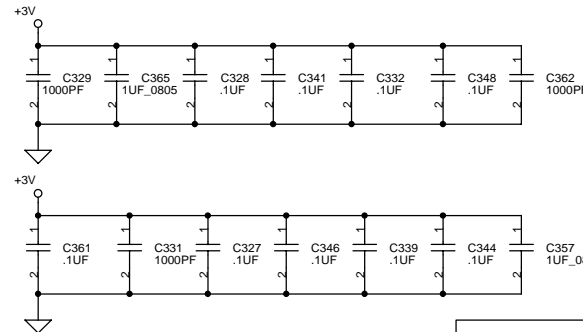
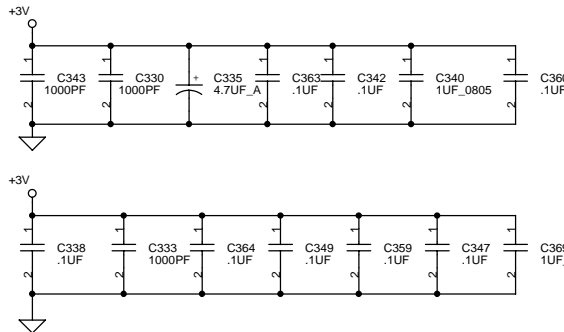
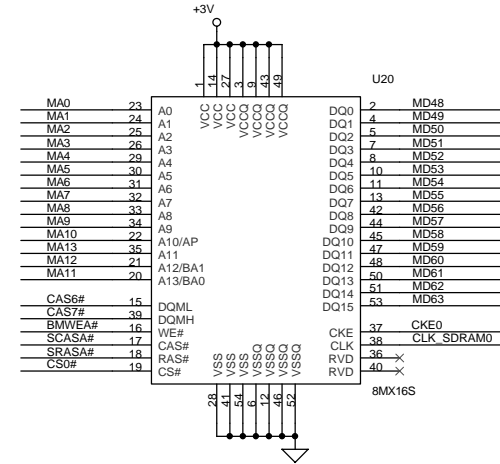
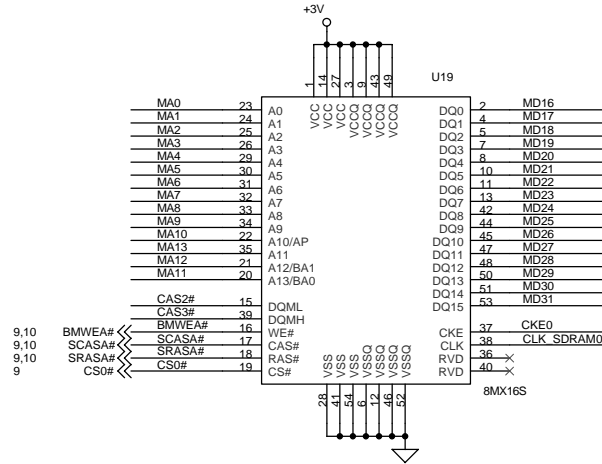
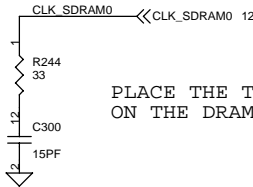
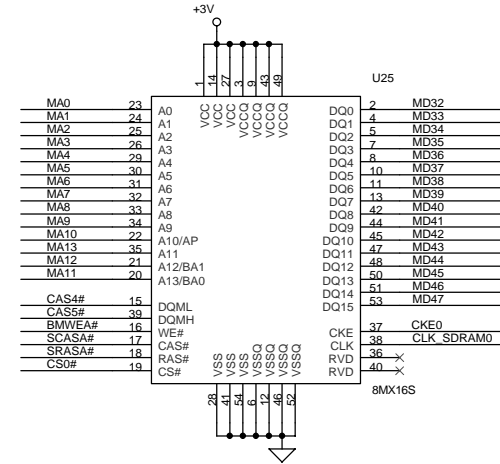
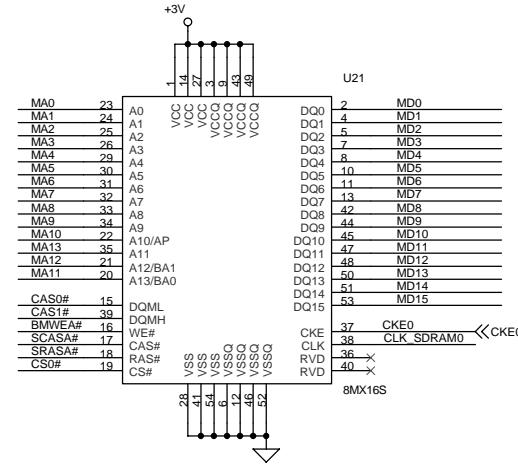
Date: Friday, December 01, 2000 Sheet 10 of 37

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

32MB/64MB SDRAM

64Mbit 4Mx16 : Row: MA0-MA11
Column: MA0-MA7
Bank address: 2
128Mbit 8Mx16 : Row: MA0-MA11
Column: MA0-MA8
Bank address: 2

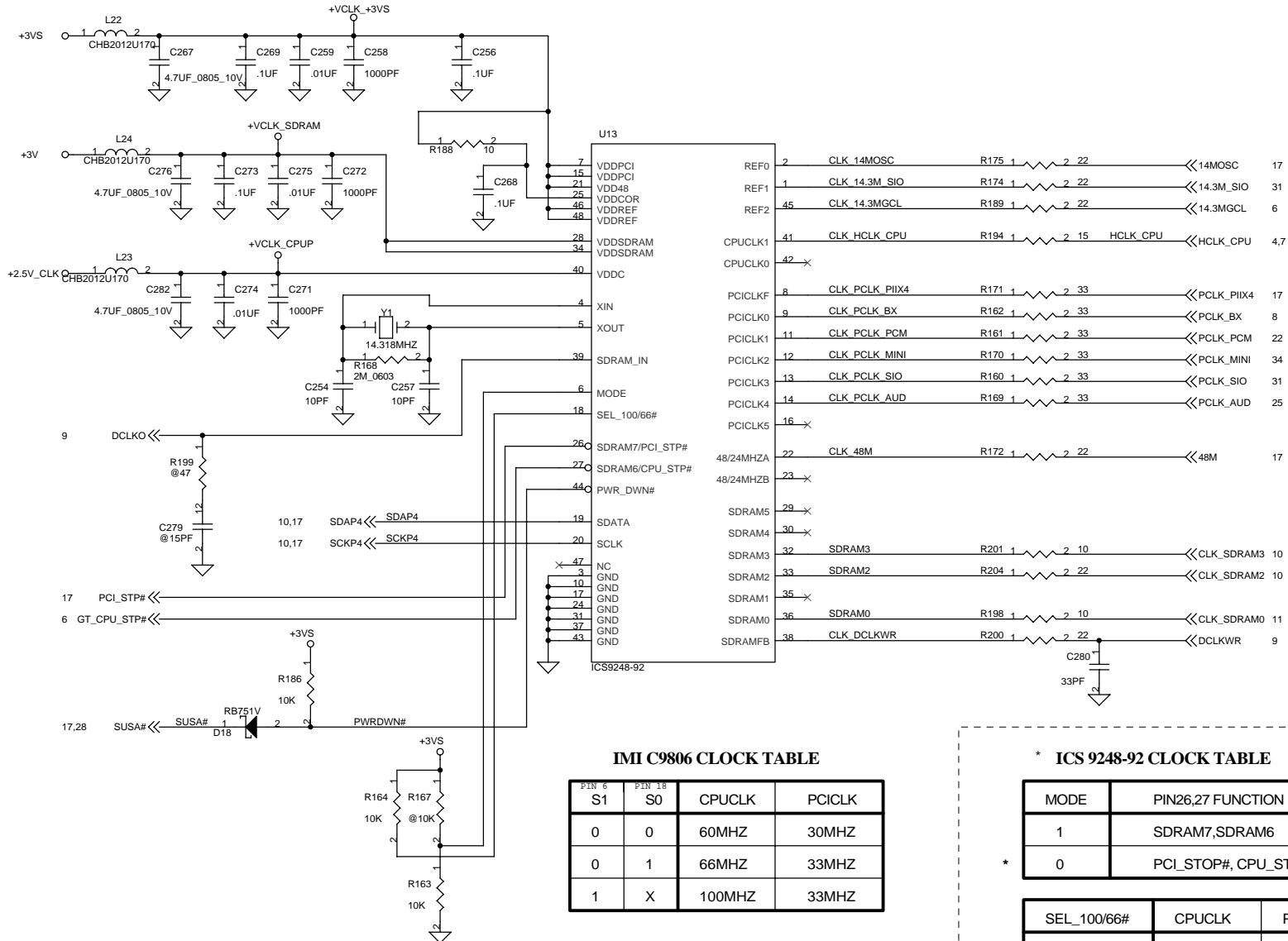
http://laptopblue.vn
BANK0



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.	
Title	SCHEMATICS, M/B LA-971
Size B	Document Number 401166
Date: Friday, December 01, 2000	Sheet 11 of 37

CLOCK GENERATOR & BUFFER



IMI C9806 CLOCK TABLE

PIN 6	PIN 18	CPUCLK	PCICLK
S1	S0		
0	0	60MHZ	30MHZ
0	1	66MHZ	33MHZ
1	X	100MHZ	33MHZ

* ICS 9248-92 CLOCK TABLE

MODE	PIN26,27 FUNCTION
1	SDRAM7,SDRAM6
0	PCI_STOP#, CPU_STOP#

SEL_100/66#	CPUCLK	PCICLK
0	66.6MHZ	33.3MHZ
1	100MHZ	33.3MHZ

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

Title
SCHEMATICS, M/B LA-971

Size B Document Number
401166

Rev 1C

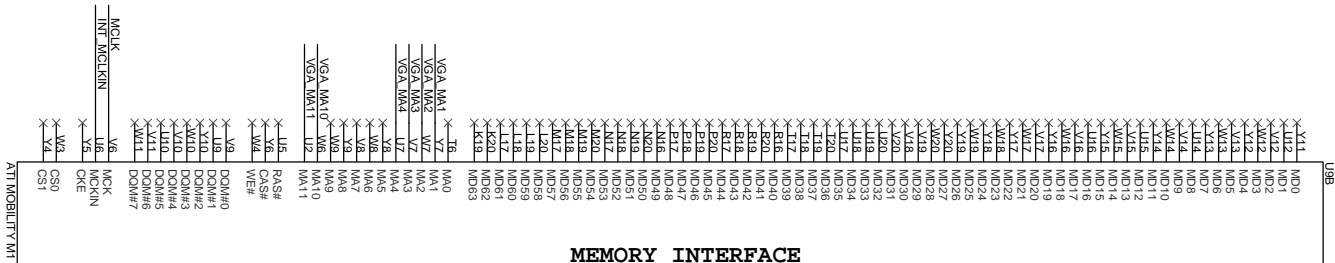
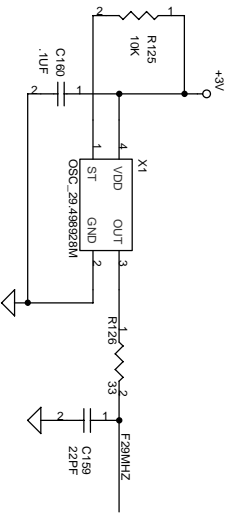
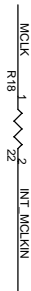
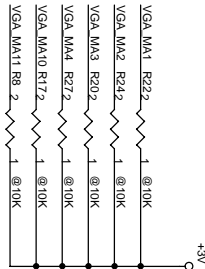
Date: Friday, December 01, 2000

Sheet 12 of 37

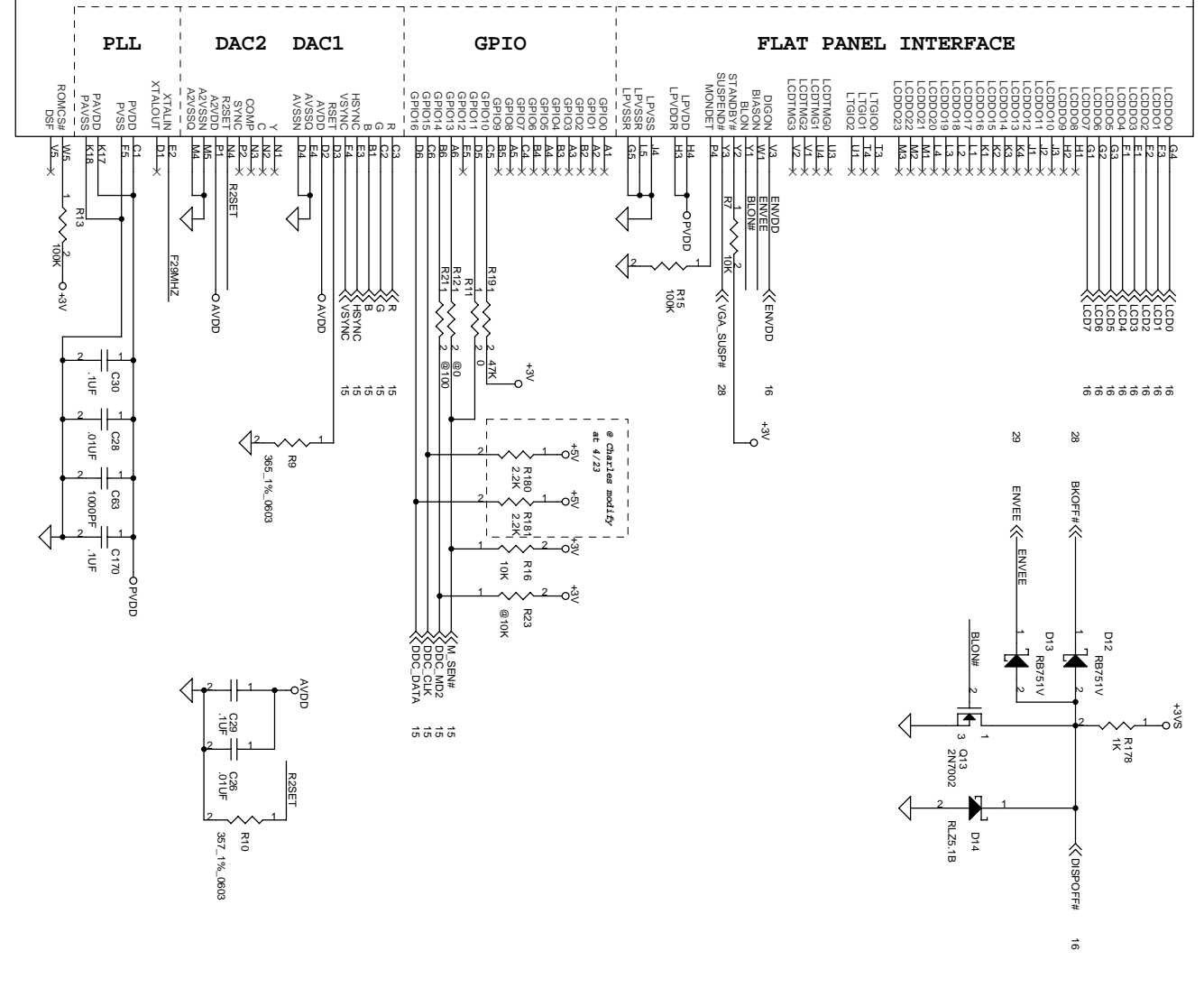


Compal Electronics, Inc.			
Title			
SCHEMATICS, M/B LA-971			
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	13 of 37

MA2	MA1	AGP CLOCK SKEW ADJUSTMENT	(DEFAULT)
DNI	DNI	INTERNAL VS EXTERNAL CLOCKS	(DEFAULT)
MA4	MA3	AGP CLOCK SKEW ADJUSTMENT	(DEFAULT)
DNI	DNI	BETWEEN X1 AND X2 CLOCKS	(DEFAULT)
MA11	MA10	AGP PLL VCO GAIN	(DEFAULT)
DNI	DNI	VCO GAIN ADJUSTMENT	(DEFAULT)



MEMORY INTERFACE



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

File	SCHEMATICS, MB/LA-971
Step	Document Number
B	401166
Date	Friday, December 01, 2000
Sheet	14 of 37
Rev	1C

Compal Electronics, Inc.

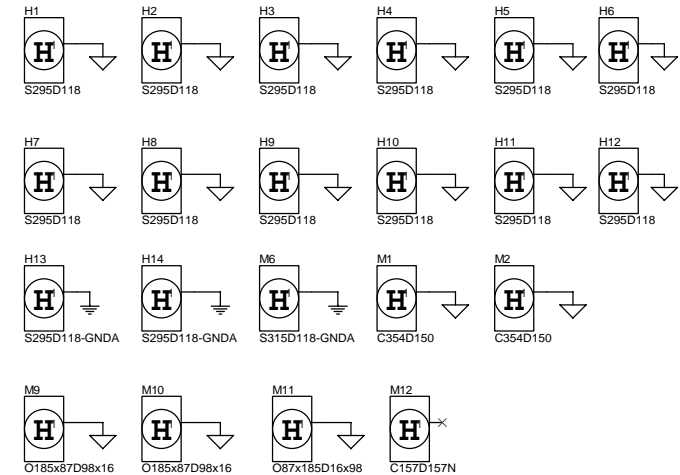
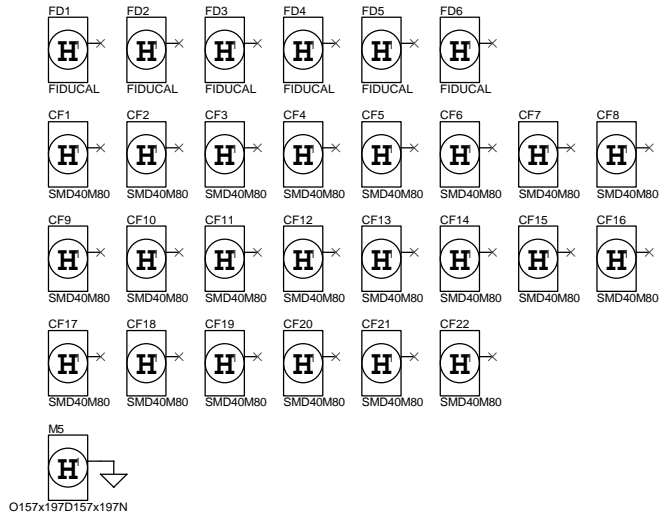
D

Compal Electronics, Inc.			
Title SCHEMATICS, M/B LA-971			
Size B	Document Number 401166	Rev 1C	
Date:	Friday, December 01, 2000	Sheet 15	of 37

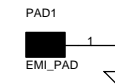
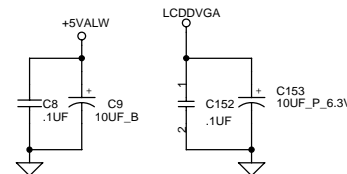
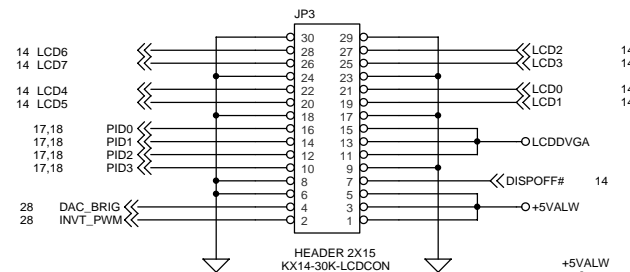
SI2301DS: P CHANNEL
VGS: -4.5V, RDS: 130 mOHM
VGS: -2.5V, RDS: 190mOHM
Id(MAX): 2.3A
VGS(MAX): +-8V

SI2302DS: N CHANNEL
VGS: 4.5V, RDS: 85 mOHM
VGS: 2.5V, RDS: 115mOHM
Id(MAX): 2.8A
VGS(MAX): +-8V

FOR LAYOUT ONLY



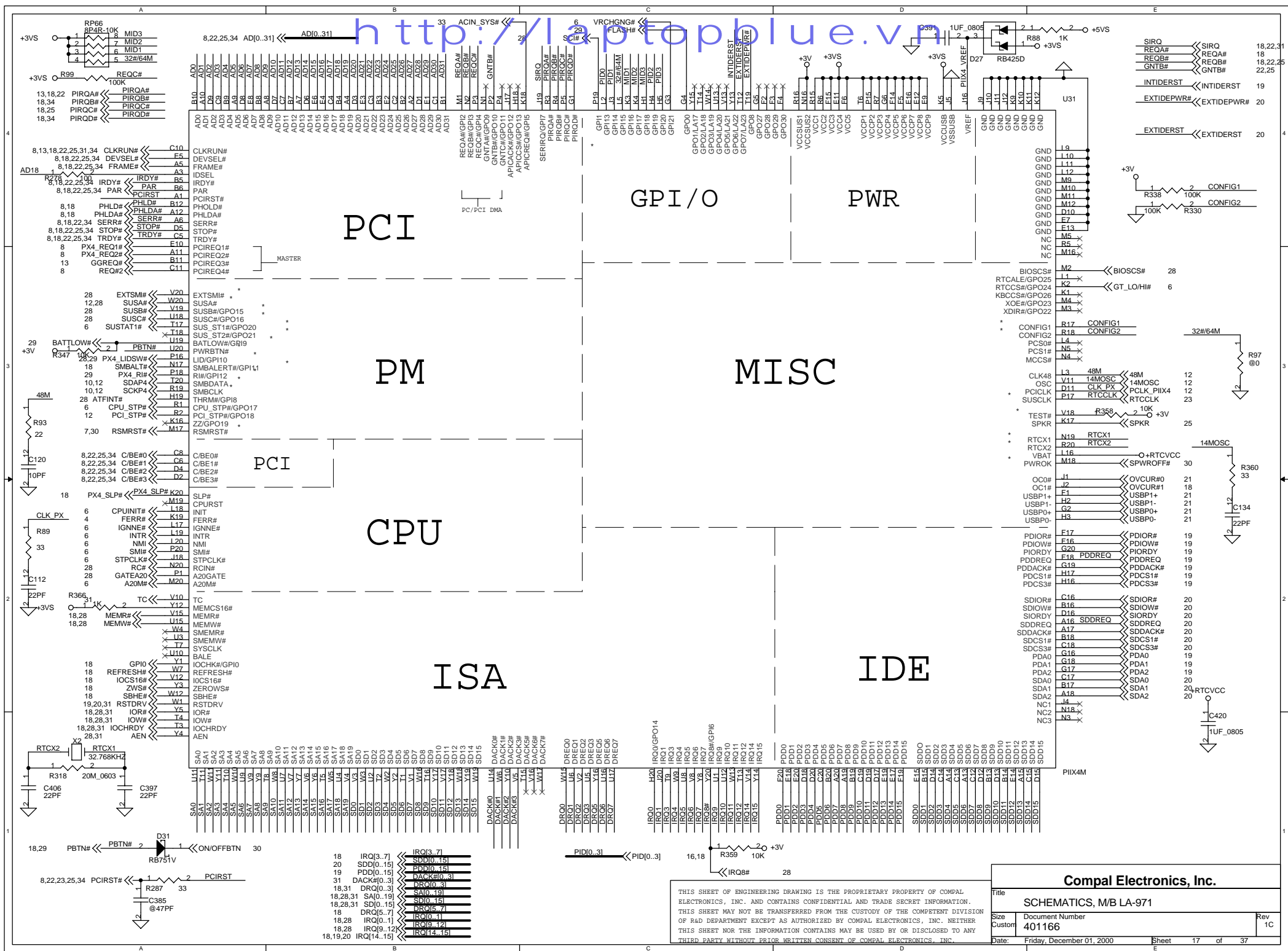
LCD & INVERTER Connector



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

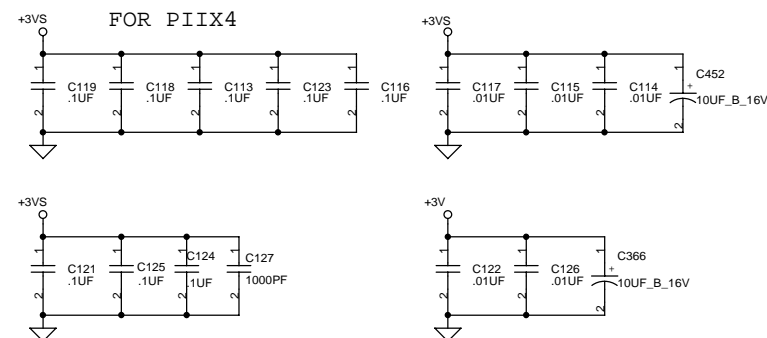
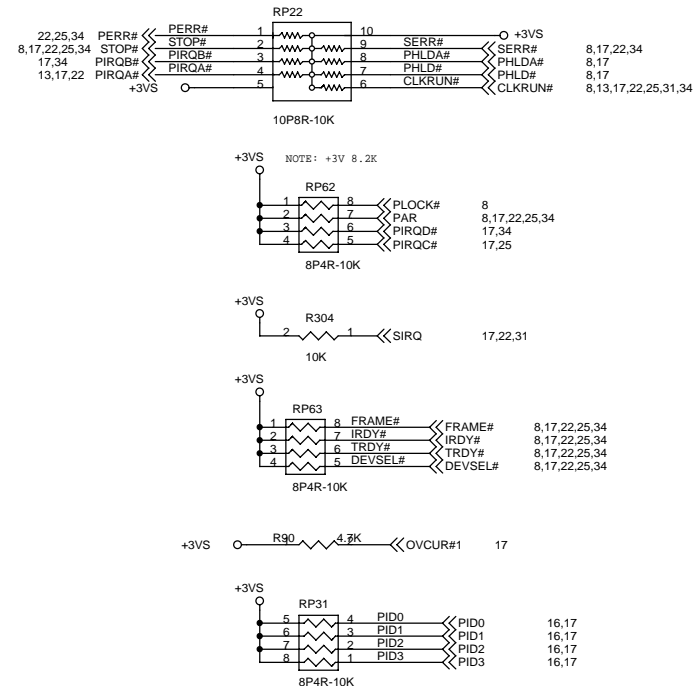
Compal Electronics, Inc.

Title		SCHEMATICS, M/B LA-971
Size	Document Number	Rev
B	401166	1C
Date:	Friday, December 01, 2000	Sheet 16 of 37



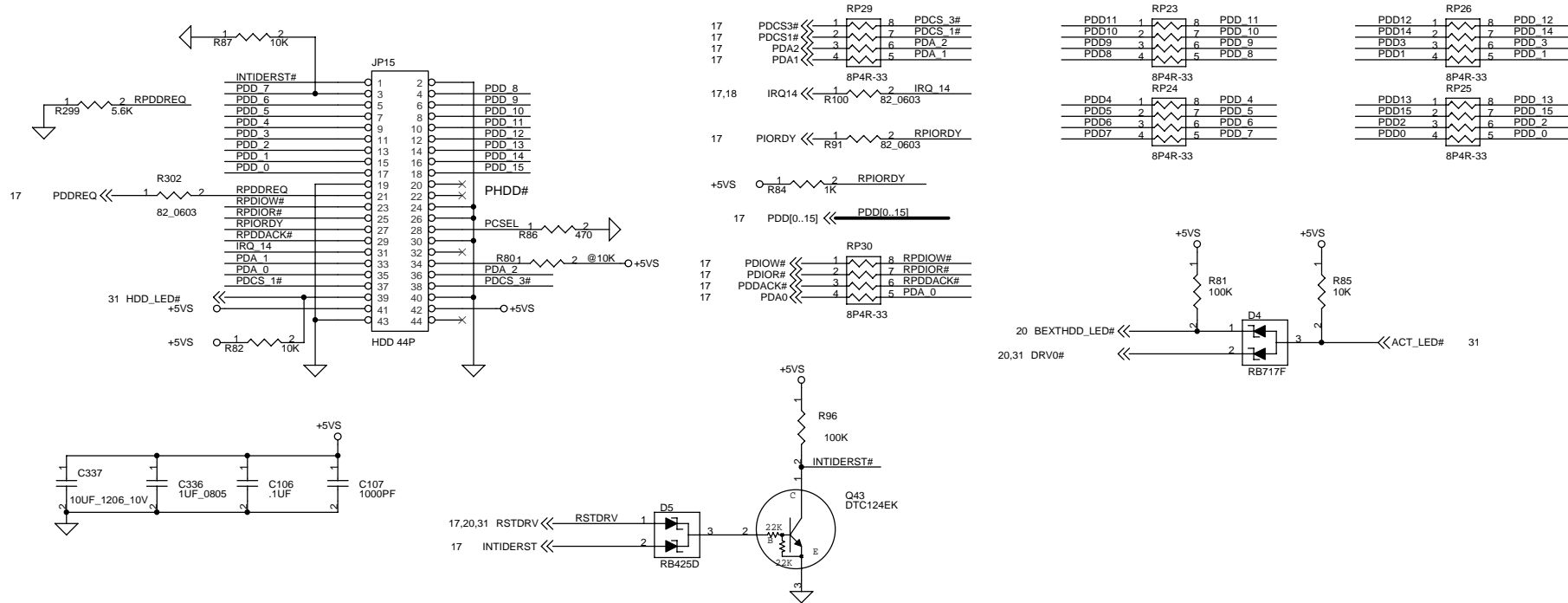
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
SCHEMATICS, M/B LA-971			
Title	Document Number	Rev	
Size	401166	1C	
Custom			
Date	Friday, December 01, 2000	Sheet	17 of 37



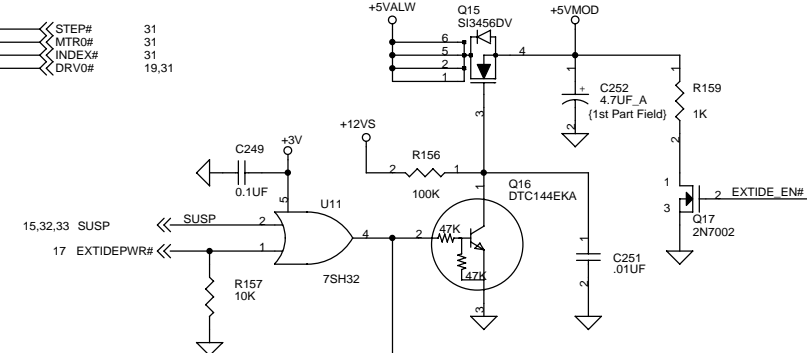
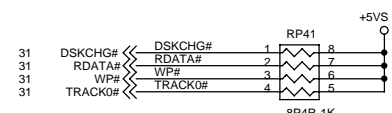
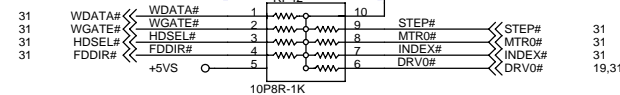
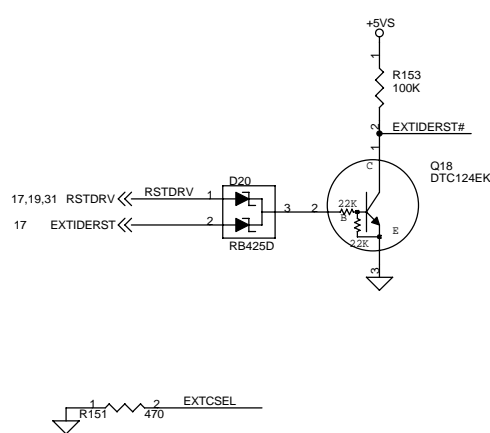
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

IDE CONN.

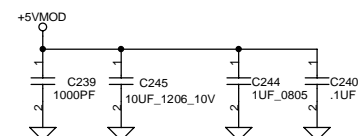
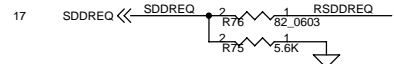
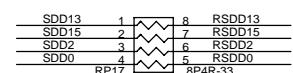
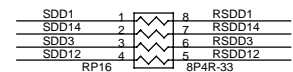
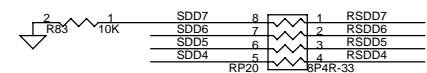
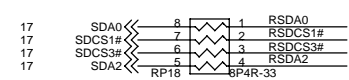
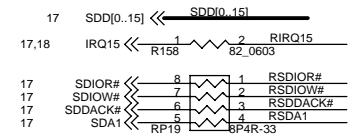
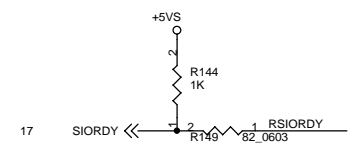
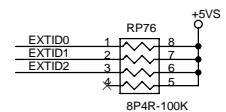
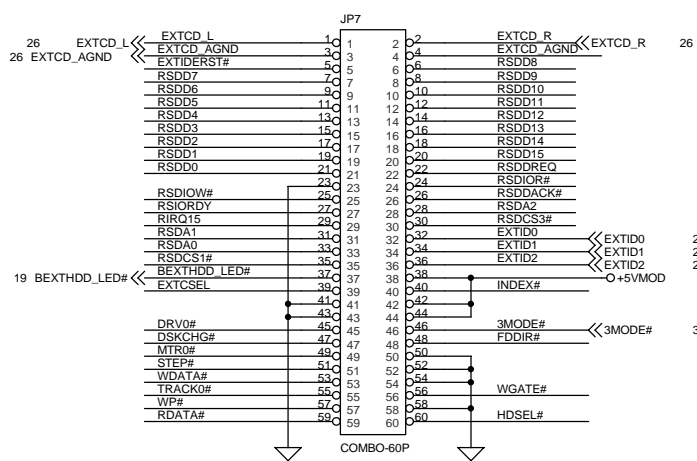


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
SCHEMATICS, M/B LA-971			
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	19 of 37

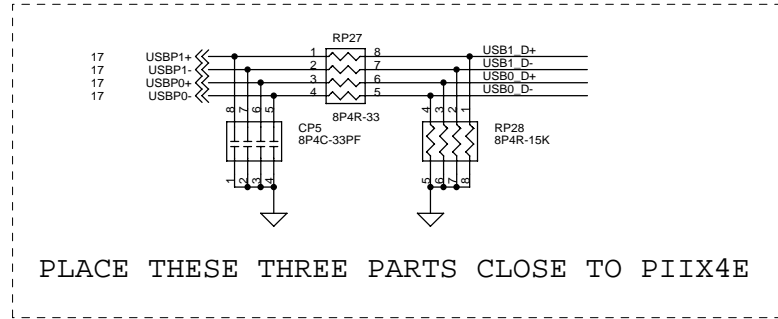


SI3456DV: N CHANNEL
VGS: 4.5V, RDS: 65 mOHM
Id(MAX): 5.1A
VGS, +20V

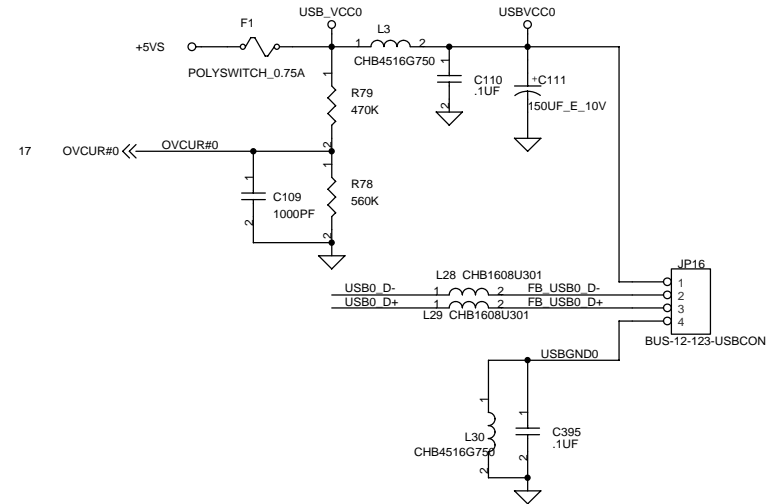


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

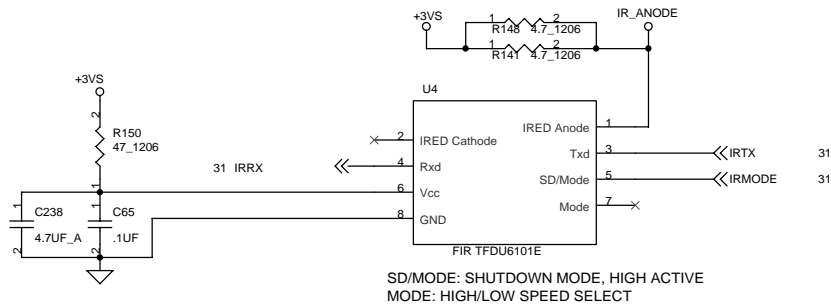
Compal Electronics, Inc.			
Title SCHEMATICS, M/B LA-971			
Size Custom	Document Number 401166		Rev 1C
Date:	Friday, December 01, 2000	Sheet	20 of 37



USB CONNECTOR



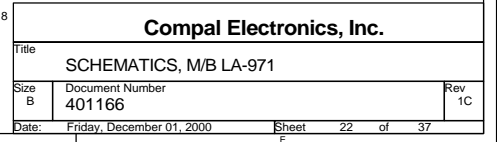
FIR Module



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION.
THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC.
NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title		SCHEMATICS, M/B LA-971	
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	21 of 37

h t t p : / / l a b . t o p b l u e . n e



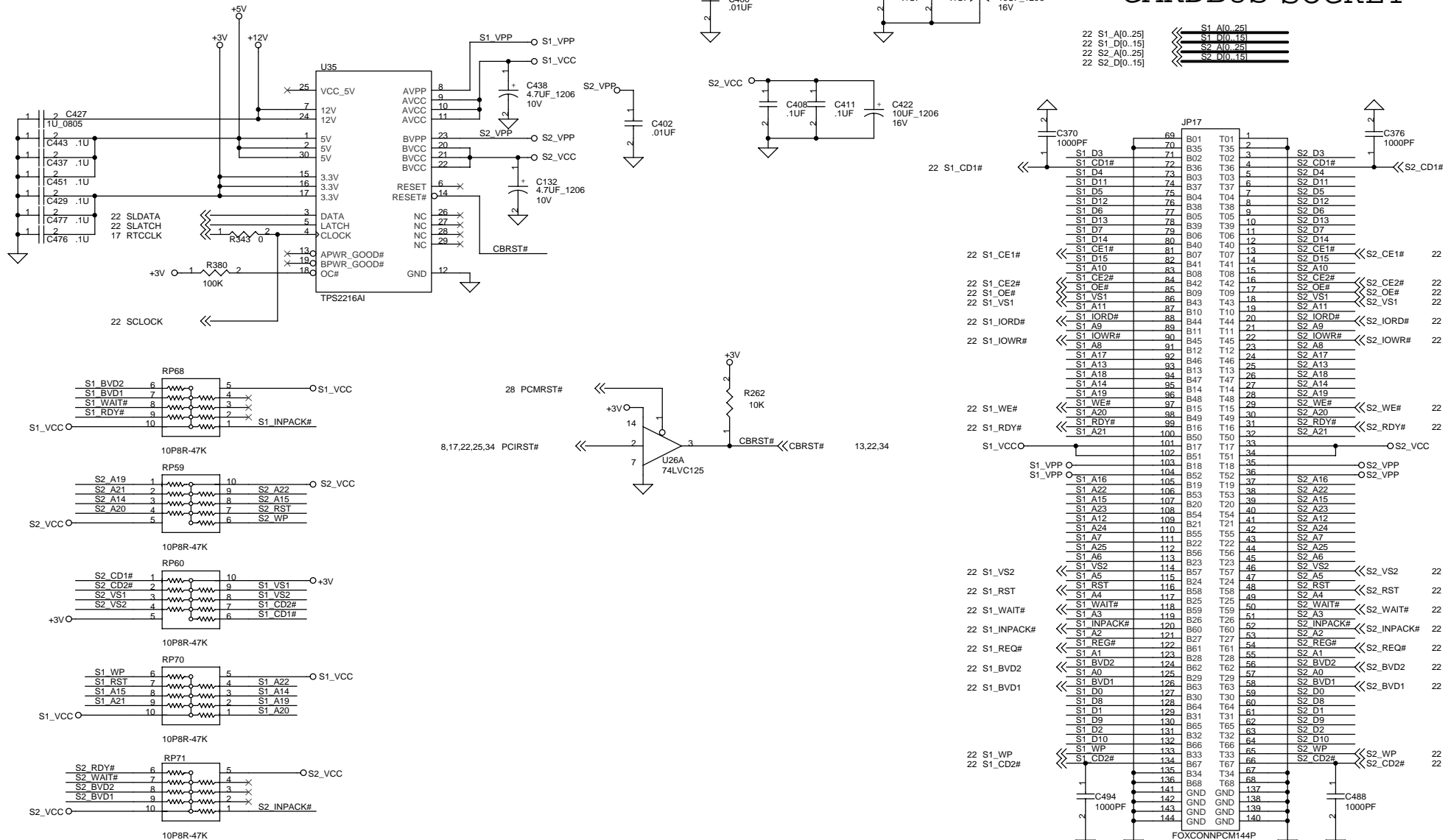
PORT to Slot A:

S1_VPP

S1_VCC

S1_VCO

CARDBUS SOCKET

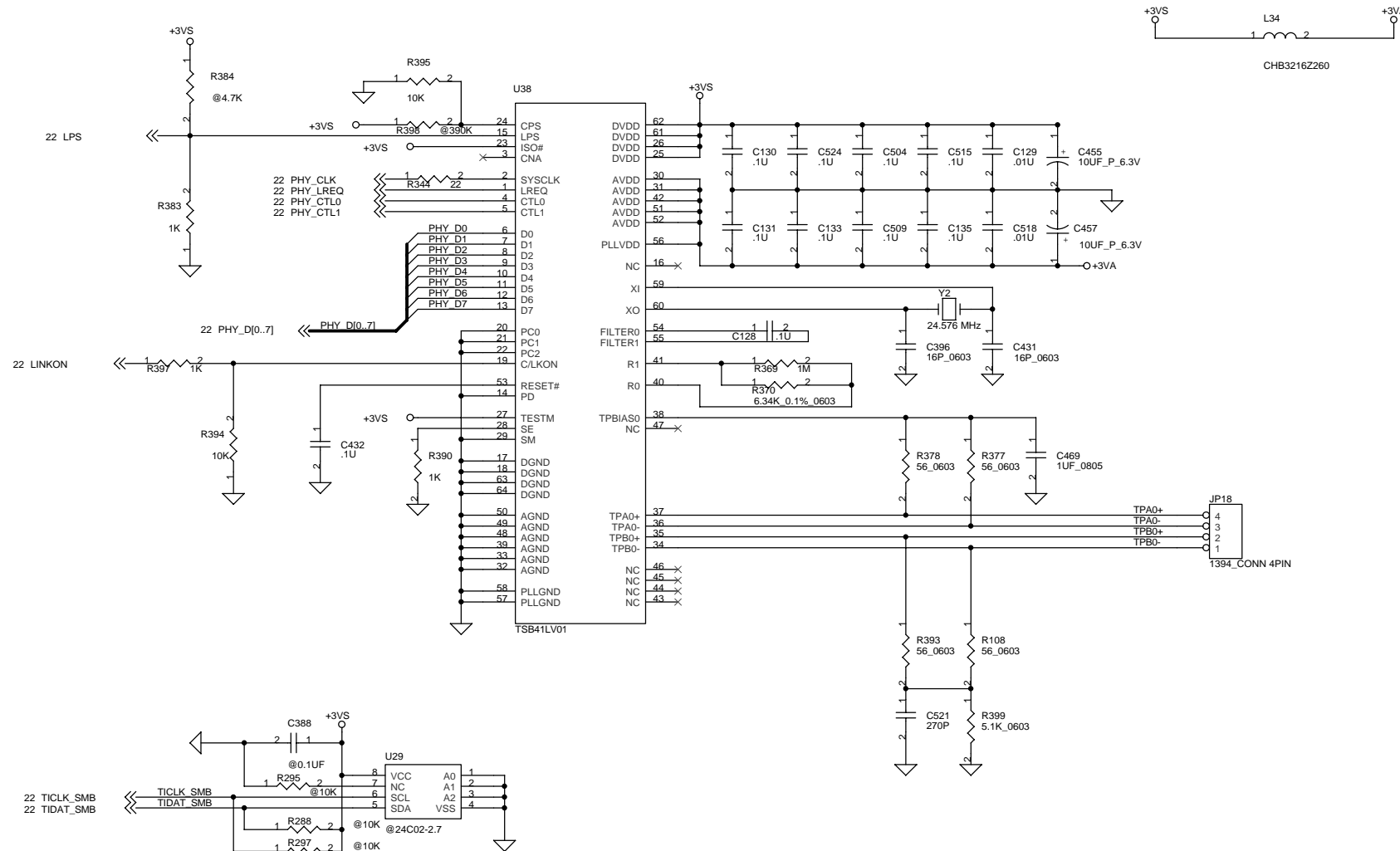


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RAD DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

Title	SCHEMATICS, M/B LA-971
-------	------------------------

Size B	Document Number 401166	Rev 1C
Date:	Friday, December 01, 2000	Sheet 23 of 37

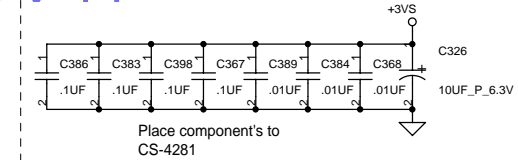


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

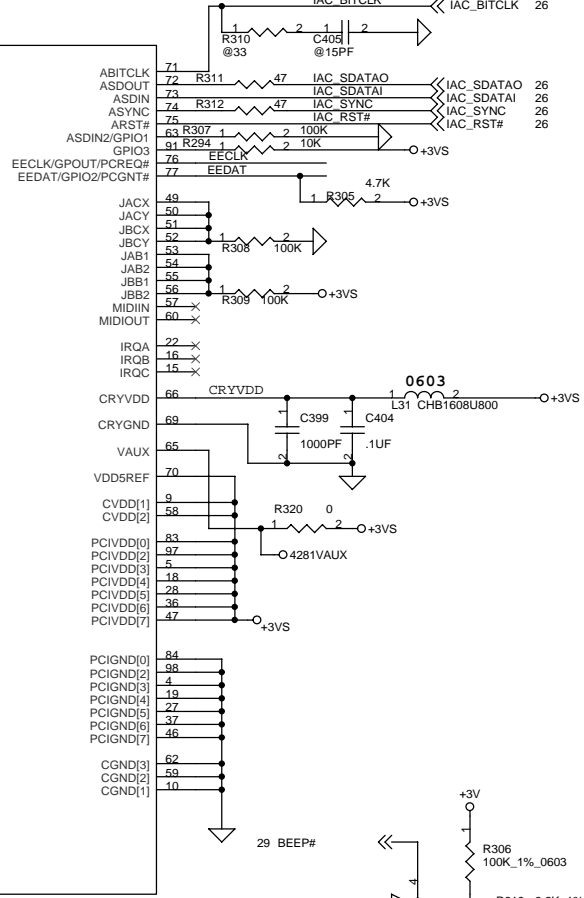
Compal Electronics, Inc.			
Title		SCHEMATICS, M/B LA-971	
Size	Document Number	Rev	
B	401166	1C	
Date:	Friday, December 01, 2000	Sheet	24 of 37

8,17,22,34 AD[0..31] << AD[0..31]

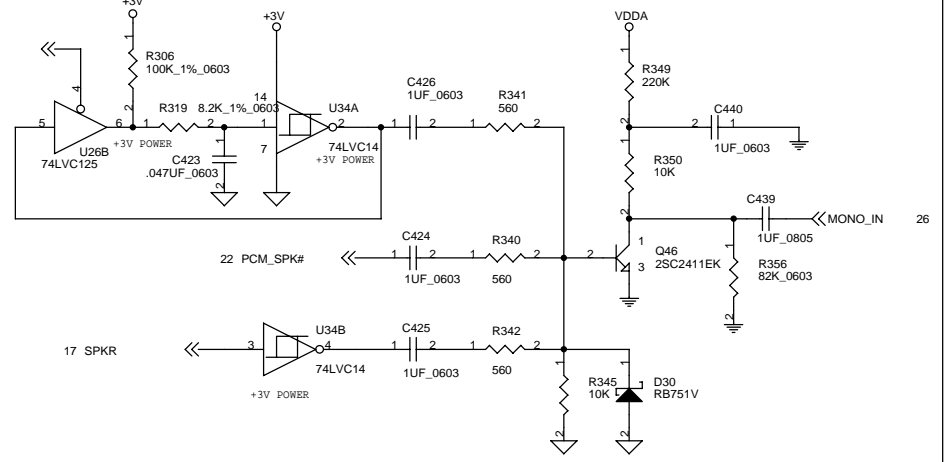
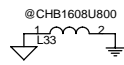
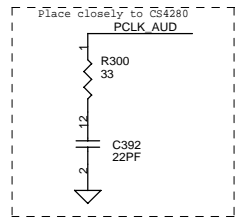
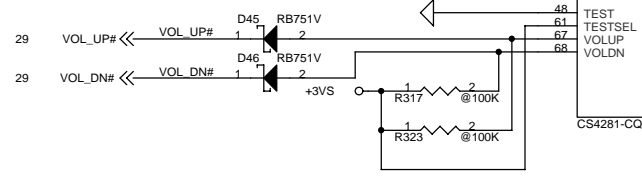
EECLK 1 R313 @33 << REC8# 17,18,22
EEDAT 1 R314 @33 << GNTB# 17,22



AD0	45	AD0
AD1	44	AD1
AD2	43	AD2
AD3	42	AD3
AD4	41	AD4
AD5	40	AD5
AD6	39	AD6
AD7	38	AD7
AD8	34	AD8
AD9	33	AD9
AD10	32	AD10
AD11	31	AD11
AD12	30	AD12
AD13	29	AD13
AD14	28	AD14
AD15	26	AD15
AD16	25	AD16
AD17	24	AD17
AD18	23	AD18
AD19	22	AD19
AD20	21	AD20
AD21	20	AD21
AD22	19	AD22
AD23	18	AD23
AD24	17	AD24
AD25	16	AD25
AD26	15	AD26
AD27	14	AD27
AD28	13	AD28
AD29	12	AD29
AD30	11	AD30
AD31	10	AD31

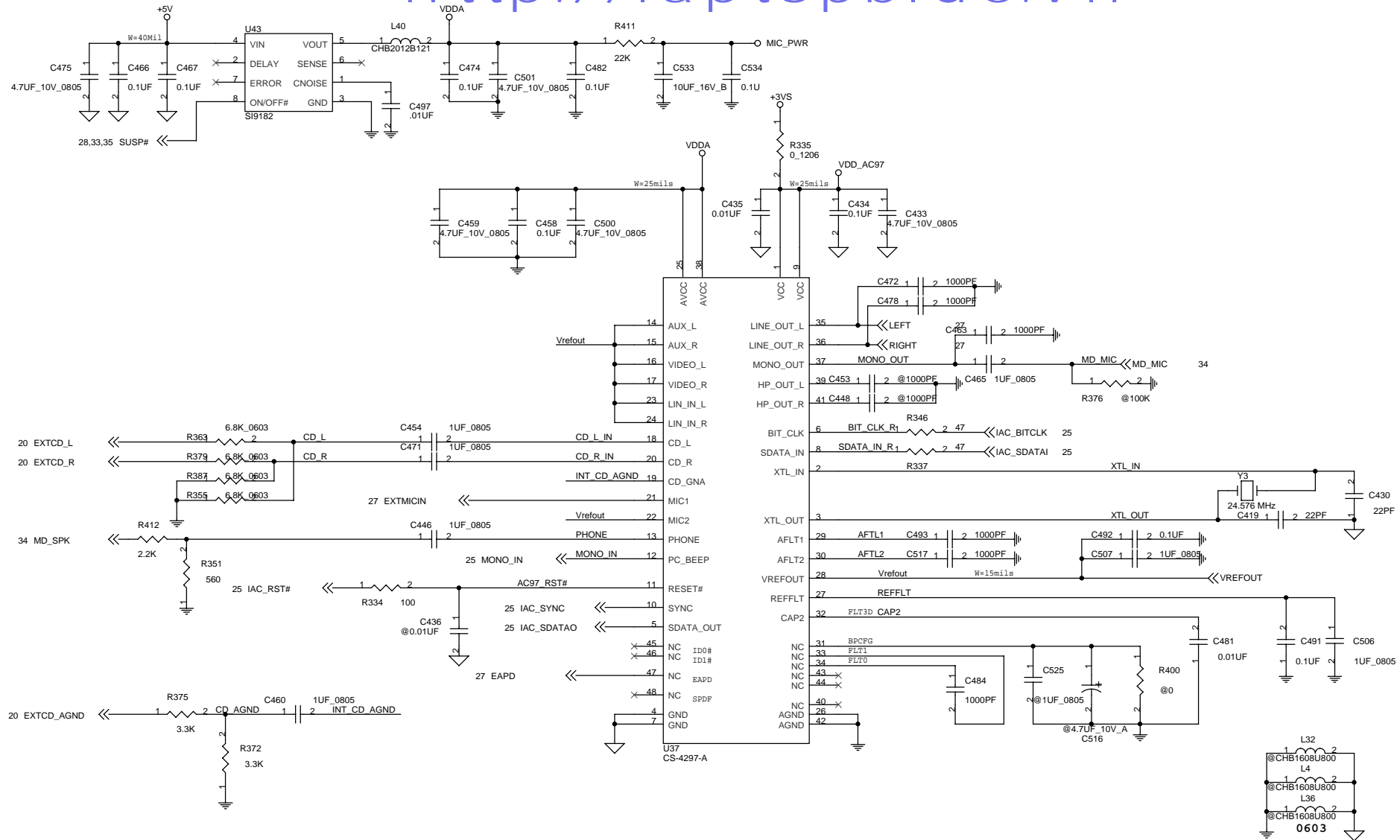


8,17,22,34	C/BE#0	<< C/BE[0]#
8,17,22,34	C/BE#1	<< C/BE[1]#
8,17,22,34	C/BE#2	<< C/BE[2]#
8,17,22,34	C/BE#3	<< C/BE[3]#
17,18	PIRQC#	<< INTA#
8,17,22,23,34	PCIRST#	<< RST#
12	PCLK_AUD	<< PCLK_AUD
8	GNT#	<< GNT#
8	REQ#	<< REQ#
AD19	8,13,17,18,22,31,34	CLKRUN#
8,17,18,22,34	FRAME#	<< FRAME#
8,17,18,22,34	IRDY#	<< IRDY#
8,17,18,22,34	TRDY#	<< TRDY#
8,17,18,22,34	DEVSEL#	<< DEVSEL#
8,17,18,22,34	STOP#	<< STOP#
18,22,34	PERR#	<< PERR#
8,17,18,22,34	PAR	<< PAR
22,29,34	PME#	<< PME#

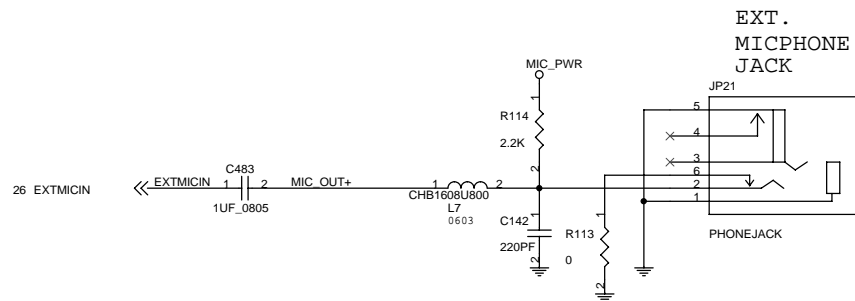
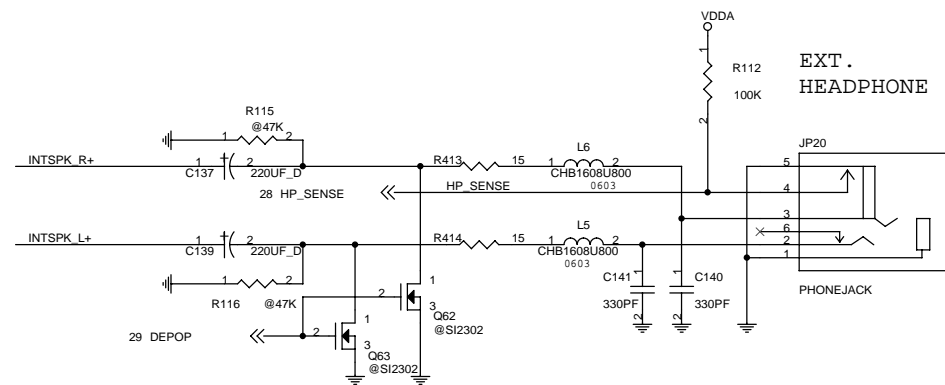
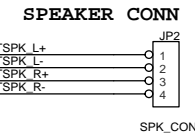
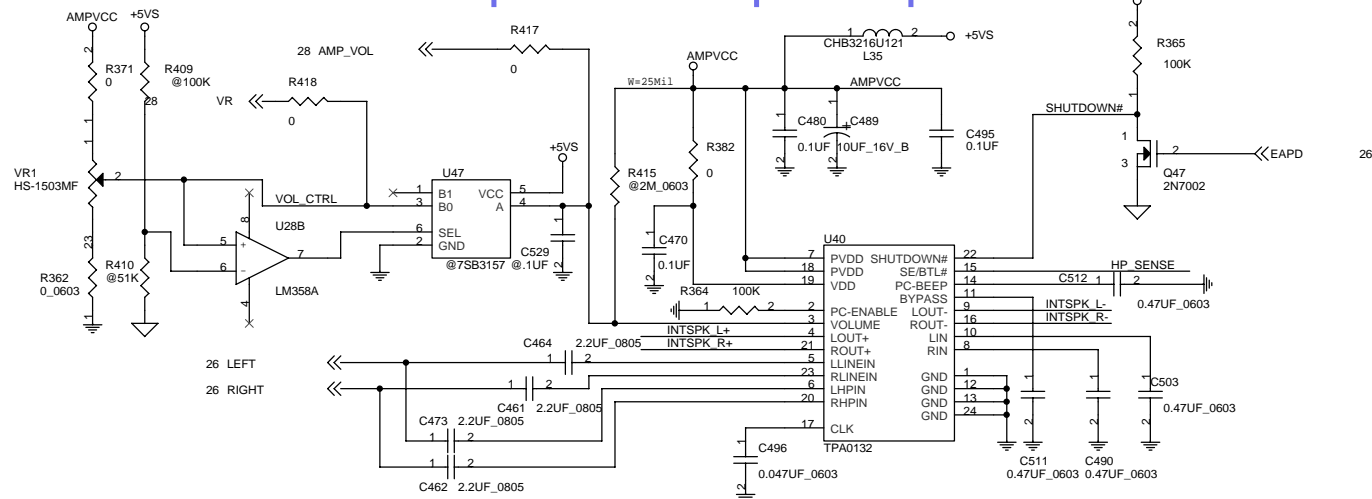


Compal Electronics, Inc.			
SCHEMATICS, M/B LA-971			
Title	Document Number	Rev	
Size	401166	1C	
Date:	Friday, December 01, 2000	Sheet	25 of 37

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

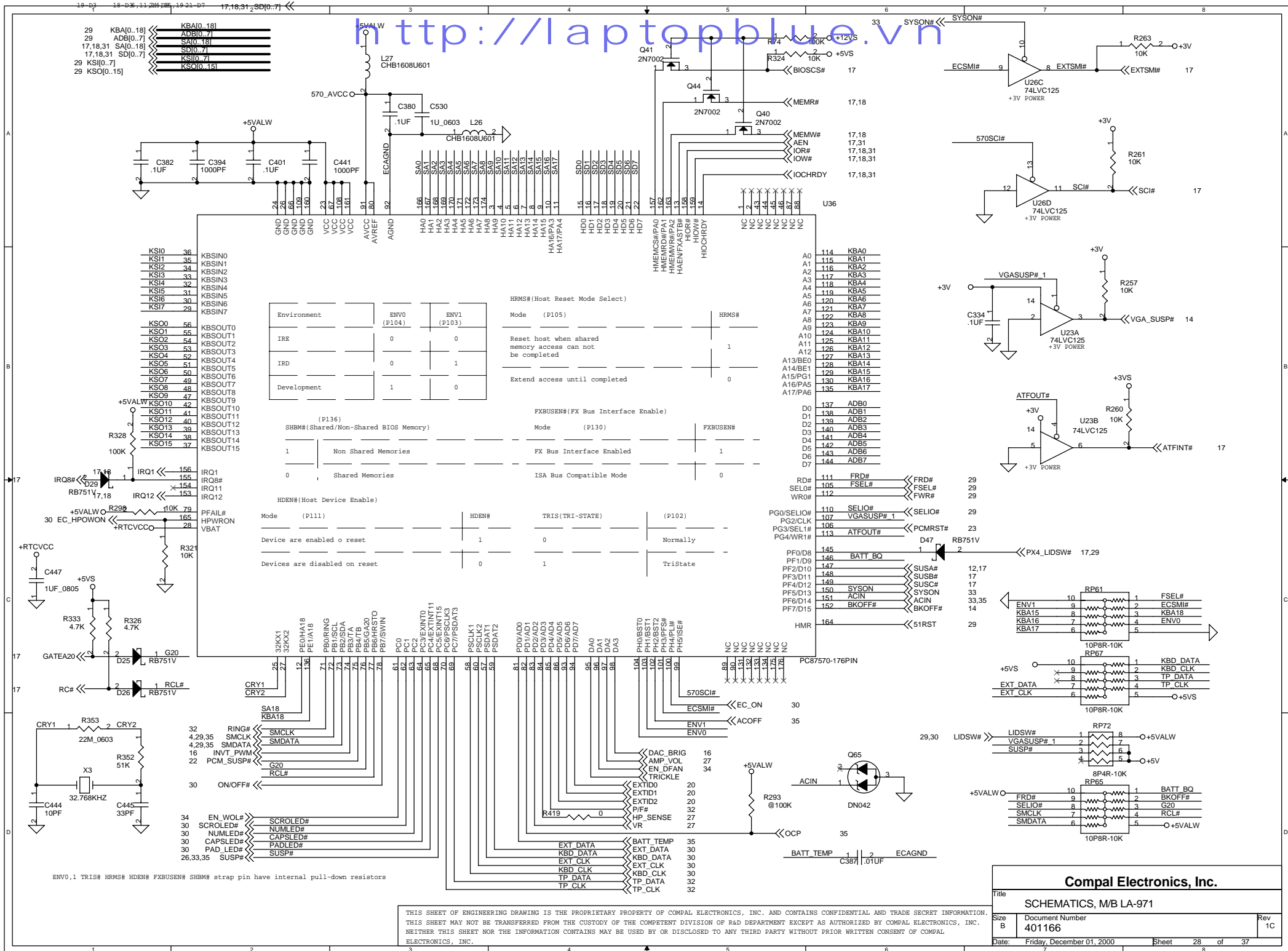


VOLUME	GAIN of Amplifier(db)
0.61~0.73V	10
0.73~0.84V	8
0.84V~0.95V	6
0.95V~1.06V	4
1.06~1.17V	2
1.17~1.28V	0
1.28~1.39V	-2
1.39~1.5V	-4
1.5~1.62V	-6



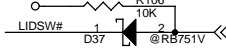
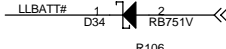
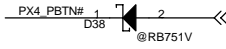
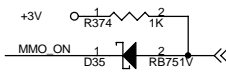
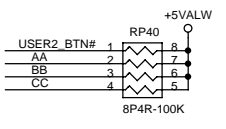
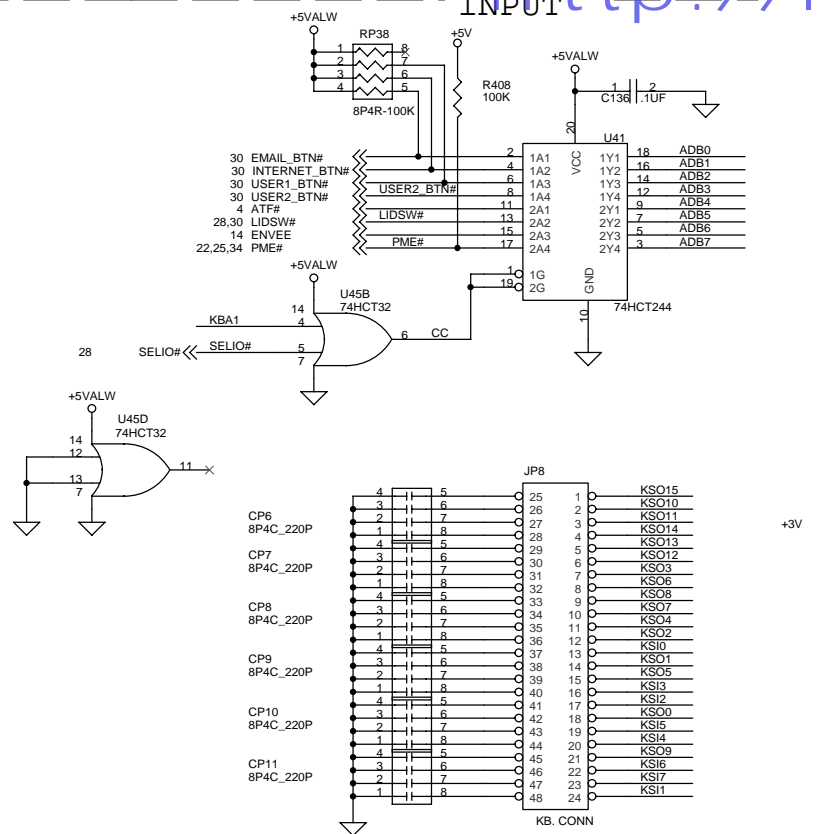
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.	
Title	SCHEMATICS, M/B LA-971
Size	Document Number
Custom	401166
Date	Friday, December 01, 2000
Sheet	27 of 37
Rev	1C

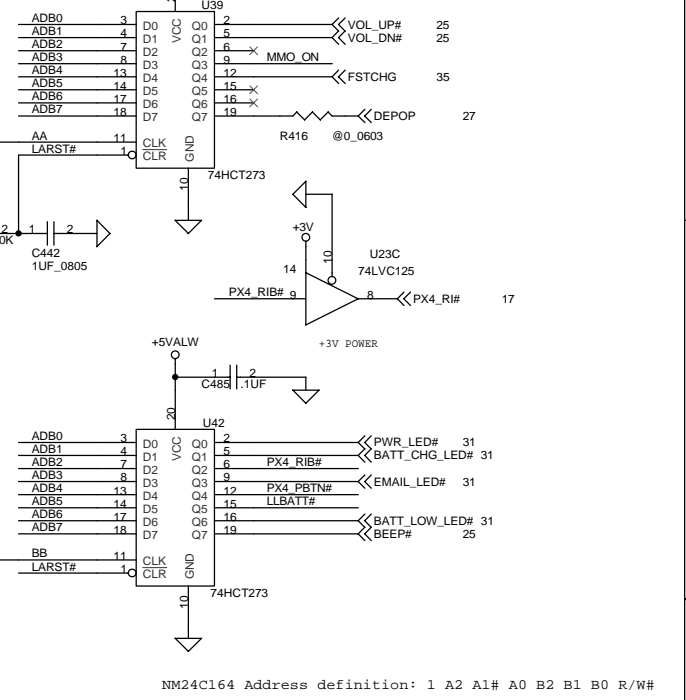
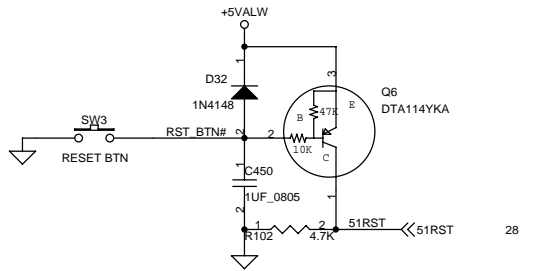


INPUT

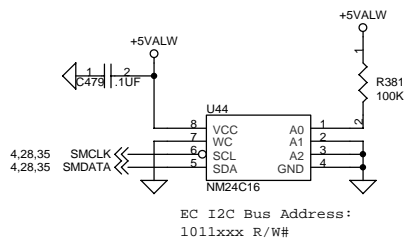
OUTPUT



RST BTN



NM24C164 Address definition: 1 A2 A1# A0 B2 B1 B0 R/W#



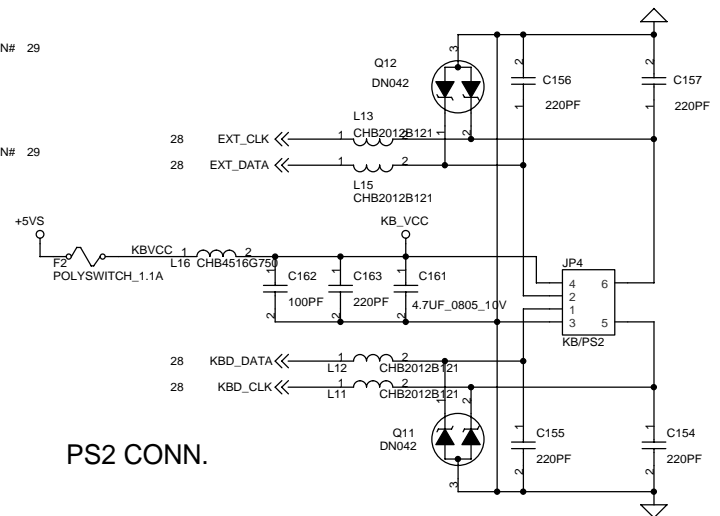
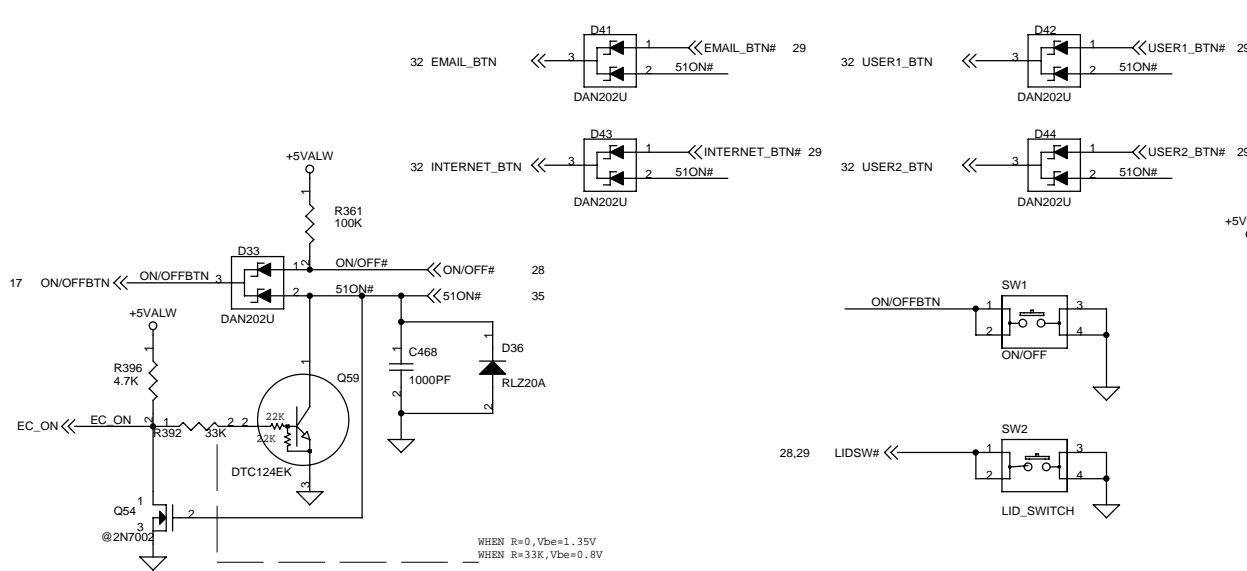
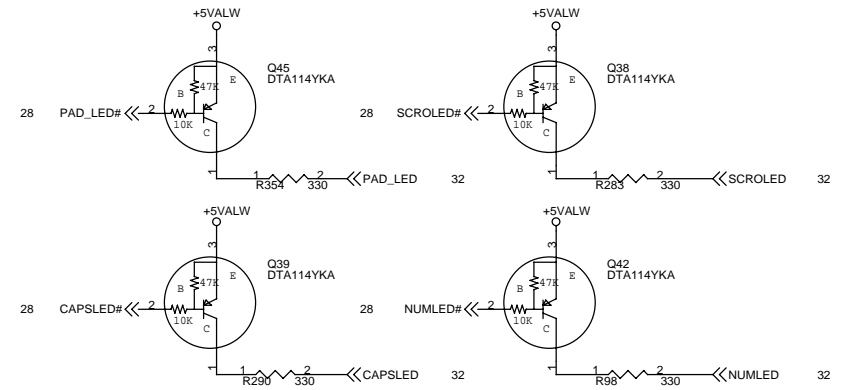
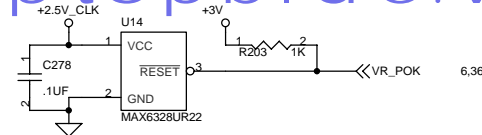
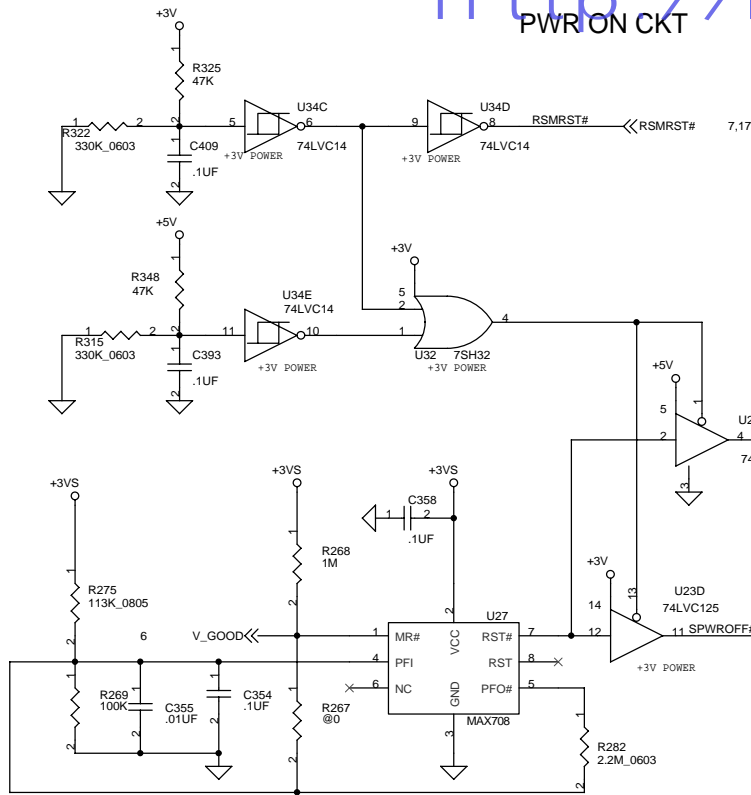
Compal Electronics, Inc.

SCHEMATICS, M/B LA-971

Size	Document Number	Rev
B	401166	1C
Date:	Friday, December 01, 2000	Sheet 29 of 37

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

PWR ON CKT

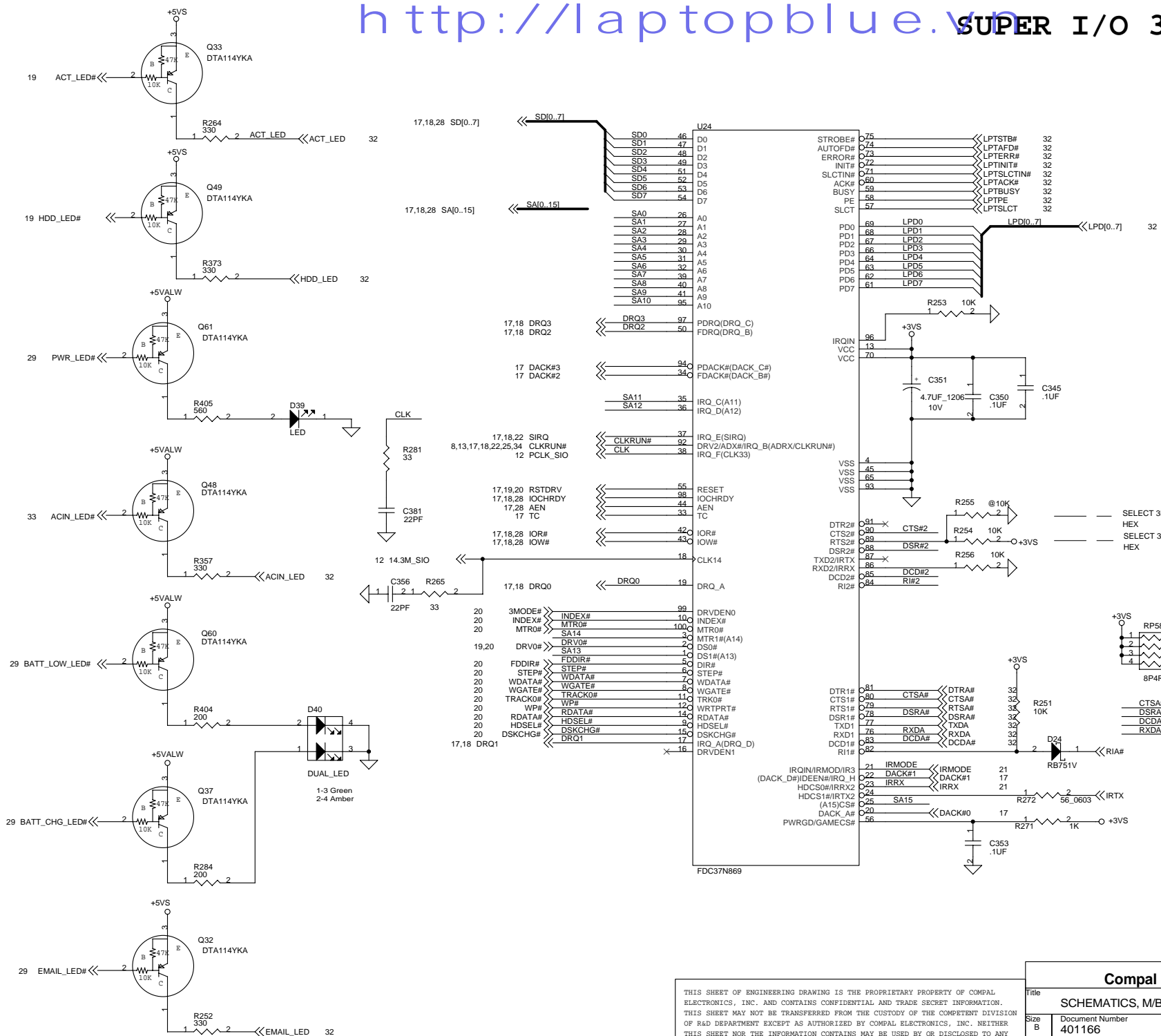


Change R214: 4.7k -> 22k, R215: 33k -> 22k, remove Q15.

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title		SCHEMATICS, M/B LA-971	
Size	Document Number	Rev 1C	
Custor	401166		
Date:	Friday, December 01, 2000	Sheet	30 of 37

SUPER I/O 37N869



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

SCHEMATICS, M/B LA-971

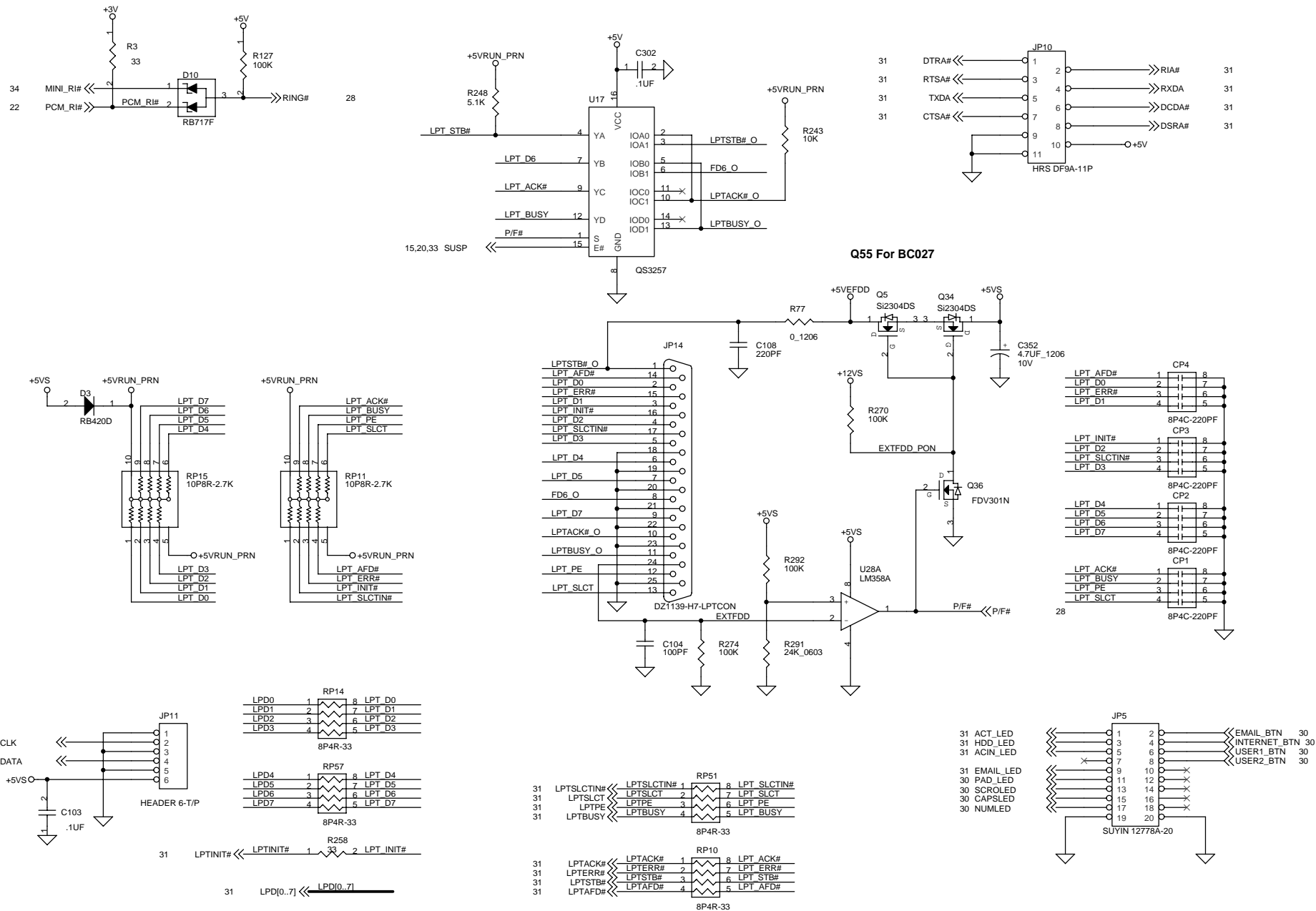
Size B	Document Number 401166
-----------	---------------------------

Rev
1C

Date: Friday, December 01, 2000 Sheet 31 of 37

SERIAL/PARALLEL PORT

http://laptopblue.vn



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
SCHEMATICS, M/B LA-971			
Size	Document Number	Rev	
Custor	401166	1C	
Date:	Friday, December 01, 2000	Sheet	32 of 37

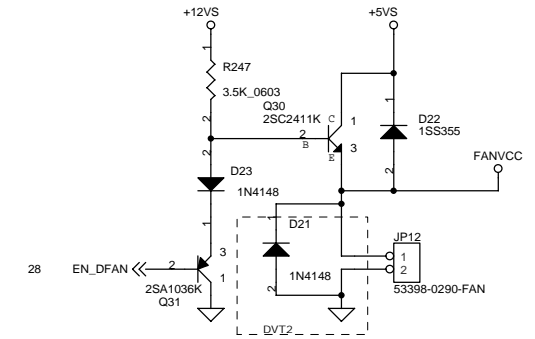
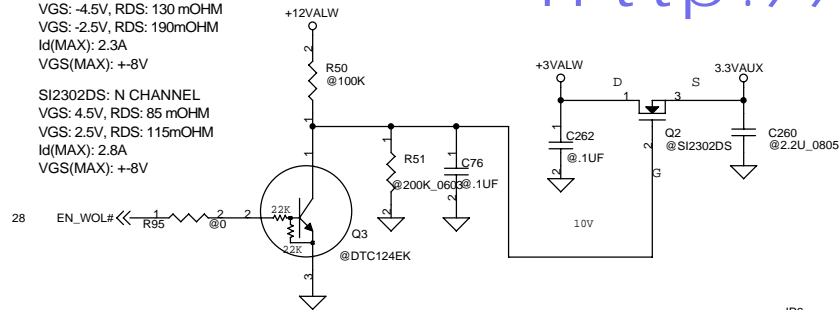


Compal Electronics, Inc.			
Title SCHEMATICS, M/B LA-971			
Size B	Document Number 401166		Rev 1C
Date:	Friday, December 01, 2000	Sheet 33 of 37	

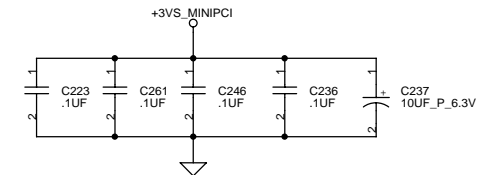
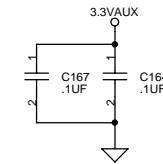
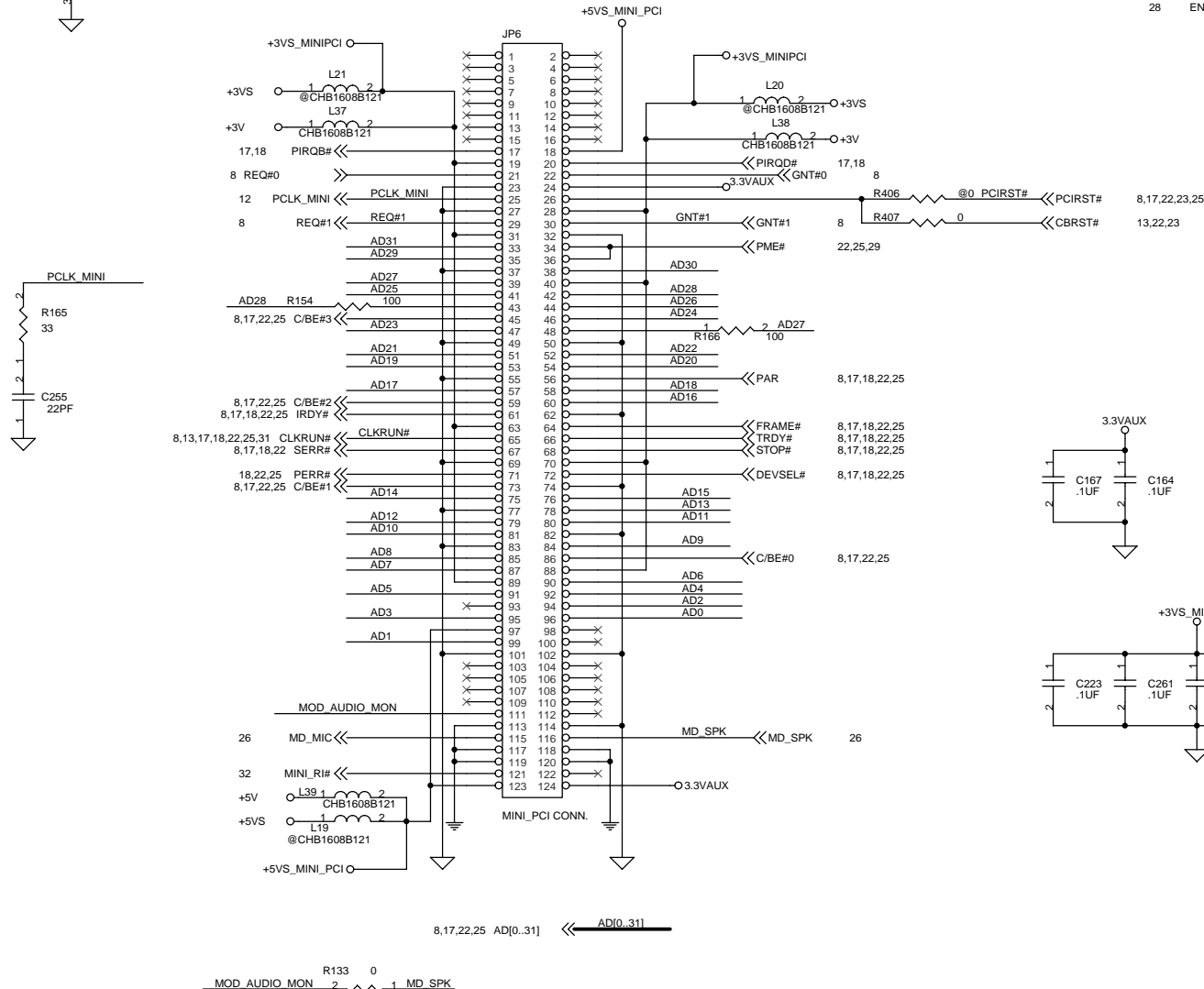
MODEM CONN.

SI2301DS: P CHANNEL
VGS: -4.5V, RDS: 130 mOHM
VGS: -2.5V, RDS: 190mOHM
Id(MAX): 2.3A
VGS(MAX): +8V

SI2302DS: N CHANNEL
VGS: 4.5V, RDS: 85 mOHM
VGS: 2.5V, RDS: 115mOHM
Id(MAX): 2.8A
VGS(MAX): +8V



FAN CONN.

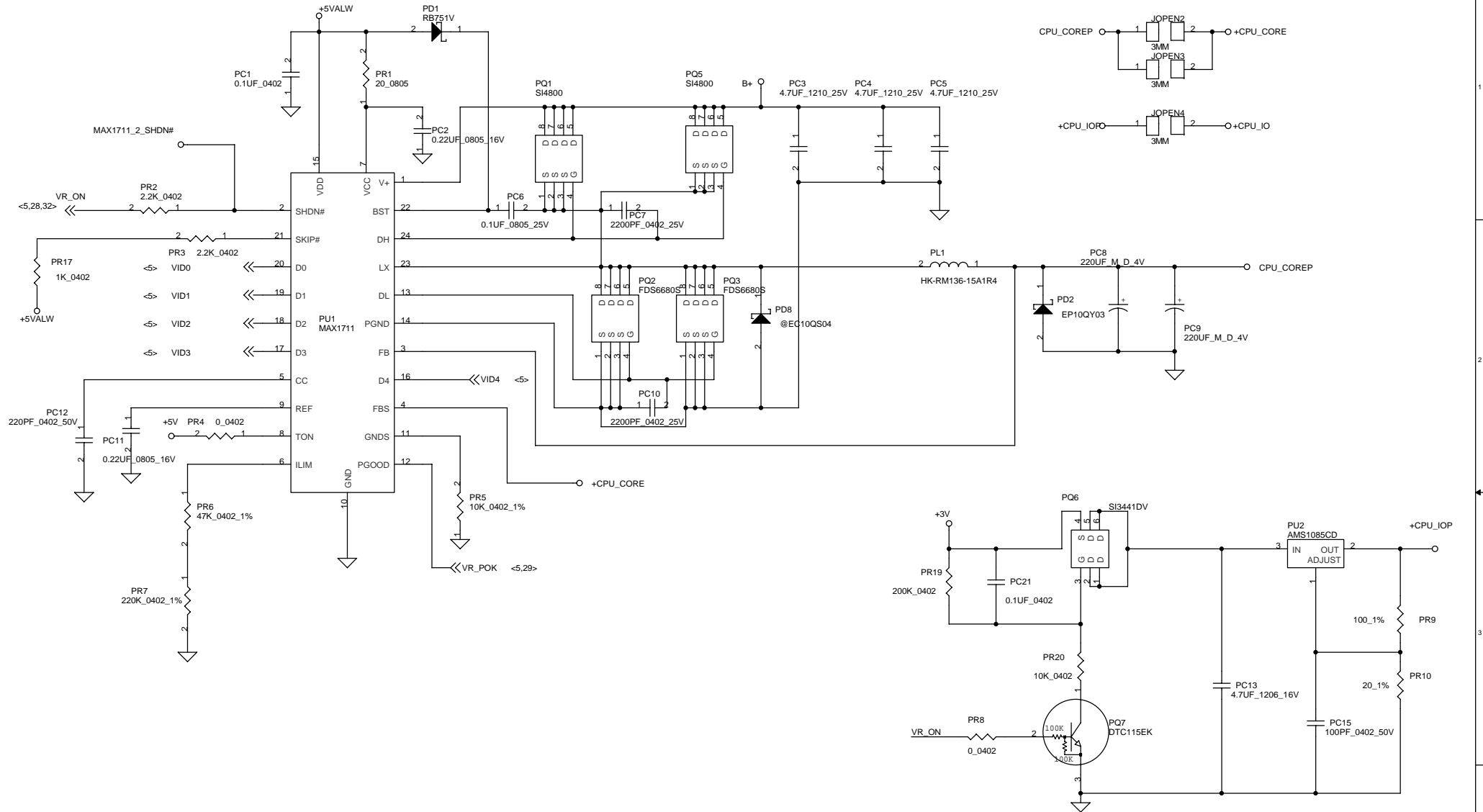


THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.

Title		SCHEMATICS, M/B LA-971
Size B	Document Number 401166	

Date: Friday, December 01, 2000 Sheet 34 of 37



THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
SCHEMATICS, M/B LA-971			
Size	Document Number	Rev	
401166		1C	
Date:	Friday, December 01, 2000	Sheet	36 of 37

888J2 PIR LIST

http://laptopblue.vn

Reason for Change Rev. Page # Description BD_Ver. Phase Reason for Change Rev. Page # Description BD_Ver. Phase Reason for Change Rev. Page # Description BD_Ver. Phase

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.		
Title		
SCHEMATICS, M/B LA-971		
Size	Document Number	Rev
B	401166	1C
Date:	Friday, December 01, 2000	Sheet 37 of 37