

Milwaukee 10A

CS BUILD

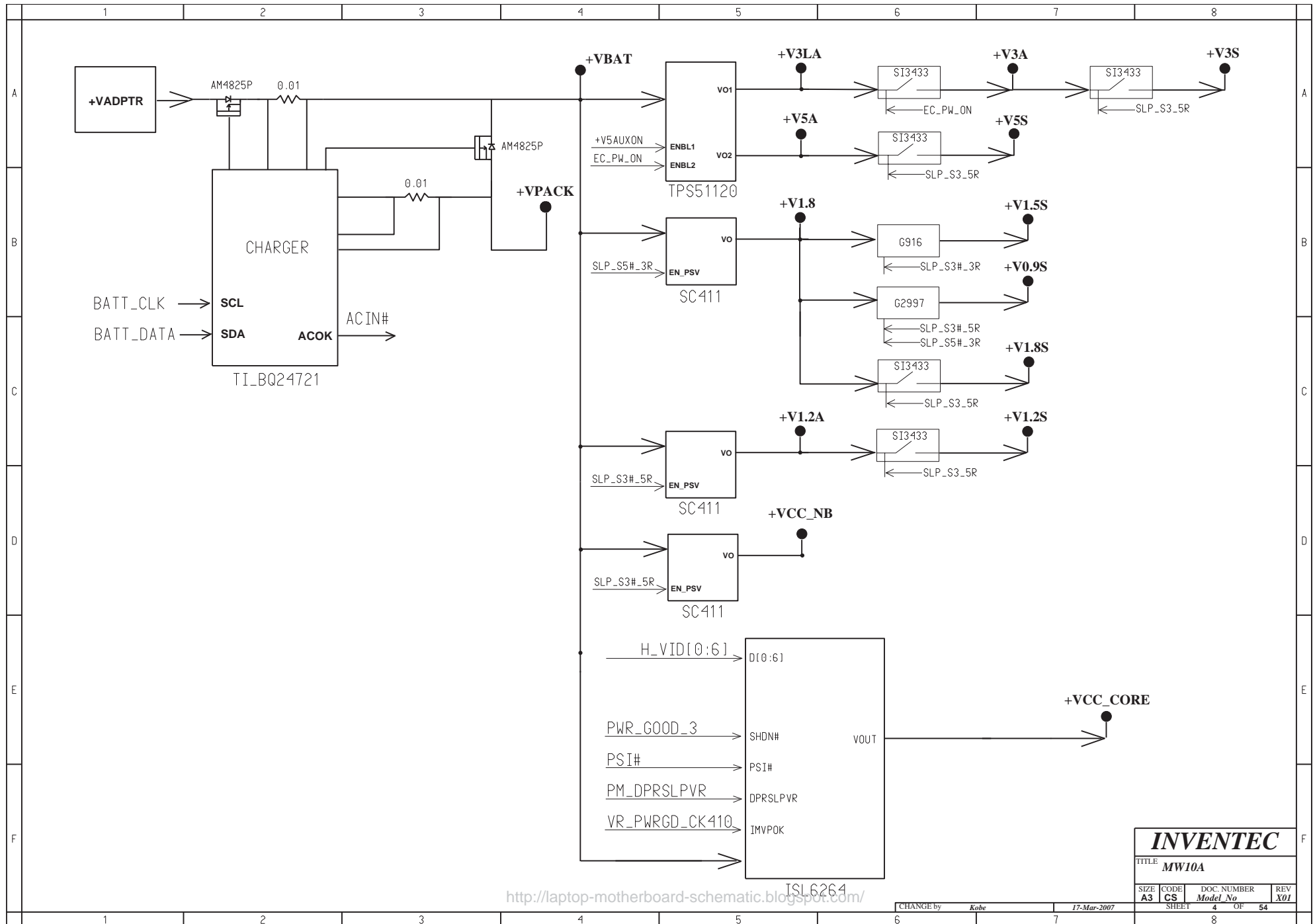
2007 0709

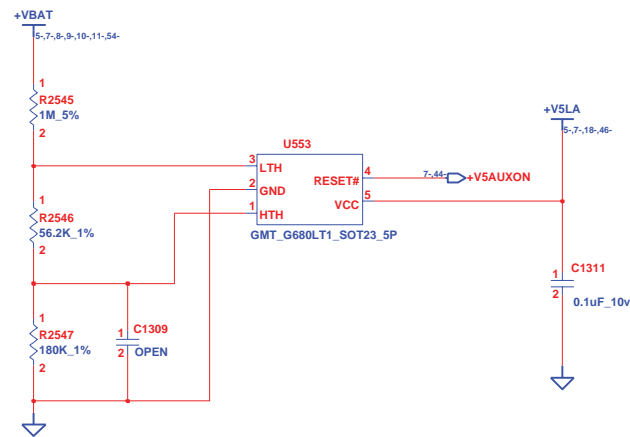
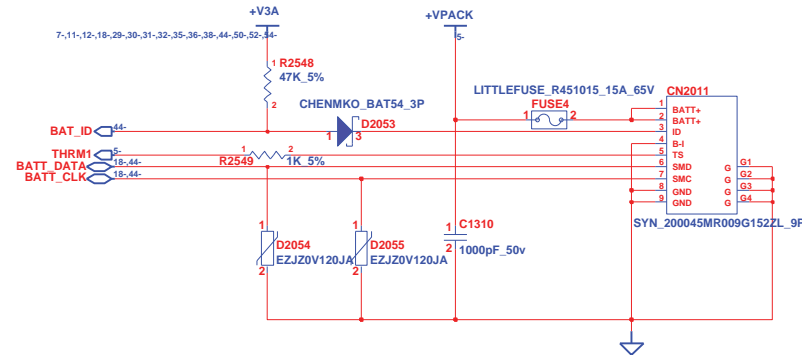
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DRAWN	EE	DATE	POWER	DATE	INVENTEC		
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CHECK							
RESPONSIBLE							
SIZE = 3					SIZE	CODE	DOC. NUMBER
FILE NAME : XXXX-XXXXXX-XX					A3	CS	Model No
P/N XXXXXXXXXXXX					REV X01		
					SHEET 1 OF 54		

TABLE OF CONTENTS

PAGE	PAGE	PAGE
1.COVER PAGE	29.S/B 1	50.LED(M/B) & HOTKEY/B CONN
2.INDEX	30.S/B 2	51.PICK BUTTON BOARD
3.BLOCK DIAGRAM	31.S/B 3	52.KILL SWITCH& HALL SENSOR
4.POWER SEQUENCE BLOCK	32.S/B 4	53.DRILL HOLE
5-12.SYSTEM POWER	33.CARDBUS CONTROLLER	54.EMI
13.CLOCK GENERATOR	34.5 IN 1 CARD SLOT & 1394	
14.CPU 1	35.EXPRESS CARD CONN	
15.CPU 2	36.LAN CONTROLLER	
16.CPU 3	37.RJ45 & TRANSFORMER	
17.CPU 4	38.WLAN & HD_DVD	
18.FAN & THERMAL CONTROLLER	39.SATA HDD1 CONN & SATA HDD2 CONN	
19.N/B 1	40.FELICA & FINGER PRINTER & CAMERA CONN	
20.N/B 2	41.ODD CONN	
21.N/B 3	42.USB CONN	
22.N/B 4	43.BLUE TOOTH CONN	
23.DDR2 DIMM0	44.KBC	
24.DDR2 DIMM1	45.K/B & TP/B CONN	
25.DDR DAMPING	46.CIR	
26.CRT CONN	47.AZALIA CODEC	
27.S-VIDEO CONN	48.AUDIO AMP & MIC & HP	
28.LCM CONN	49.MDC 1.5 CONN	





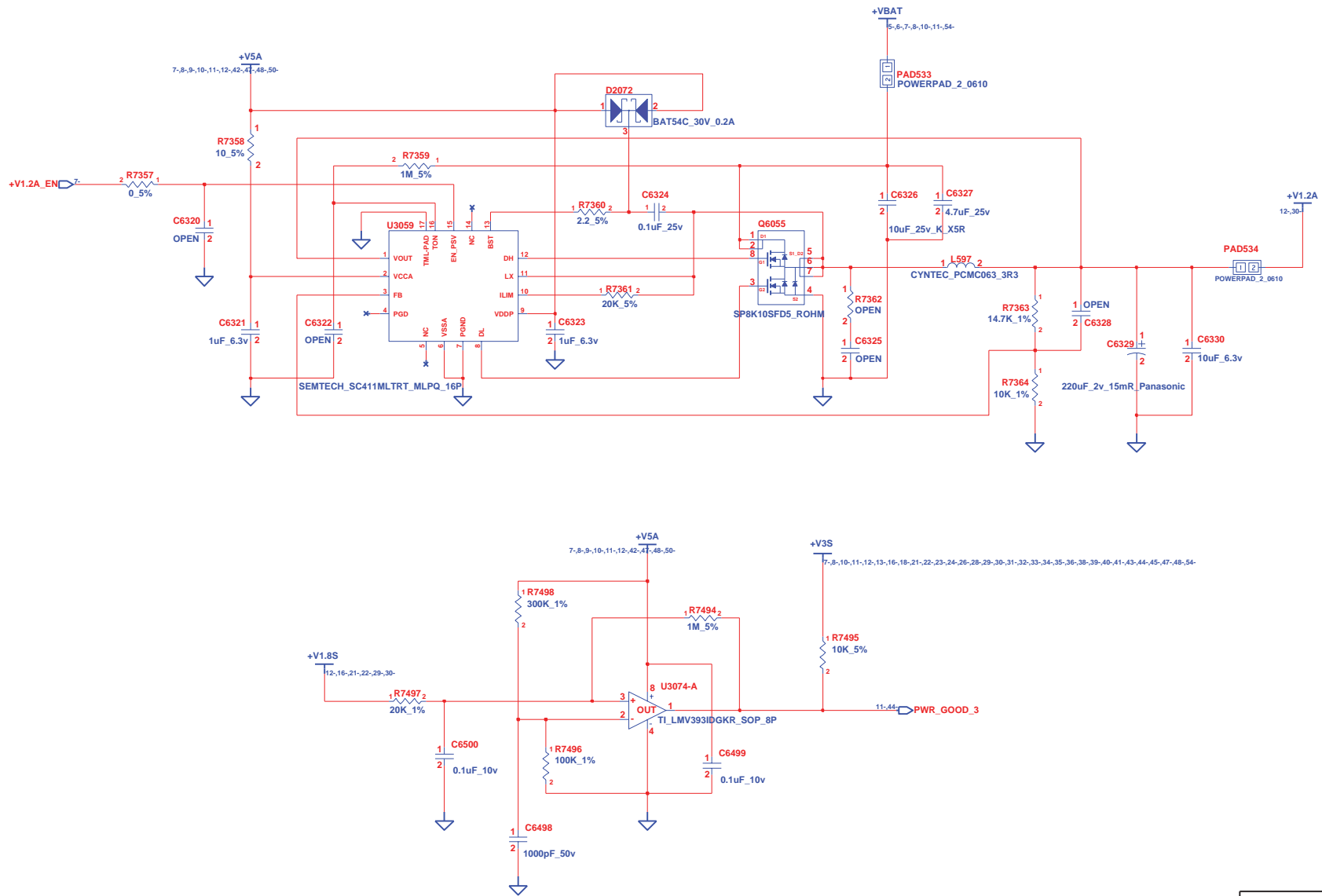
The schematic diagram illustrates the system power section of a laptop motherboard, featuring several voltage regulators and associated components:

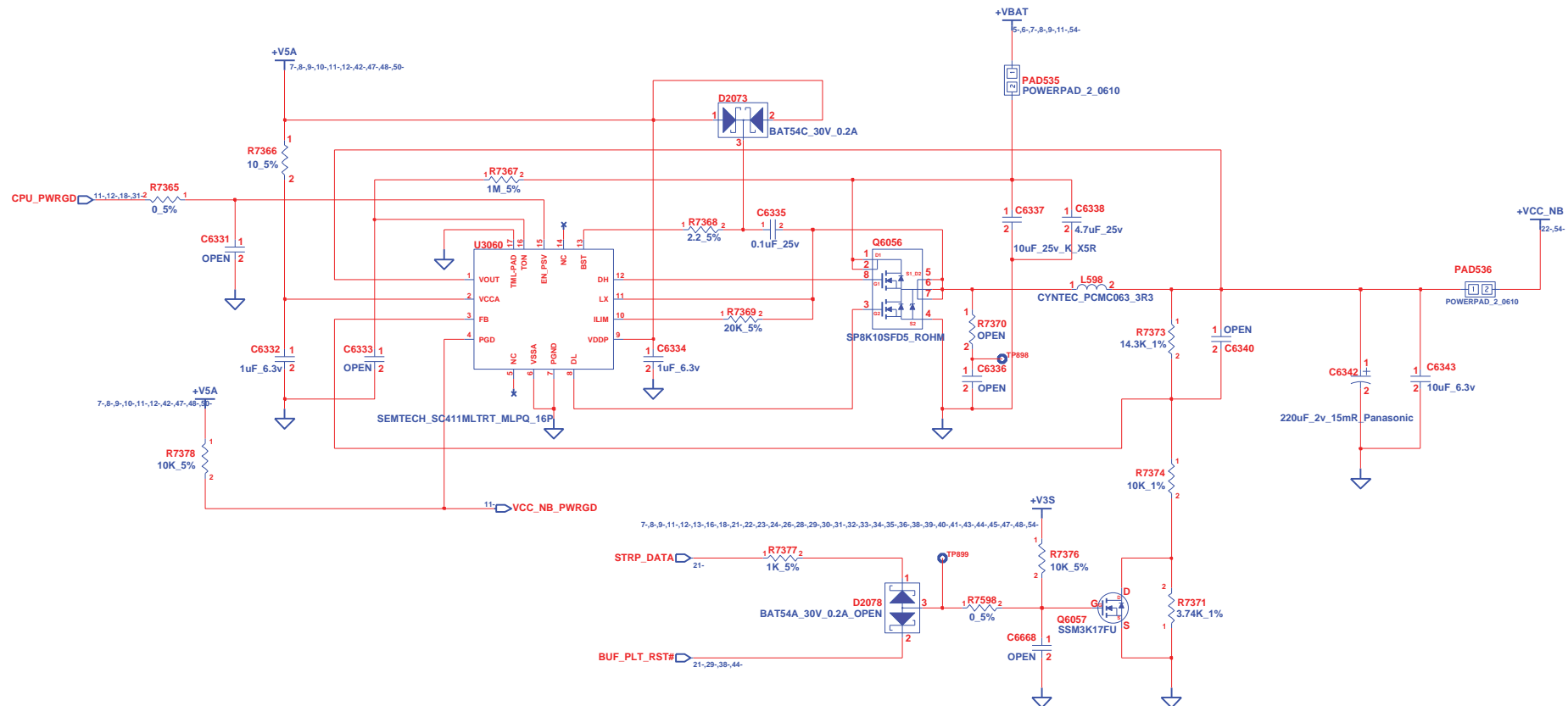
- U3057 (SEMTECH_SC411MLTRT_MLPQ_16P):** A multi-channel DC-DC converter regulating +V5A, +V1.8, +V1.5S, and +V0.9S. It includes feedback resistors R7347, R7348, R7349, R7350, R7351, R7352, R7353, R7354, R7355, and R7356. Capacitors include C6301, C6302, C6303, C6308, C6309, C6311, C6313, C6315, C6316, C6317, C6312, and C6314.
- U3058 (GMT_G2997F6U_MSOP10_10P):** A DC-DC converter regulating +V5A, +V1.8, +V0.9S, and +V0.5S. It includes feedback resistors R7487, R7488, and R7489. Capacitors include C6495, C6496, C6312, C6314, C6318, and C6319.
- U3073 (GMT_G916T1uF_SOT23_5_5P):** A DC-DC converter regulating +V3S. It includes feedback resistors R7487, R7488, and R7489. Capacitors include C6495 and C6496.
- Q6052 (2N7002W):** A MOSFET used for the +V1.5S regulation.
- Q6053 (FDS8884) and Q6054 (FDS6690AS):** MOSFETs used for the +V5A and +V1.8 regulation.
- Q6055 (BAT54C_30V_0.2A):** A diode used for the +V5A regulation.
- Q6056 (FDS6690AS):** A MOSFET used for the +V1.8 regulation.
- Q6057 (FDS6690AS):** A MOSFET used for the +V0.9S regulation.
- Q6058 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6059 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6060 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6061 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6062 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6063 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6064 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6065 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6066 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6067 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6068 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6069 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6070 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6071 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6072 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6073 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6074 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6075 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6076 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6077 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6078 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6079 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6080 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6081 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6082 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
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- Q6100 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6101 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
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- Q6103 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
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- Q6110 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6111 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6112 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6113 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6114 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6115 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
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- Q6140 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6141 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6142 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6143 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q6144 (FDS6690AS):** A MOSFET used for the +V0.5S regulation.
- Q**

TITLE			
MW10A SYSTEM POWER (+V2.5S / +VCCP)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model No</i>	X01

CHANGE by	<i>Kobe</i>	<i>9-Mar-2007</i>
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CS Model No
SHEET 8 OF 54

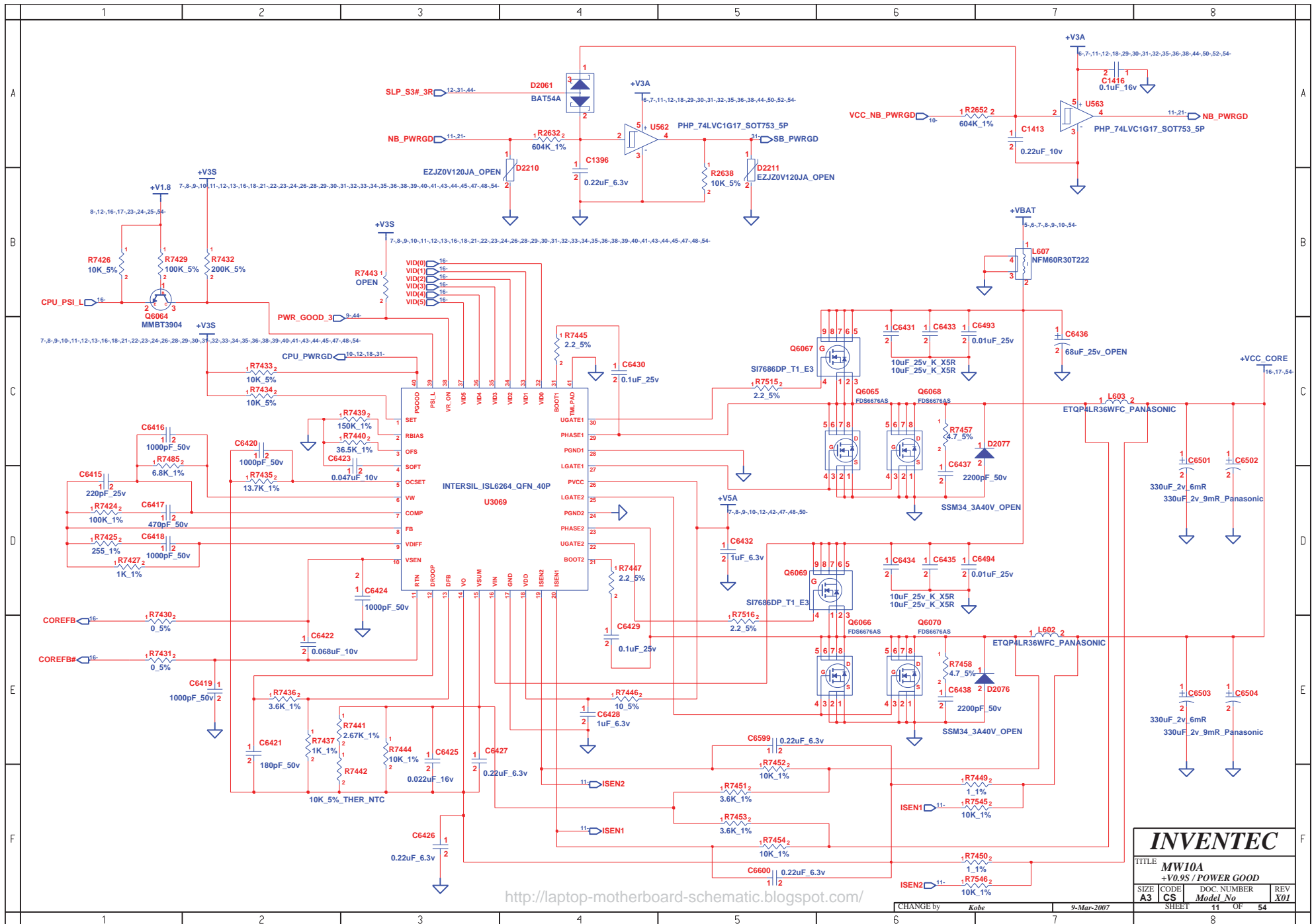


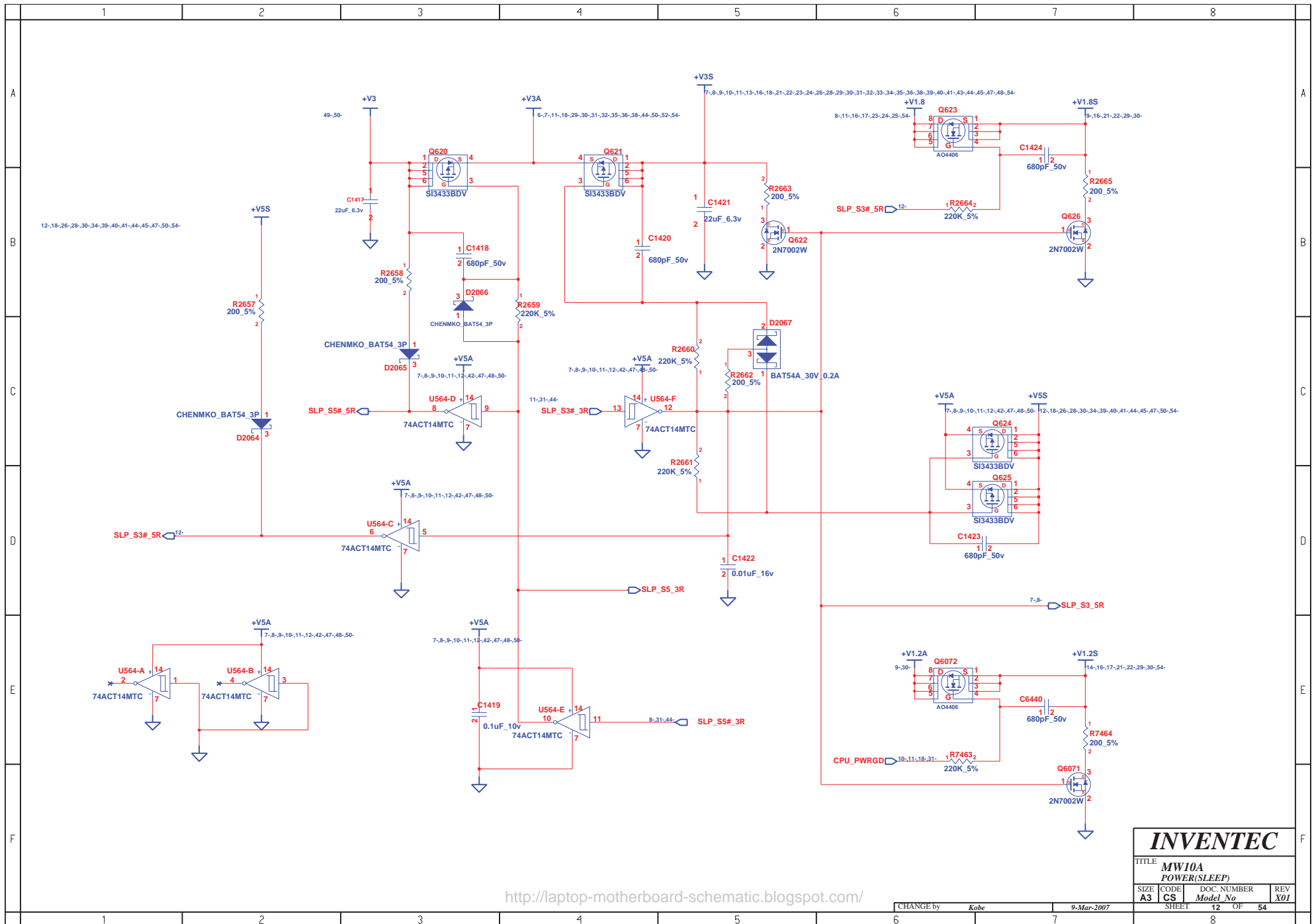


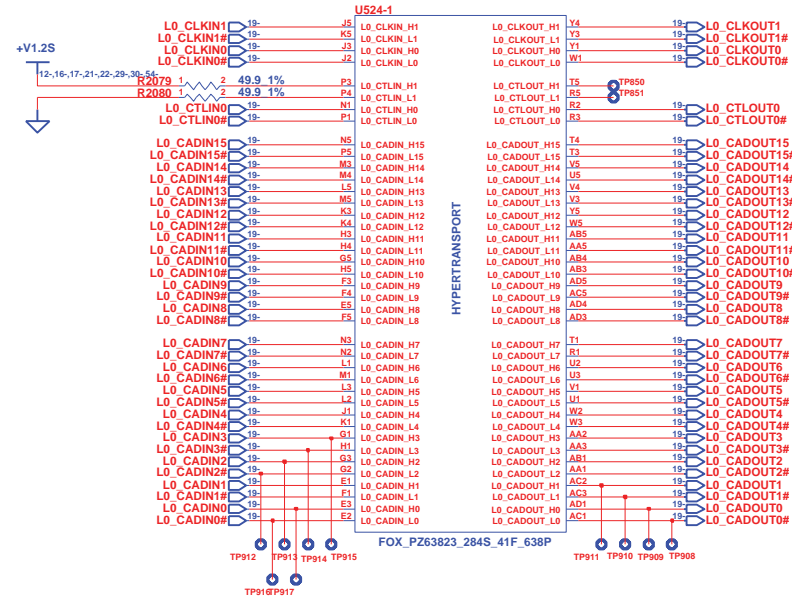
7;-8;-9;-10;-11;-12;-42;-47;-48;-50-

STRP_DATA 21-
BUF_PLT_RST# 21;-29;-38;-44-

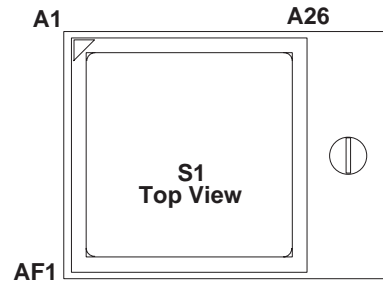
STRP_DATA	+VCC_NB
0	1.0
1	1.2

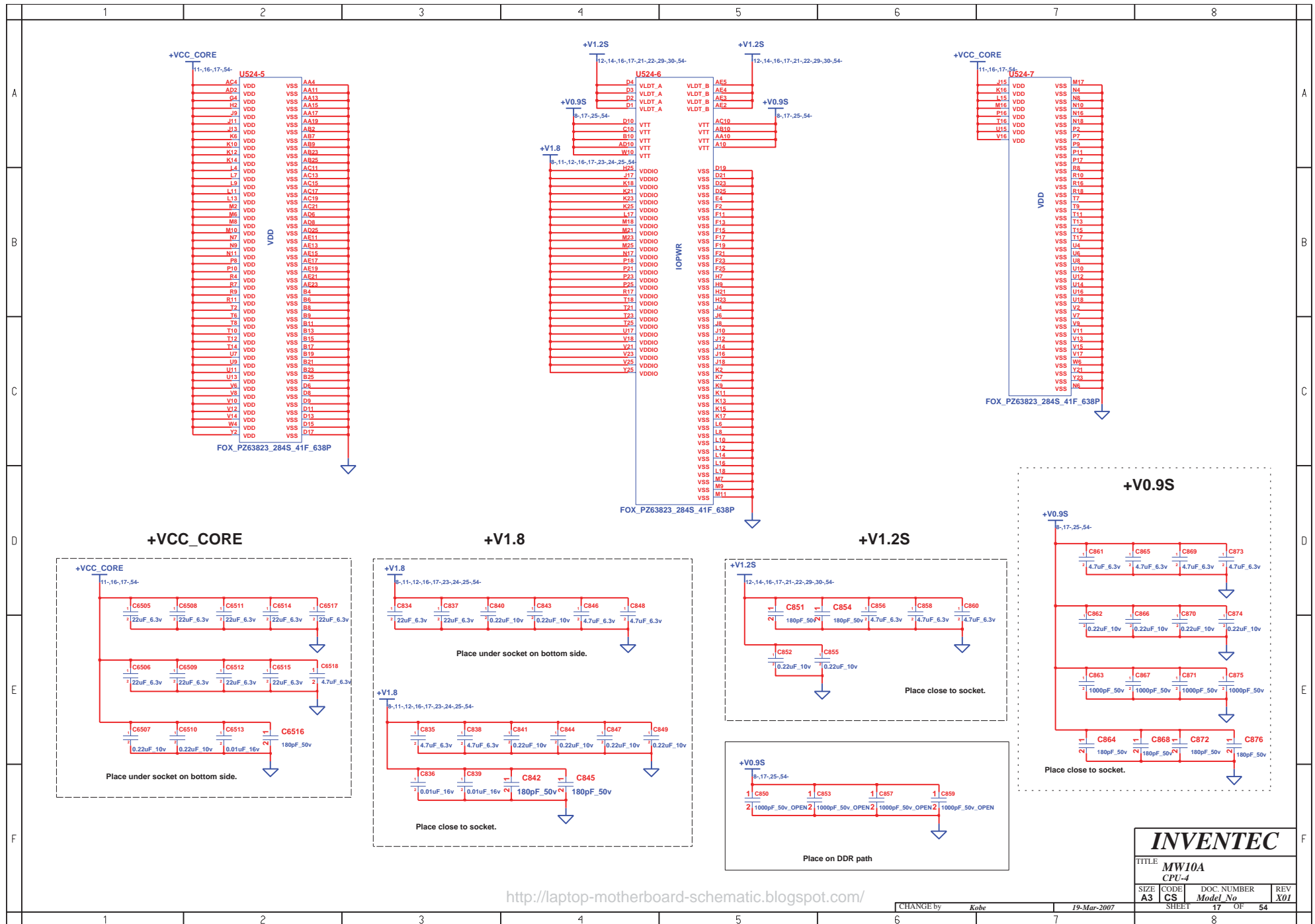


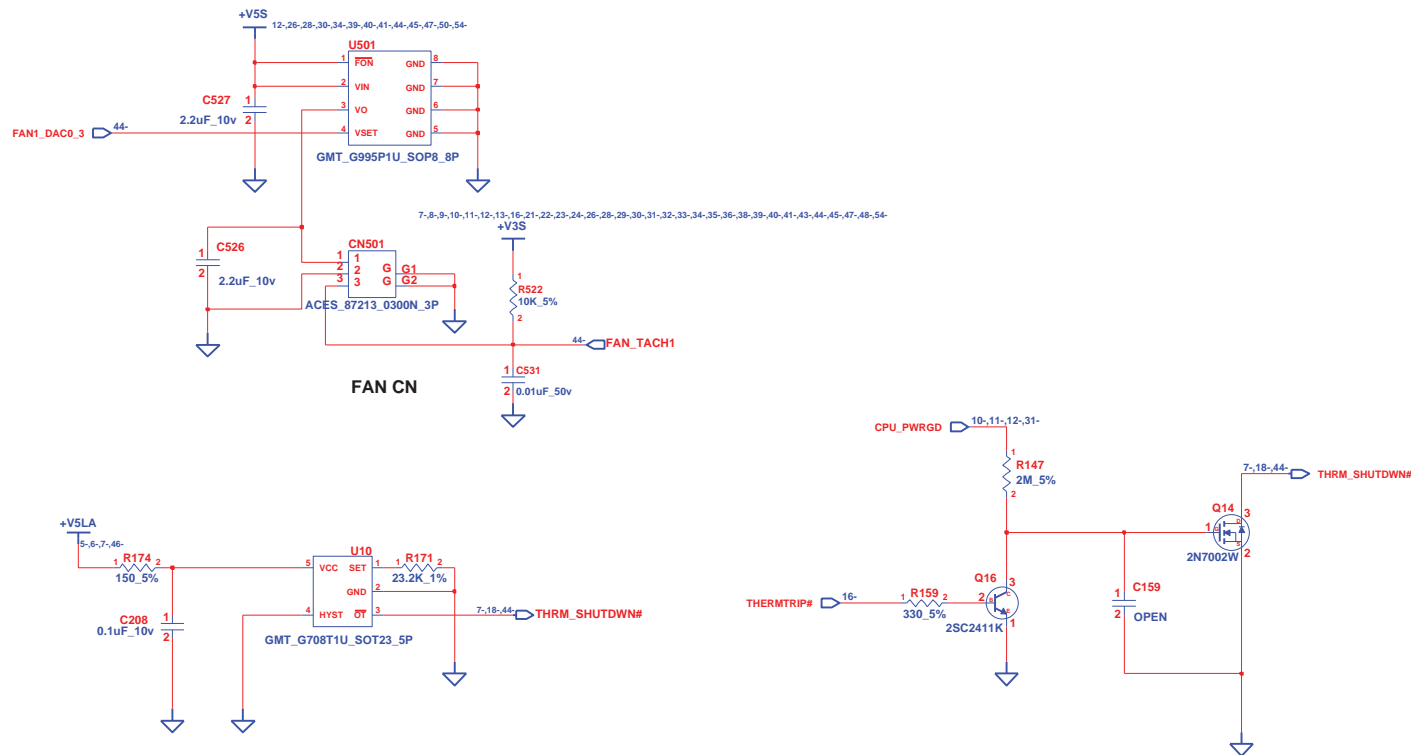




Layout: Add stitching caps if crossing plane split.

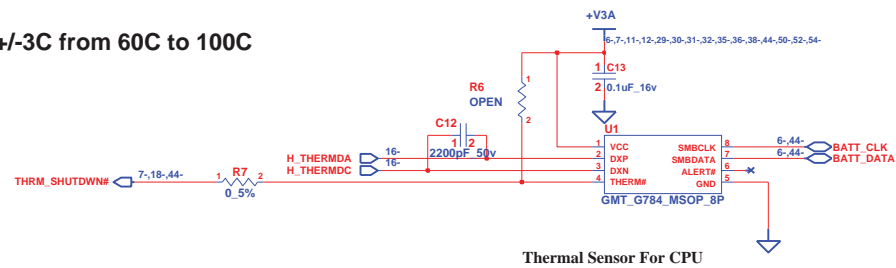




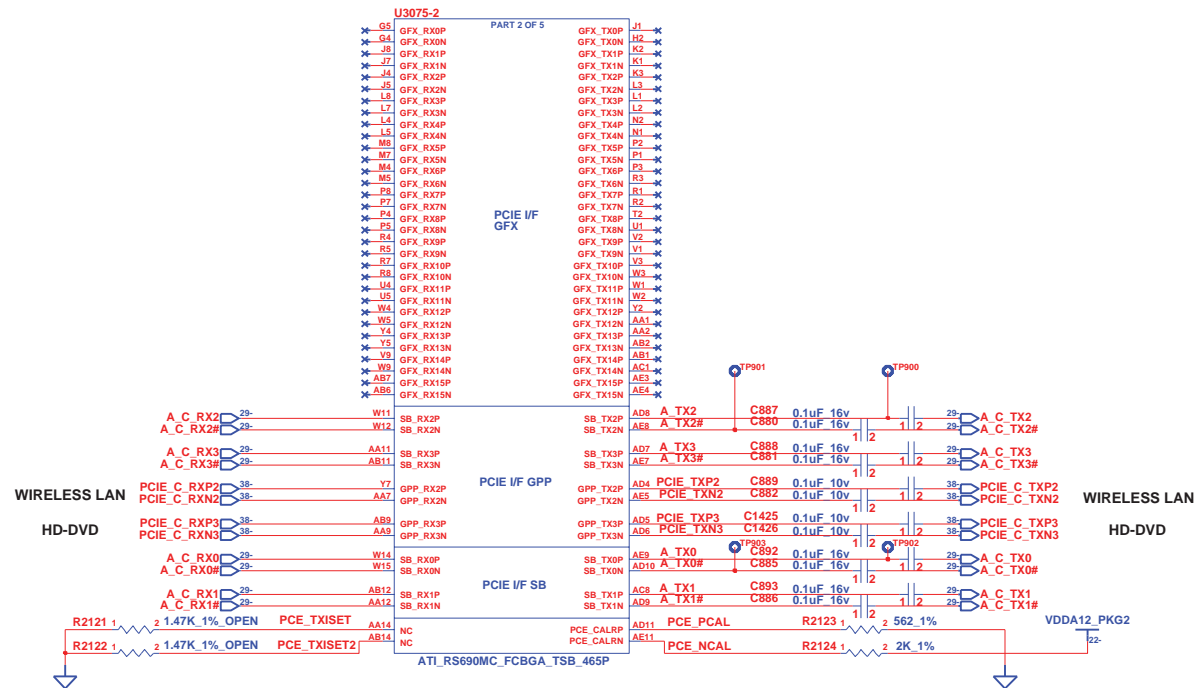


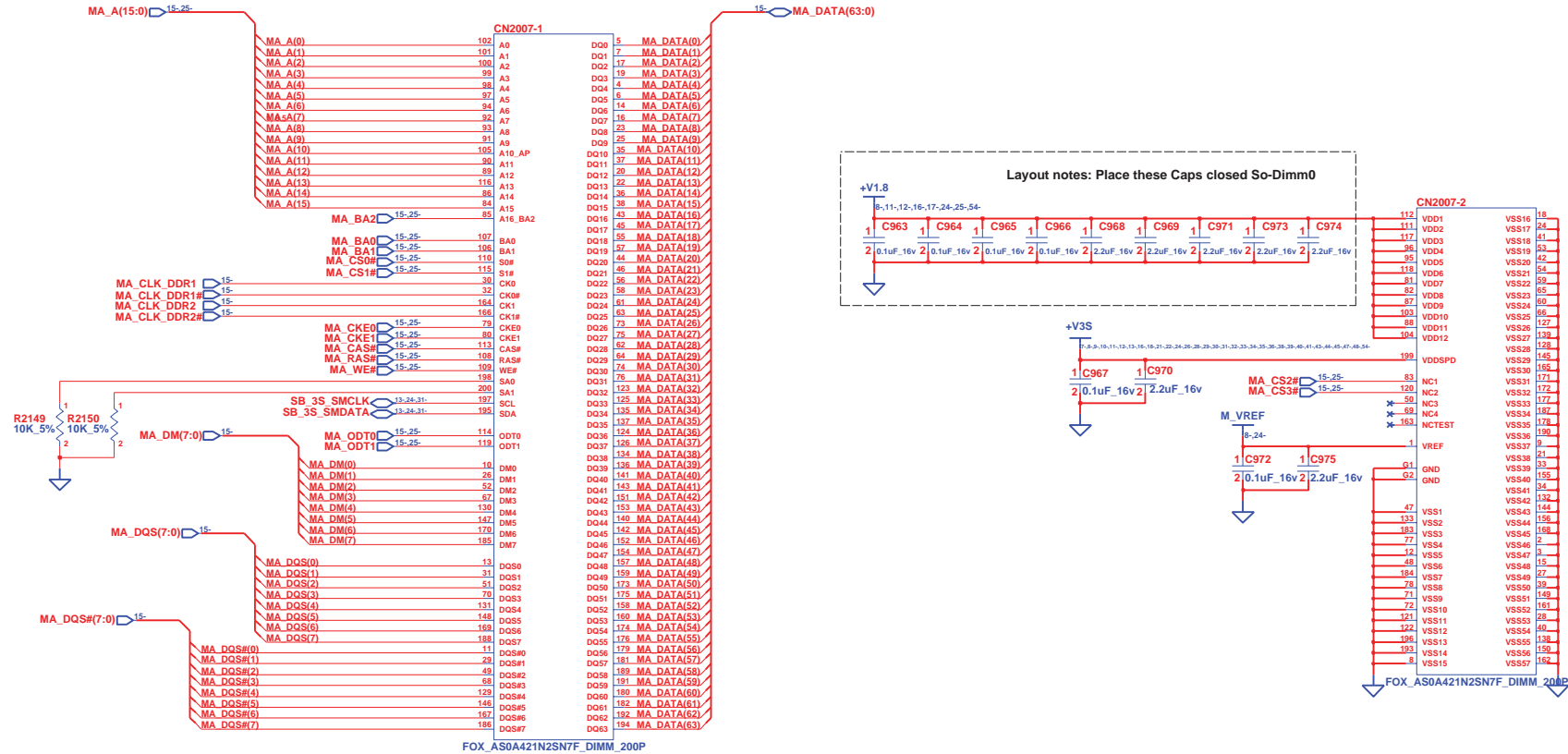
Thermal shutdown at 86C +/-3C from 60C to 100C

Hysteresis is 30C

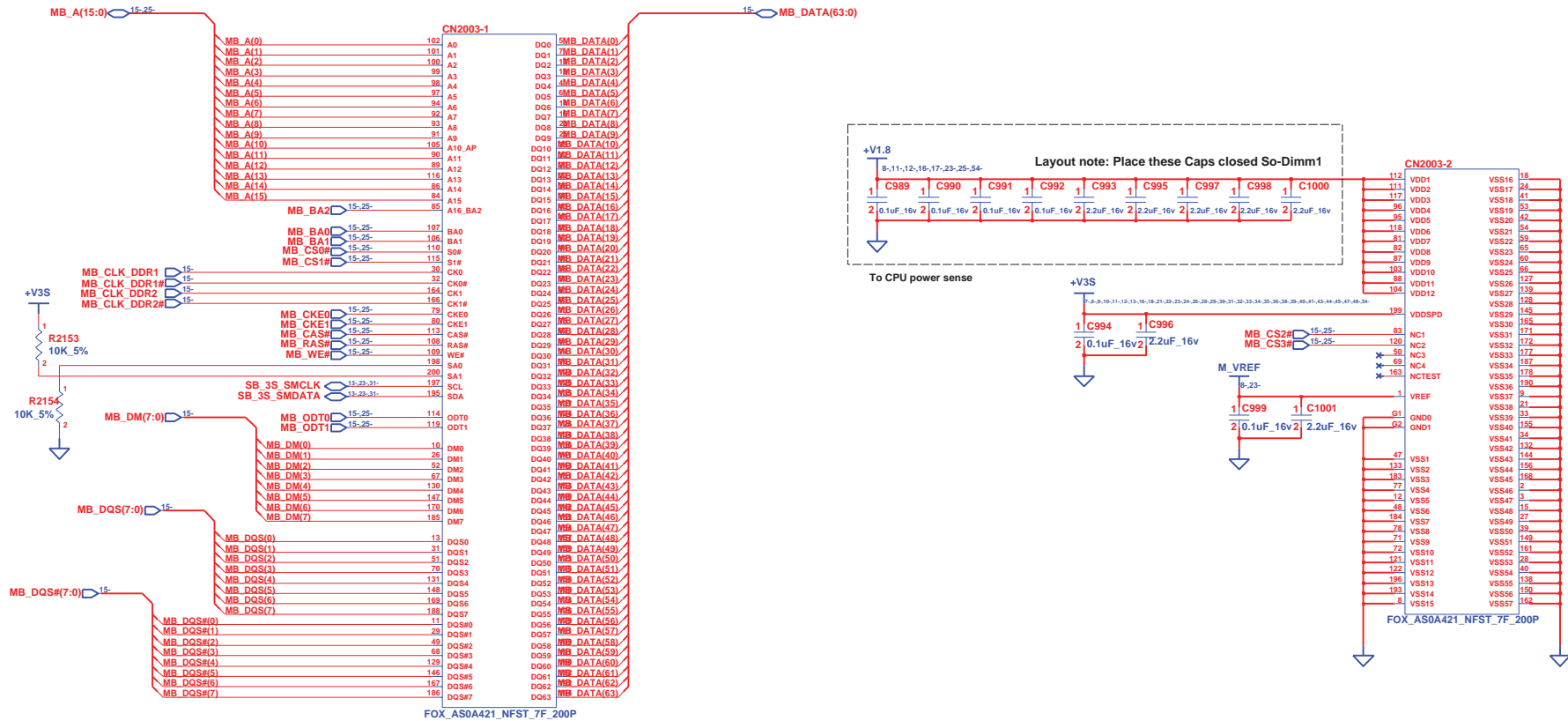


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MW10A			
THERMAL&FAN CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

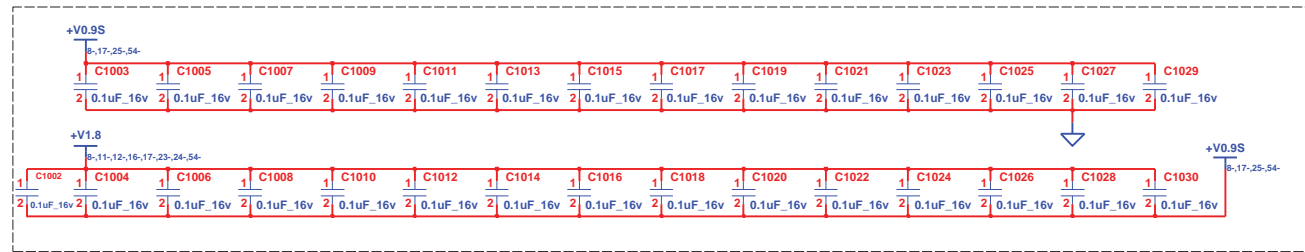




DIMM0_TOP_9.2mm

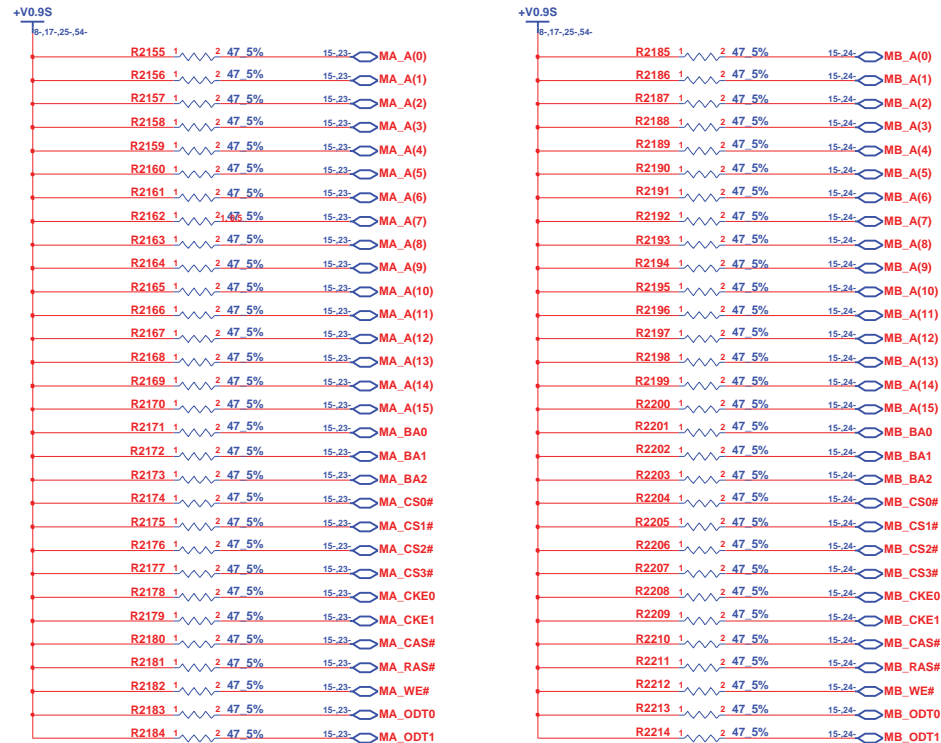


DIMM1_BTM_4.0mm



To CPU power sense

Place CAPs close to DIMM



INVENTEC

TITLE			
MW10A			
DDR2-DAMPING			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

http://laptop-motherboard-schematic.blogspot.com/

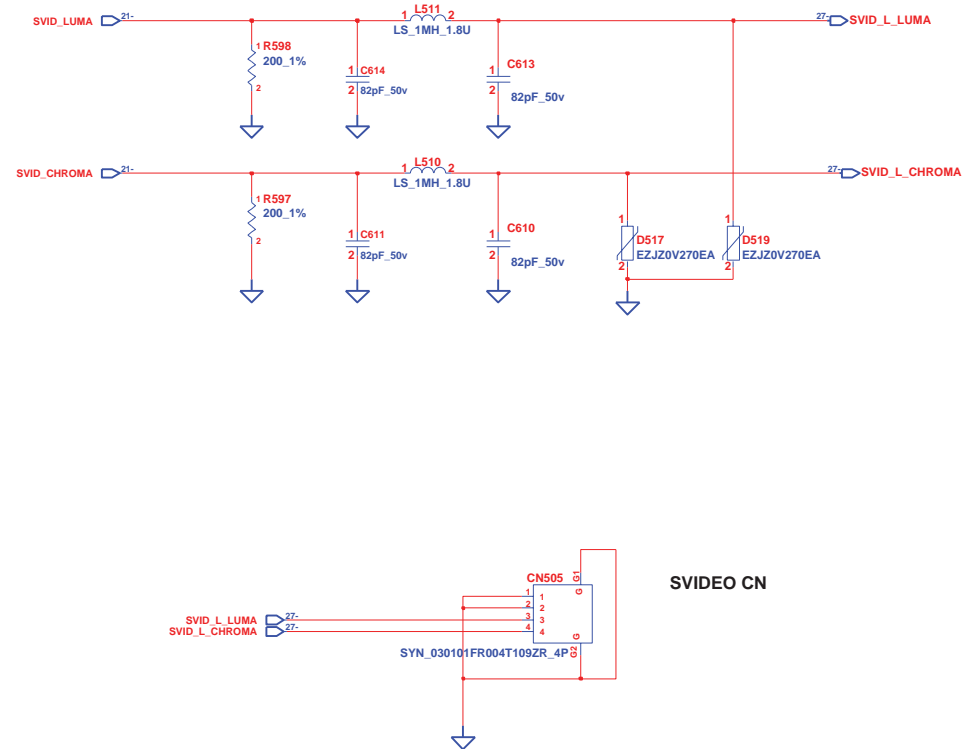
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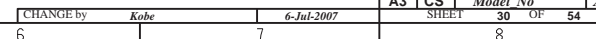
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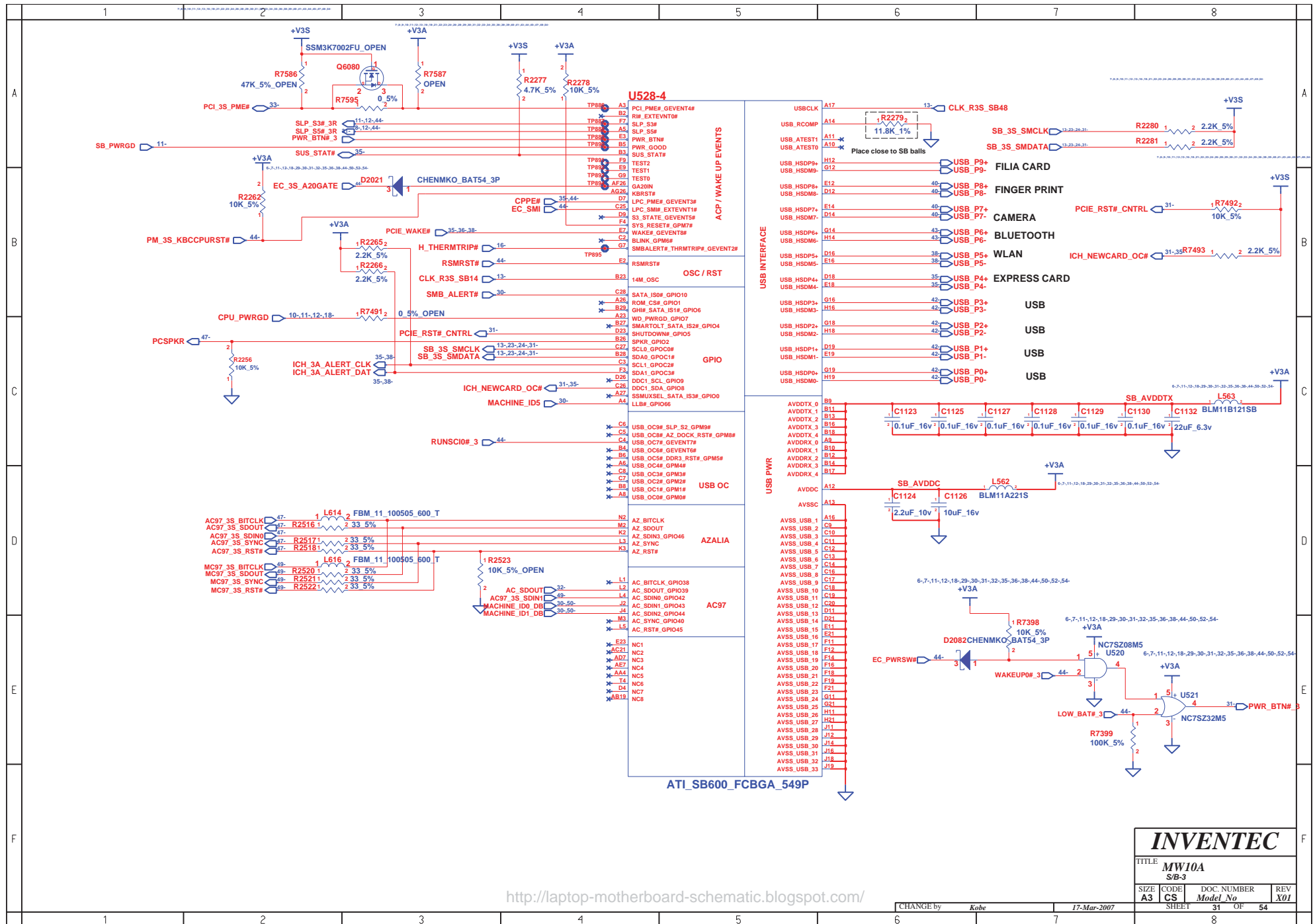
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CHANGE by	<i>Kobe</i>	<i>15-Mar-2007</i>
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<i>INVENTEC</i>			
TITLE <i>MW10A RGB CONNECTOR</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
SHEET		26	OF 54

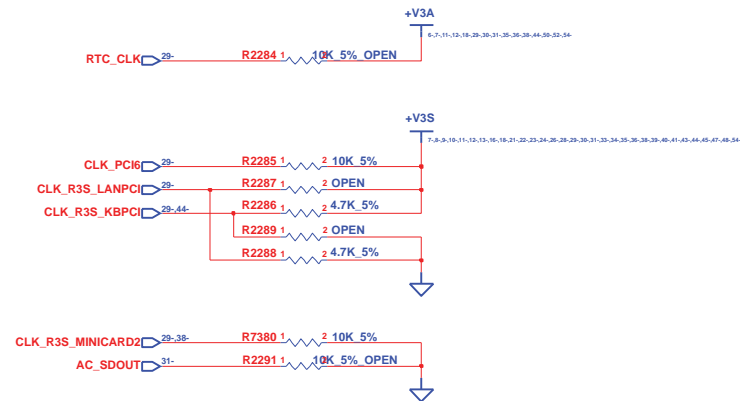


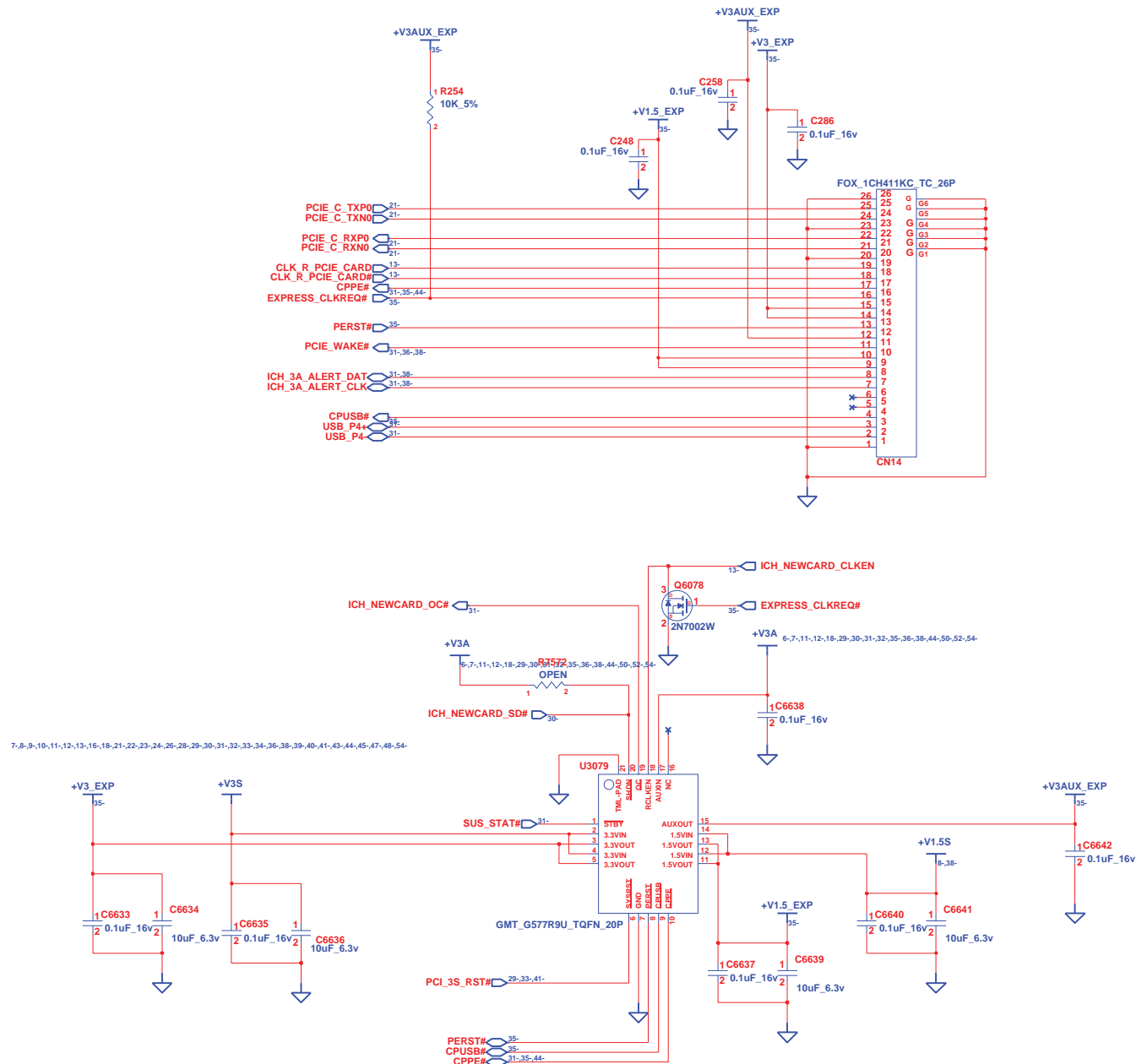




Required Straps:

	AC_SDOUT	RTC_CLK	CLK_R3S_MINICARD2	PCI_CLK6	PCI_CLK0 PCI_CLK1
PULL HIGH	USE DEBUG STRAPS	INTERNAL RTC	USE INT. PLL48	CPU K8	HH: PCI ROM HL: SPI ROM
PULL LOW	IGNORE DEBUG STRAPS	EXTERNAL RTC	USE EXT. 48M CLK	CPU P4	LH: LPC ROM LL: FWH ROM





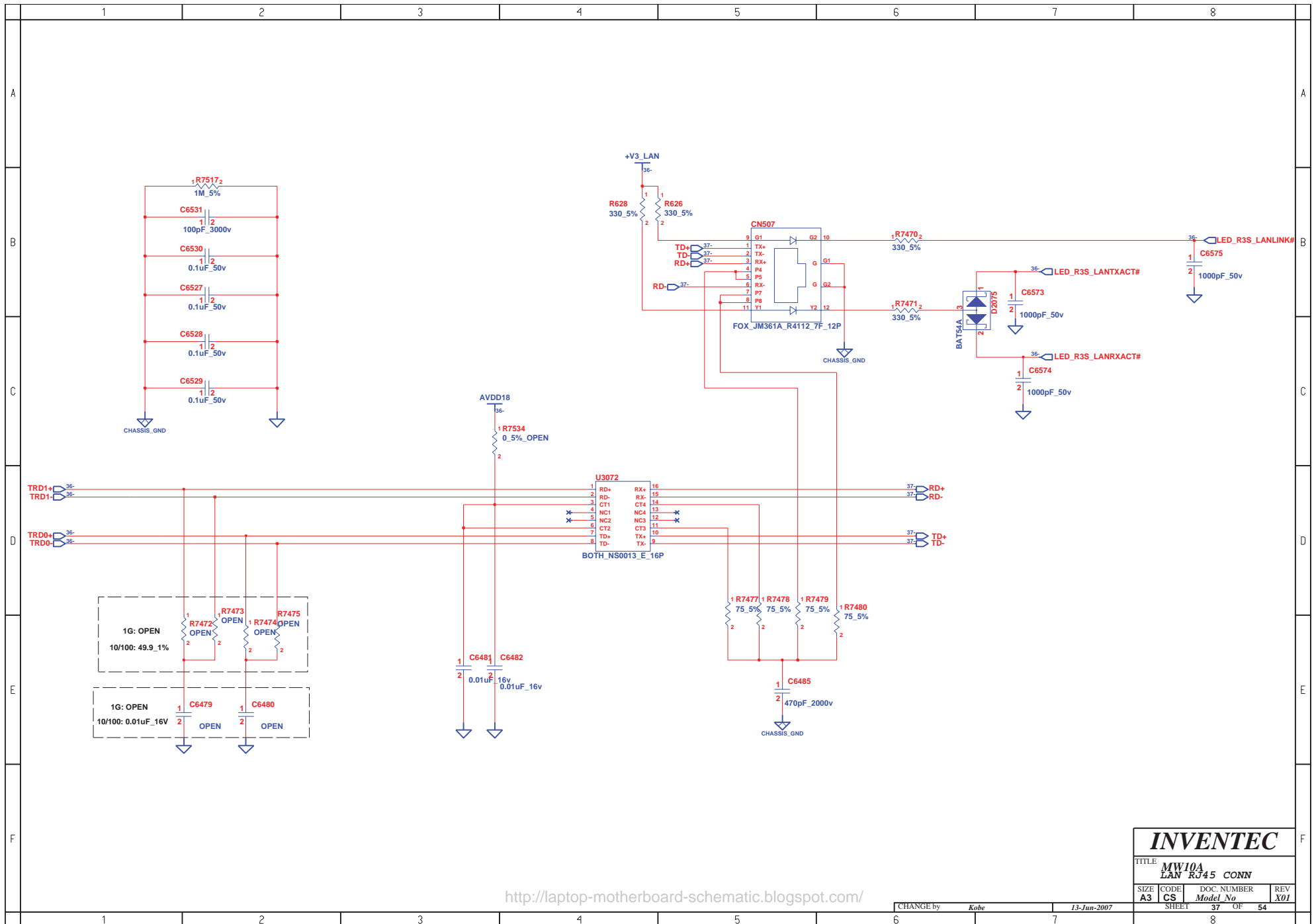
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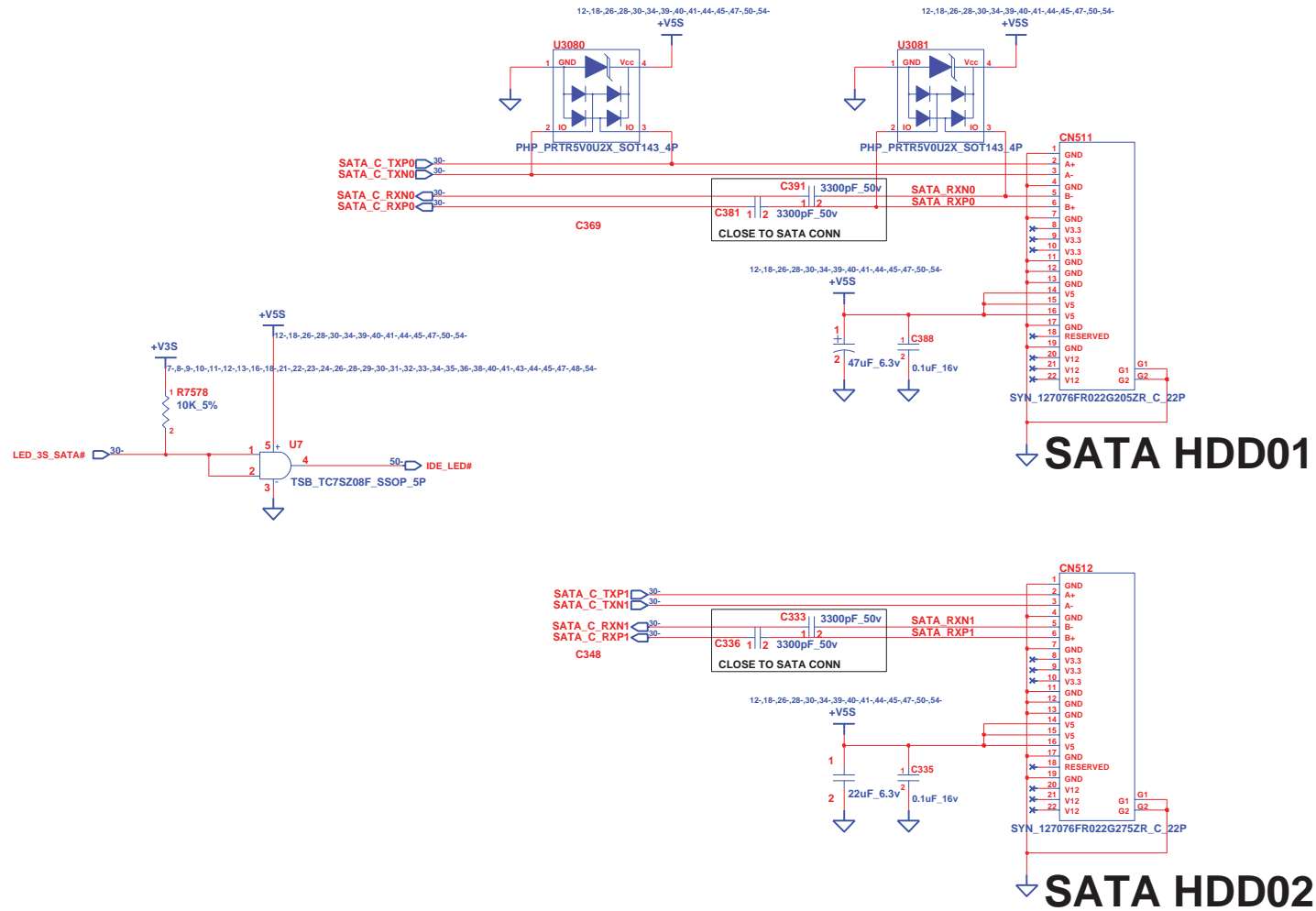
INVENTEC

TITLE			
MW10A			
Express Card			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by Kobe 6-Jun-2007

SHEET 35 OF 54





The schematic diagram illustrates the electrical connections for the MW10A ODD motherboard. Key components and connections include:

- Capacitors:** C791, C790, C788, C789 (0.1uF_16V) for EMI filtering; C138 (22uF_6.3V), C132 (0.1uF_10V), C129 (0.1uF_10V) for power supply filtering.
- Resistors:** R144 (8.2K_5%), R148 (5.6K_5%), R109 (330_5%), R107 (330_5%).
- Connectors:** D14 (EZJ20V270EA), CN508 (SYN_800031MR050S133ZL_50P).
- Power Supply:** +V3S and +V5S connections with associated decoupling components.
- PCI 3S Interface:** Signals for PIDE_3S_D(15-0), PIDE_3S_RST#, PIDE_3S_DREQ, PIDE_3S_IOR#, PIDE_3S_IOW#, PIDE_3S_IORDV, PIDE_3S_A(2:0), PIDE_3S_A(0), PIDE_3S_A(2), PIDE_3S_CSM(0), and PIDE_3S_CSM(1).

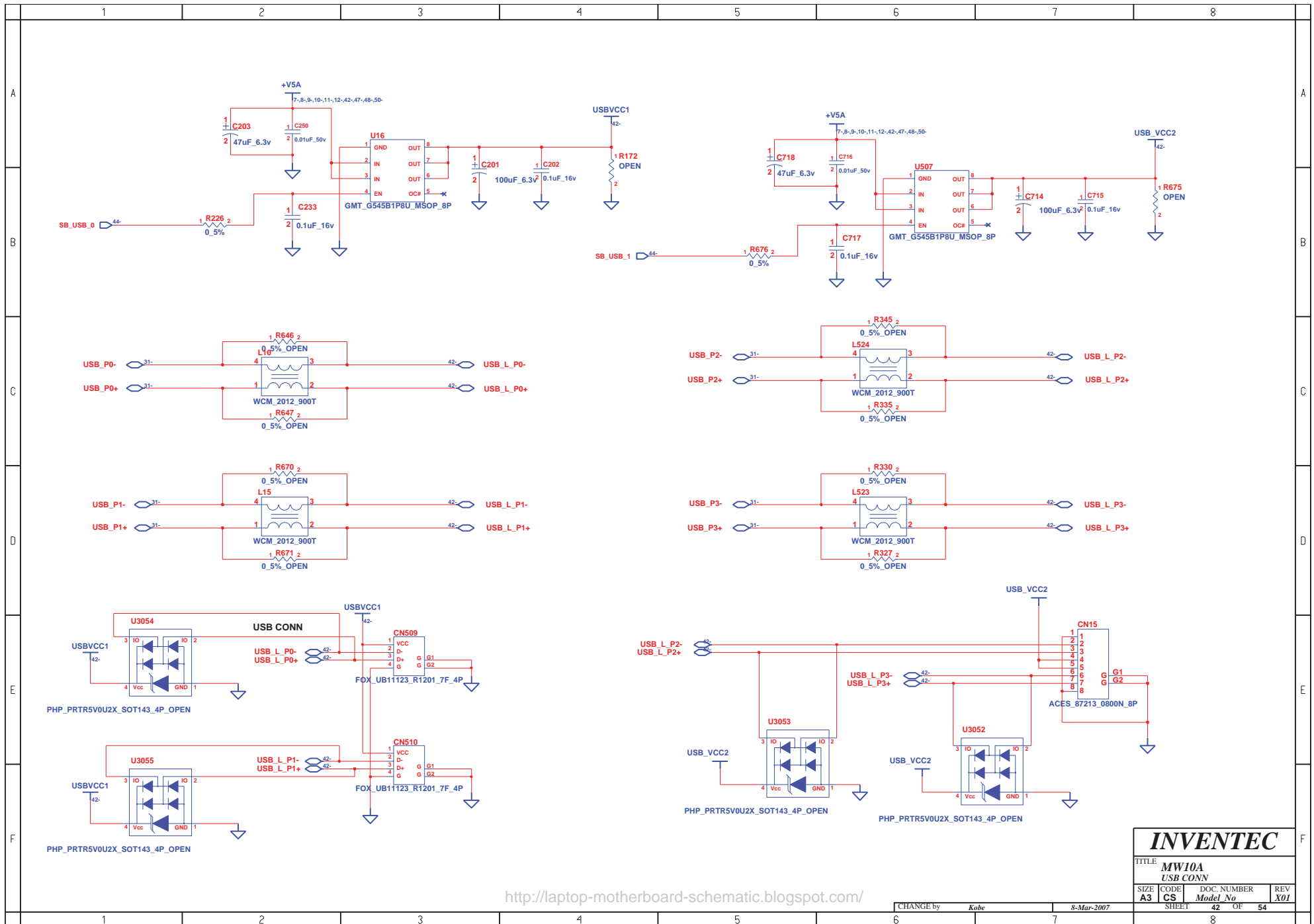
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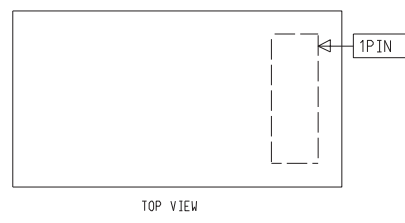
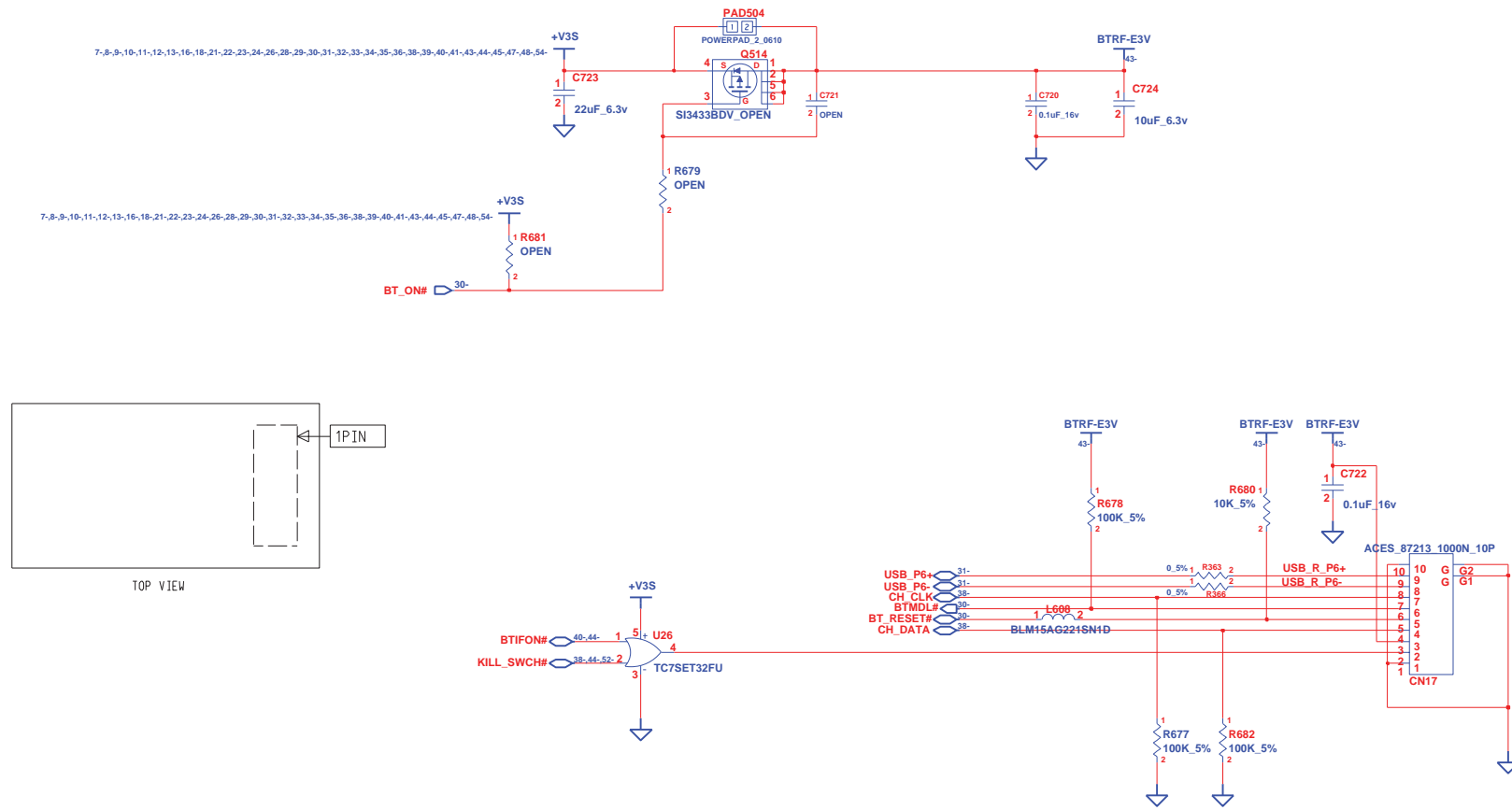
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MW10A ODD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

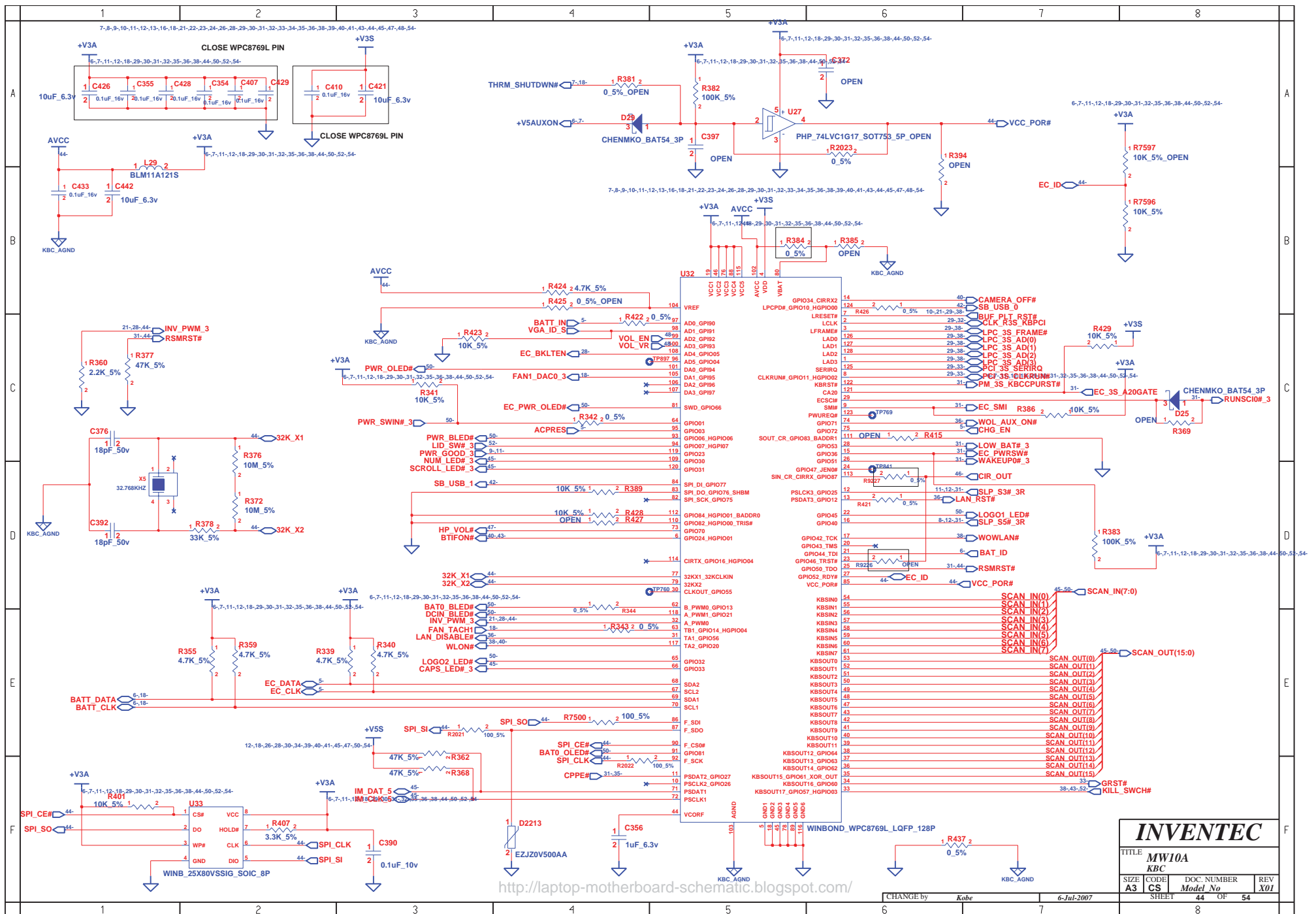
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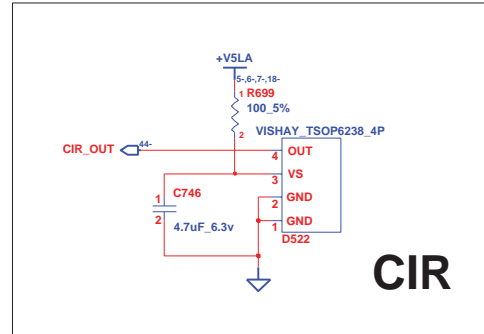
CHANGE by	<i>Kobe</i>	8-Mar-2007
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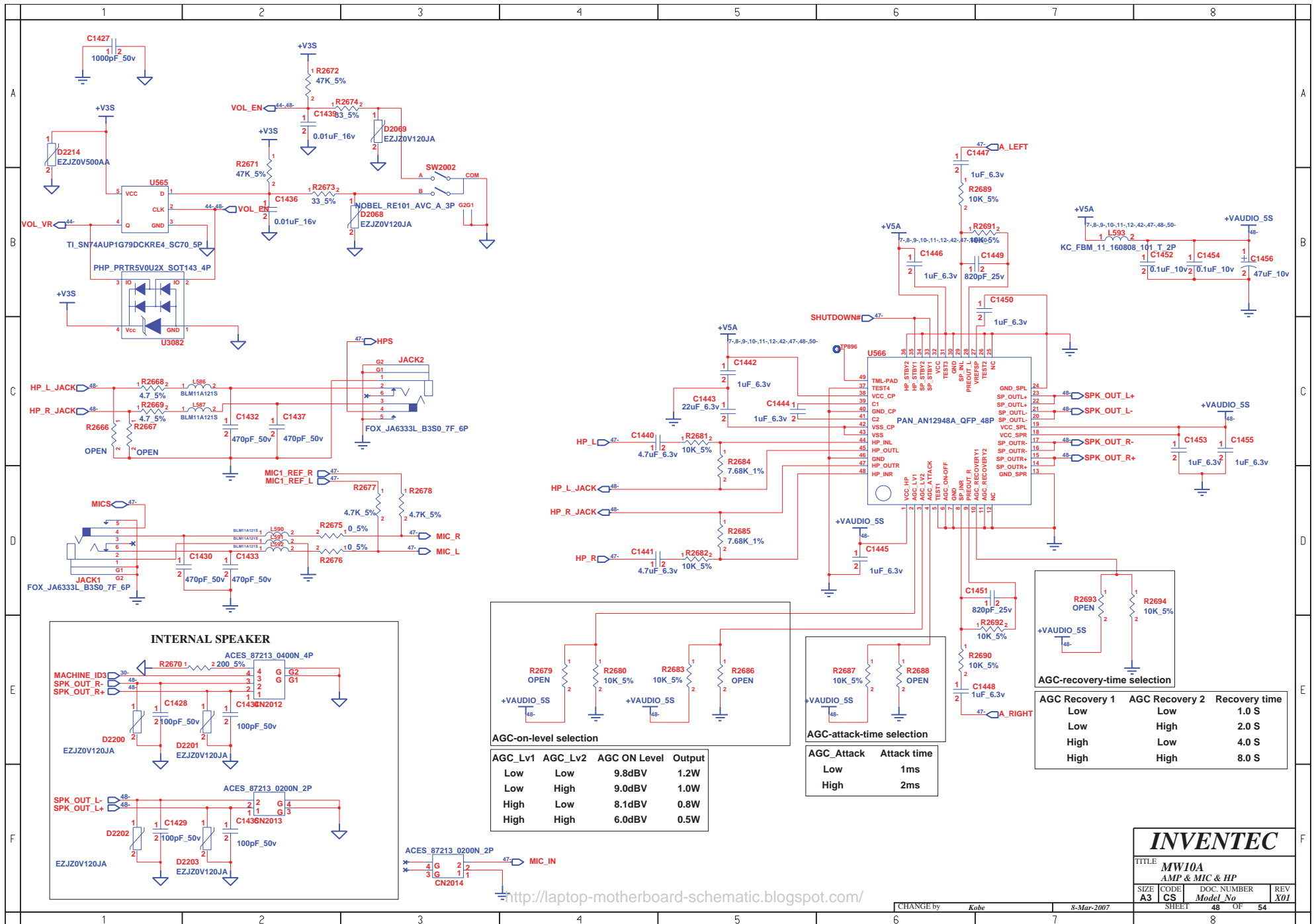
CS Model No
SHEET 41 OF 54

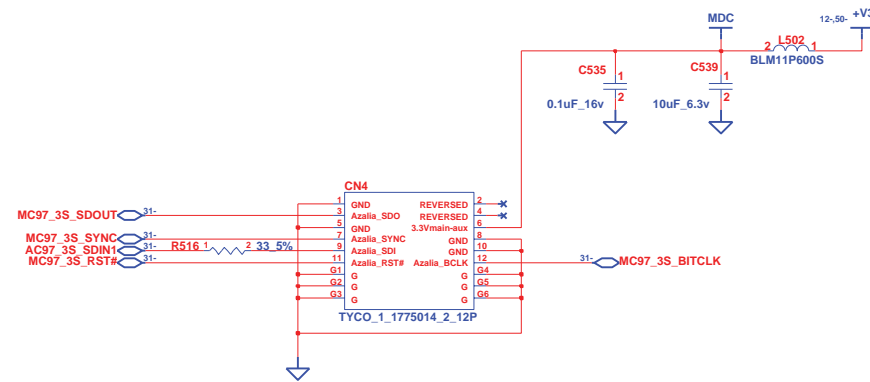


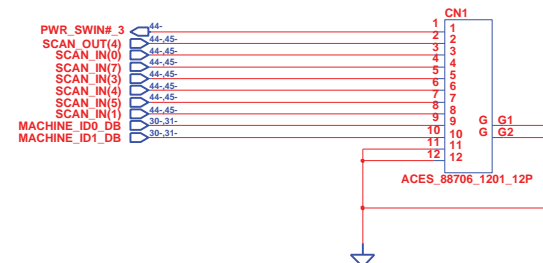
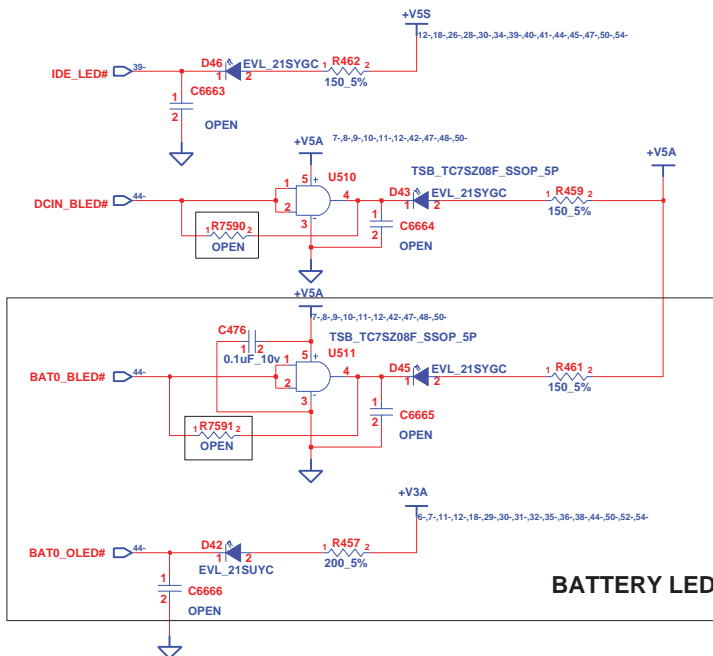
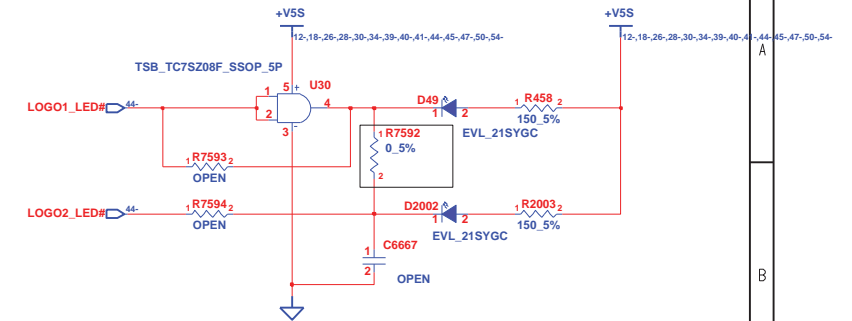
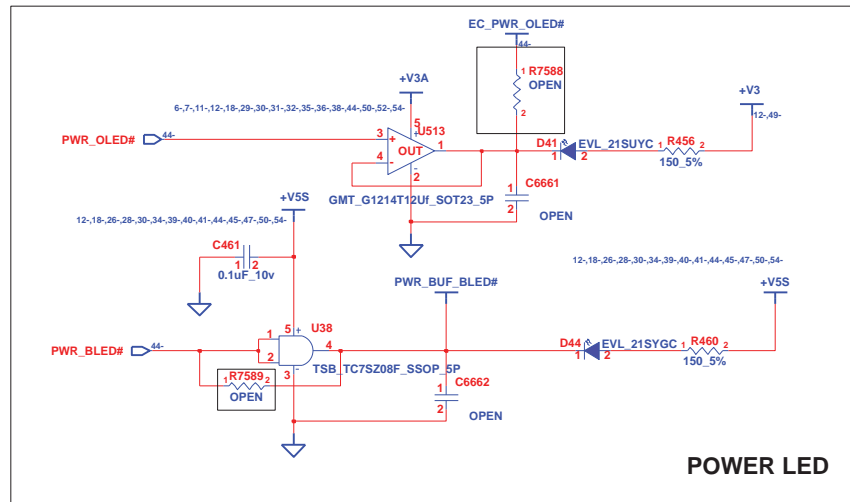






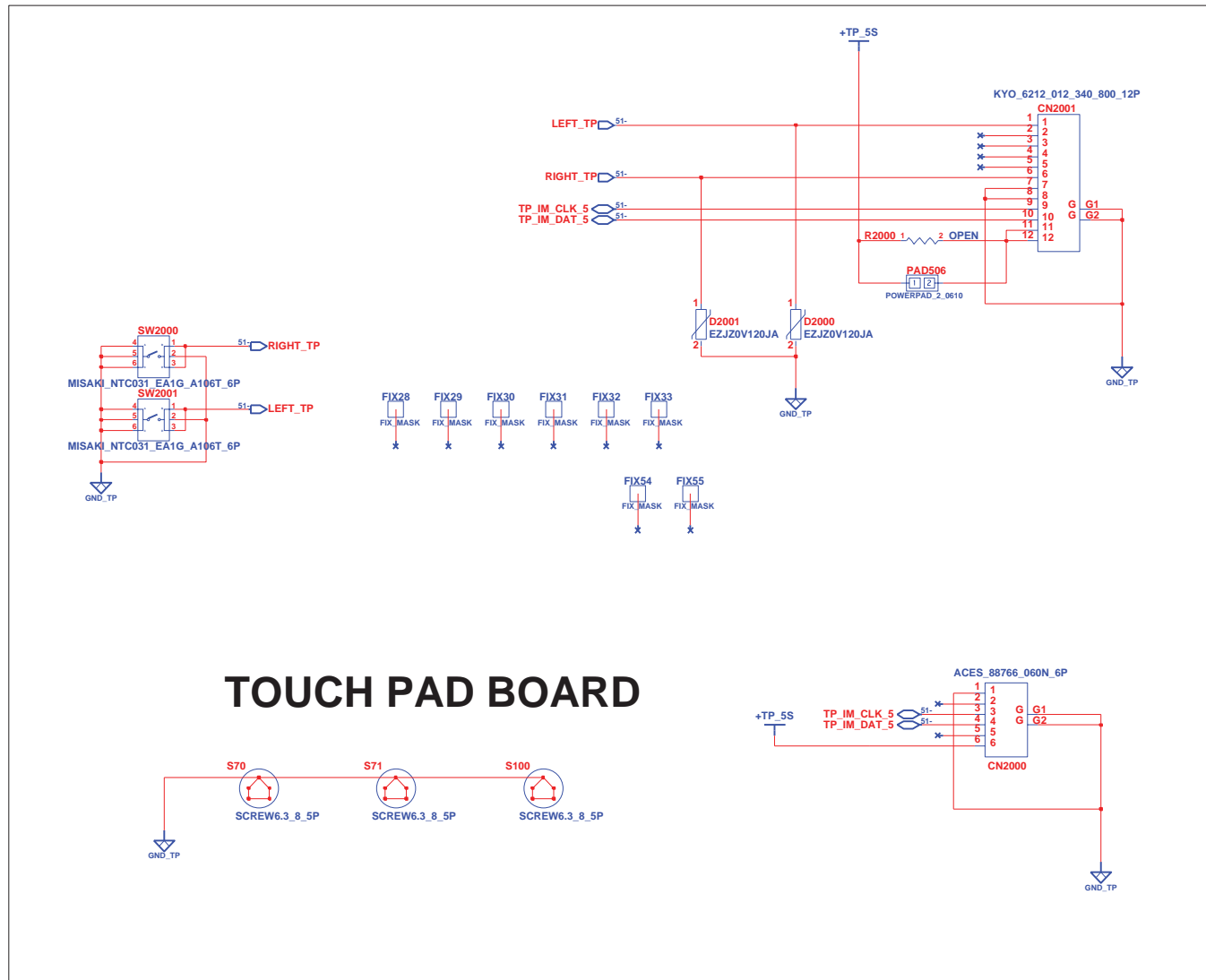






INVENTEC

TITLE			
MW10A			
LED			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01



TOUCH PAD BOARD

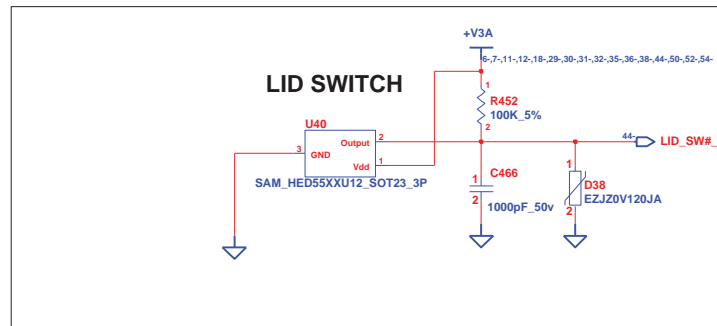
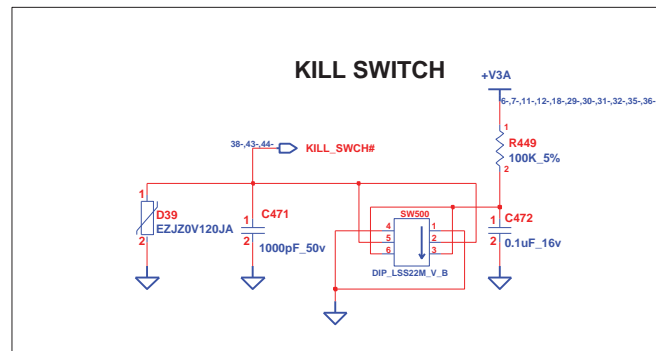
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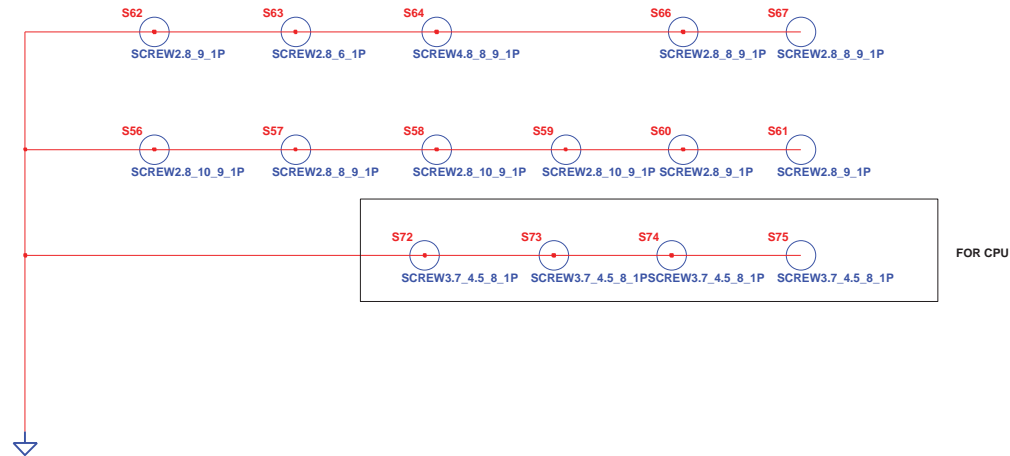
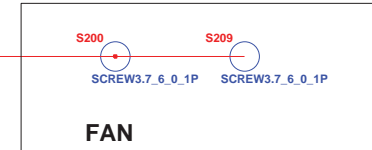
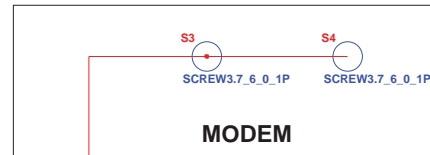
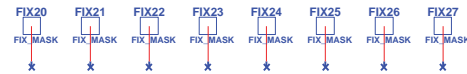
TITLE			
MW10A			
Touch Pad Board			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

<http://laptop-motherboard-schematic.blogspot.com/>

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SHEET 51 OF 54





INVENTEC			
TITLE MW10A SCREW			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
CHANGE by Kobe		22-Mar-2007	
SHEET		53 OF 54	

