IALAA

Minnesota 10A/10AG

LA3631P REV 1A Schematic

AMD Turion,Sempron/ATI RX690/RS690MC / ATI SB600

2007-05-04 Rev. 1A

Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

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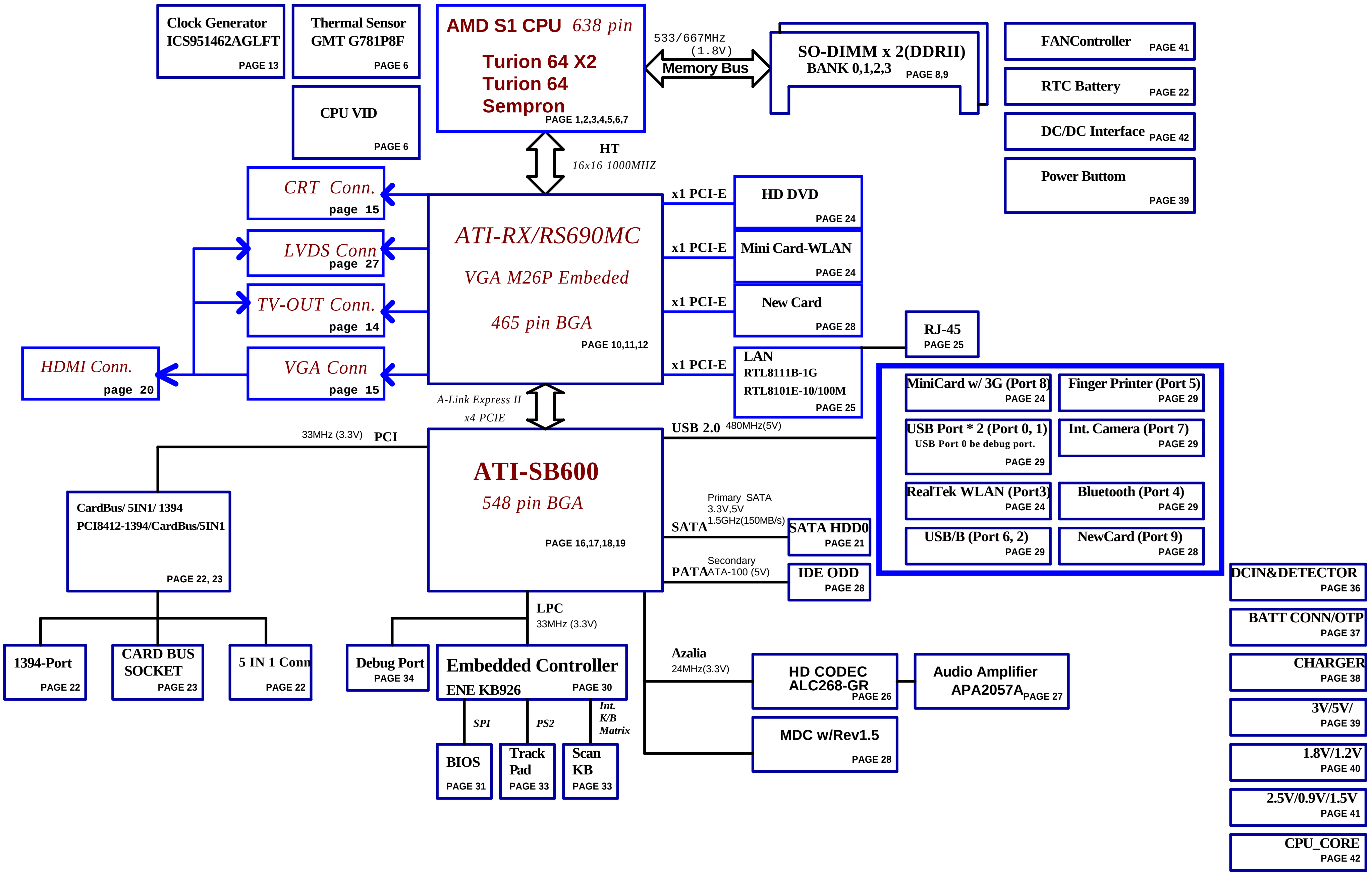
Title	Cover	
Size: B	Document Number: IALAA-Minnesota10A LA3631P	Rev: 1A
Date: Monday, May 14, 2007	Sheet 1 of 45	

Compal confidential

IALAA Minnesota10A FUNCTION BLOCK DIAGRAM

File Name : IALAA Minnesota10A LA3631P

P/N :



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: Black Diagram
				Size: Custom; Document Number: IALAA-Minnesota10A LA3631P; Rev: 1A
				Date: Monday, May 14, 2007; Sheet 2 of 45

Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	ON	ON	ON
B+	AC or battery power rail for power circuit.	ON	ON	ON
+RTCVCC	RTC power	ON	ON	ON
+VSB	B+ switched power rail	ON	ON	ON
+5VALW	5V always on power rail	ON	ON	ON
+3VALW	3.3V always on power rail	ON	ON	ON
+1.2VALW	1.2V always on power rail	ON	ON	ON
+1.8V	1.8V power rail	ON	ON	OFF
+0.9V	0.9V switched power rail	ON	ON	OFF
+5VS	5VS switched power rail	ON	OFF	OFF
+3VS	3.3VS switched power rail	ON	OFF	OFF
+2.5VS	2.5VS switched power rail	ON	OFF	OFF
+1.8VS	1.8VS switched power rail	ON	OFF	OFF
+1.5VS	1.5VS switched power rail	ON	OFF	OFF
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.2V_HT	1.2VS switched power rail	ON	OFF	OFF

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

STATE \ SIGNAL	SLP_S3#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	ON	OFF	OFF	OFF

ID Table for AD channel

Vcc	3.3V +/- 5%
Ra	100K +/- 5%

Board ID	Rb	V_{AD_BID} min	V_{AD_BID} typ	V_{AD_BID} max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BTN_ID	
0	
1	
2	
3	
4	
5	
6	
7	

BTO	BOM STURCTURE

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
1394/ CardBus/ 5IN1	AD20	2/ 2	PIRQE/F/G

EC SM Bus1 address

Device	HEX	Address
Smart Battery	16H	0001 011X b
24C16	A0H	1010 000X b

EC SM Bus2 address

Device	HEX	Address
CPU Thermal-G781P8F	98H	1001 100X b
VGA Thermal-		

ATi SB600 SM Bus address

SM Bus0 address

Device	HEX	Address
Clock GEN. (ICS951462AGLFT)		
DDR DIMM0	A4	
DDR DIMM1	A6	
Mini Card-WLAN		
Mini Card-3G		
New Card		

SM Bus1 address

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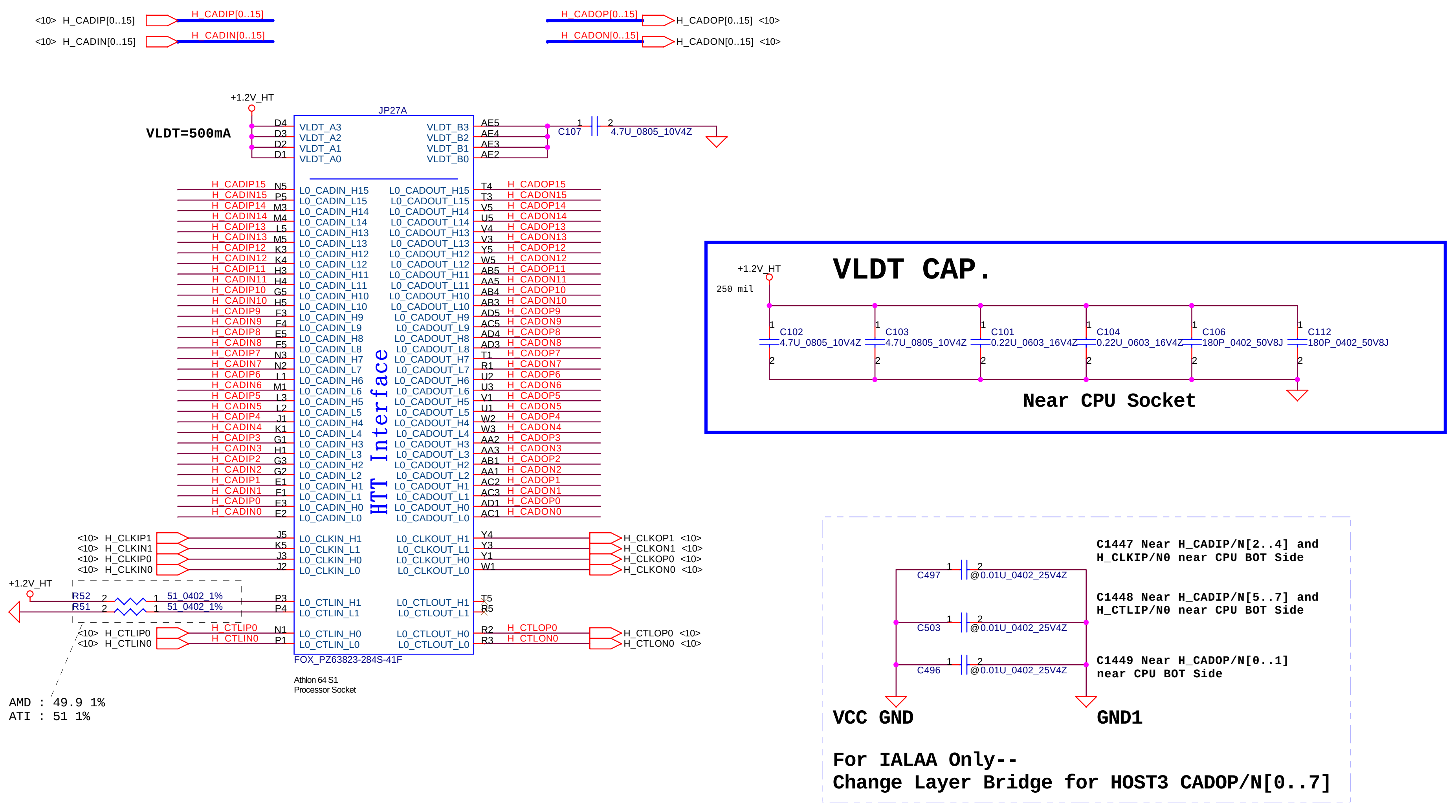

Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: S1g1 HT I/F
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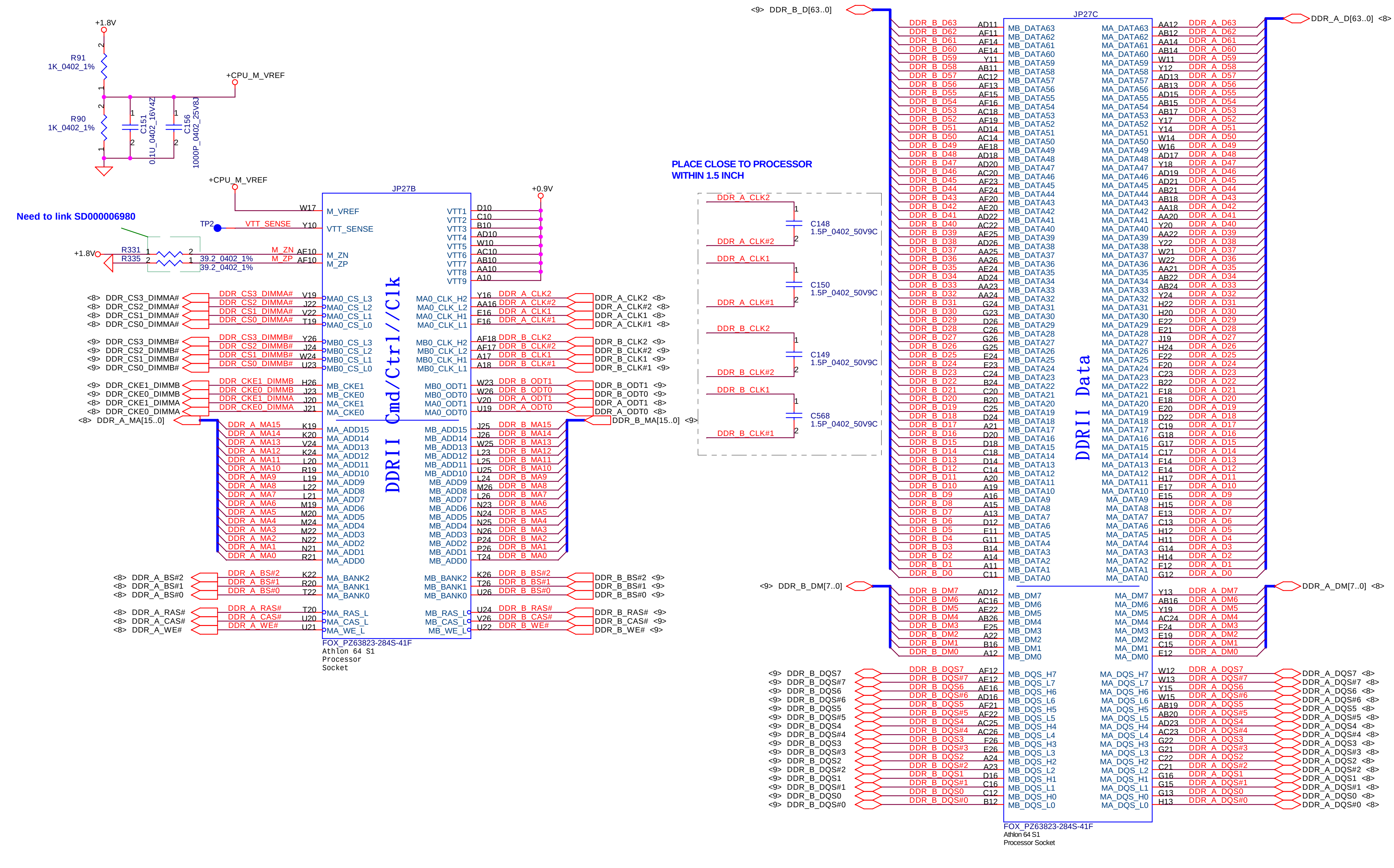
				Date: Monday, May 14, 2007; Sheet 4 of 45

Processor DDR2 Memory Interface



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: S1g1 DDRII I/F

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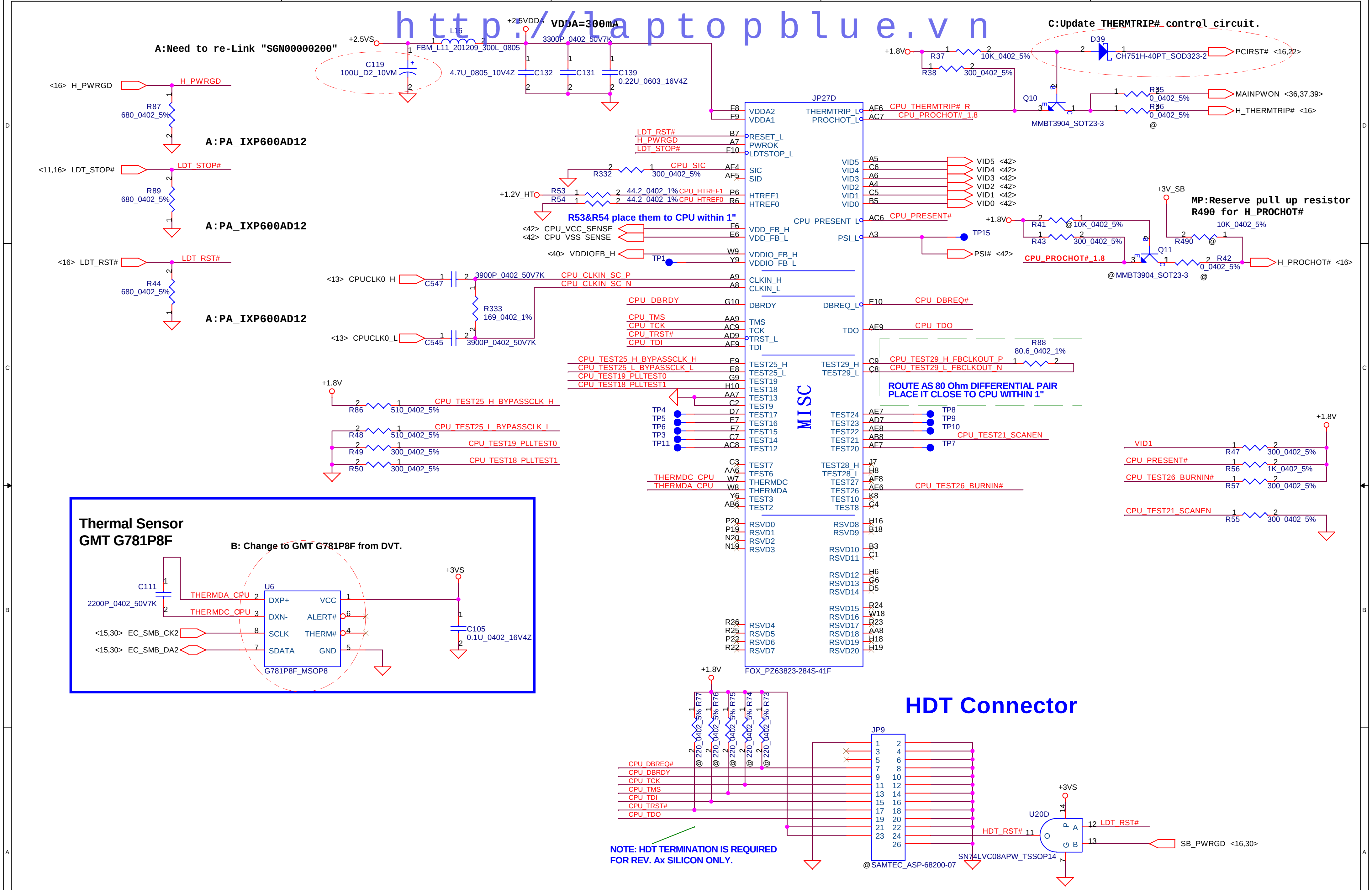
Date: Monday, May 14, 2007 |



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

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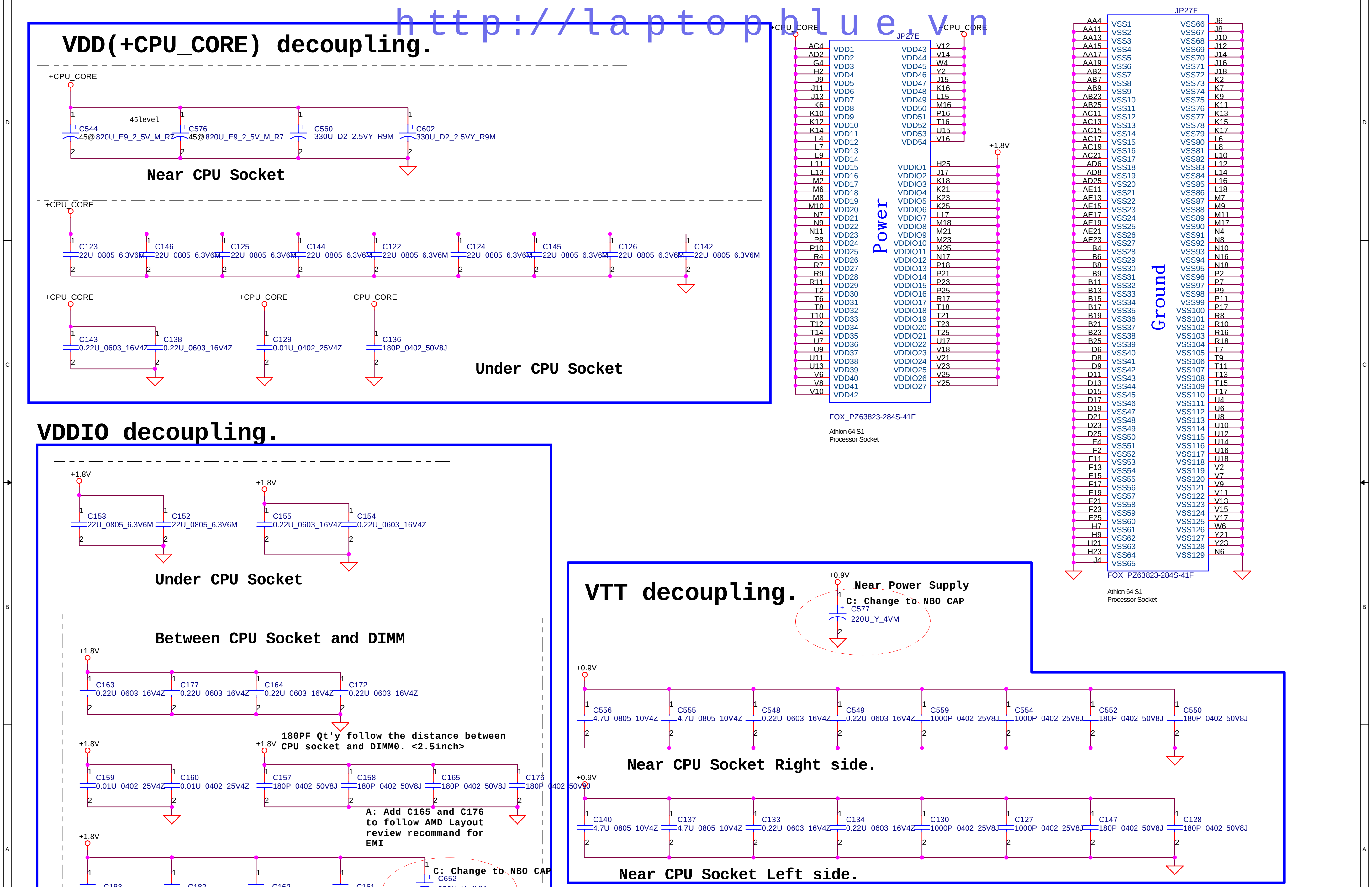
Title: **S1g1 CTRL**

Size: Custom — Document Number: IALAA-Minnesota10A LA3631P — Rev: 1A

Date: Wednesday, May 16, 2007 — Sheet 6 of 45



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

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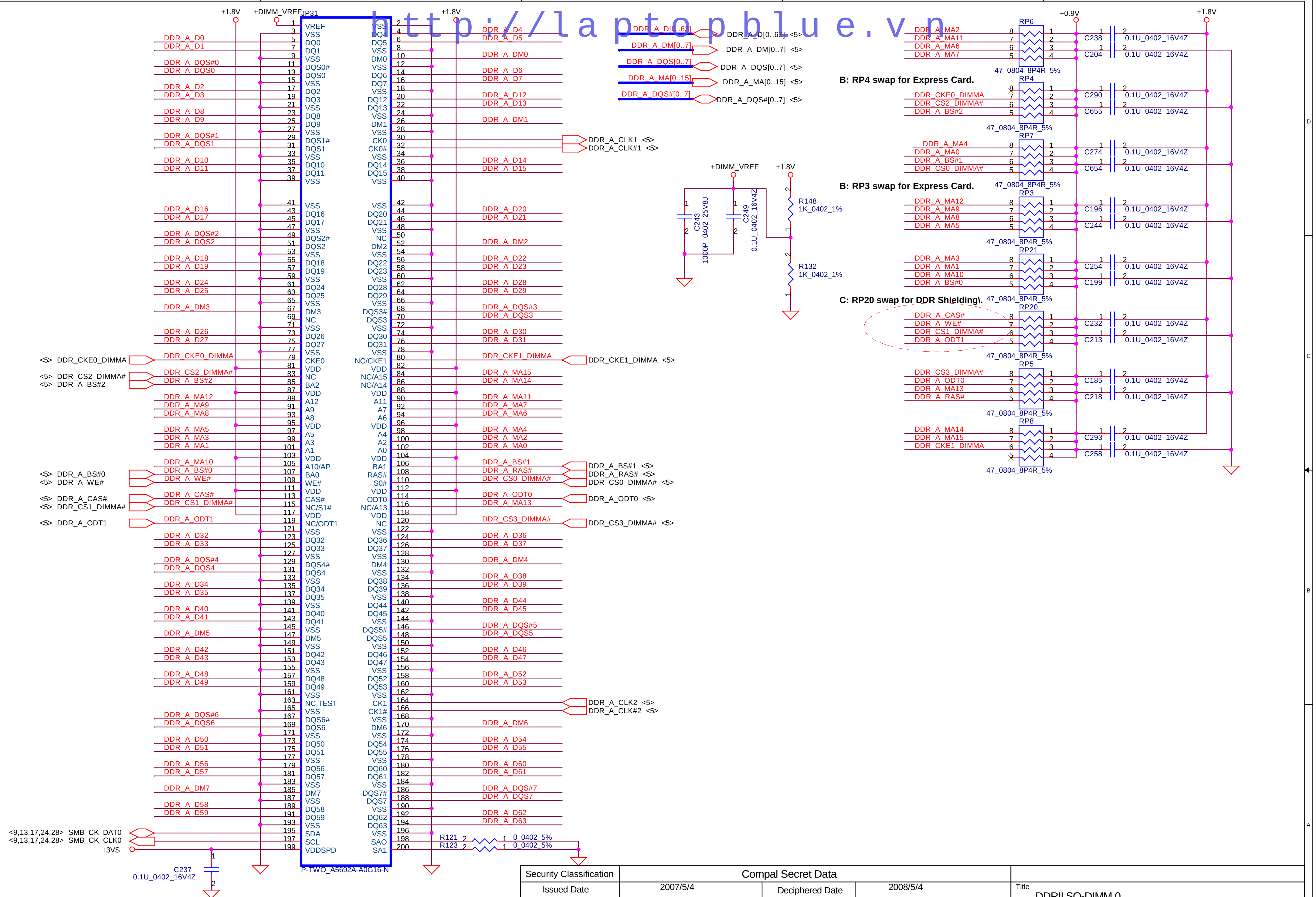
Title: **S1g1 PWR & GND**

Size	Document Number	Rev
Custom	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007



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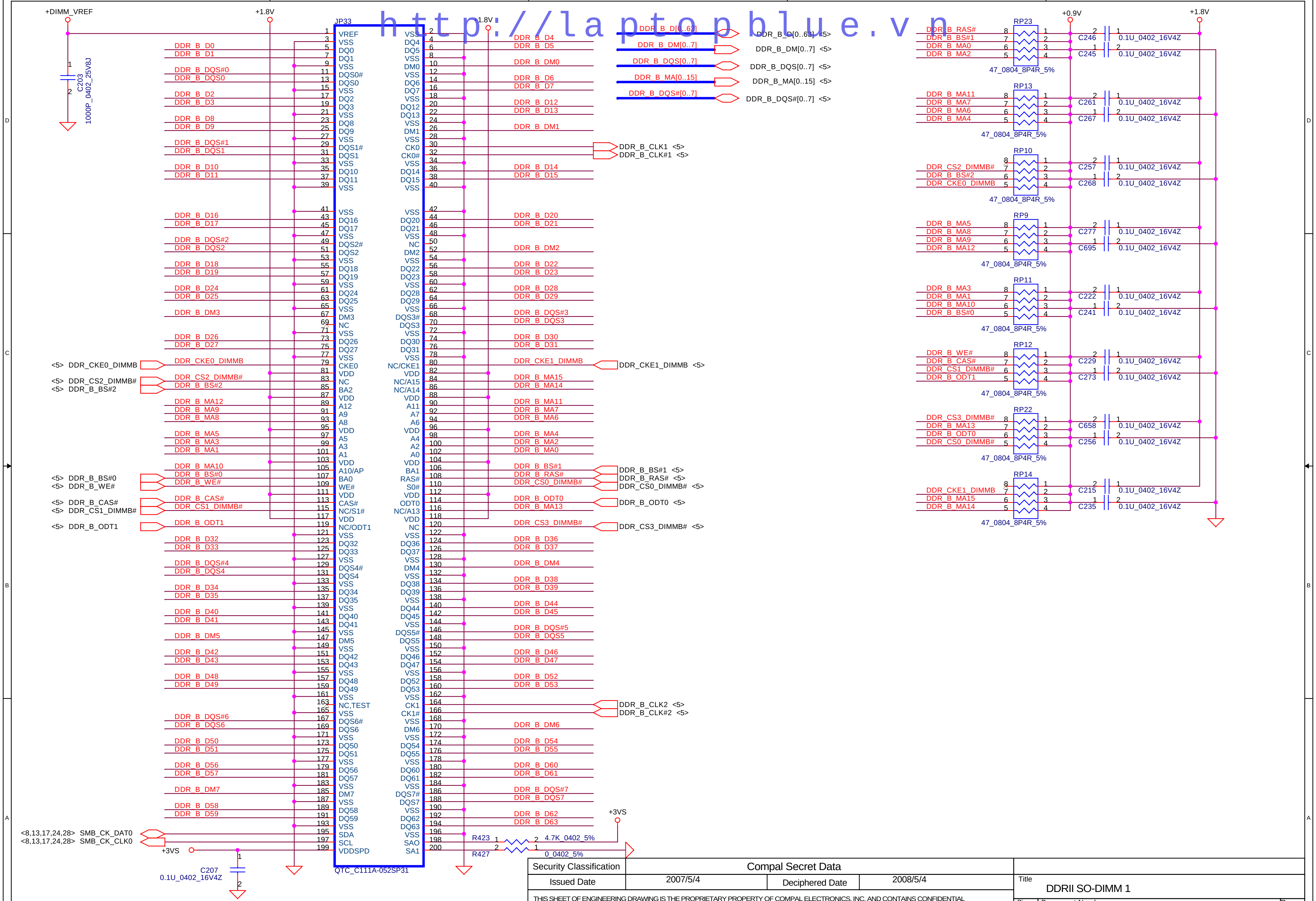
Title: DDRII SO-DIMM 0

Size	Document Number	Rev
Custom	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007 — Sheet 8 of 45

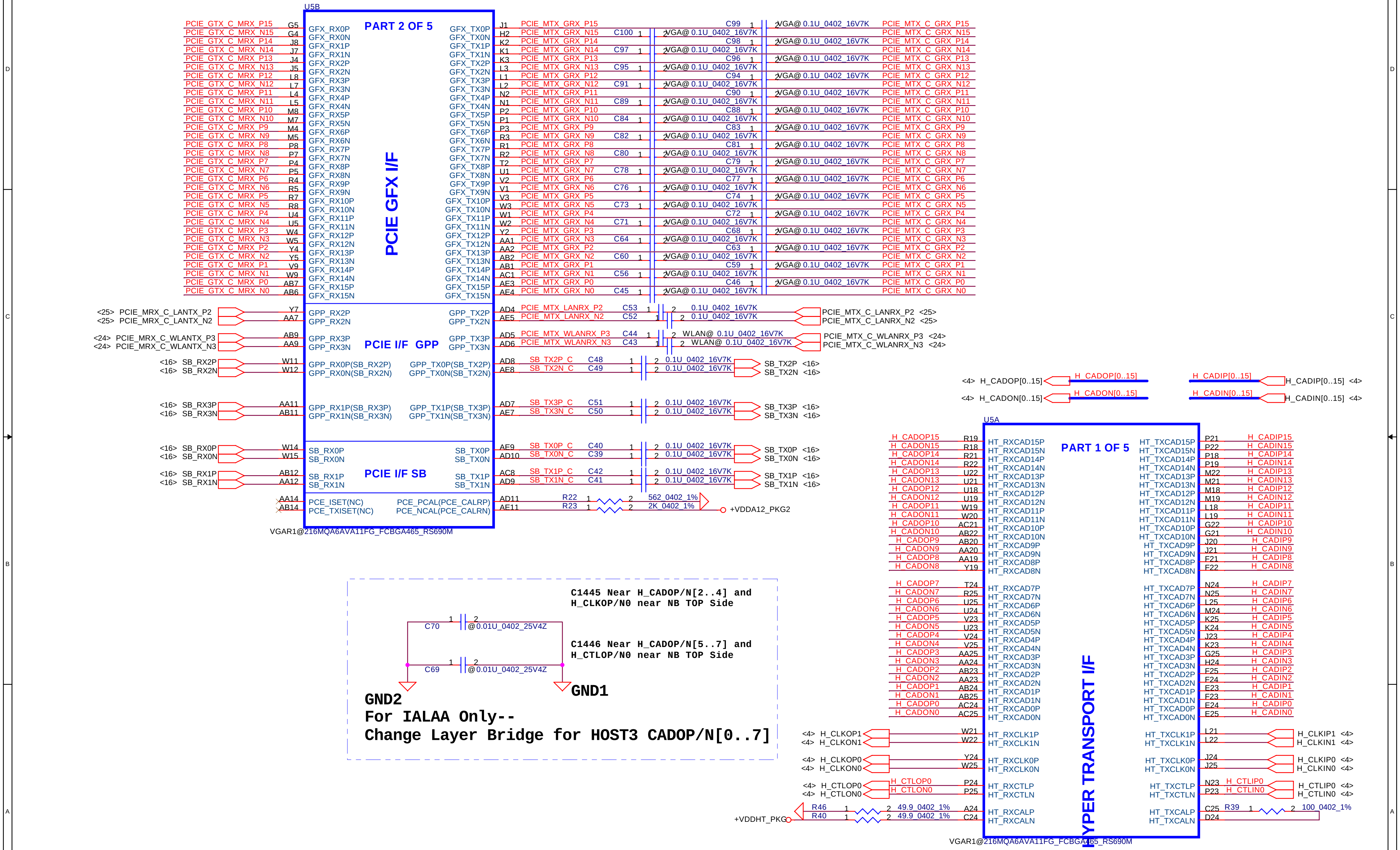


Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: DDRII SO-DIMM 1

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007



Security Classification	Compal Secret Data		Title
Issued Date	2007/5/4	Deciphered Date 2008/5/4	RX690/RS690MC HT/VMEM

Size	Document Number	Rev
Custom	IALAA-Minnesota10A LA3631P	1A

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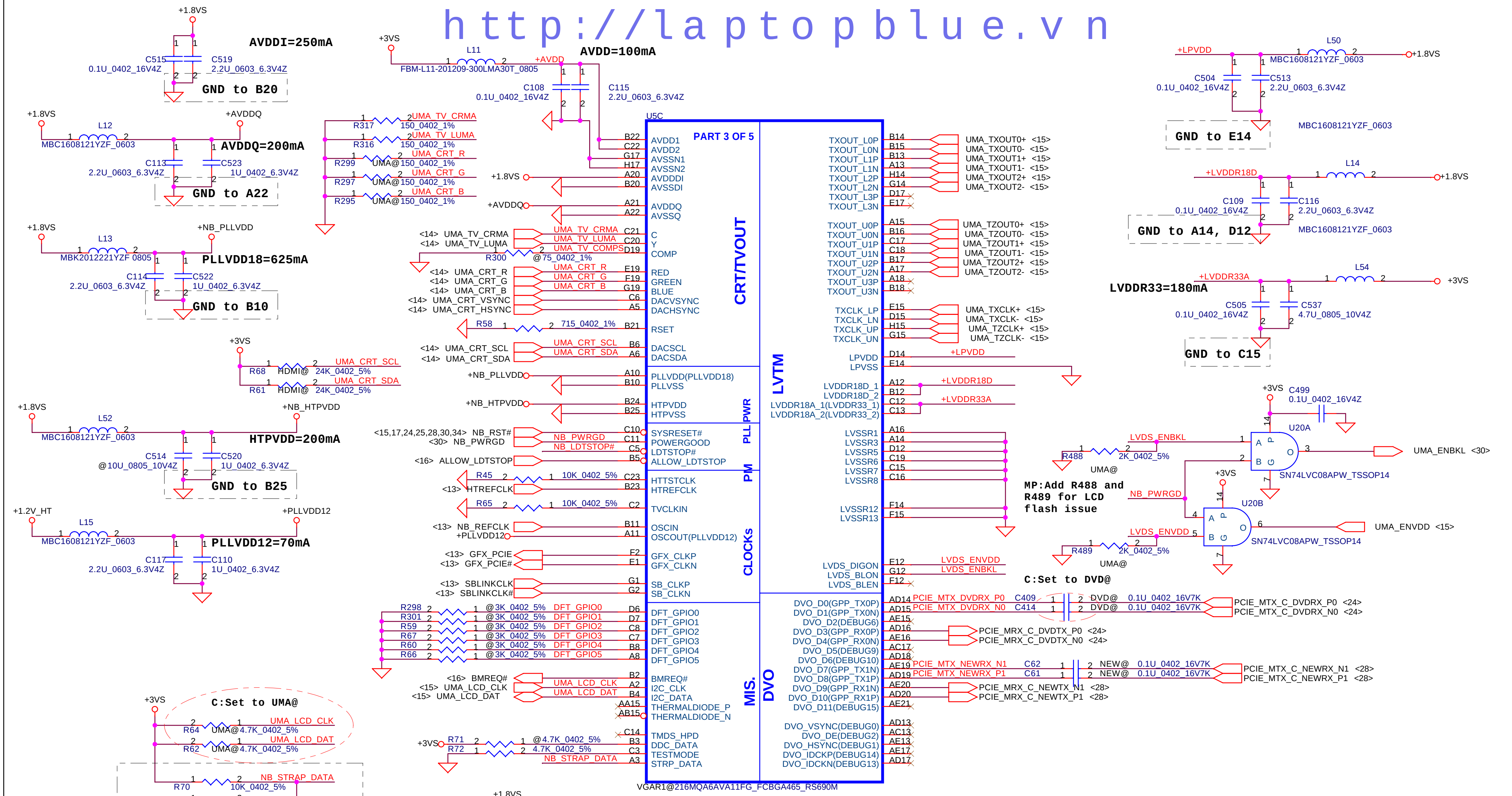
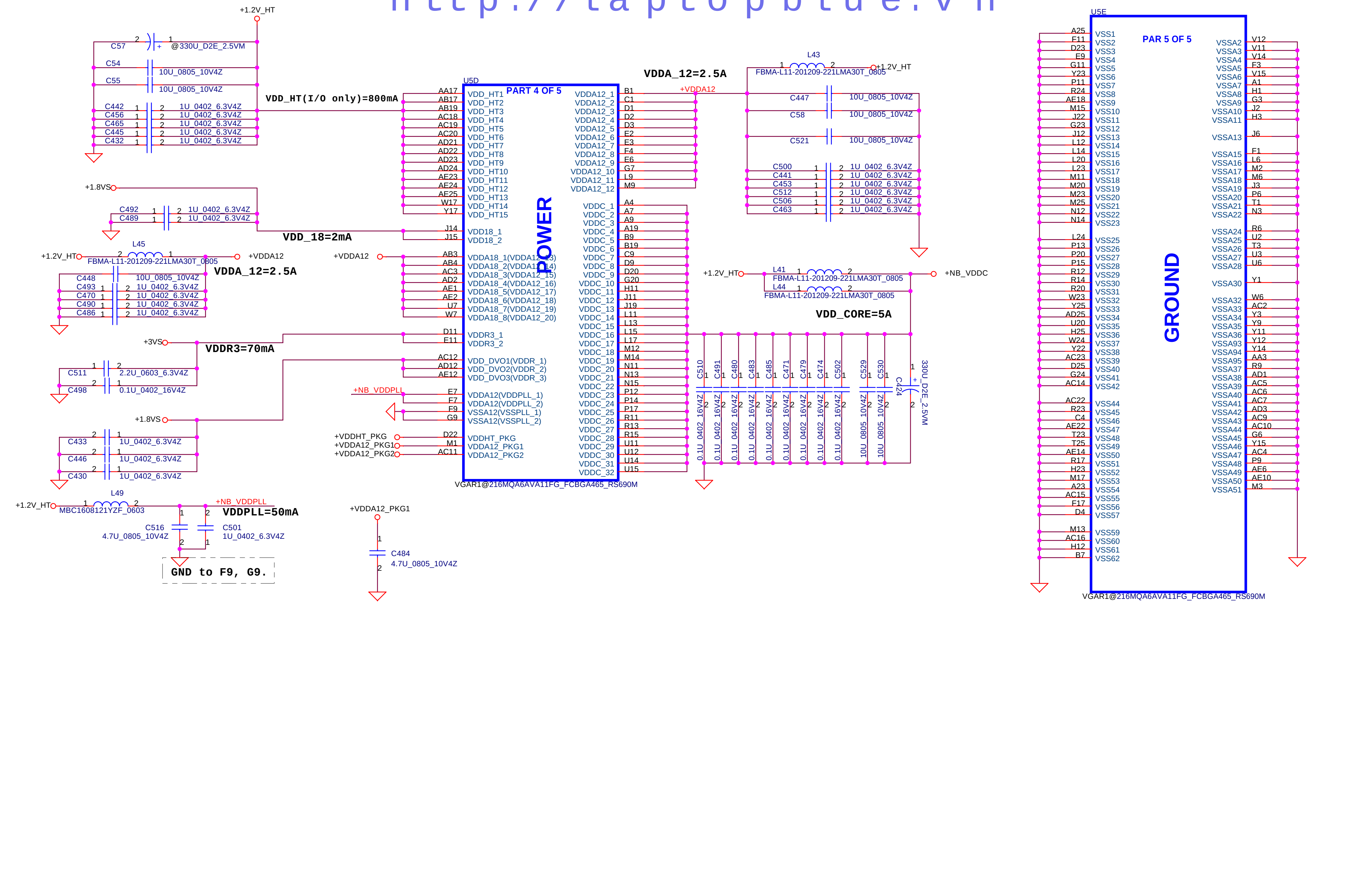


	RS690 DFT_GPIO0	DFT_GPIO1	RS690 only DFT_GPIO[4:2]	DFT_GPIO5
PULL HIGH (internally pulled high)	Memory side port not available DEFAULT	Bypass the loading of EEPROM straps and use Hardware default values DEFAULT	These pin straps are used to configure PCI-E GPP mode: 111: register defined (register default to Config E) DEFAULT 110: 4-0-0-0-0 Config A 101: 4-4 Config B 100: 4-2-2 Config C 011: 4-2-1-1 Config D 010: 4-1-1-1-1 Config E others: register defined (register default to Config E)	Enable debug bus via the memory IO pads, if available in the package use default values DEFAULT
PULL LOW	Memory side port available	I2C Master can load strap values from EEPROM if connected, or use default values if not connected		use the memory data bus to output the debug bus

Security Classification	Compal Secret Data		Title
Issued Date	2007/5/4	Deciphered Date 2008/5/4	RX690/RS690MC VIDEO_IF/CLOCK GEN

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title	RX690/RS690MC Power/GND
Size: Custom	Document Number: IALAA-Minnesota10A LA3631P
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Date:	Monday, May 14, 2007
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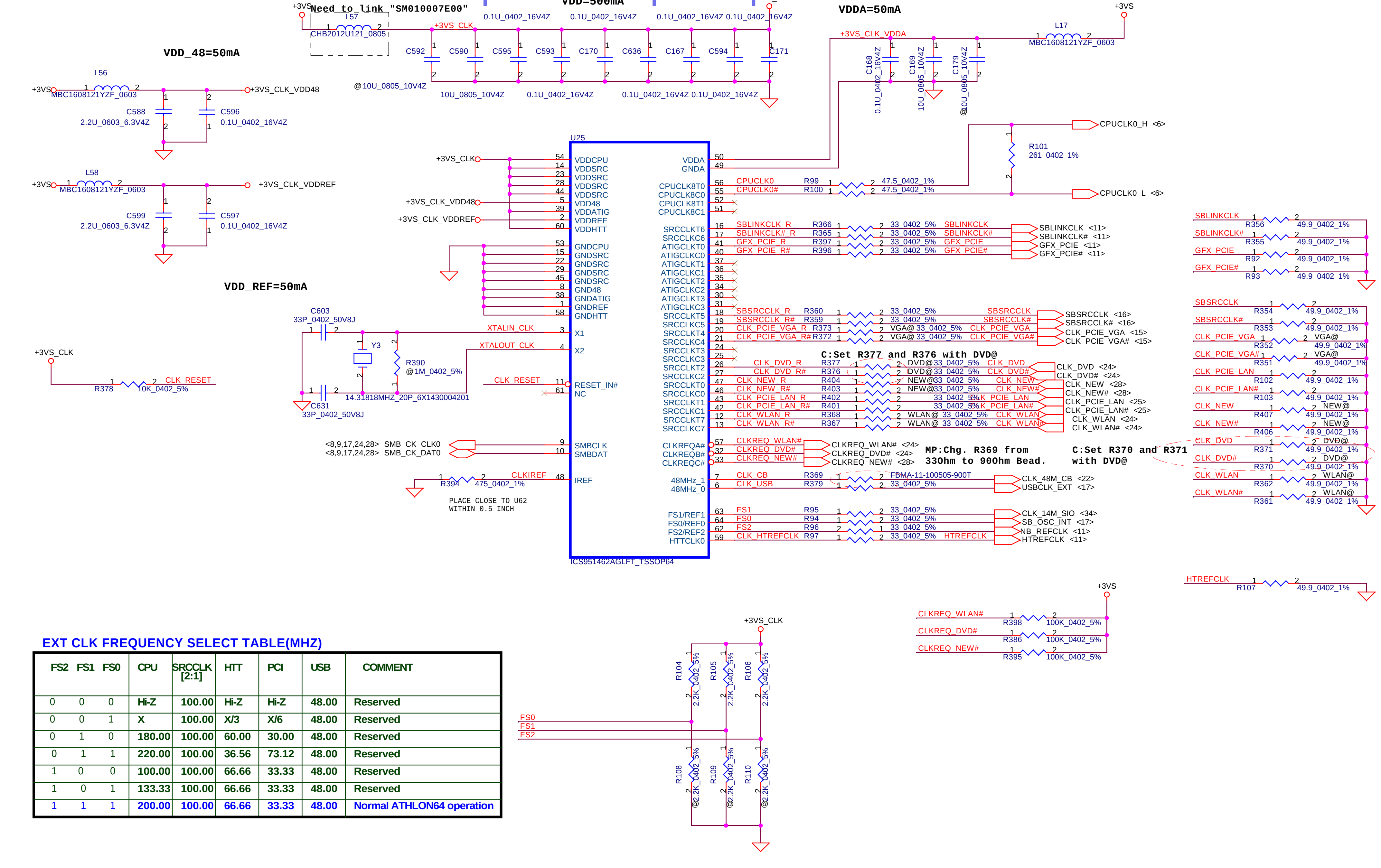


EXT CLK FREQUENCY SELECT TABLE(MHZ)

FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal ATHLON64 operation

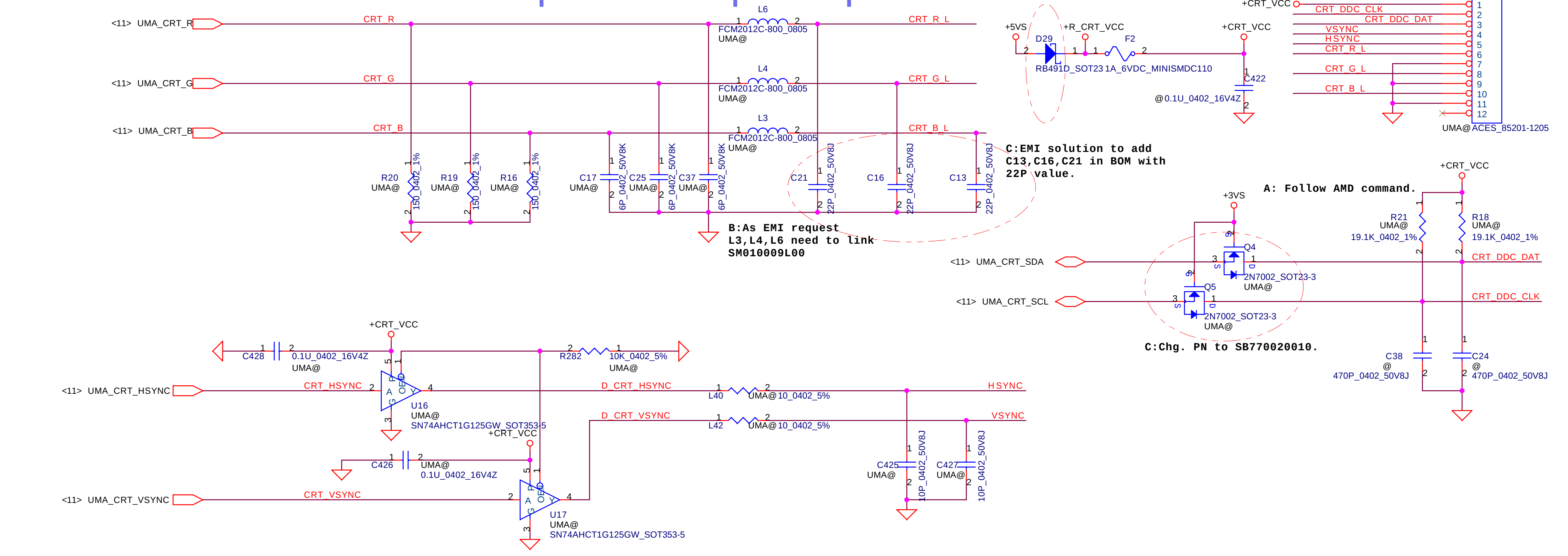
Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: Clock Generator

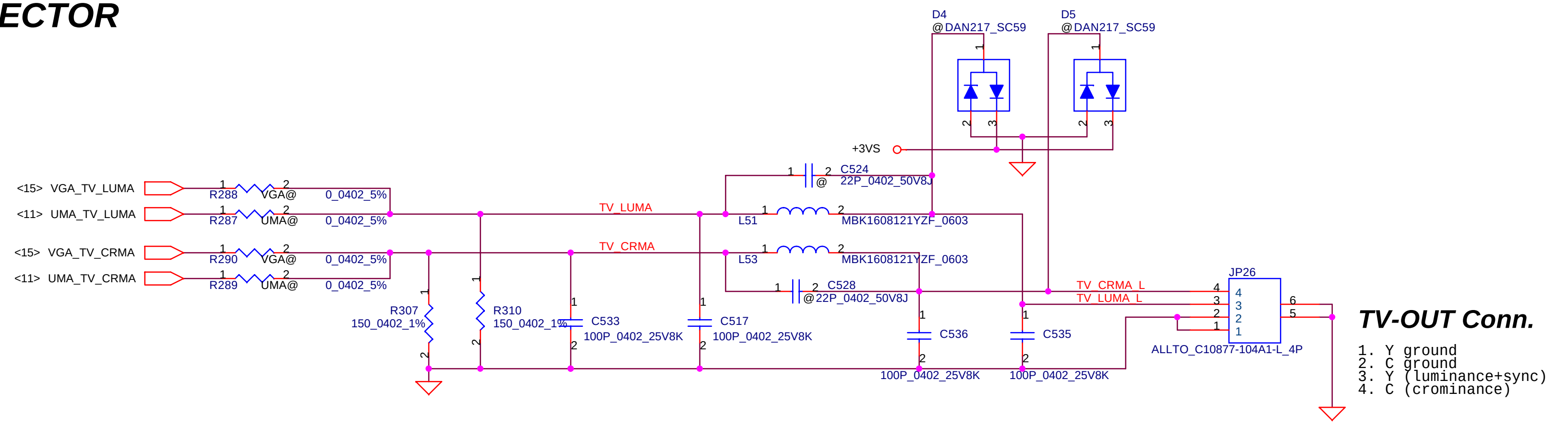
Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Wednesday, May 16, 2007 | Sheet 13 of 45

CRT CONNECTOR



TV-OUT CONNECTOR



TV-OUT Conn.

1. Y ground
2. C ground
3. Y (luminance+sync)
4. C (crominance)

Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

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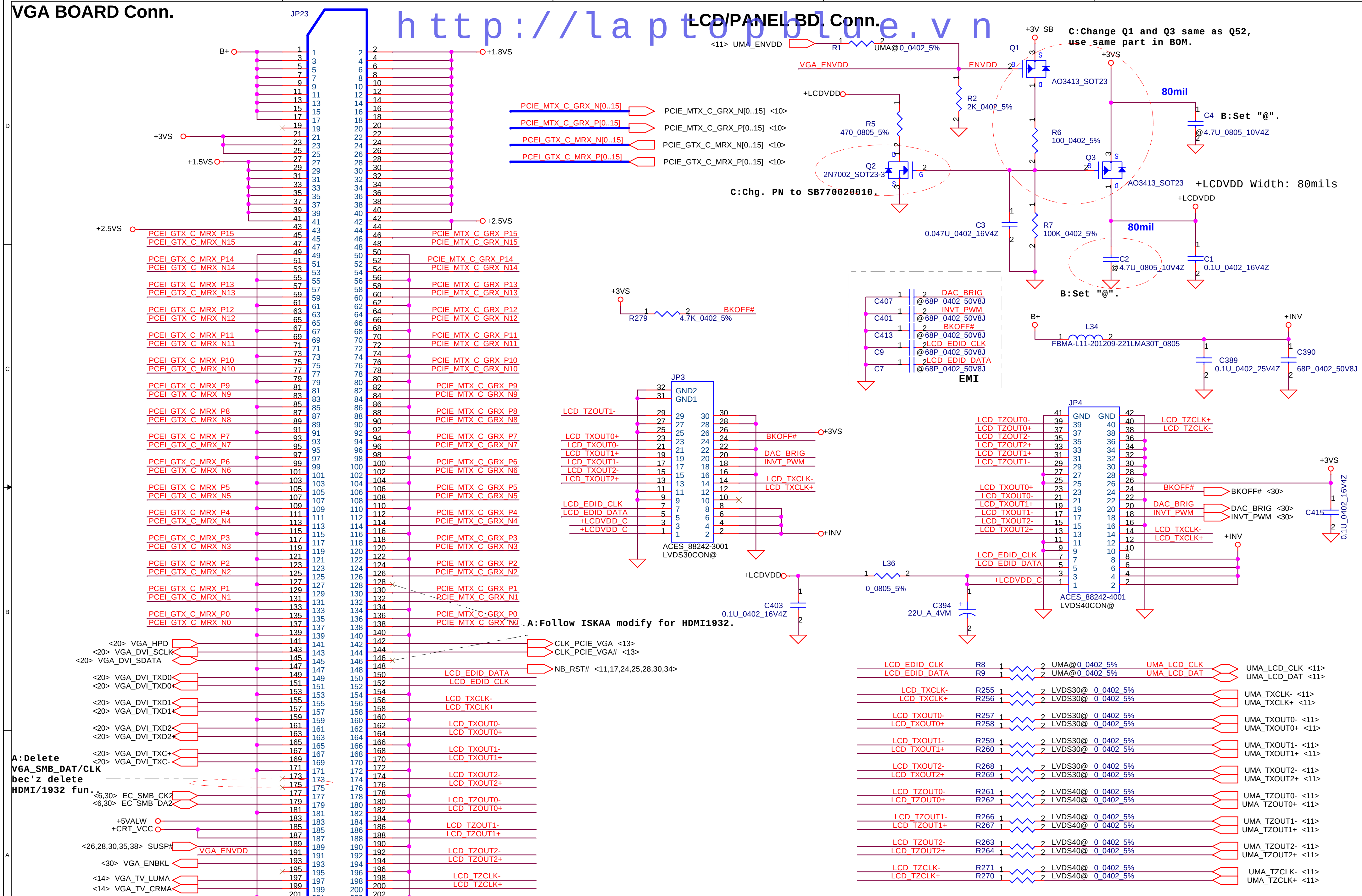
Title: TV-OUT, LVDS CONNECTOR

Size	Document Number	Rev
B	IALAA-Minnesota10A LA3631P	1A



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

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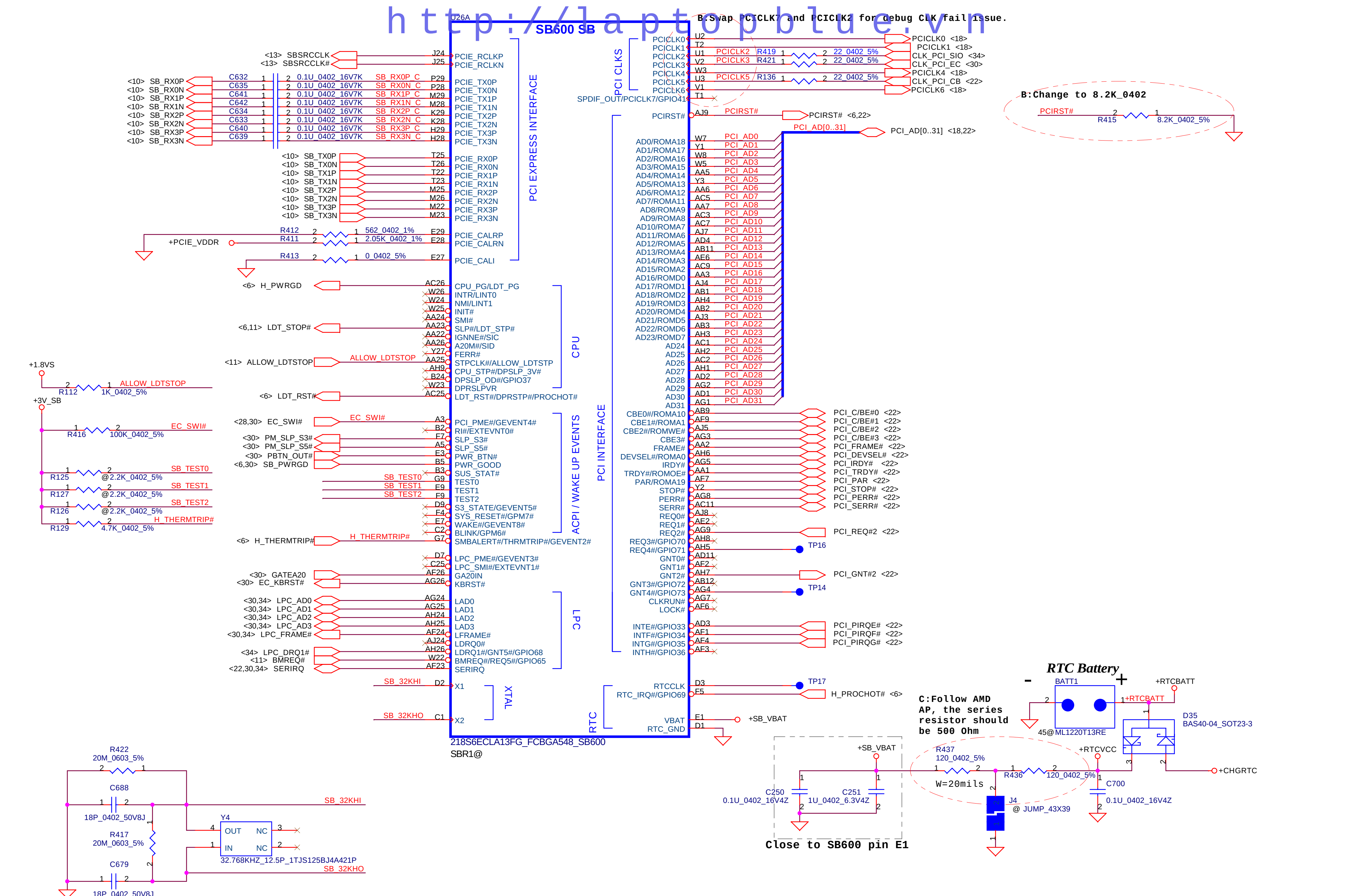
Title: CRT CONNECTOR

Size	Document Number	Rev
B	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007

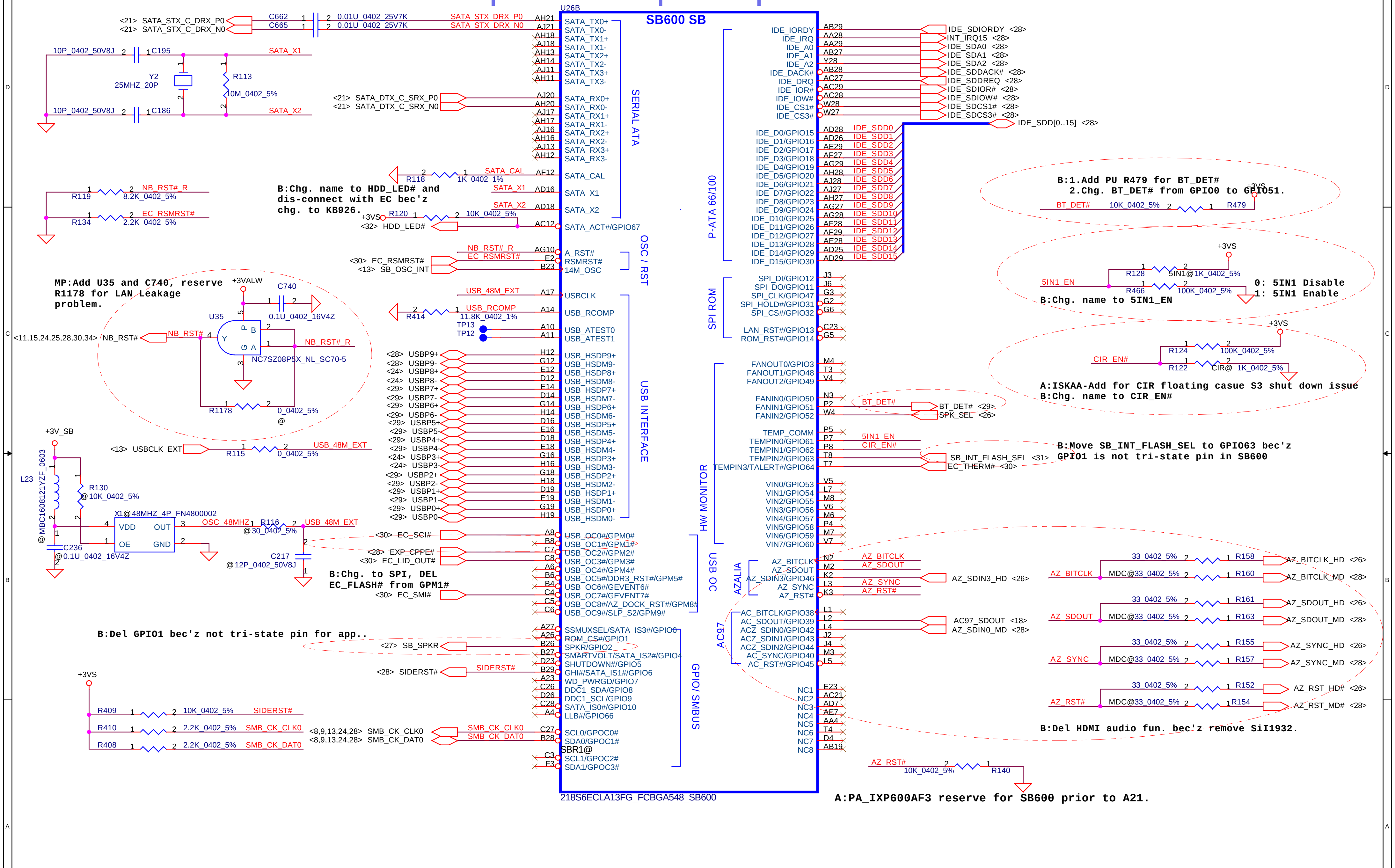


Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: SB600-PCI_EXP/PCI/LPC/RTC

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |



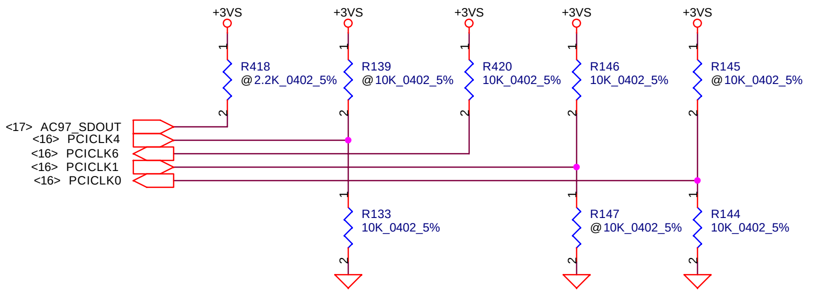
Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: SB600-USB/ACPI/AC97/GPIO

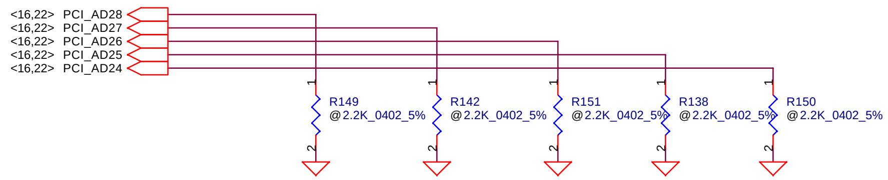
Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |

Standard Straps



Debug Straps



	AC_SDOUT	PCI_CLK4 <PCICLK4>	PCI_CLK6 <PCICLK6>	PCI_CLK1 <CLK_PCI_LAN>	PCI_CLK0 <PCICLK0>
PULL HIGH	USE DEBUG STRAPS	USE INT. PLL48	CPU IF=AMD DEFAULT	ROM TYPE: H, H = Reserve H, L = LPC ROM L, H = SPI ROM L, L = FWH ROM	DEFAULT
PULL LOW	IGNORE DEBUG STRAPS <int'l PD> DEFAULT	USE EXT. 48MHZ DEFAULT	CPU IF=Intel		

	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23 <A11 only>
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	DISABLE BOOTFAIL TIMER DEFAULT
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	ENABLE BOOTFAIL TIMER

Un-Used Inputs Setting--GPIO pins

GPIO4/SMARTVOLT/SATA_IS2#	Config. GPIO to Output Mode.
GPIO5/SHUTDOWN#	Config. GPIO to Output Mode.
GPIO7/WD_PWRGD	Config. GPIO to Output Mode.
GPIO8/DDC1_SDA	Config. GPIO to Output Mode.
GPIO9/DDC1_SCL	Config. GPIO to Output Mode.
GPIO10/SATA_IS0#	Config. GPIO to Output Mode.
GPIO41/PCICLK7/SPDIF_OUT	Config. GPIO to Output Mode.
GPIO50/FANIN0	Config. GPIO to Output Mode.
GPIO51/FANIN1	Config. GPIO to Output Mode.
GPIO52/FANIN2	Config. GPIO to Output Mode.
GPIO53/VIN0	Config. GPIO to Output Mode.
GPIO54/VIN1	Config. GPIO to Output Mode.
GPIO55/VIN2	Config. GPIO to Output Mode.

Un-Used Inputs Setting--GPM pins

GPIO56/VIN3	Config. GPIO to Output Mode.
GPIO57/VIN4	Config. GPIO to Output Mode.
GPIO58/VIN5	Config. GPIO to Output Mode.
GPIO59/VIN6	Config. GPIO to Output Mode.
GPIO60/VIN7	Config. GPIO to Output Mode.
GPIO61/TEMPIN0	Config. GPIO to Output Mode.
GPOC2#/SCL1	Config. GPIO to Output Mode.
GPOC3#/SDA1	Config. GPIO to Output Mode.

Un-Used Inputs Setting--GPM pins

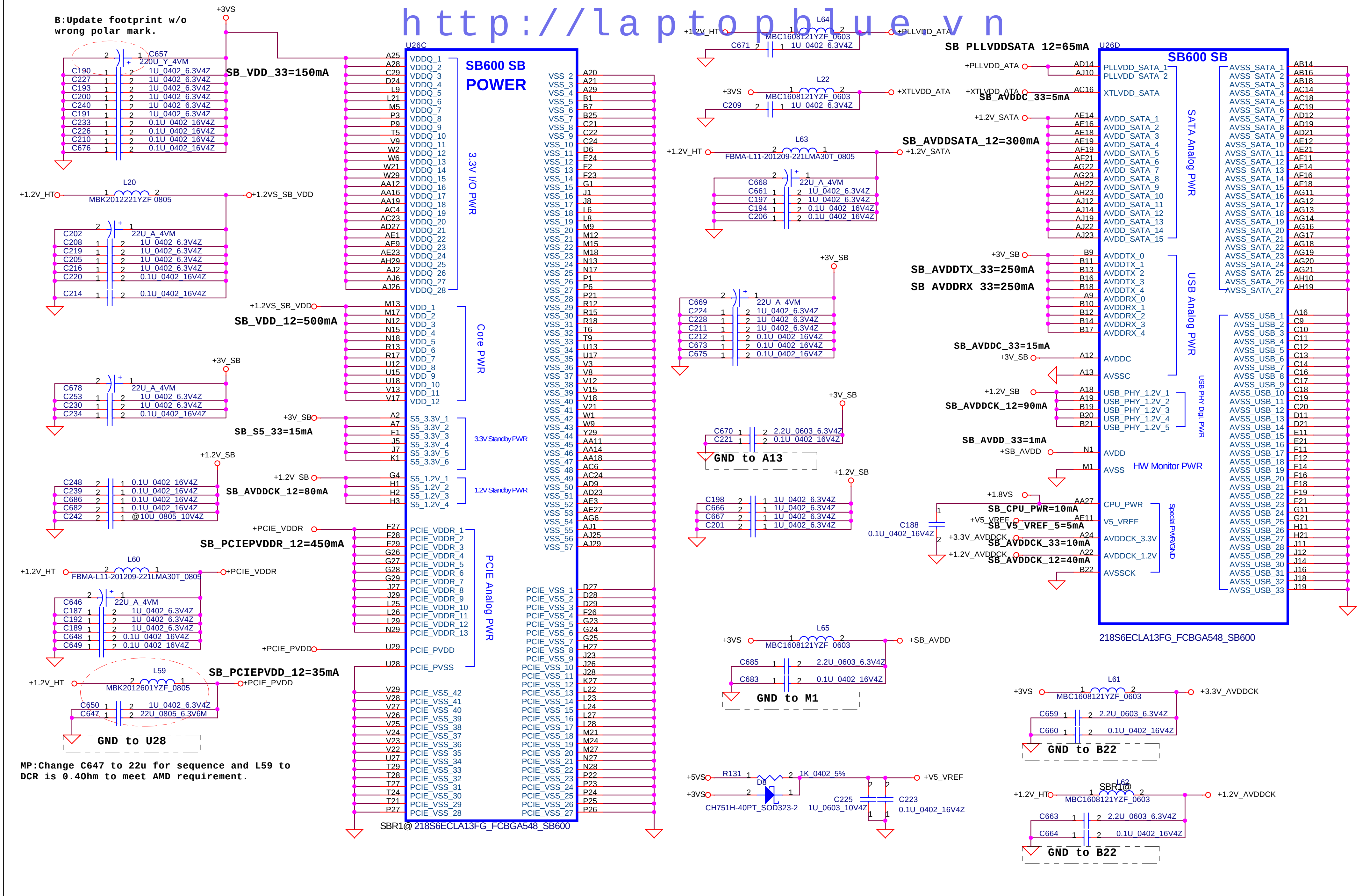
GPM5#/DDR3_RST#/USB_OC5#	Config. for internal PU.
GEVENT5#/S3_STATE	Config. for internal PU.

Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: SB600-IDE/SATA/STRAPS

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |

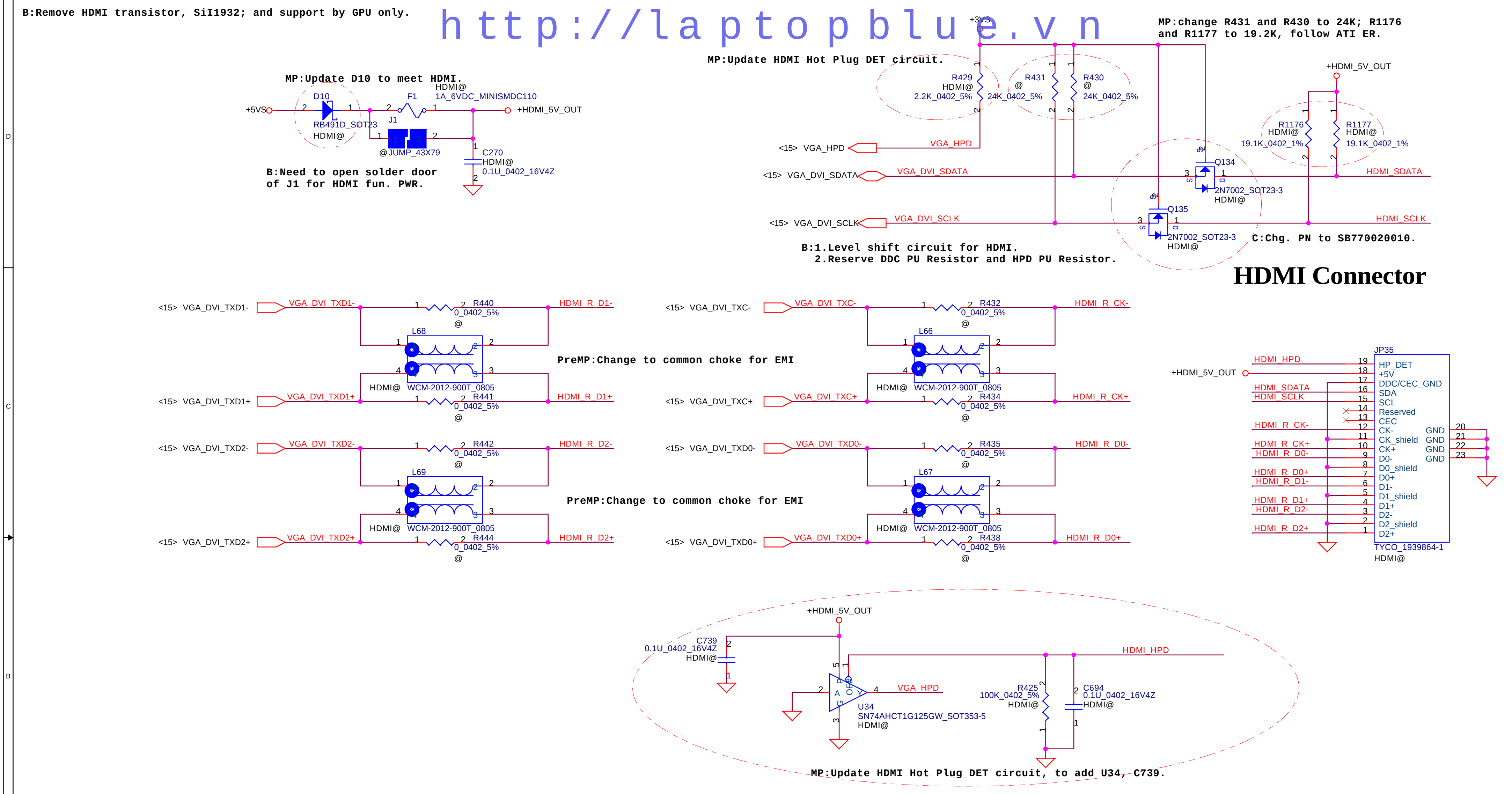


Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: SB600-Power/GND

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |

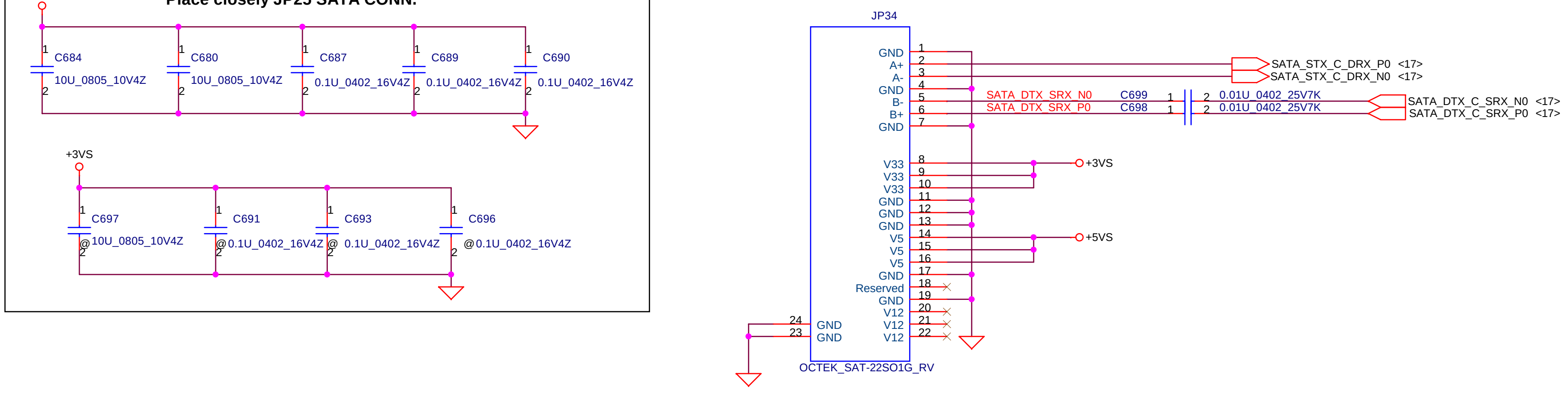


Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: SiI1392&HDMI Connector

Size: Custom — Document Number: IALAA-Minnesota10A LA3631P — Rev: 1A

Date: Monday, May 14, 2007 —

SATA HDD Conn.

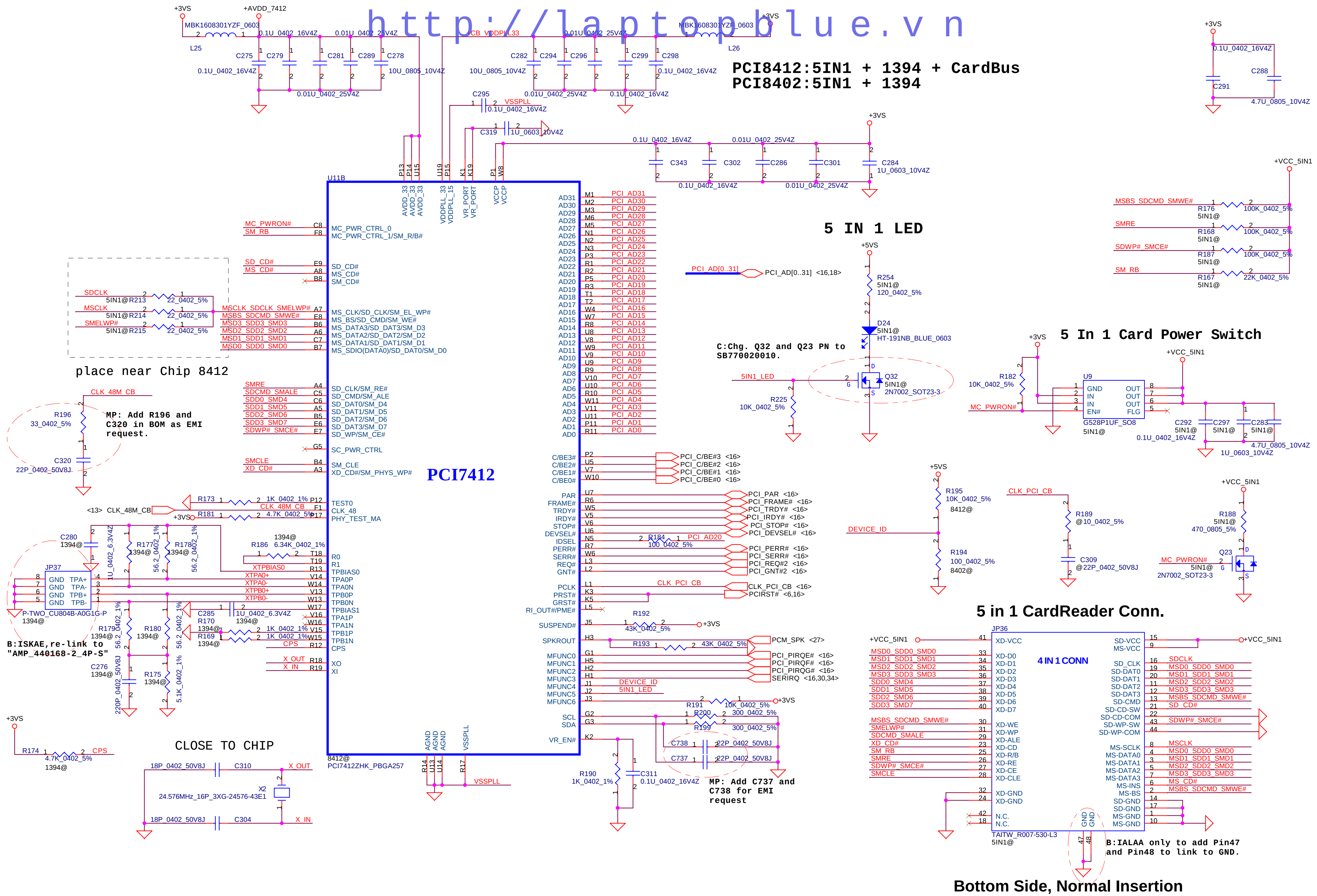


Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

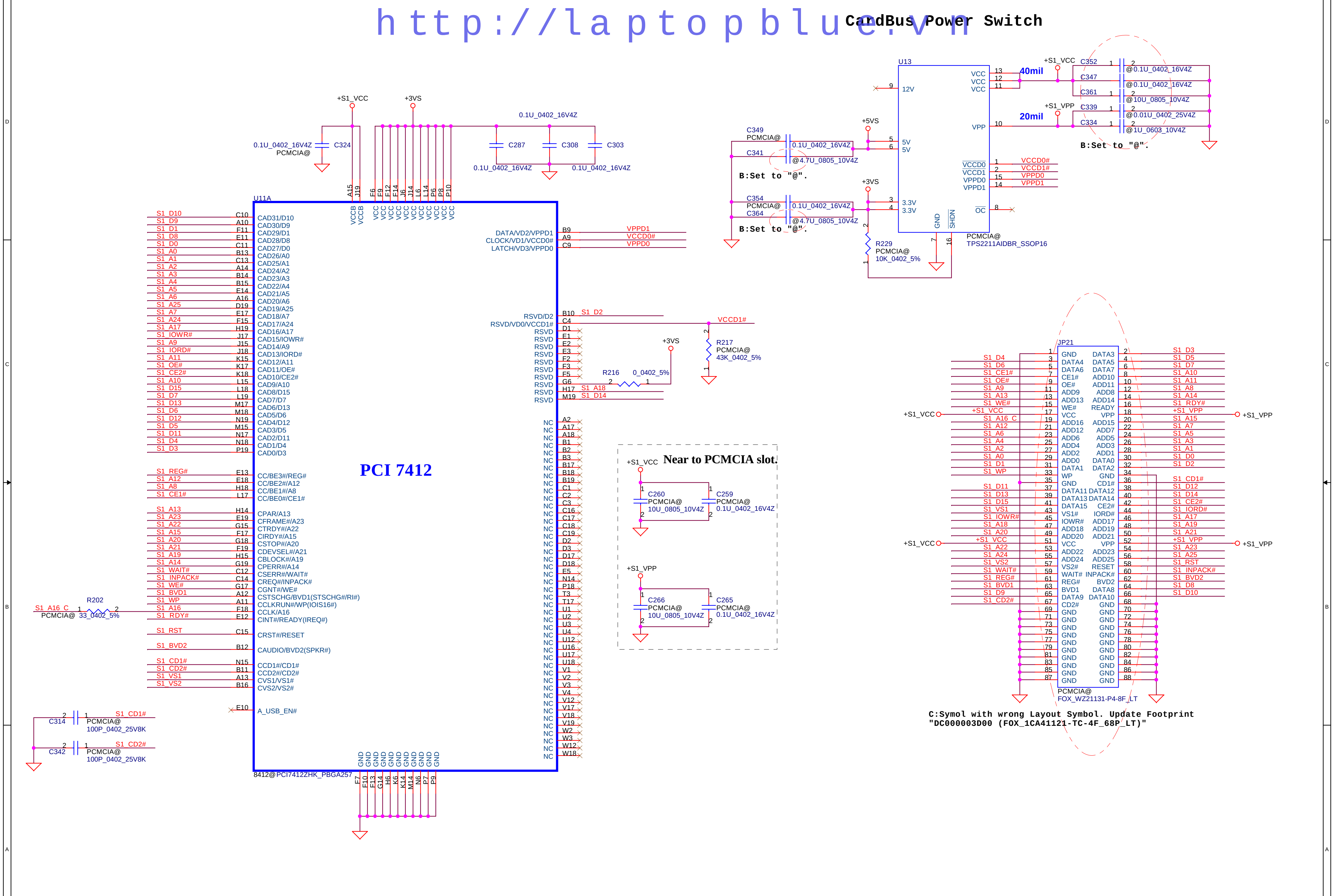
Title: HDD/CDROM

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: TI PCI8412 PCI/1394/5IN1
				Size / Document Number: IALAA-Minnesota10A LA3631P / Rev 1A
				Date: Tuesday, May 15, 2007 / Sheet 22 of 45



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

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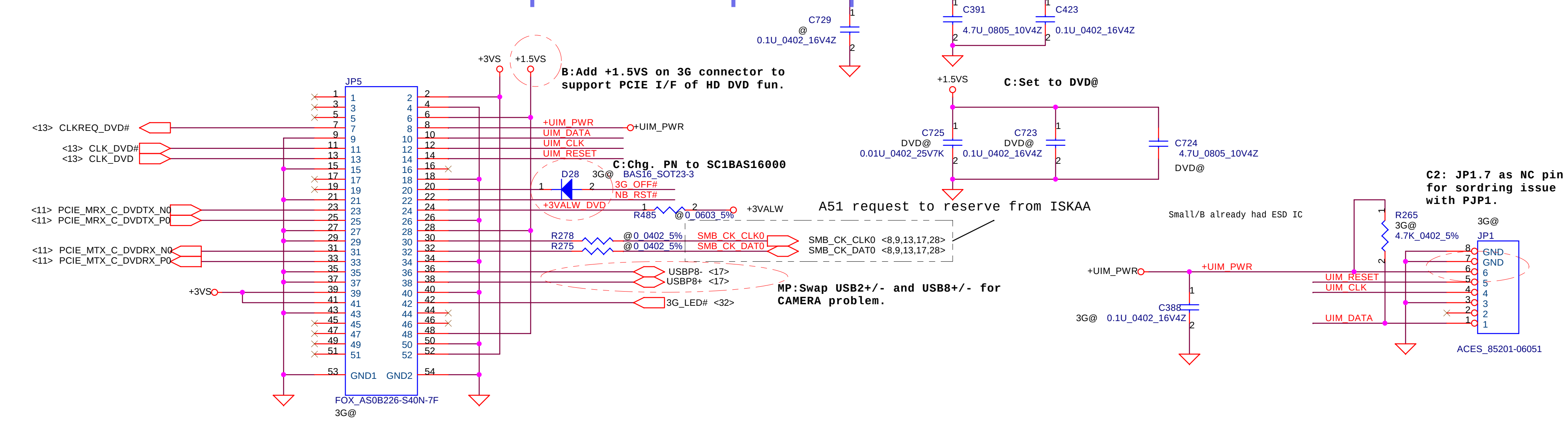
Title: **TI PCI8412/CB socket**

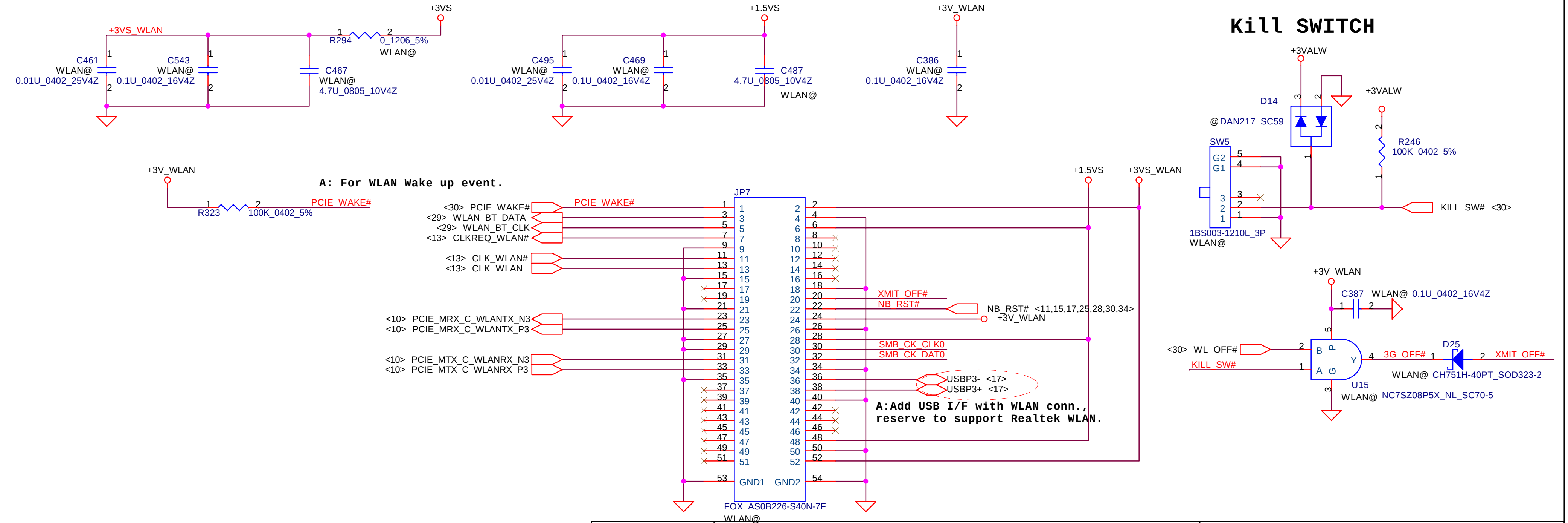
Size | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |

Mini-Express Card for 3G



Mini-Express Card for WLAN



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Issued Date	2007/5/4	Deciphered Date	2008/5/4

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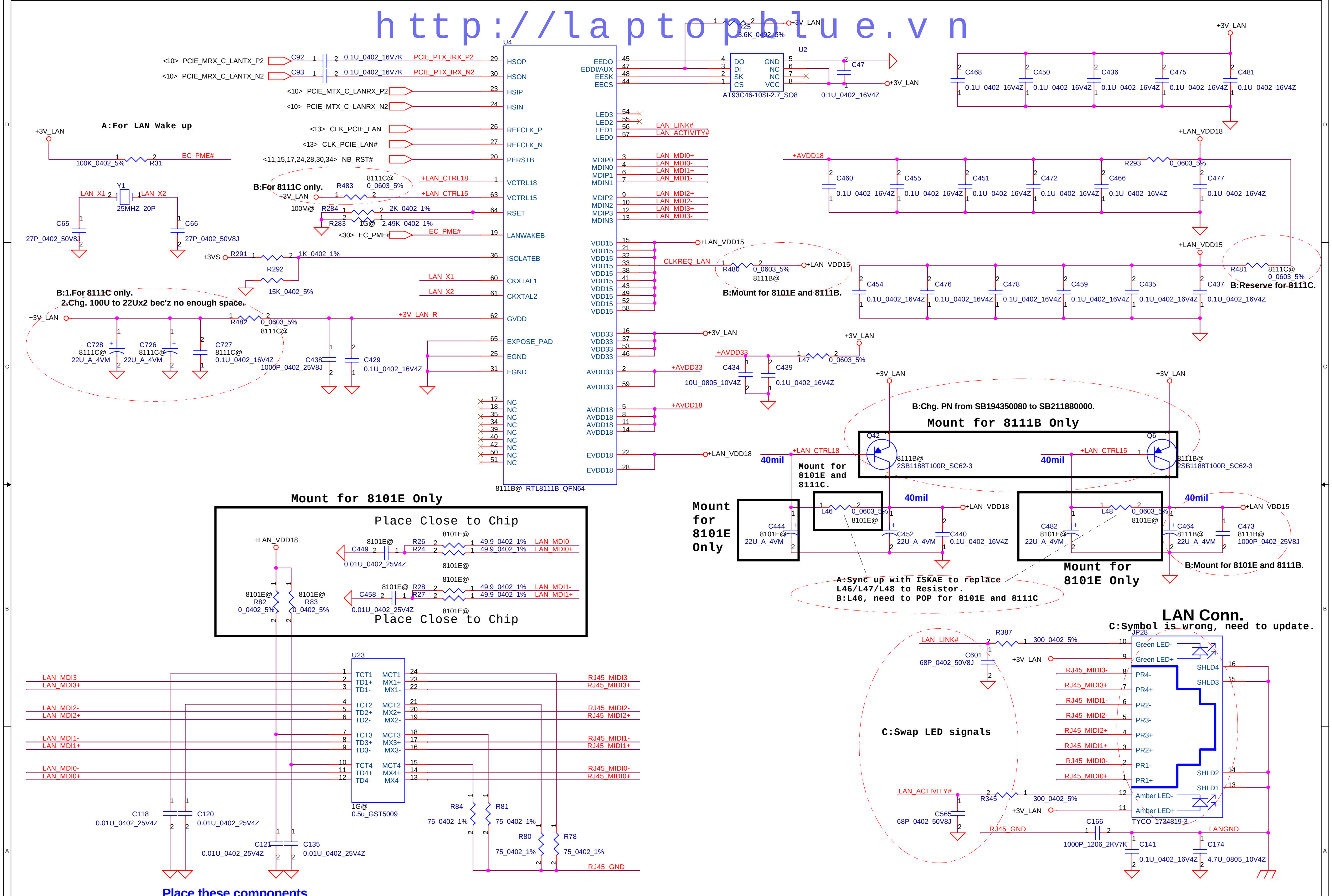
Title	MINI PCI SLOT	
Size B	Document Number IALAA-Minnesota10A LA3631P	Rev 1A
Date: Monday, May 14, 2007	Sheet 24 of 45	



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Title: RTL8111B(1G)/8101E(10/100)

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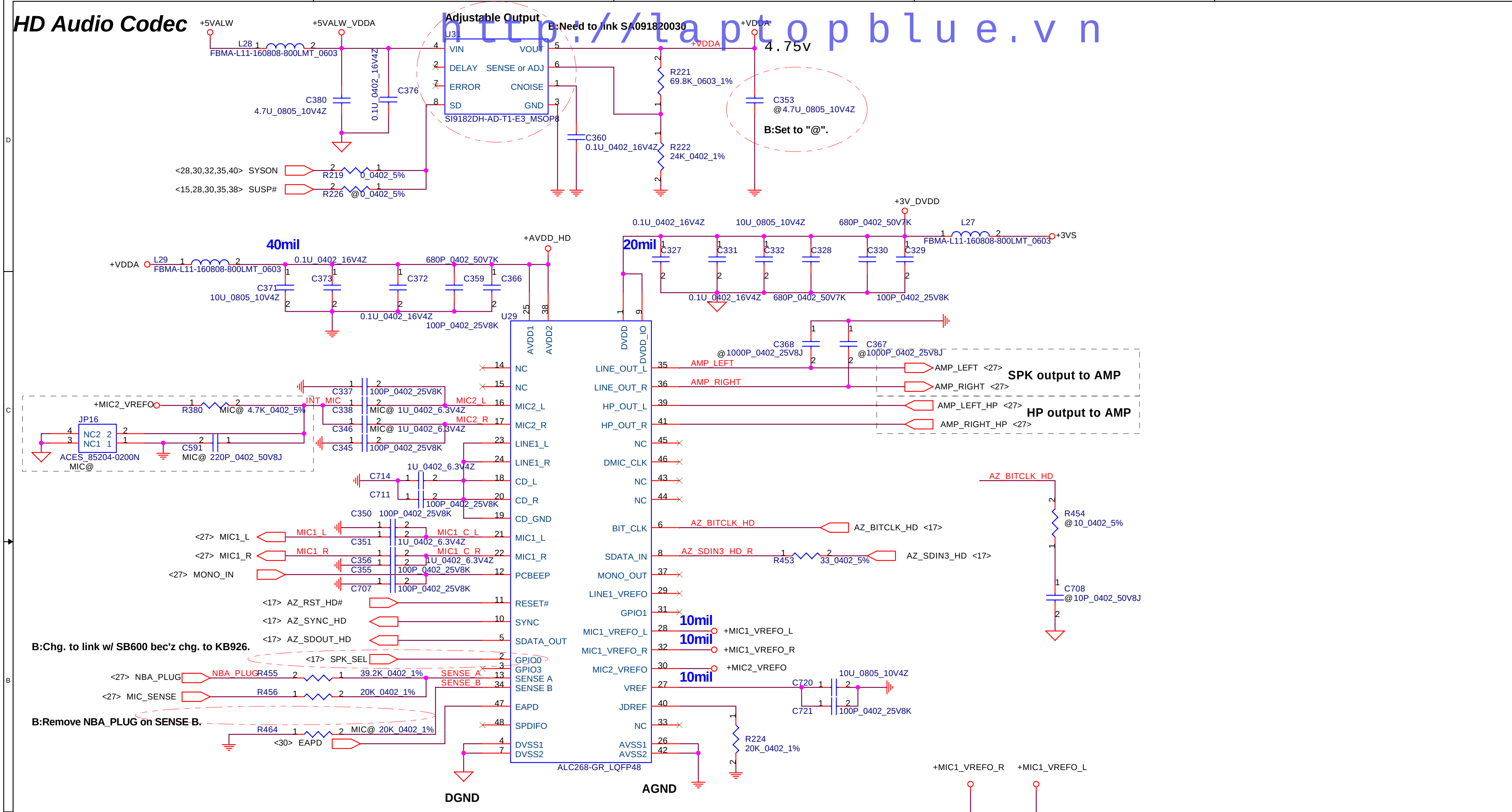
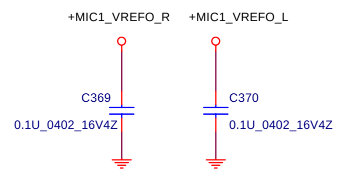
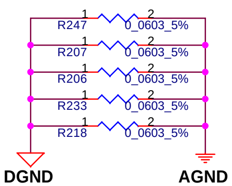
Date: Monday, May 14, 2007 | Sheet 25 of 45

HD Audio Codec





DGND To AGND Bypass



Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 35, 36)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 43, 44)
	5.1K	PORT-H (PIN 45, 46)

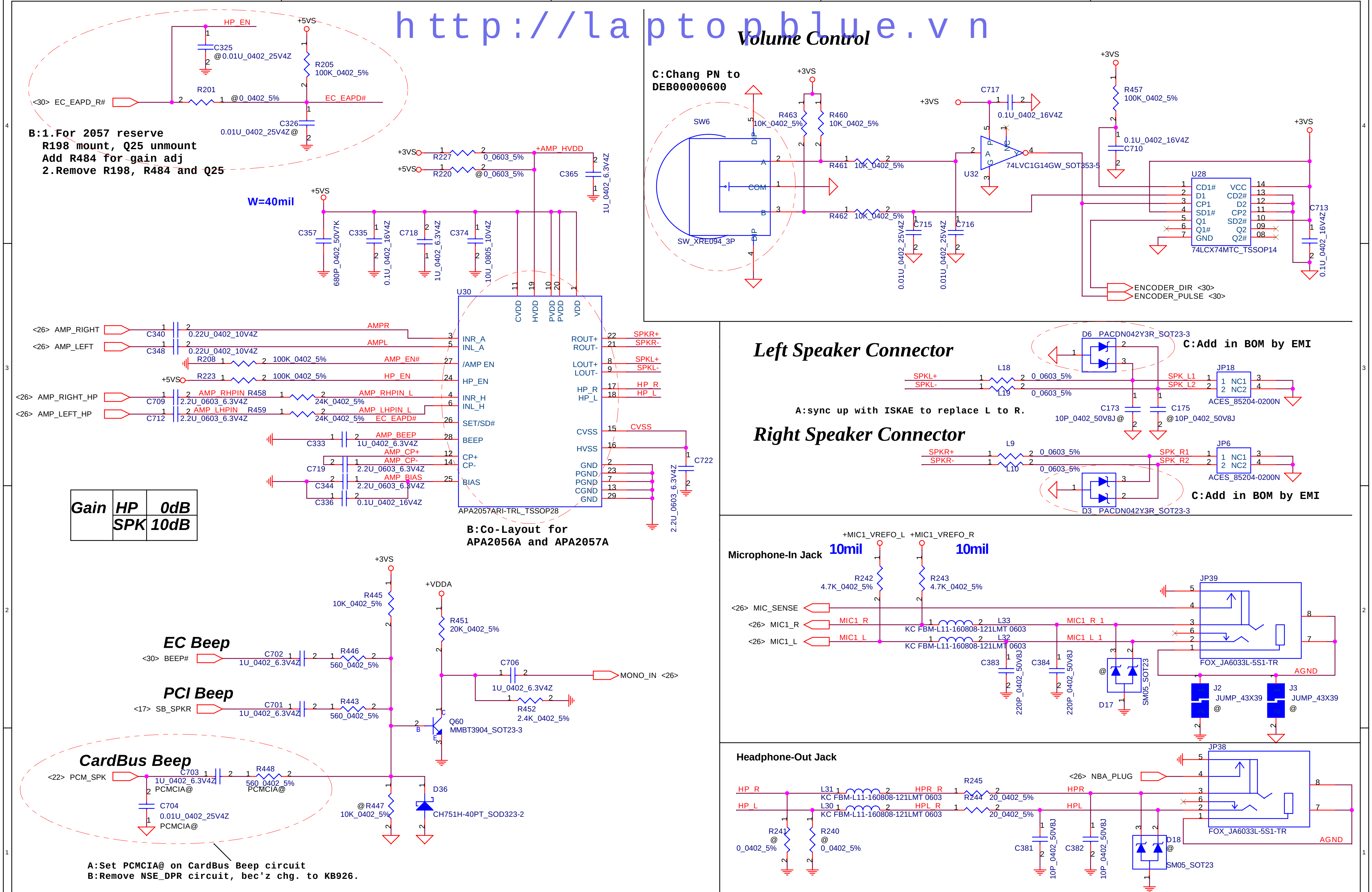
Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title: HD CODEC ALC861D

Size: B | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

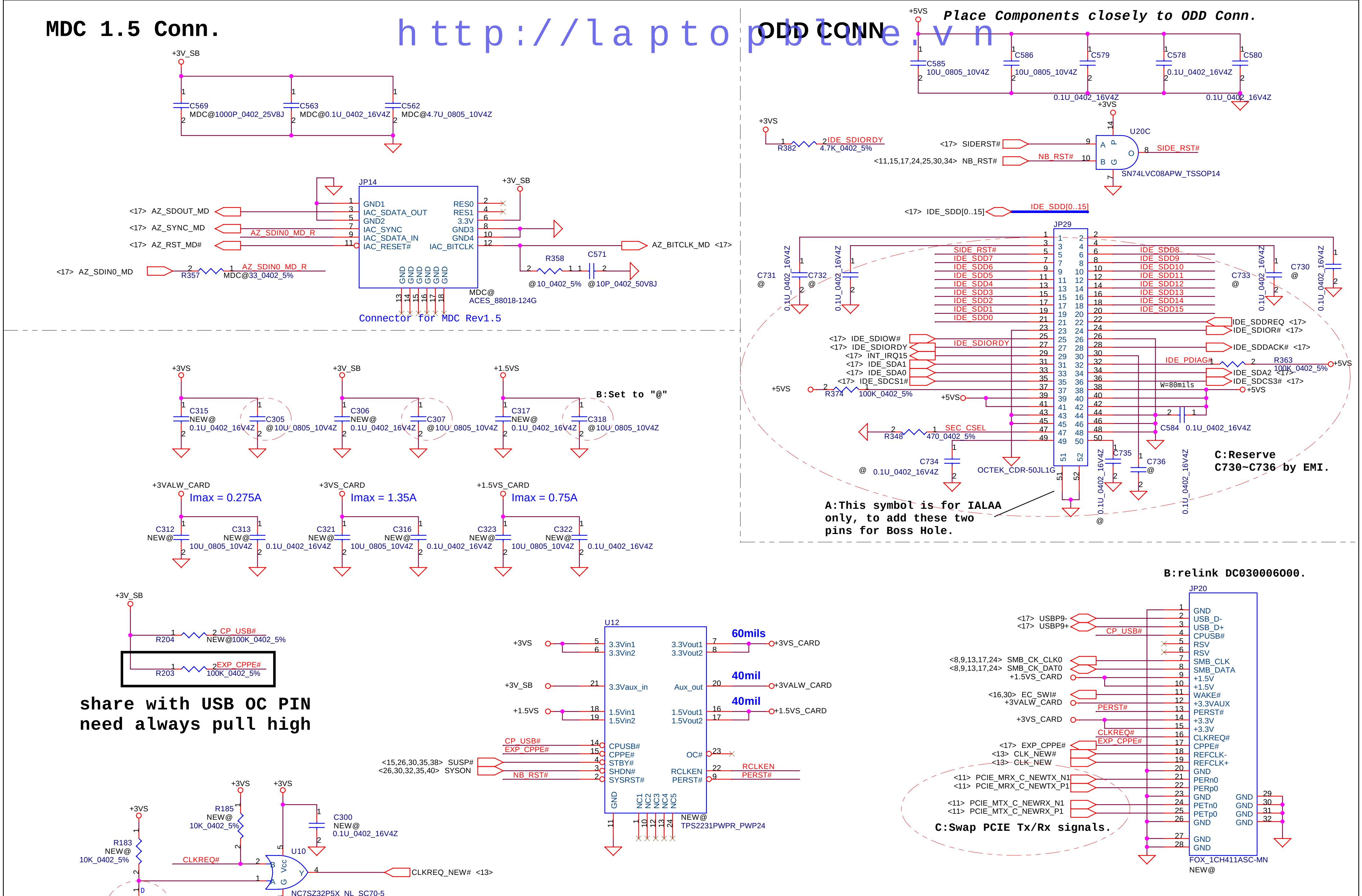
Date: Monday, May 14, 2007 |



Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date 2008/5/4	Title: AMP&Audio Jack/MDC

Size B | Document Number IALAA-Minnesota10A LA3631P | Rev 1A

Date: Monday, May 14, 2007 |



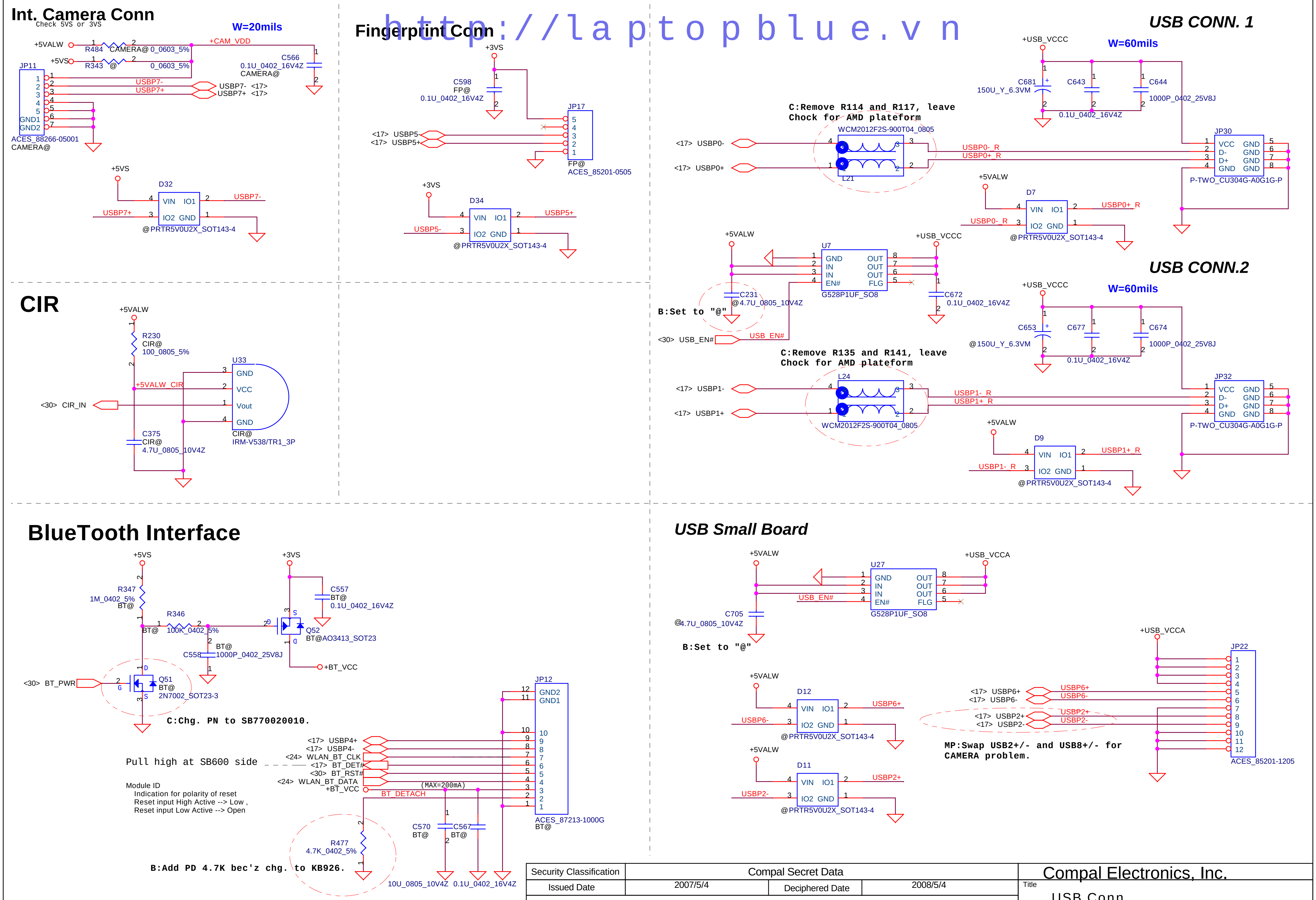
Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title: TPM/ ODD CONNECTORS

Size	Document Number	Rev
B	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007 Sheet 28 of 45



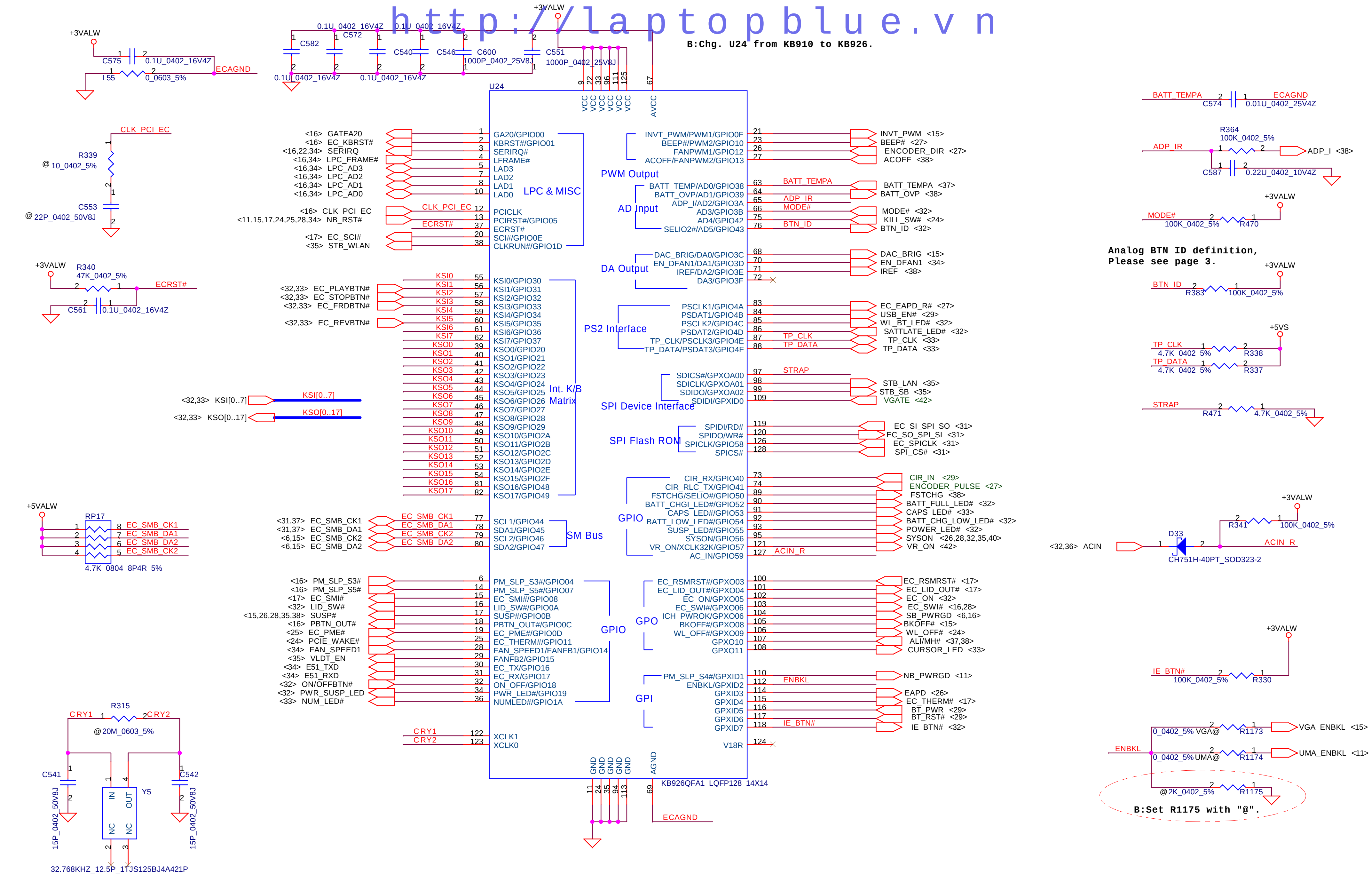
Security Classification	Compal Secret Data	
Issued Date	2007/5/4	Deciphered Date 2008/5/4

Compal Electronics, Inc.

Title: USB Conn.

Size	Document Number	Rev
B	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007 Sheet 29 of 45



MP:ENE's recommend, change C541 and C542 from 10P to 27P.

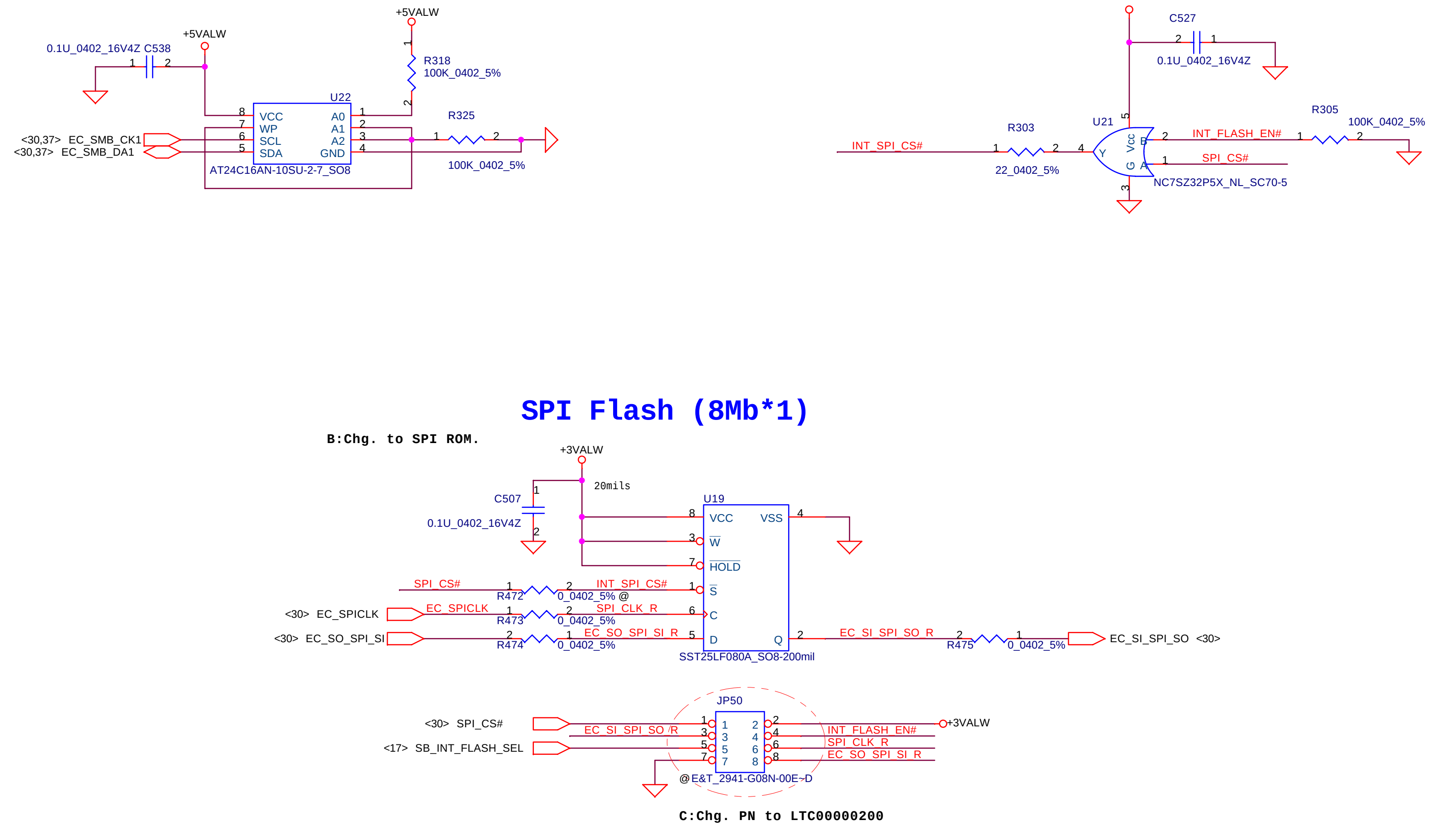
MP:ENE's recommend, change back C541 and C542 from 27P to 15P.

Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: ENE-KB910

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

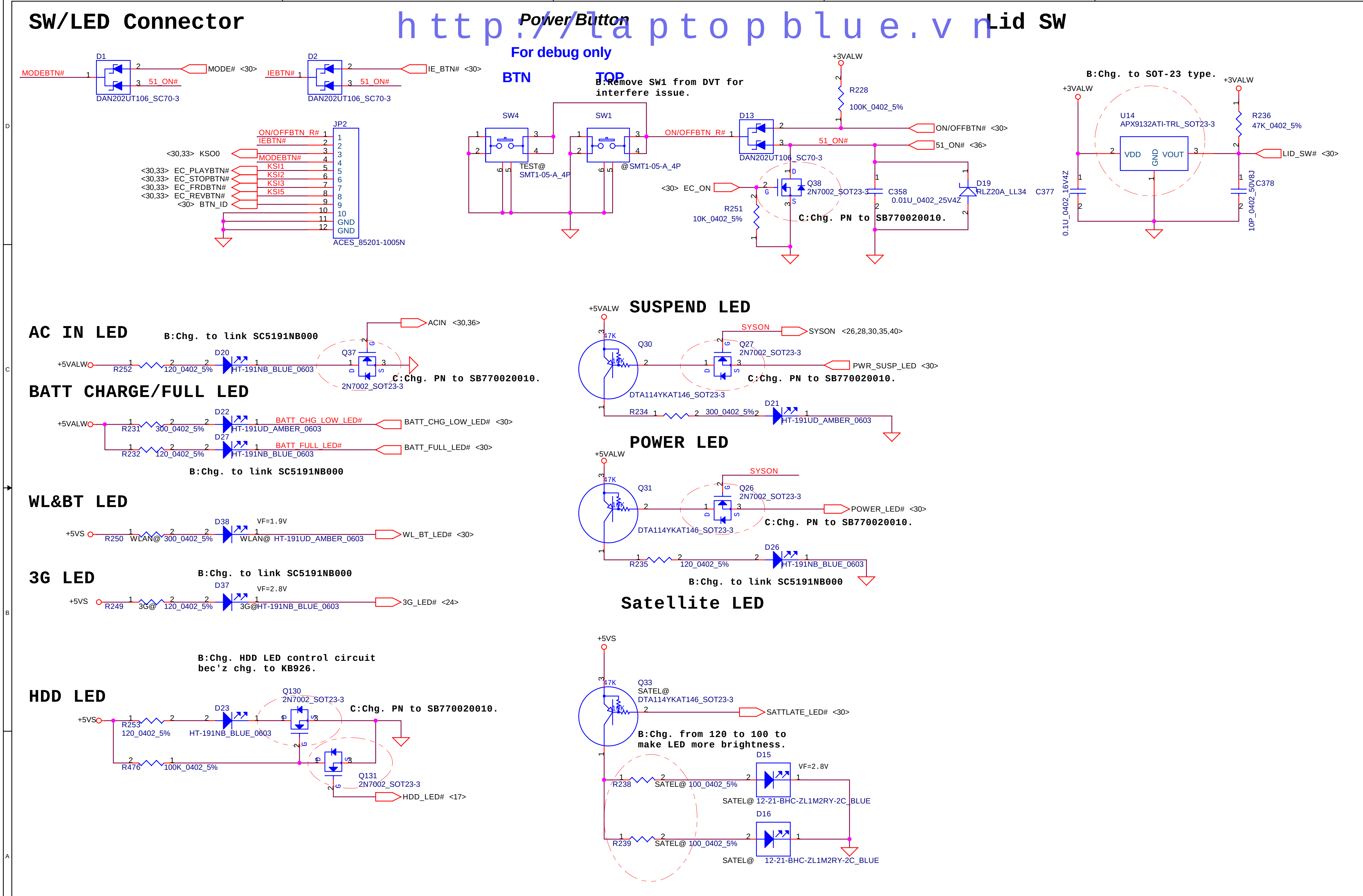
Date: Monday, May 14, 2007 |



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title	BIOS& I/O PORT	
Size: B	Document Number: IALAA-Minnesota10A LA3631P	Rev: 1A
Date: Monday, May 14, 2007	Sheet 31 of 45	



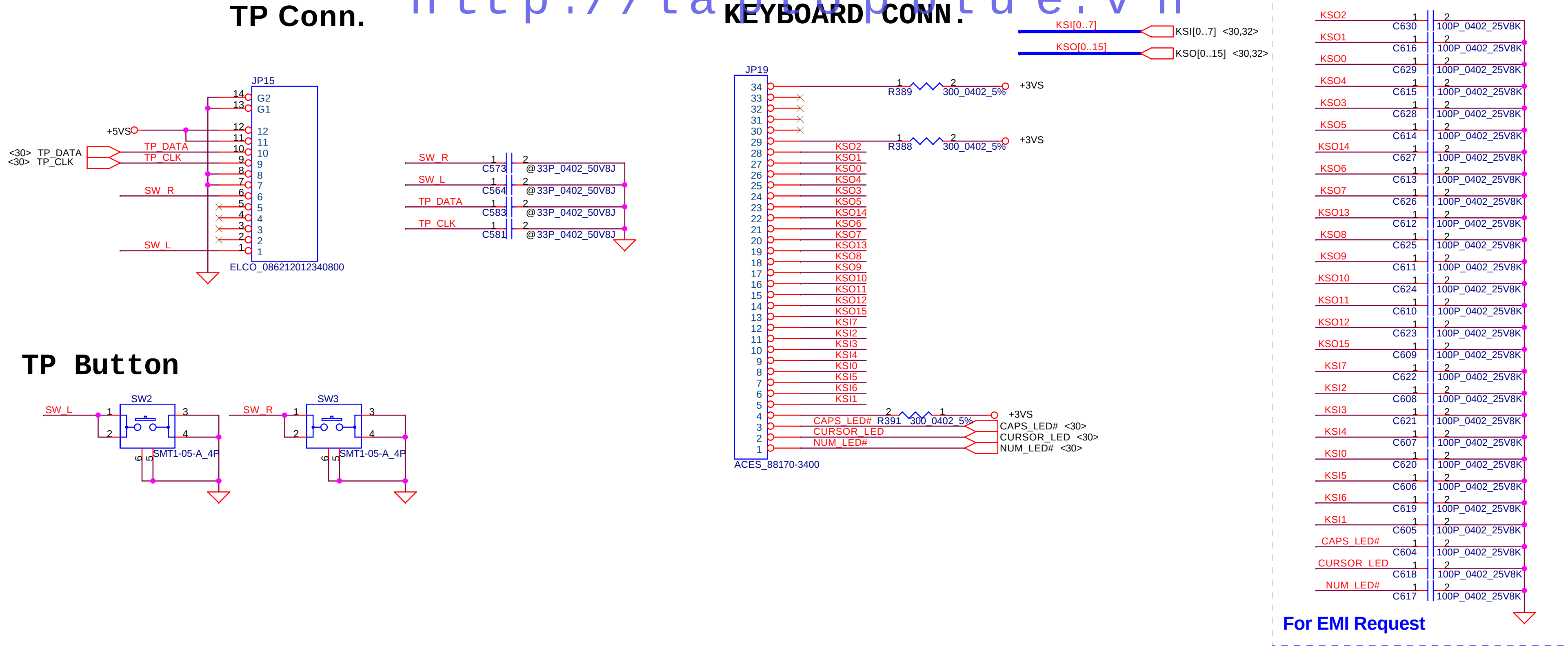
Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title: Comm. SW/ Sub Conn./LEDS

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |

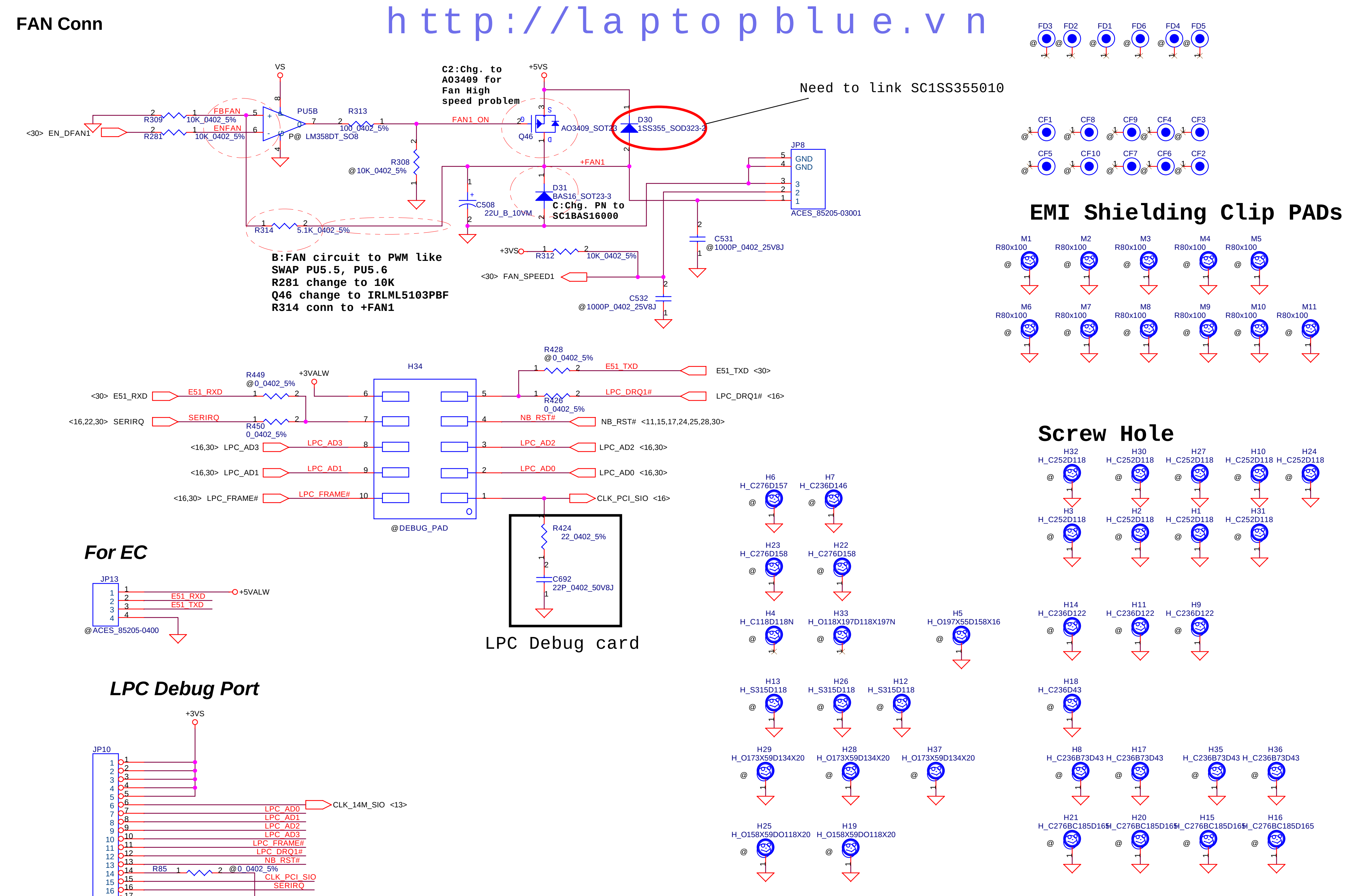


Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Title: KB/Touch Pad& hibernation

Size	Document Number	Rev
Custom	IALAA-Minnesota10A LA3631P	1A

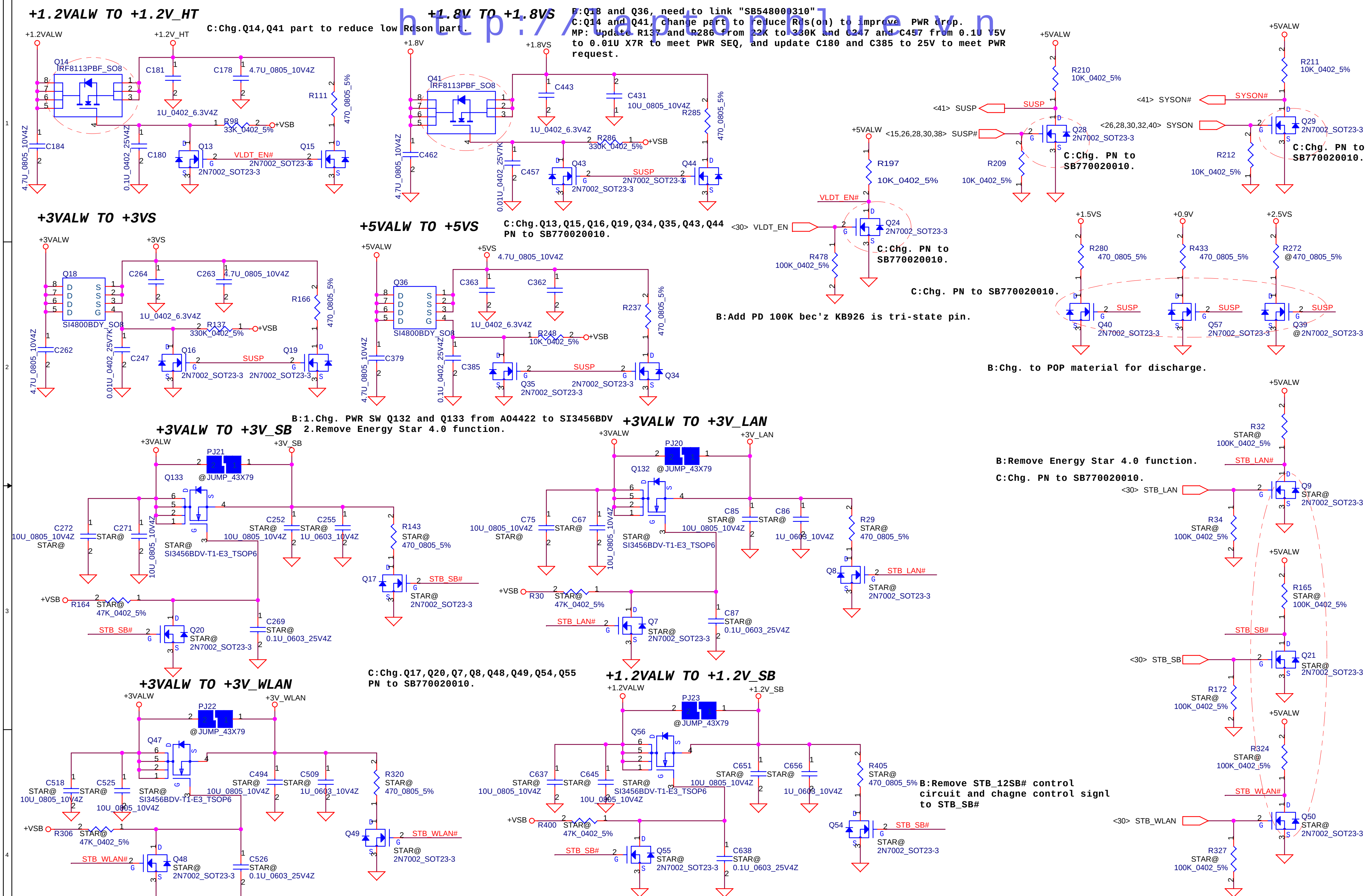
Date: Monday, May 14, 2007



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: FAN & MDC

Size	Document Number	Rev
Custom	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007



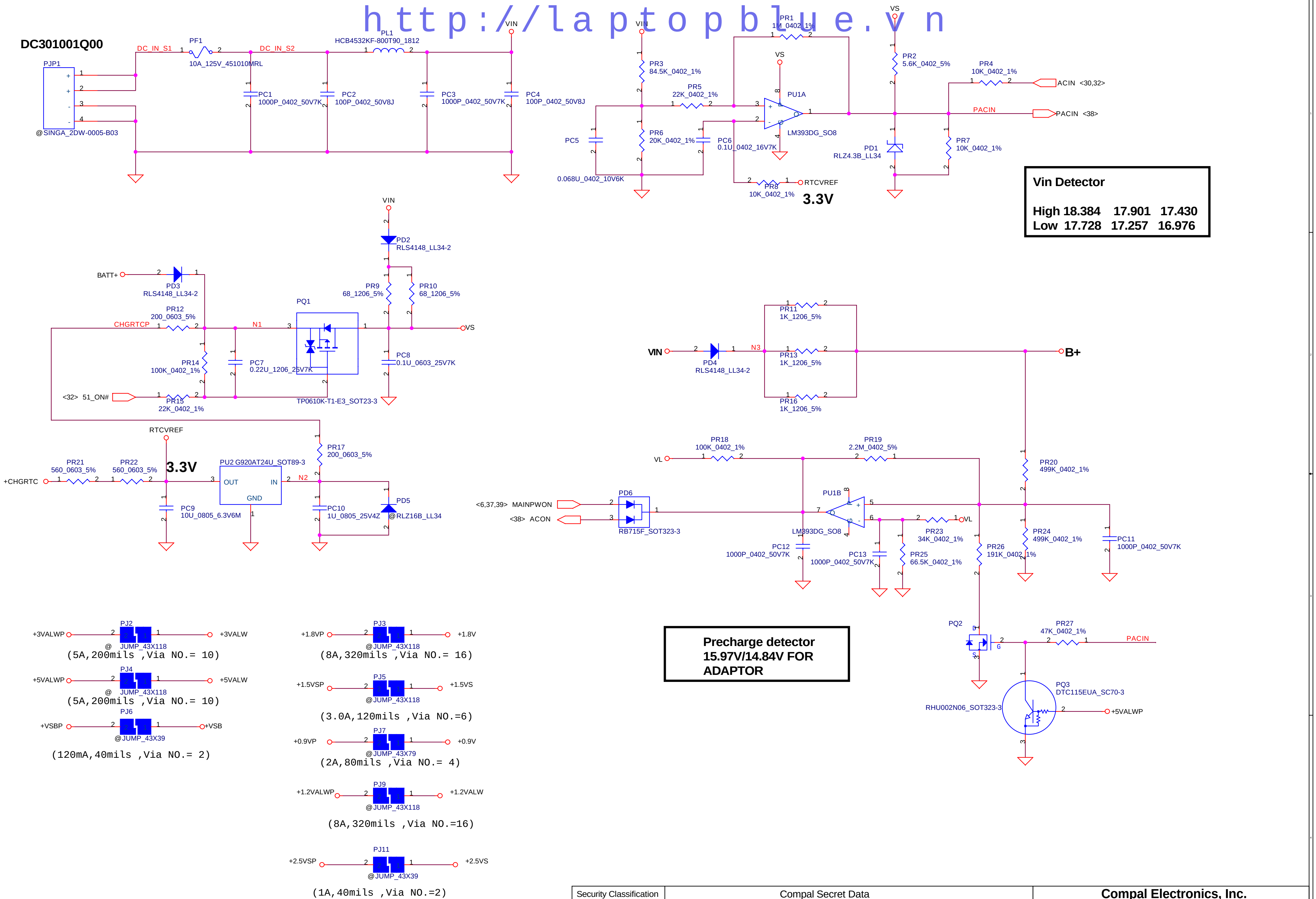
Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title: DC-DC INTERFACE

Size	Document Number	Rev
B	IALAA-Minnesota10A LA3631P	1A

Date: Monday, May 14, 2007 Sheet 35 of 45



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: DCIN & DETECTOR
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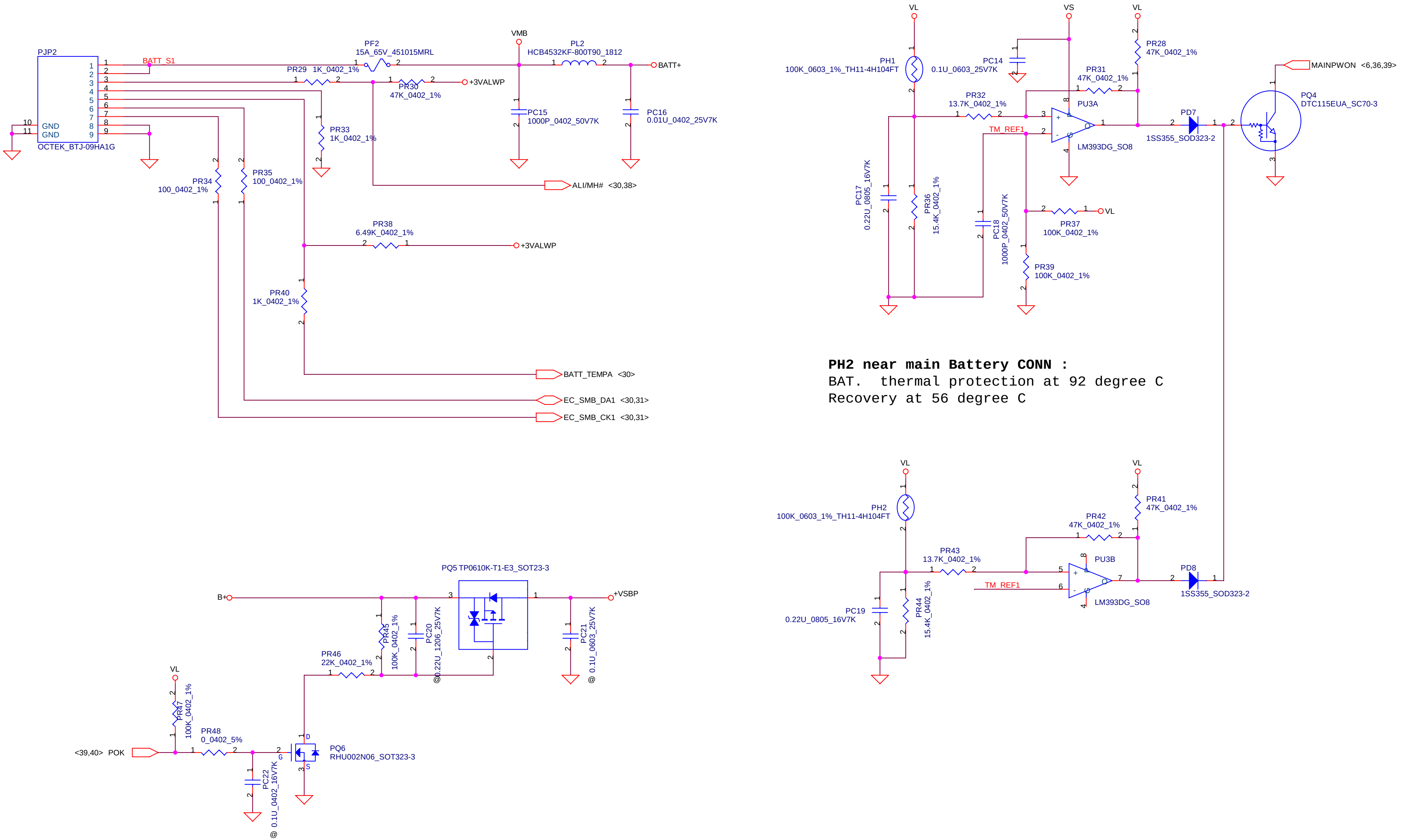
				Date: Monday, May 14, 2007 Sheet 36 of 45

PH1 under CPU botten side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C

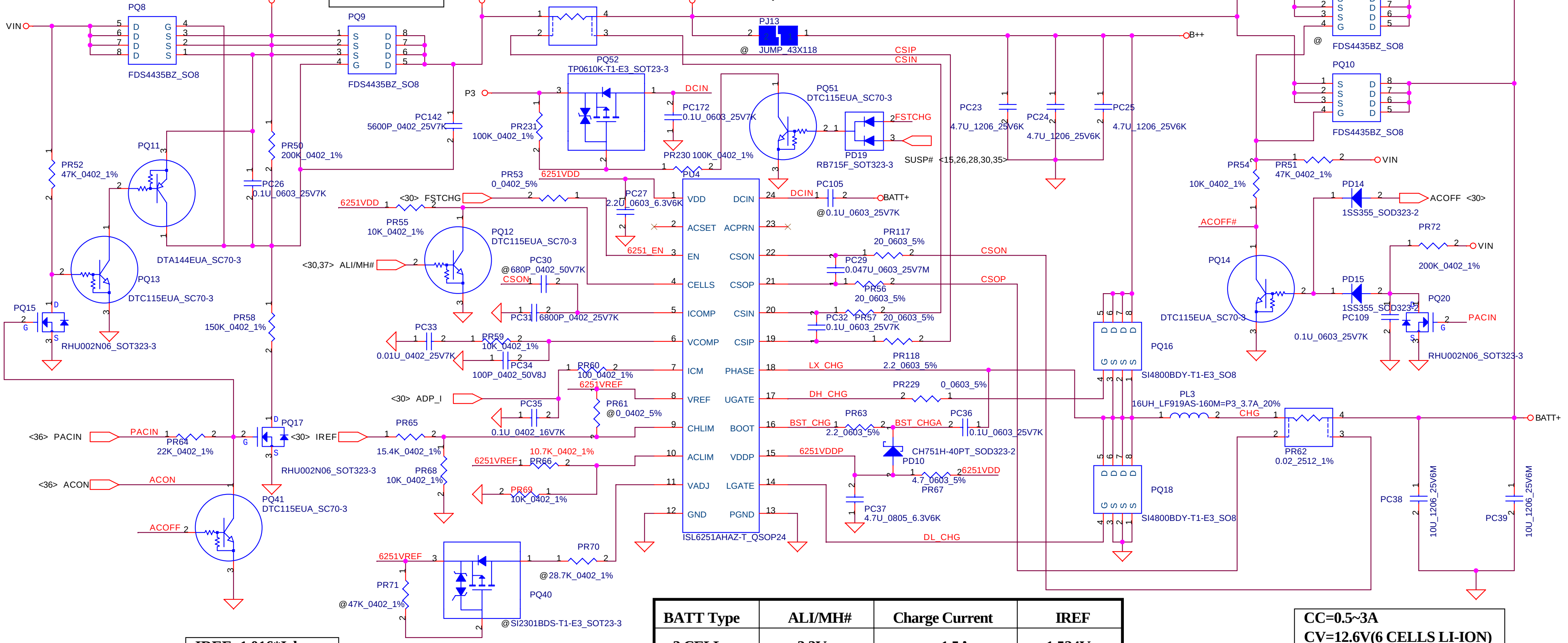
PH2 near main Battery CONN :
BAT. thermal protection at 92 degree C
Recovery at 56 degree C



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: BATTERY CONN / OTP
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size / Document Number: IALAA-Minnesota10A LA3631P / Rev 1A
				Date: Monday, May 14, 2007 / Sheet 37 of 45

75W Iadapter=0~3.947A PR49=0.02 ohm CP=3.71A PR66=10.7K
90W Iadapter=0~4.737A PR49=0.015 ohm CP=4.459A PR66=19.6K
120W Iadapter=0~6.315A PR49=0.010 ohm CP=5.936A PR66=19.6K PR69=4.53K

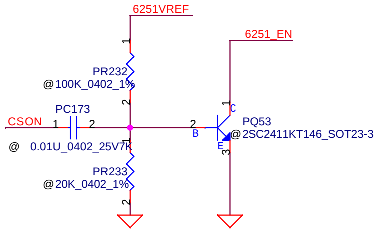
Iadp=0~3.6A

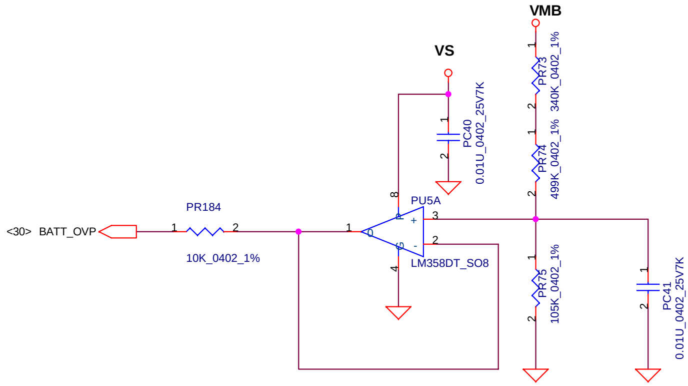


BATT Type	ALI/MH#	Charge Current	IREF
3 CELL	3.3V	1.5A	1.524V
6 CELL	3.3V	3.0A	3.048V
9 CELL	3.3V	3.0A	3.048V

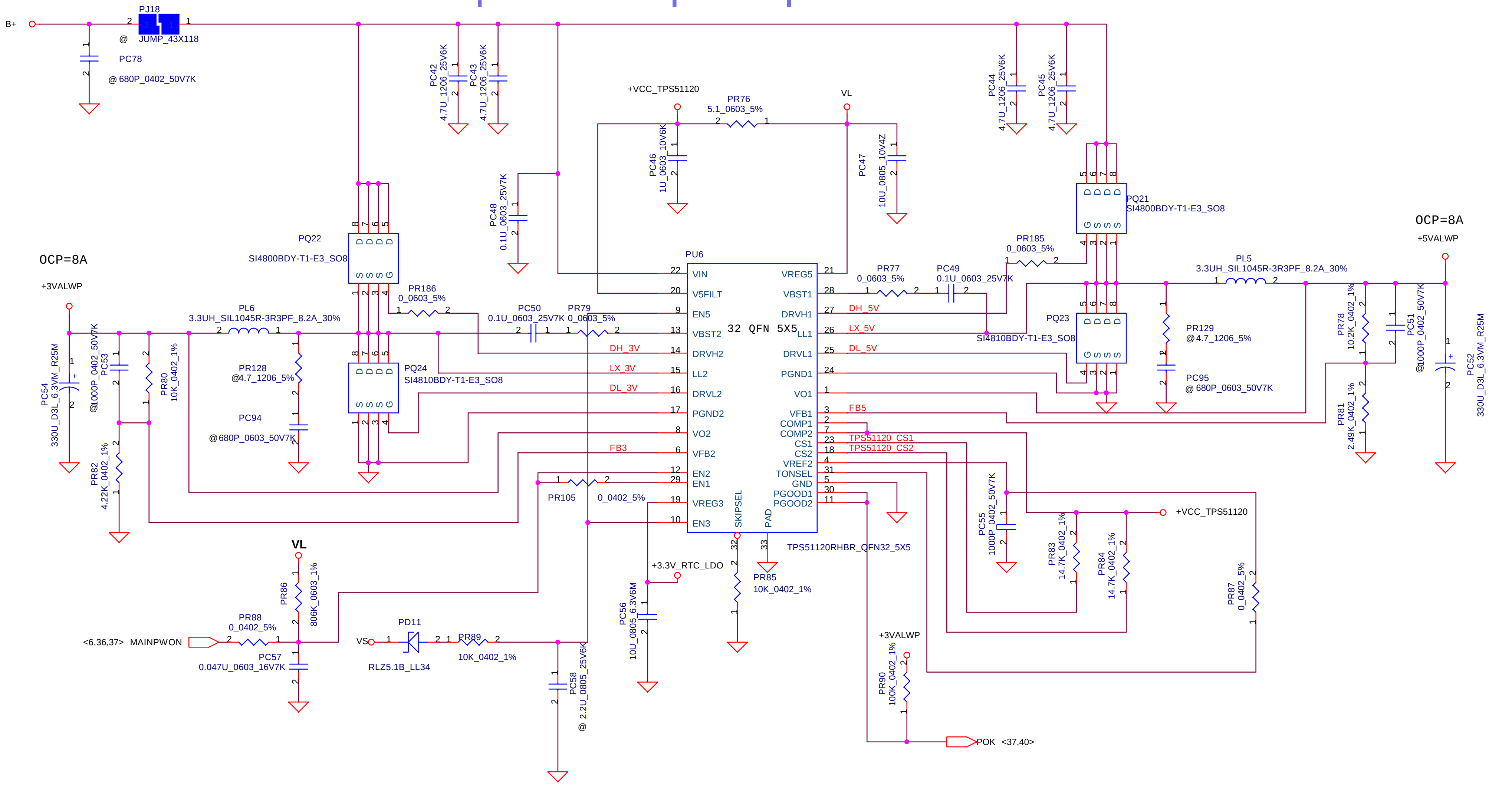
CC=0.5~3A
CV=12.6V(6 CELLS LI-ION)

IREF=1.016*Icharge
IREF=0.508V~3.048V





LI-3S :13.5V----BATT-OVP=1.5V
LI-4S :18V----BATT-OVP=2V
BATT-OVP=0.111*BATT+



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

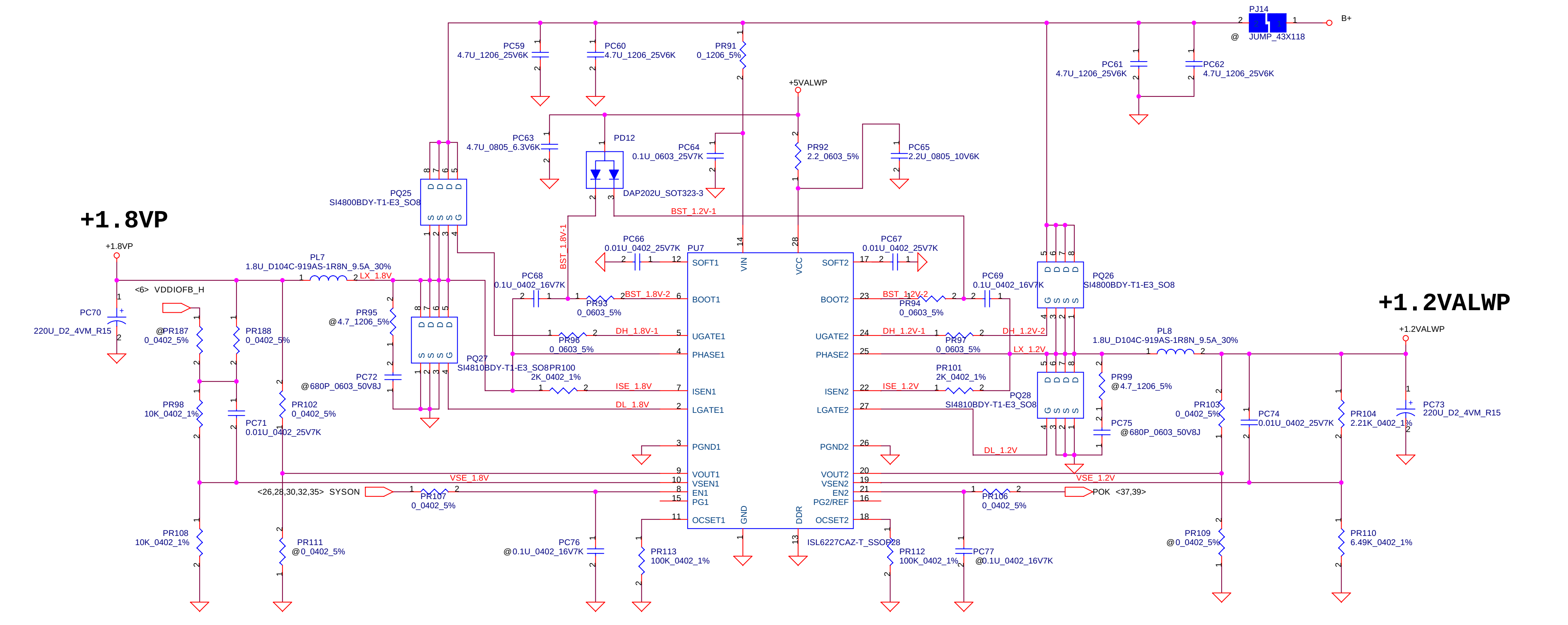
Compal Electronics, Inc.

Title: +5V/+3V

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Tuesday, May 15, 2007

http://laptopblue.vn



Security Classification	Compal Secret Data			Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: 1.8V / 1.2V
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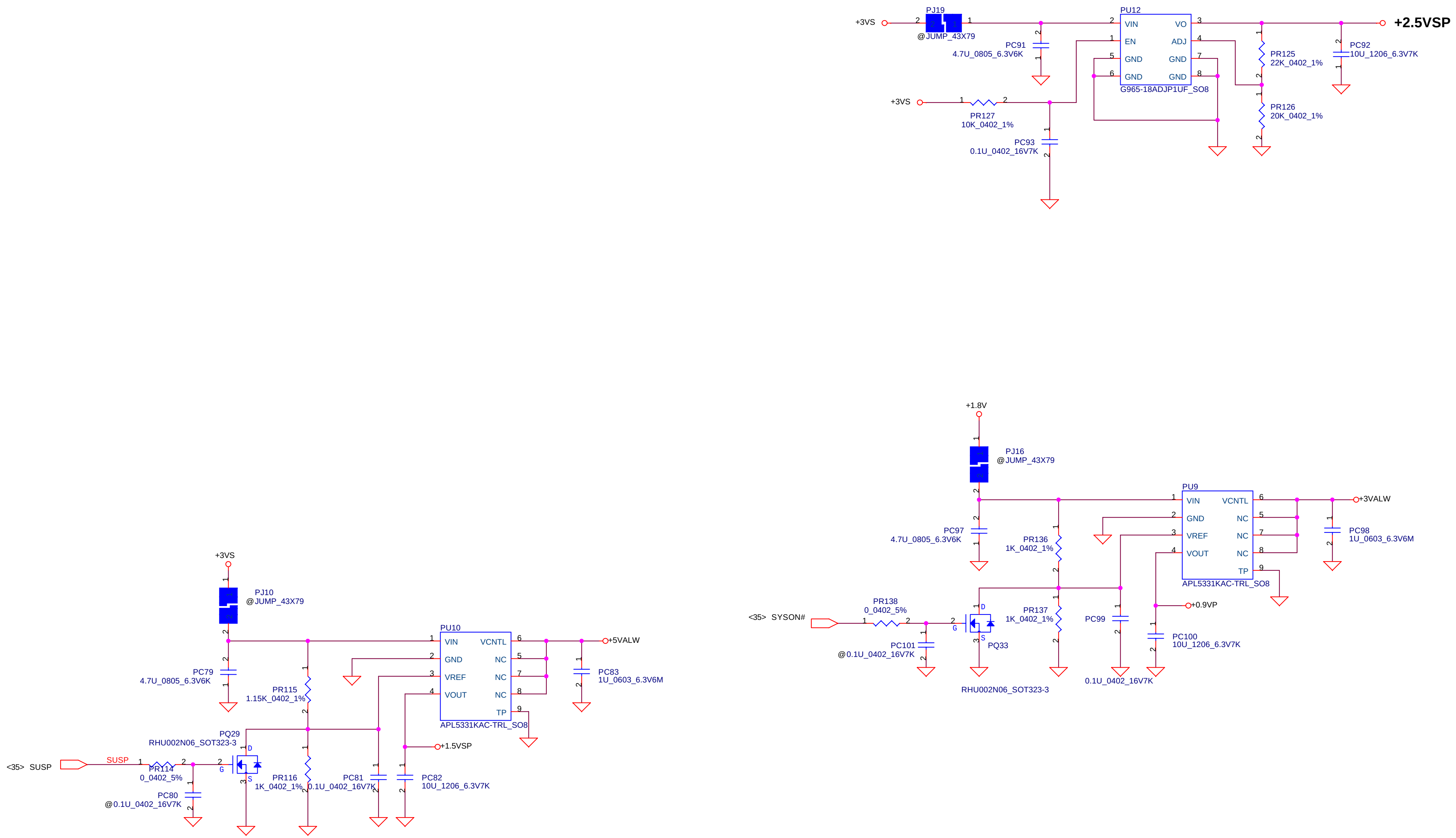
				Date: Monday, May 14, 2007 / Sheet 40 of 45

http://laptopblue.vn

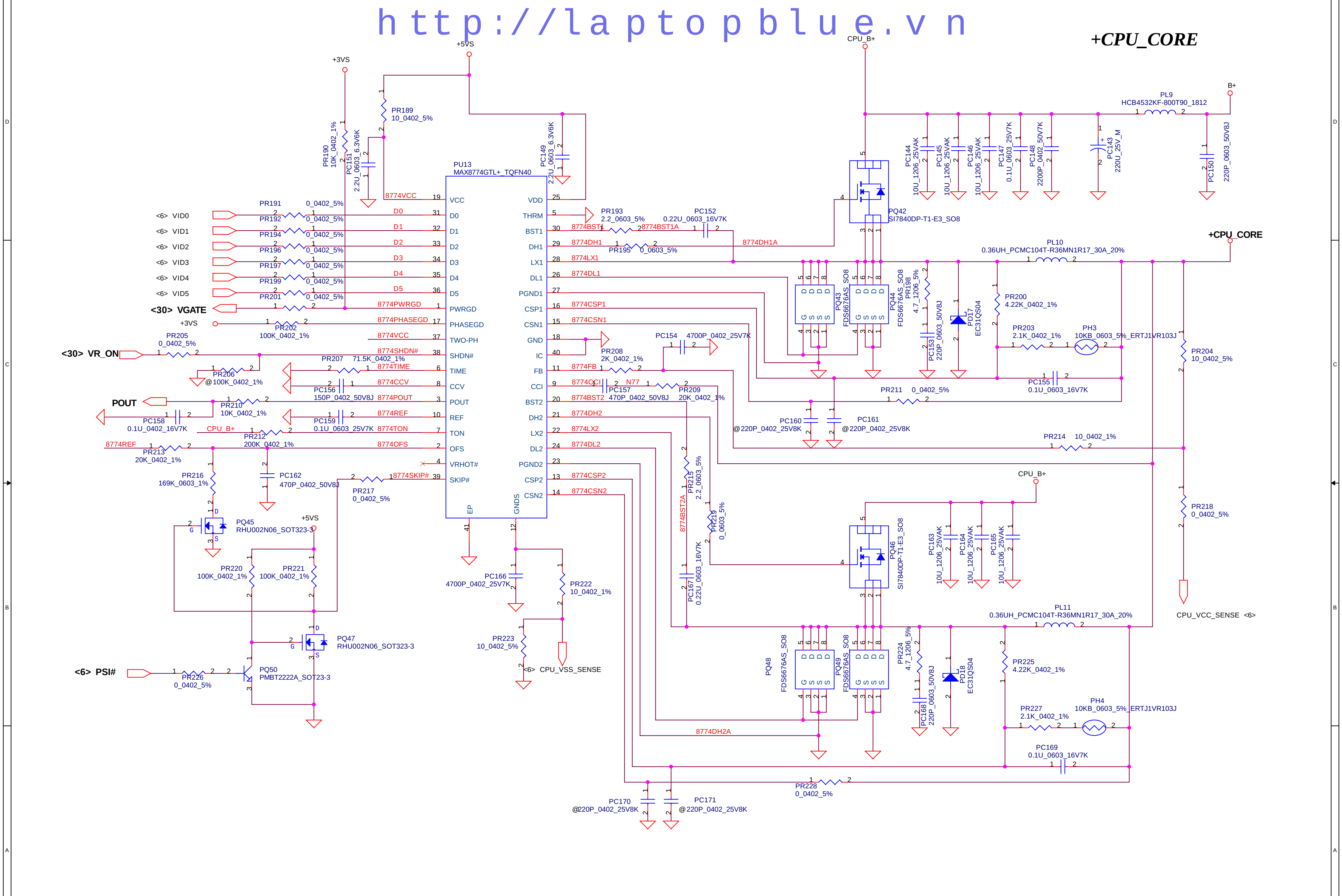


Security Classification	Compal Secret Data		Compal Electronics, Inc.
Issued Date	2007/5/4	Deciphered Date 2008/5/4	Title: 0.9V//1.5V/2.5V

Size | Document Number: IALAA-Minnesota10A LA3631P | Rev 1A

Date: Monday, May 14, 2007 |

+CPU_CORE



Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title: +CPU_CORE

Size: Custom | Document Number: IALAA-Minnesota10A LA3631P | Rev: 1A

Date: Monday, May 14, 2007 |

POWER PIR LIST

	page	Reason for change	Modify list
DVT	37	Design change PF2 value	Change PF2 Fuse from 12A to 15A
	38	Design change PU4 schematic	Add PR117,PR118.
			Change value PC29,PC32,PR56,PR57,PQ18
			Remove PR232,PR233,PC173,PC105
	42	improve CPU-core ripple	Change PR214,PR222 from 100 to 10
			Change PC155,PC169 from 0.22u to 0.1u
			Change PR213 from 31.6K to 20K
		Add EMI soultion	Change PC150,PC153,PC168 from 680P to 220P
			Change boost resistors PR193,PR215 from 0 to 2.2
			Change low-side mosfet PQ43,PQ44,PQ48,PQ49 at CPU-core.
PVT	37	Design change PH1 temperature set value	Change PR36 from 22K to 15.4K
Pre-MP	37	Design change PH1 temperature set value	Change PR44 from 22K to 15.4K

HW4 Product Improvement Record (P.I.R.)

Phase: to	Date:			Writer:		
Page#	Action Plan (add; del; change)	Location or Net_List	Before value (Attached file)	After value (Attached file)	Detail Discretion and Root Cause	Rev.	DL/DM Check

01. 2007/2/12 P30

Rev 0.3 LA-3631P Implement items:

01. P06 Update THERMTRIP# control circuit, follow AMD request.
02. P07 Change C652 and C557 to SGN00000800
03. P08 Swap RP20 signals for DDR shielding.
04. P11,P13,P24 Set BOM Structure for UMA@, DVD@
05. ALL Change 2N7002 main source to SB770020010 for unit.
06. P27 Add D6, D3, ESD Diod for EMI, Chenge Encoder, SW6, PN to DEB00000600
07. 15 Unit Q1,Q3,Q52 w/ same part.
08. 24,P34 Change D31 and D28 to SC1BAS16000
09. P14 Add C13,C16,C21 with 22P for EMI.
10. P16 Change R436 and R437 to 120 Ohm to meet ADM request
11. P29 Remove R113, R117, R135 and R141 to leave chock for AMD platform
12. P28 Reserve C730~C736 by EMI request.
13. P21 Swap JP28 LED behaivor signals.
14. P28 Swap JP20 RX/TX Lanes sigals for Express Card cna't detect issue.

Rev 0.4 LA-3631P Implement items:

01. P24 Cut JP1.7 Pad for soldring issue.
02. P34 Change Q46 to SB934090000 for Fan can't meet Max. speed issue.

Rev 1.0 LA-3631P Implement items:

01. P22 Reserve C737, C738 for EMI solution
02. P6 Reserve R490 for PROCHOT funcation
03. P11 Add R488, R489 for LCD flash when boot issue

Rev 1A LA-3631P Implement items:

01. P22 Add R196 and C320 in BOM as EMI requirment.
02. P17 Add U35 and C740, reserve R1178 for LAN Leakage problem
03. P19 Change C647 to 22u for sequence and L59 to DCR is 0.4Ohm to meet AMD requirement.
04. P20 Change R431 and R430 to 24K; R1176 and R1177 to 19.2K, follow ATI ER.
05. P13 Change R369 from 33Ohm to 90Ohm Bead.
06. P30 ENE's recommend, change back C541 and C542 from 27P to 15P.
07. P06 Reserve pull up resistor R490 for H_PROCHOT#
08. P24/P29 Swap USB2+/- and USB8+/- for CAMERA problem.
09. P14/P20 Change D10 and D29 part to meet HDMI and CRT power requirment.
10. P20 Update HDMI Hot Plug DET circuit, to add U34, C739.

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Issued Date	2007/5/4	Deciphered Date	2008/5/4	Title: PIR

Size | Document Number | Rev 1A

Date: Wednesday, May 16, 2007 |

45@ Part

PJP1
45@ DC-IN JACK

PCB

ZZZ
DAZ02500101 LA-3631P

CHIPSET(R1)

U5
UMAR1@ RS690MC

U5
UMAR3@ RS690MC

U5
VGAR3@ RX690

U26
SBR3@ SB600

LAN

U4
8101E@ RTL8101E

C473
8101E@ 1000P_0402_25V8J

U4
8111C@ RTL8111C

L46
8111C@ 0_0603_5%

C464
8101E@ 22U_A_4VM

R480
8101E@ 0_0603_5%

TRANSFORMER

U23
100M@ TST1284

Card BUS

U11
8402@ PCI8402

Security Classification	Compal Secret Data		
Issued Date	2007/5/4	Deciphered Date	2008/5/4

Compal Electronics, Inc.

Title: PIR

Size | Document Number | Rev 1A

Date: Tuesday, May 15, 2007