

OSAKA 30

MP BUILD 2004-0830

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC TITLE OSAKA 30
DESIGN					
CHECK					
RESPONSIBLE					
SIZE =	3		VER :		SIZE A3 CODE CS DOC NUMBER TC7302ZAA000 REV A05
FILE NAME :	XXXX-XXXXXX-XX				SHEET 54 OF 54
P/N	XXXXXXXXXXXX				

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5-13.SYSTEM POWER	37.S/B STRAPPING
14.CLOCK GENERATOR	38.K/B CONTROLLER
15.CPU(1/2)	39.SUPER I/O
16.CPU(2/2)	40.SERIAL & PARALLEL PORT
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18.FAN & THERMAL CONTROLLER	42.AC97 CODEC
19.N/B(1/5)	43.AUDIO AMP & PHONE JACK
20.N/B(2/5)	44.MDC CONN
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22.N/B(4/5)	46.ODD CONN
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24.N/B STRAPPING	48.CARDBUS CONTROLLER(CB712)
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30.DDR DIMM0 DAMPING	54.SWITCH & D/B
31.DDR PULL-UP & DECOUPLING	
32.CRT& SVEDIO CONN	

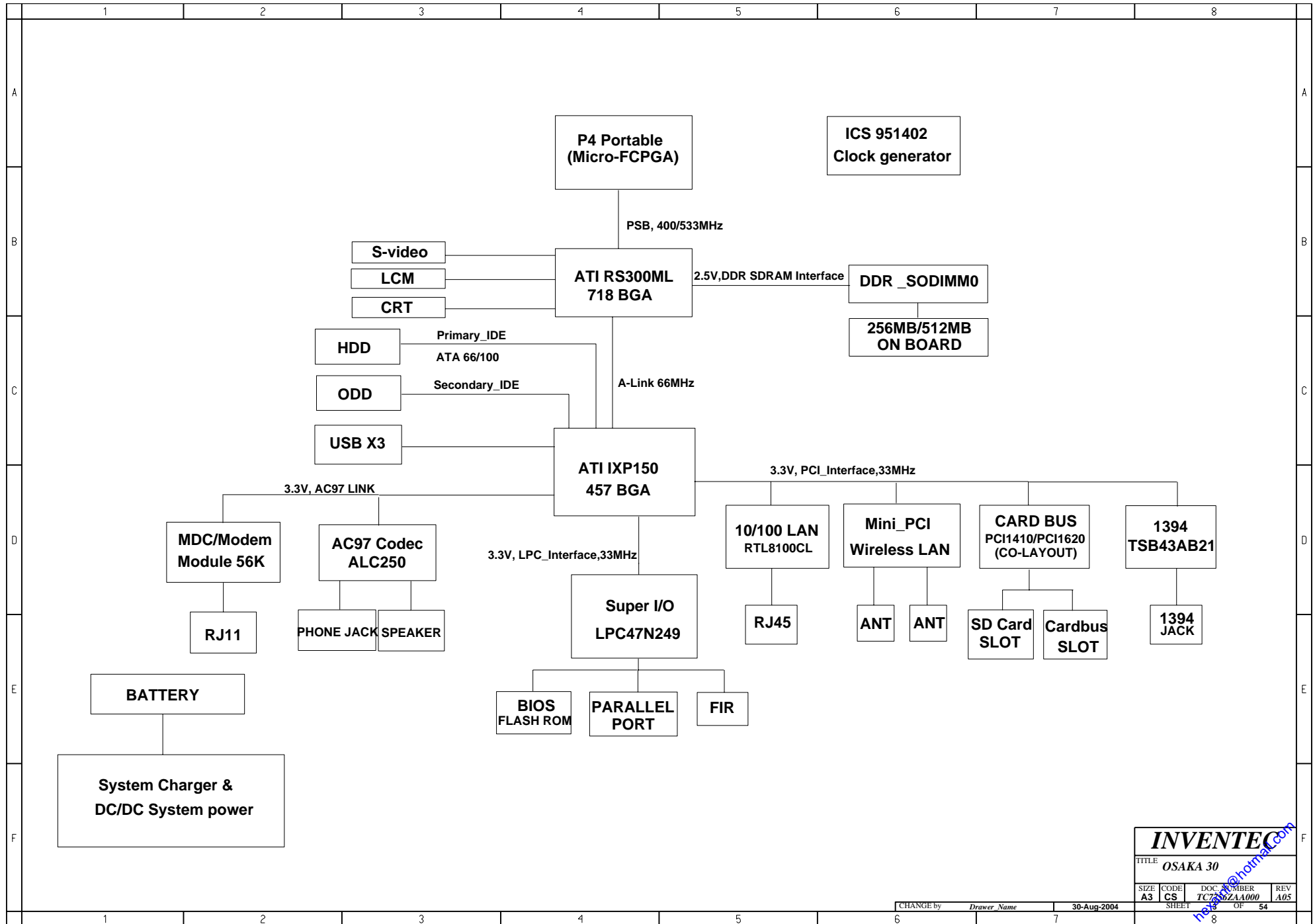
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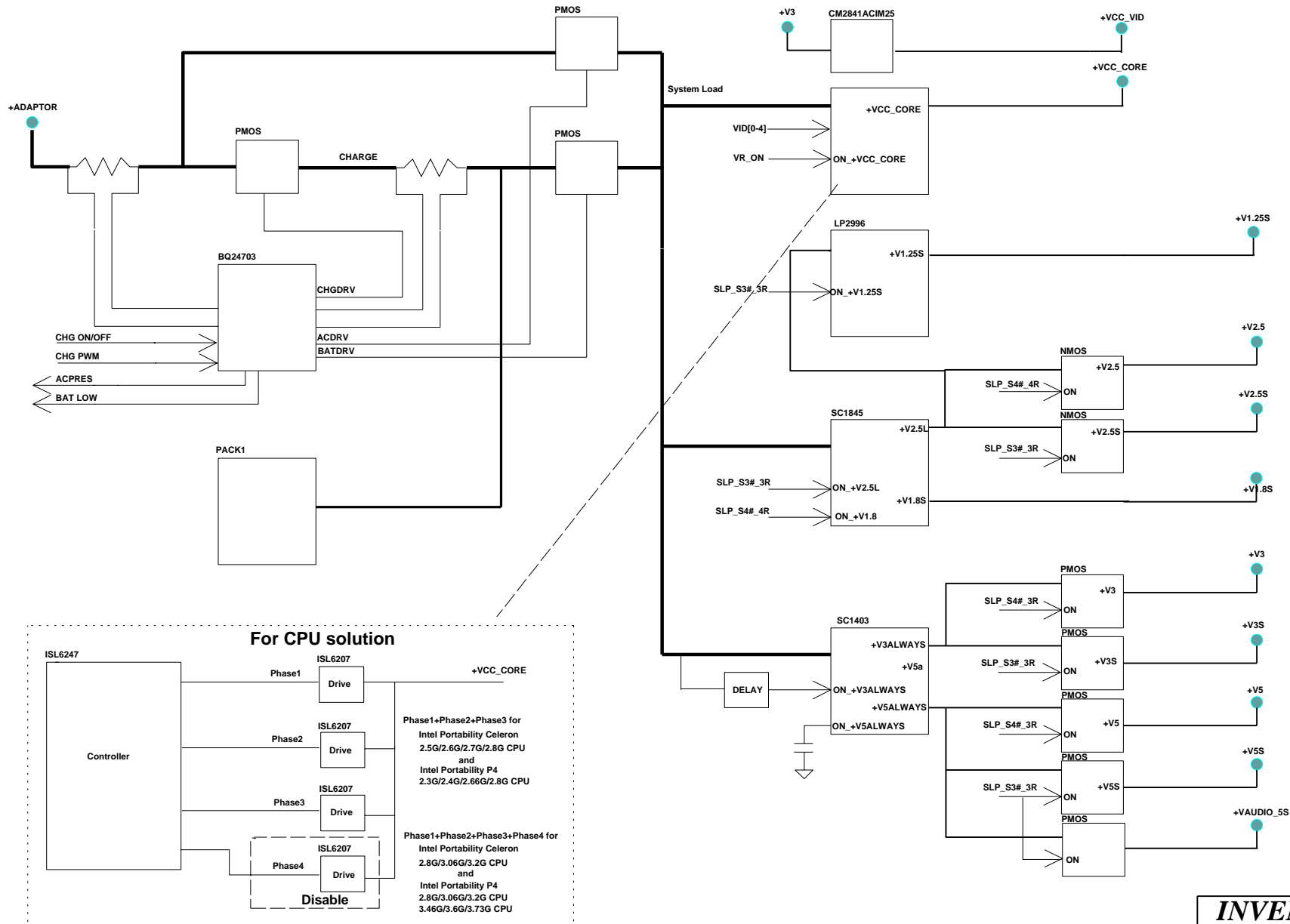
TITLE **OSAKA 30**

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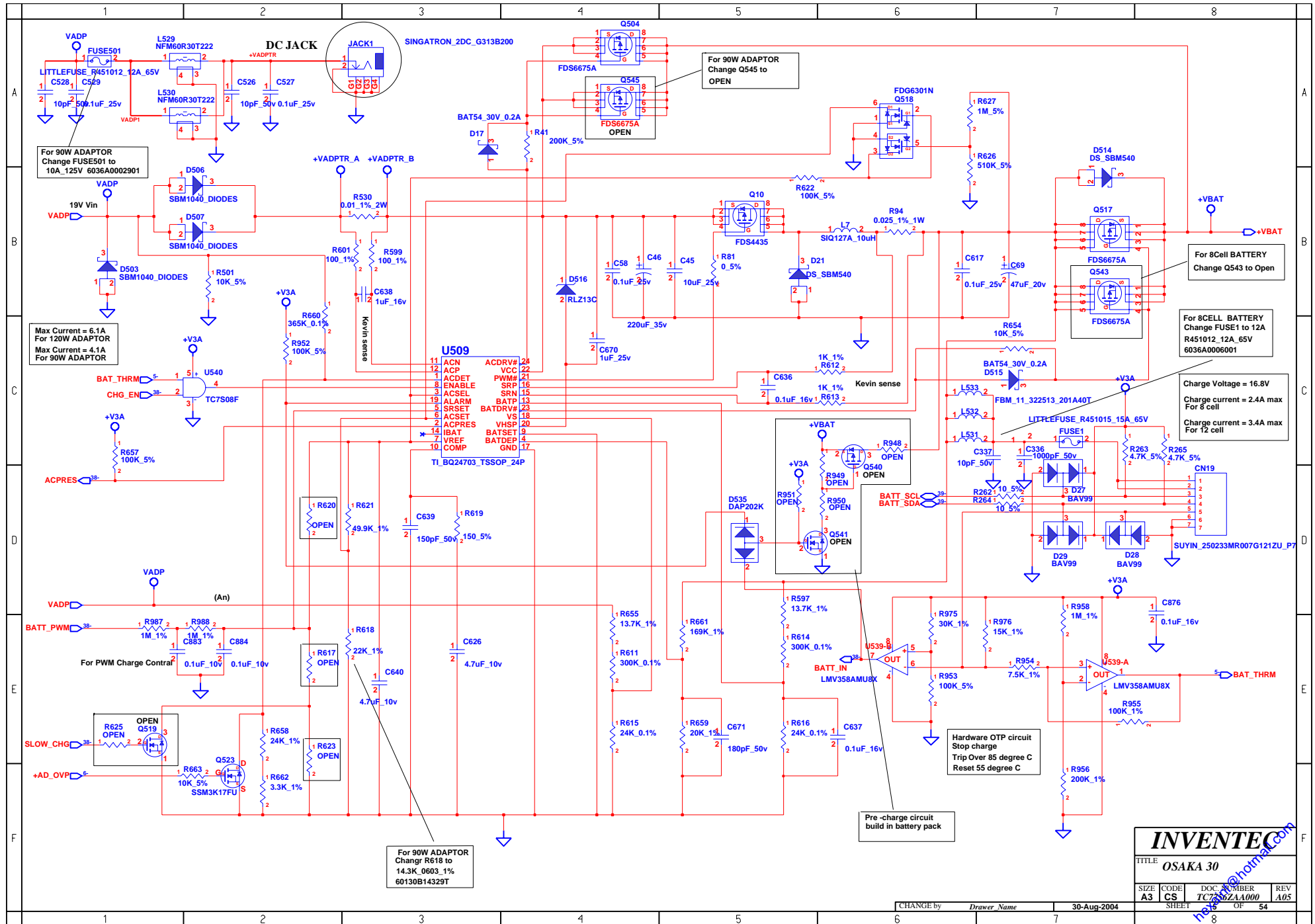
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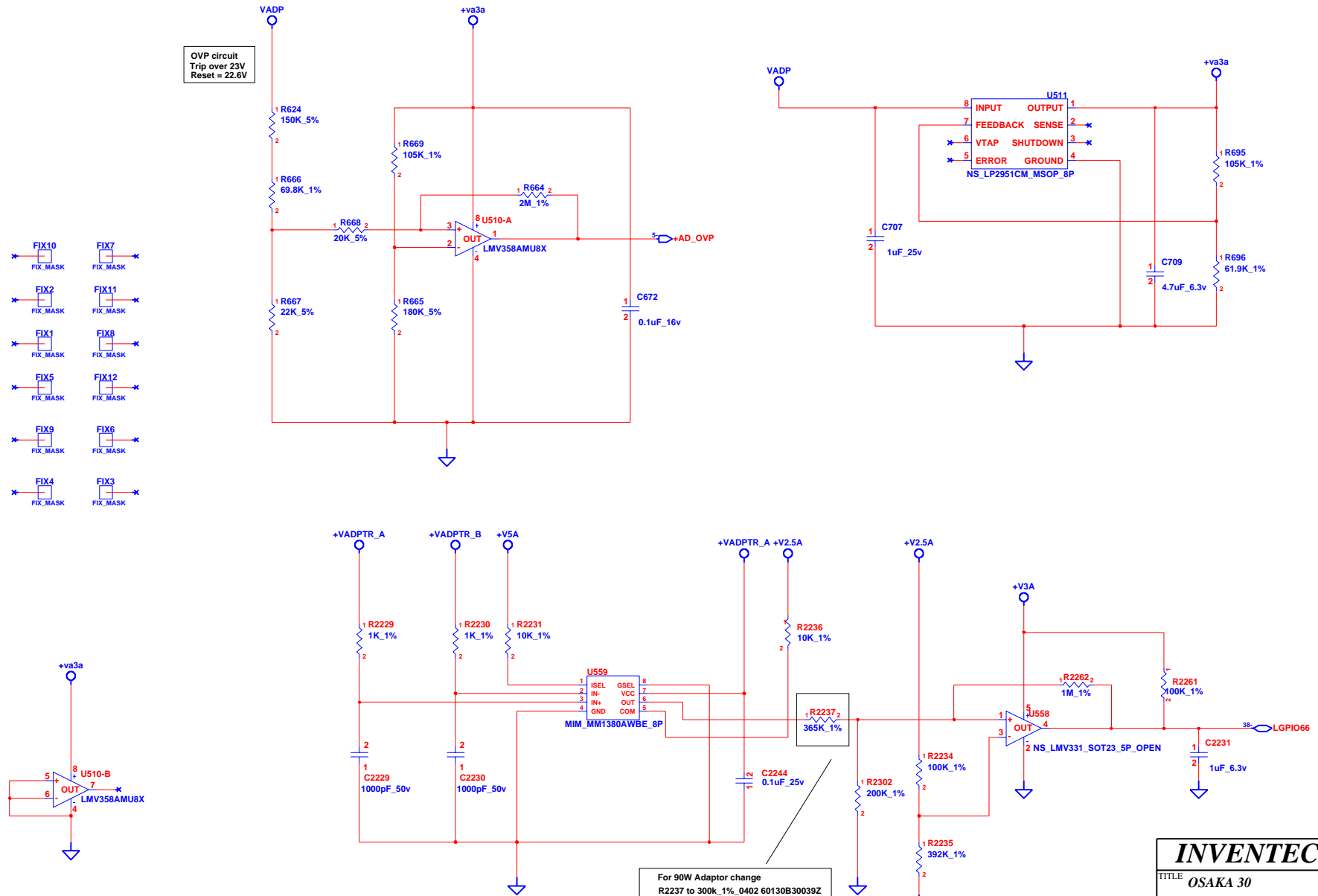
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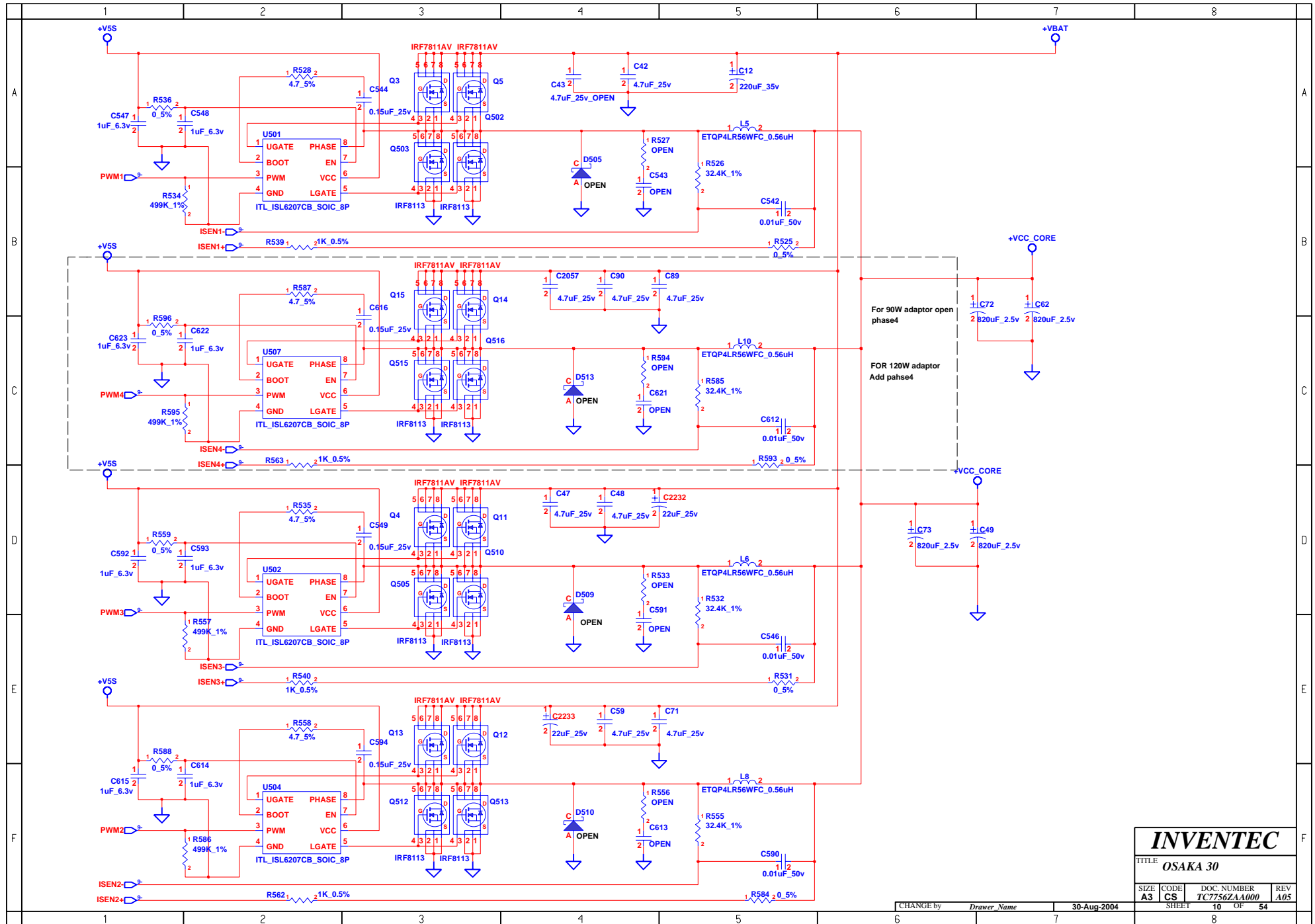
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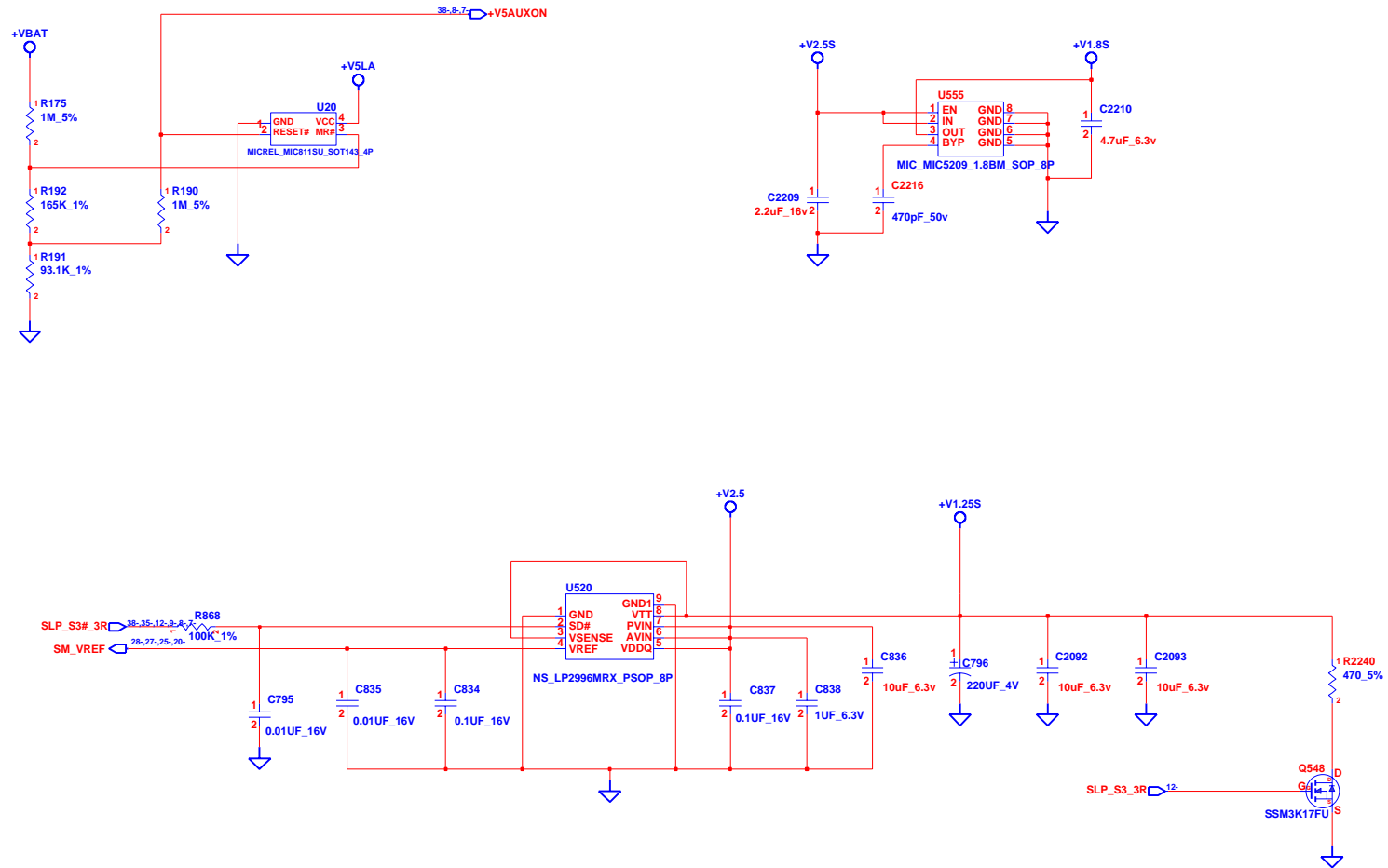
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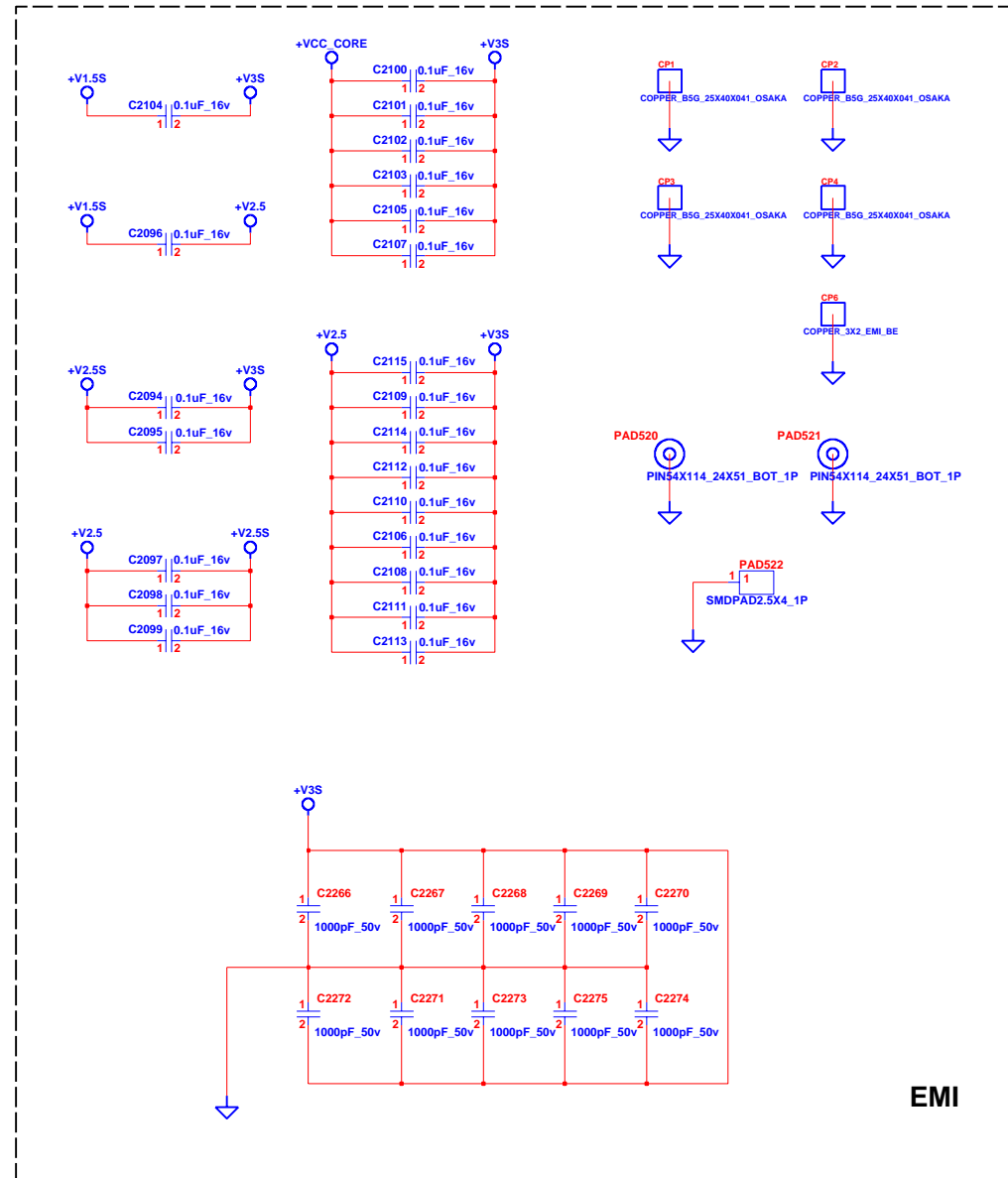
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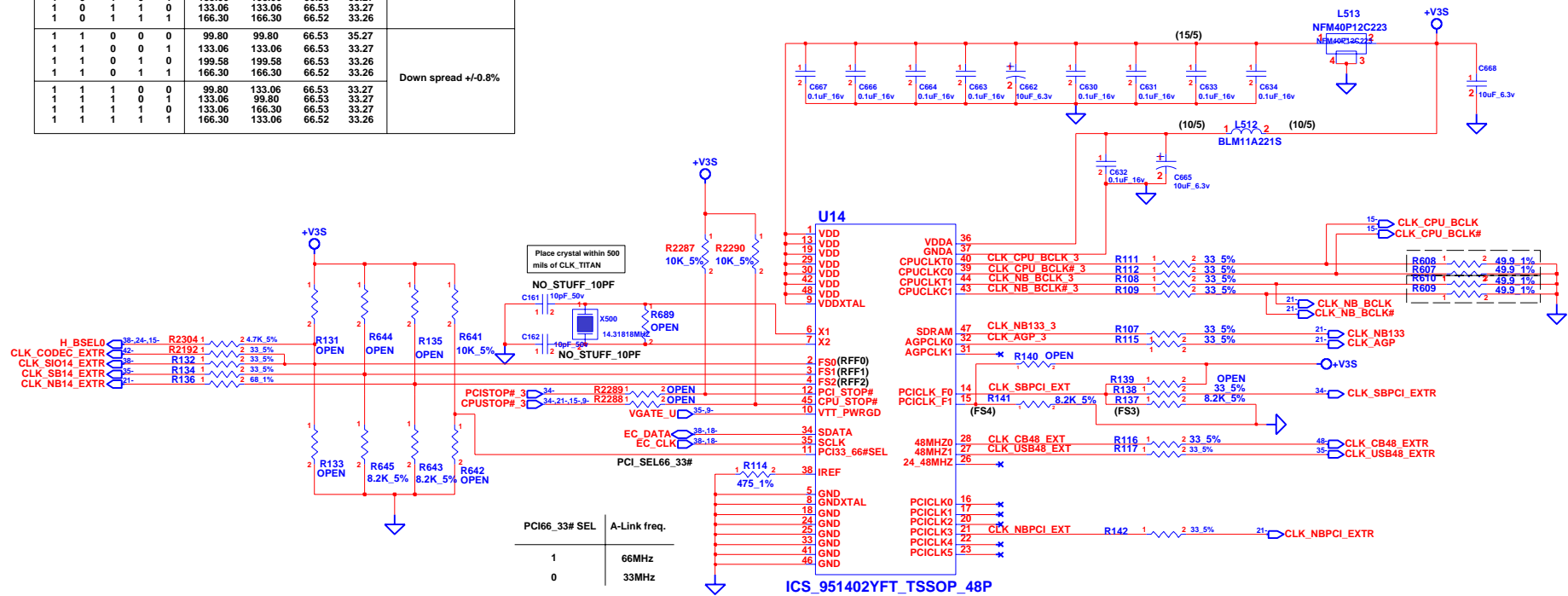


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INVENTEC			
TITLE: OSAKA 30			
SIZE: A3	CODE: CS	DOC. NUMBER: TC736ZAA000	REV: A05
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EXT CLK FREQUENCY SELECT TABLE

FS4	FS3	FS2	FS1	FS0	CPU MHz	MEM MHz	66MHz	PCI MHz	With Spread Enabled...
0	0	0	0	0	100.01	100.01	66.67	33.34	Spread OFF OR Center spread +/-0.3%
0	0	0	0	1	133.33	133.33	66.67	33.33	
0	0	0	1	0	200.01	200.01	66.67	33.34	
0	0	0	1	1	166.64	166.64	66.66	33.33	
0	0	1	0	0	100.01	133.34	66.67	33.34	
0	0	1	0	1	133.34	100.01	66.67	33.34	Spread OFF OR Center spread +/-0.3%
0	0	1	1	0	133.16	166.45	66.58	33.29	
0	0	1	1	1	166.45	133.16	66.58	33.29	
0	1	0	0	0	105.00	105.00	70.00	35.00	
0	1	0	0	1	140.00	140.00	70.00	35.00	
0	1	0	1	0	66.67	66.67	66.67	33.34	Down spread +/-0.6%
0	1	0	1	1	175.00	175.00	70.00	35.00	
0	1	1	0	0	109.99	109.99	73.33	36.66	
0	1	1	0	1	146.66	146.66	73.33	36.67	
0	1	1	1	0	201.00	201.00	70.00	35.00	
0	1	1	1	1	183.27	183.27	73.31	36.65	Down spread +/-0.8%
1	0	0	0	0	99.80	99.80	66.53	33.27	
1	0	0	0	1	133.06	133.06	66.53	33.27	
1	0	0	1	0	199.58	199.58	66.53	33.26	
1	0	0	1	1	166.30	166.30	66.52	33.26	
1	0	1	0	0	99.80	133.06	66.53	33.27	Down spread +/-0.8%
1	0	1	0	1	133.06	99.80	66.53	33.27	
1	0	1	1	0	133.06	133.06	66.53	33.27	
1	0	1	1	1	166.30	166.30	66.52	33.26	
1	1	0	0	0	99.80	99.80	66.53	35.27	
1	1	0	0	1	133.06	99.80	66.53	33.27	Down spread +/-0.8%
1	1	0	1	0	133.06	133.06	66.53	33.26	
1	1	0	1	1	166.30	166.30	66.52	33.26	
1	1	1	0	0	99.80	133.06	66.53	33.27	
1	1	1	0	1	133.06	99.80	66.53	33.27	
1	1	1	1	0	133.06	133.06	66.53	33.27	Down spread +/-0.8%
1	1	1	1	1	166.30	166.30	66.52	33.26	
1	1	1	1	1	166.30	133.06	66.52	33.26	
1	1	1	1	1	166.30	133.06	66.52	33.26	
1	1	1	1	1	166.30	133.06	66.52	33.26	



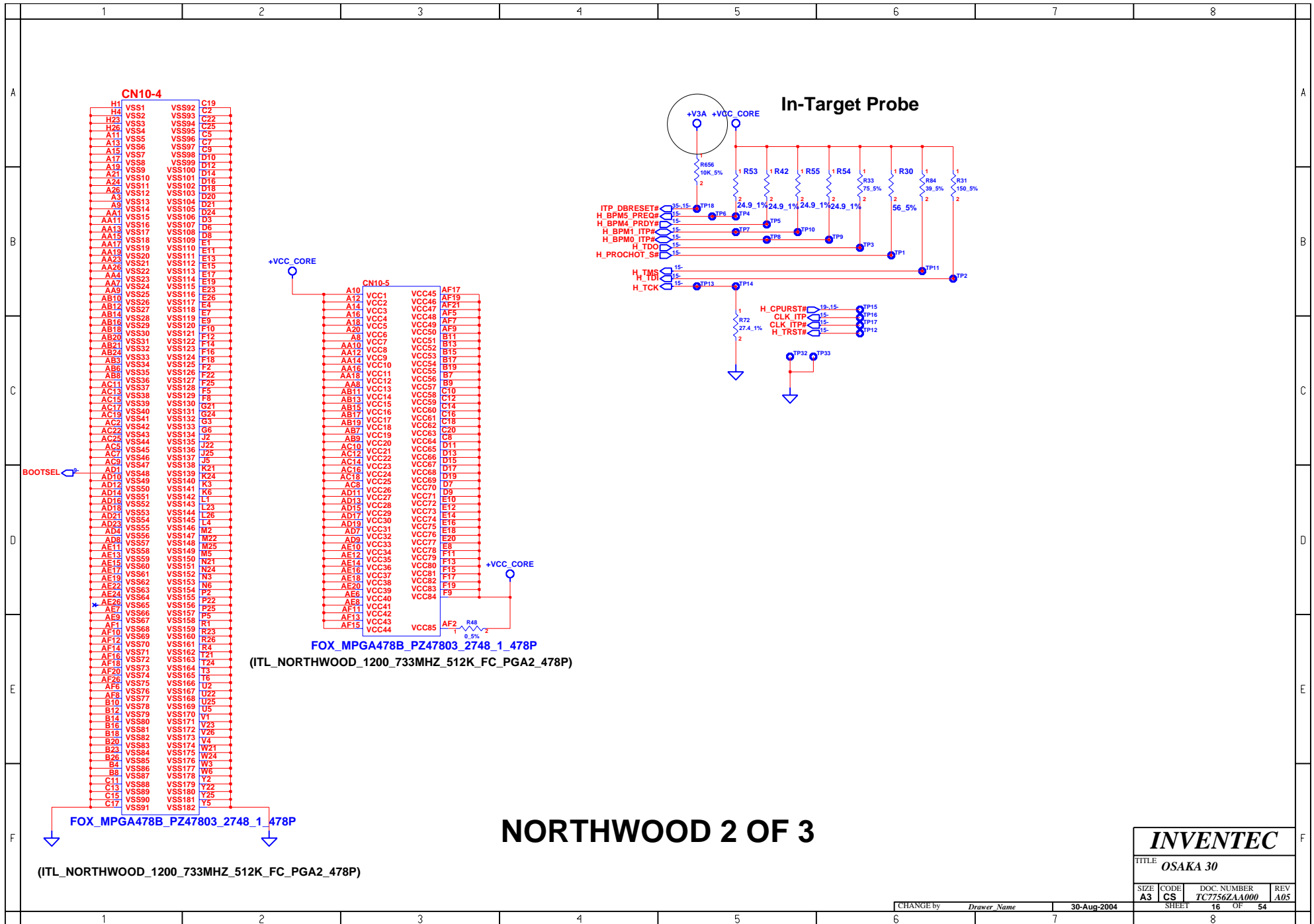
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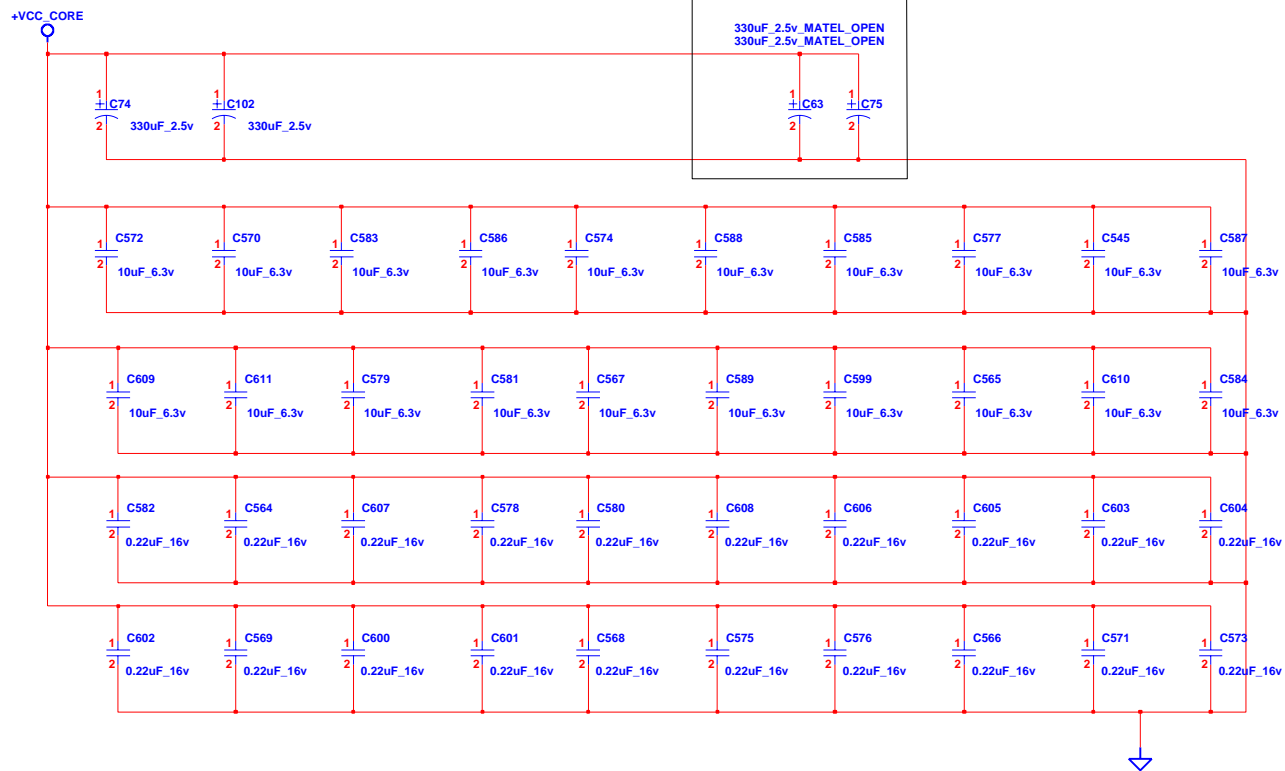
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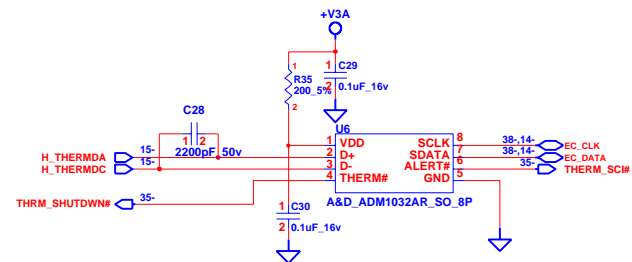
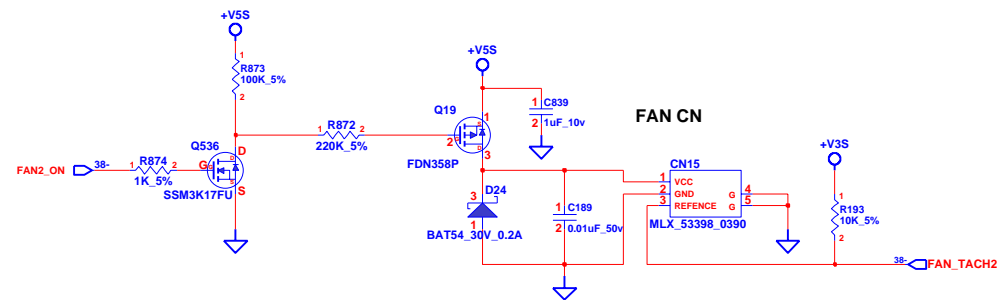
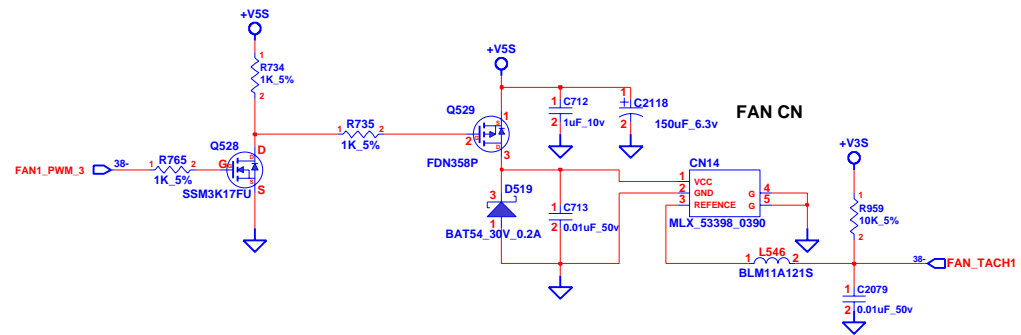
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Thermal Sensor For CPU

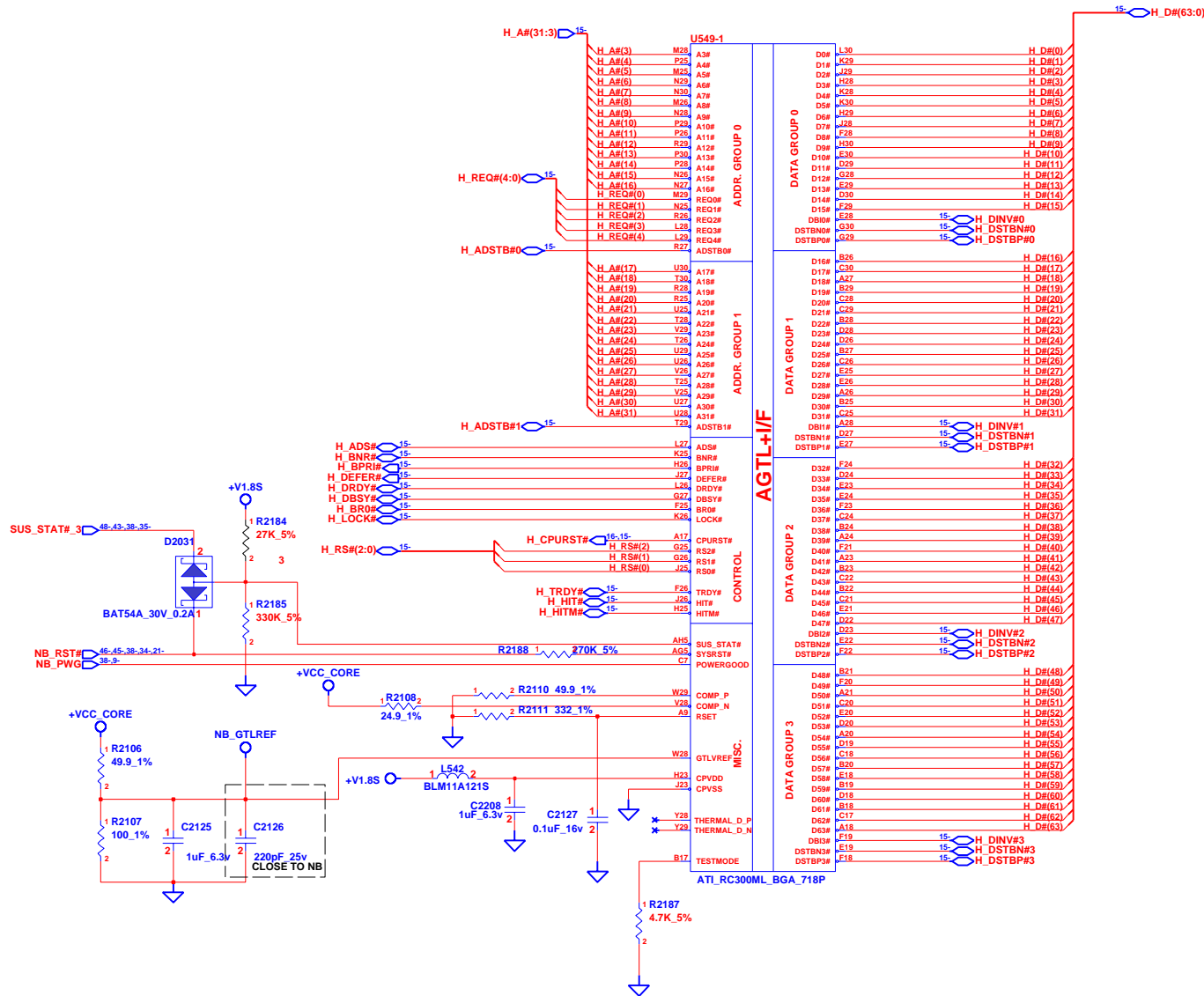
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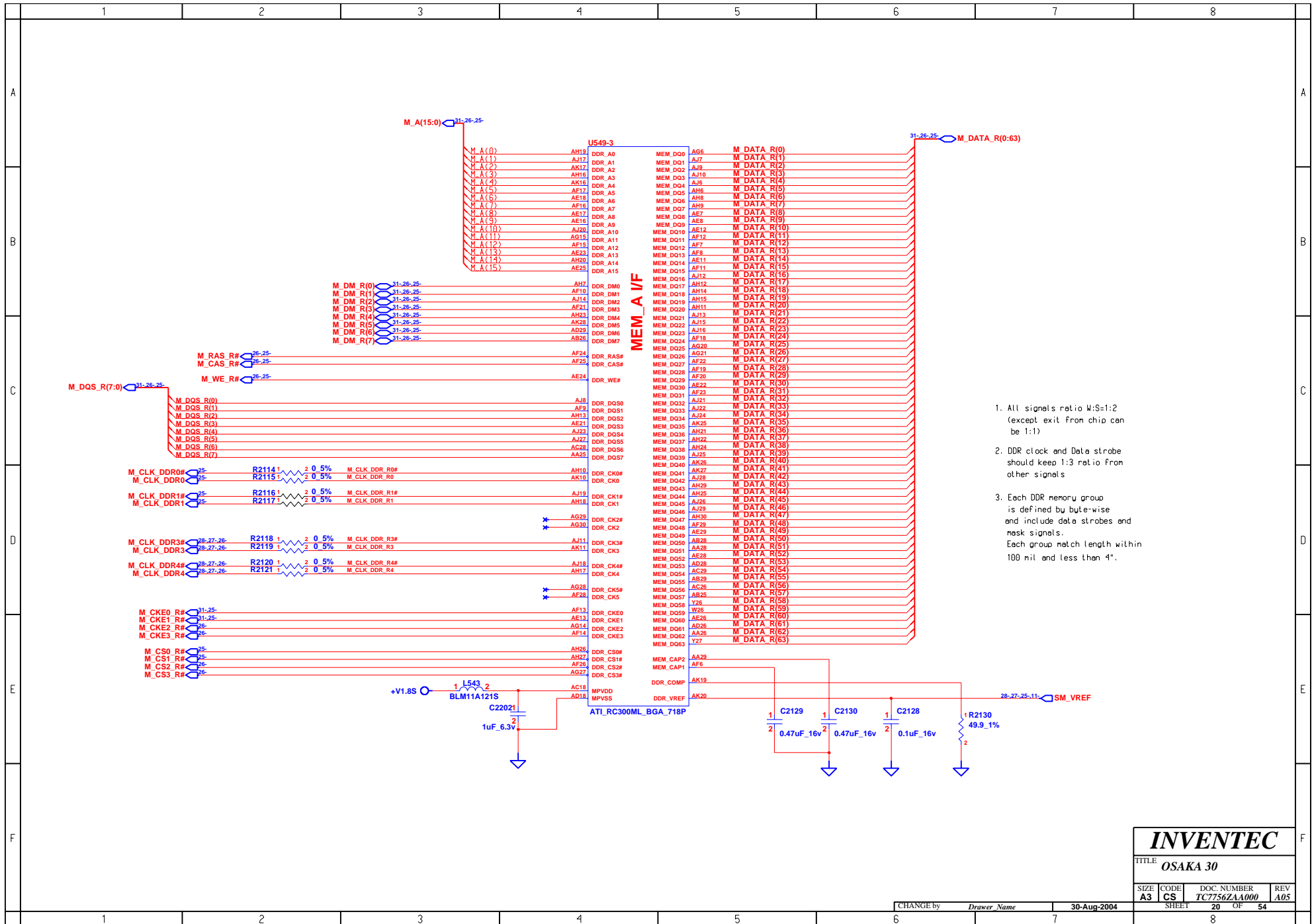
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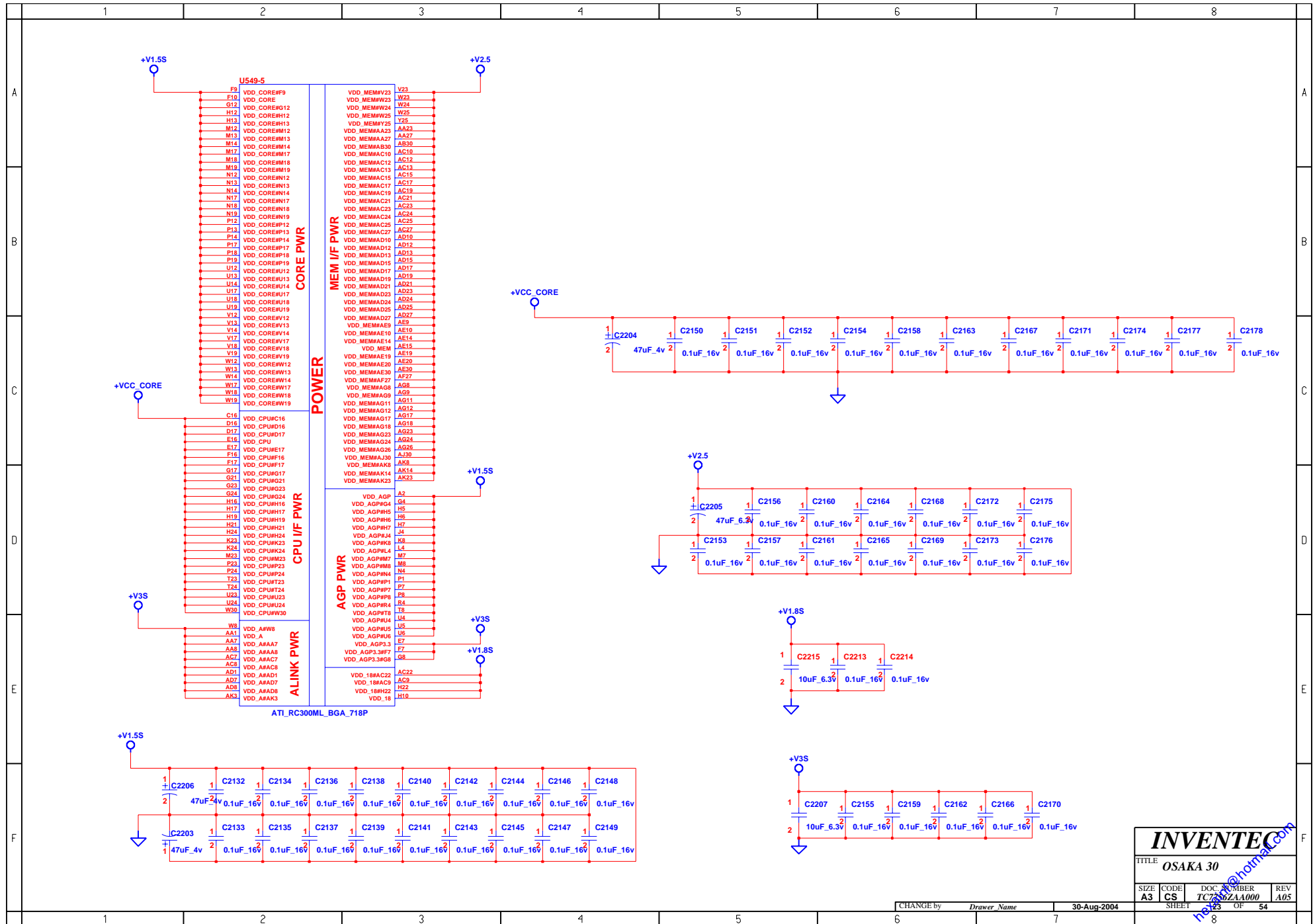
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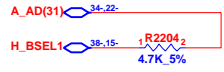
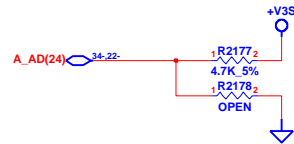
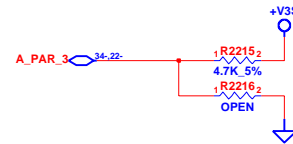
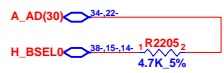
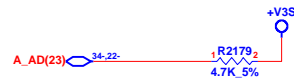
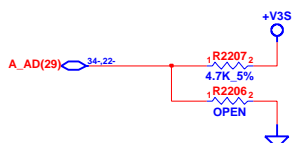
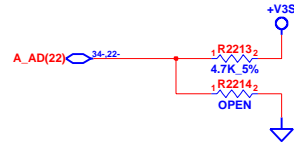
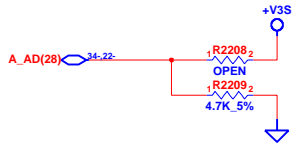
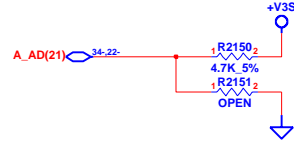
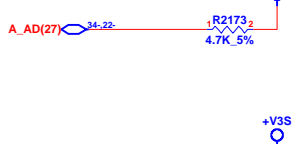
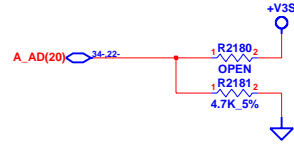
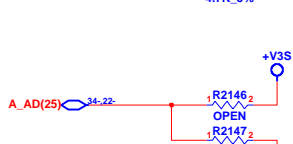
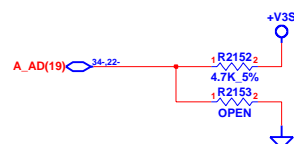
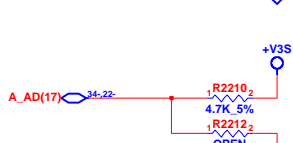
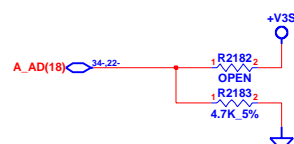
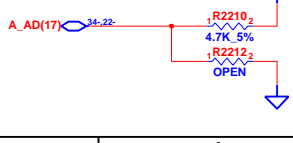
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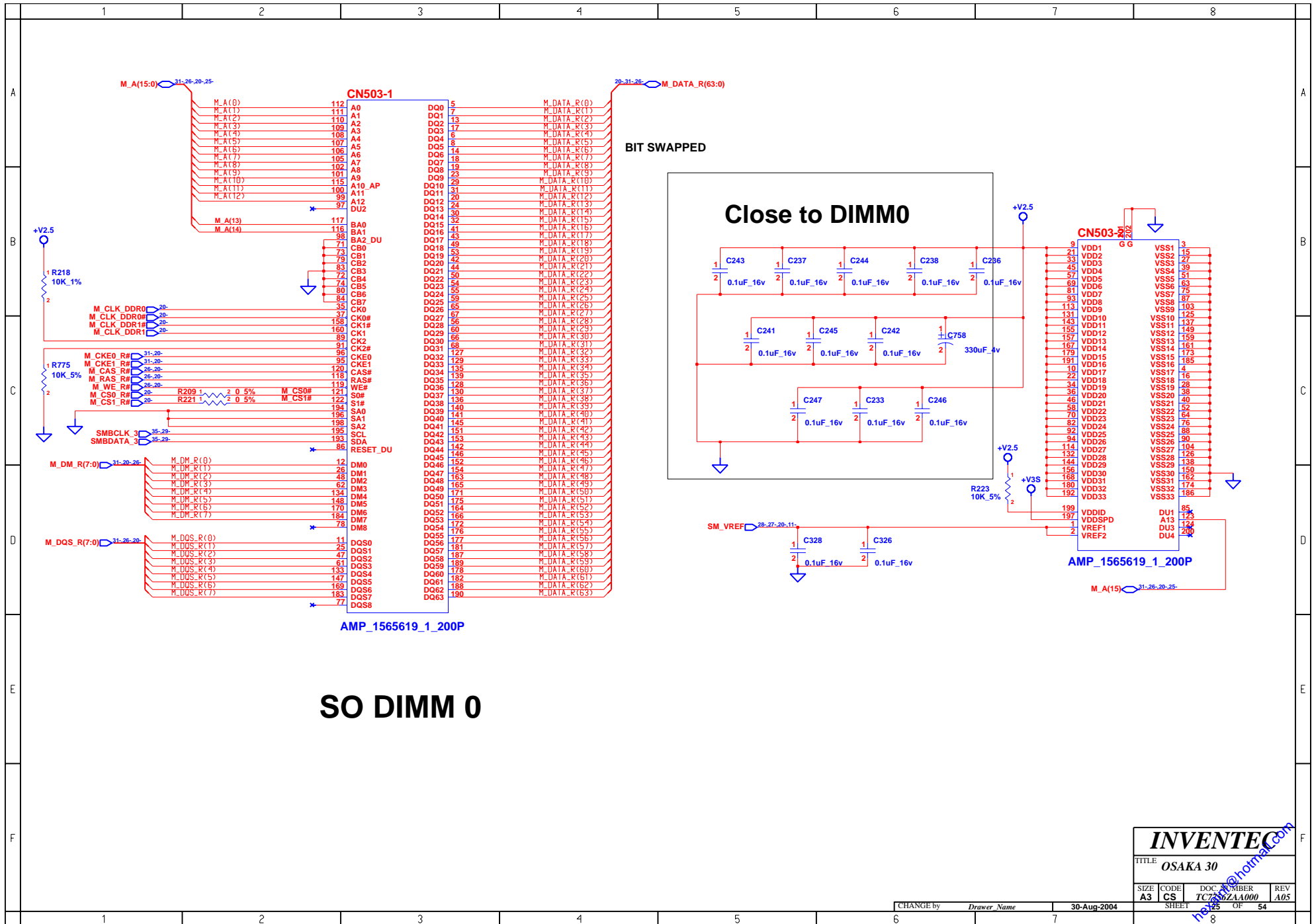


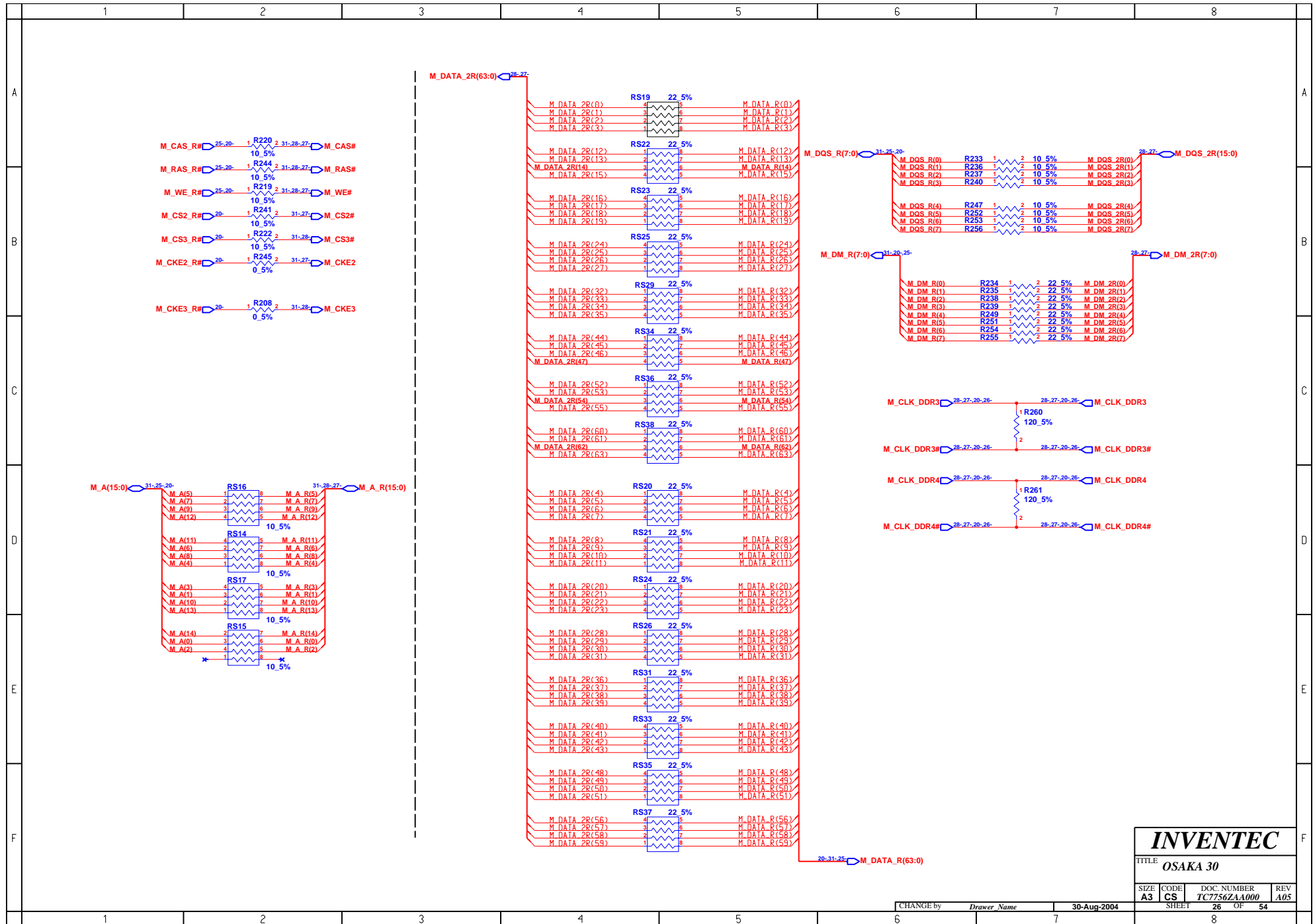
	1	2	3	4	5	6	7	8	
A		AD(31:30) : FSB CLK SPEED 0 0 : 100MHZ DEFAULT : 00 0 1 : 133MHZ 1 0 : 200MHZ 1 1 : 166MHZ		AD24 : MOBILE CPU SELECT 0 : BANIAS CPU DEFAULT : 1 1 : OTHER CPU		PAR:EXTENDED DEBUG MODE 0 : DEBUG MODE DEFAULT : 1 1 : NORMAL	A		
B				AD23 : CLOCK BYPASS DISABLE 0 : TEST MODE DEFAULT : 1 1 : NORMAL			B		
C		AD29 : STRAP CONFIGURATION 0 : REDUCED SET DEFAULT : 1 1 : FULL SET		AD22 : OSC PAD OUTPUT PCICLK 0 : PCICLK OUT DEFAULT : 1 1 : OSC CLK OUT			C		
D		AD28 : SPREAD SPECTRUM ENABLE 0 : DISABLE DEFAULT : 0 1 : ENABLE		AD21 : AUTO_CAL ENABLE 0 : DISABLE DEFAULT : 1 1 : ENABLE			D		
E		AD27 : FrcShortRest# 0 : TEST MODE DEFAULT : 1 1 : NORMAL MODE		AD20 : INTERNAL CLK GEN ENABLE 0 : DISABLE DEFAULT : 0 1 : ENABLE			E		
F		AD26 : ENABLE IOQ 0 : IOQ = 1 DEFAULT : 1 1 : IOQ = 12		AD19 : MEMORY IO VOLTAGE 0 : 1.8V DEFAULT : 1 1 : 2.5V			F		
		AD25,AD17 : CPU VOLTAGE(1:0) 0 0 : 1.05V DEFAULT : 01 0 1 : 1.35V 1 1 : 1.75V 1 0 : 1.45V		AD18 : ENABLE PHASE CALIBRATION 0 : DISABLE DEFAULT : 0 1 : ENABLE					
									
	1	2	3	4	5	6	7	8	

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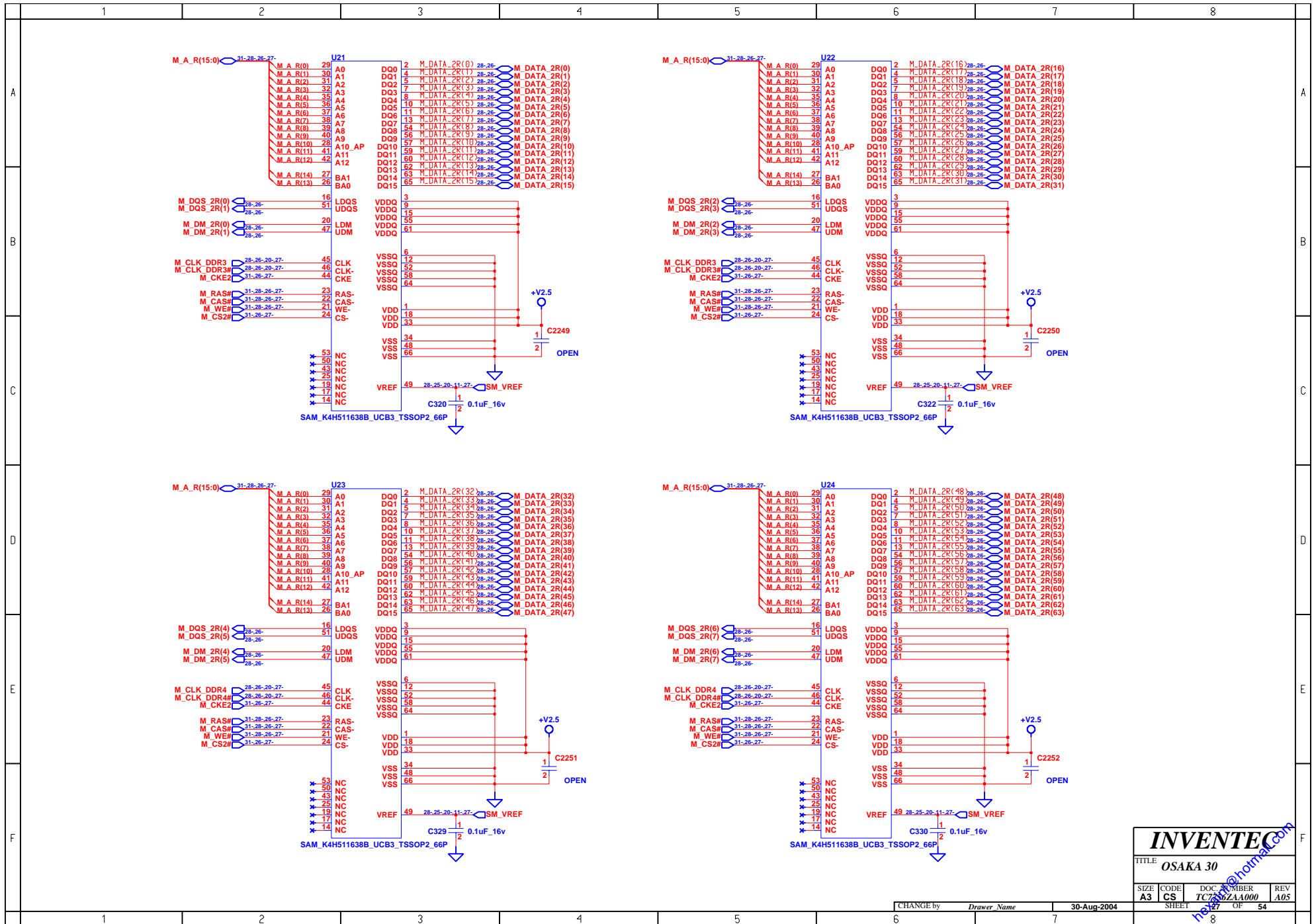
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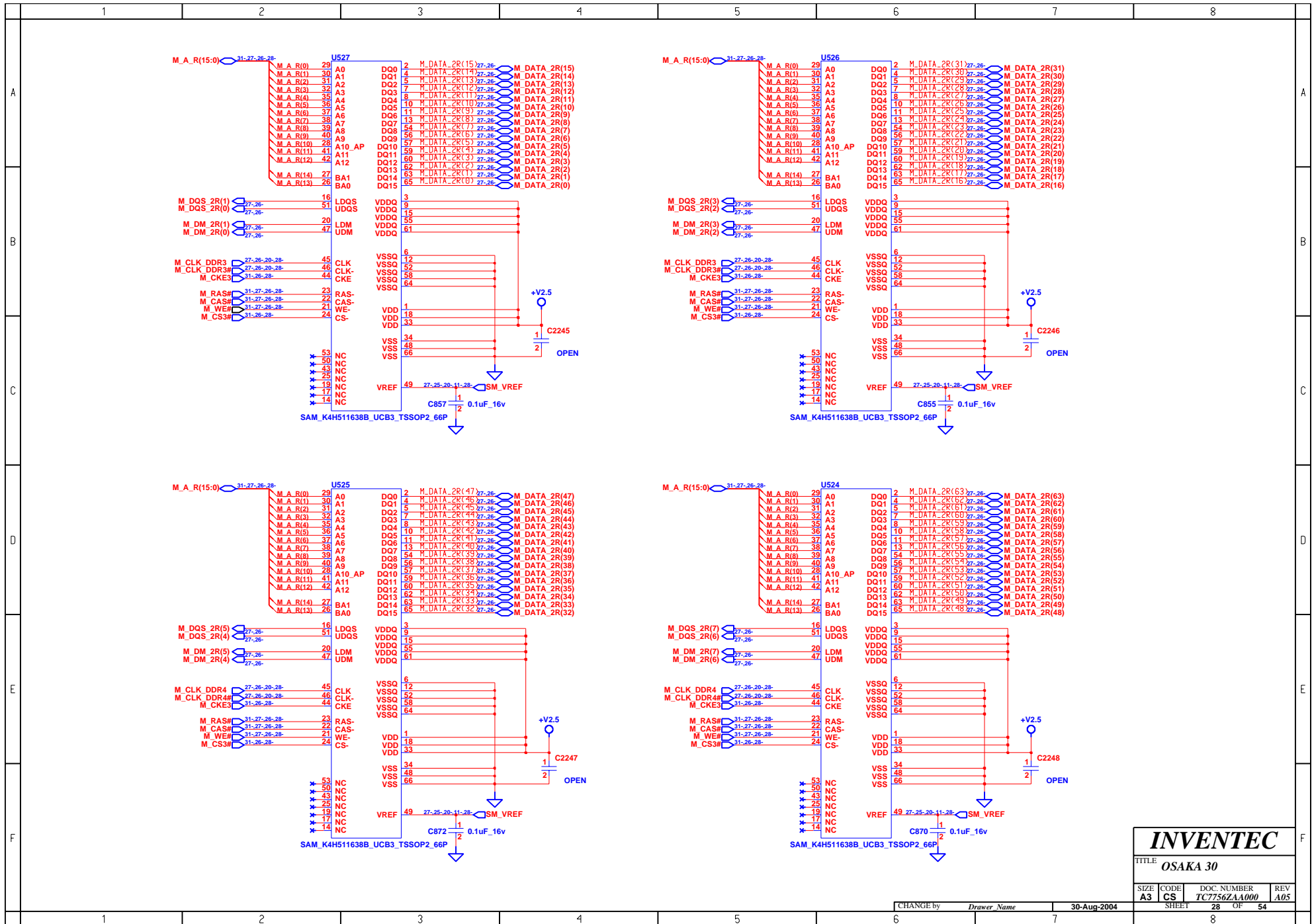
TITLE
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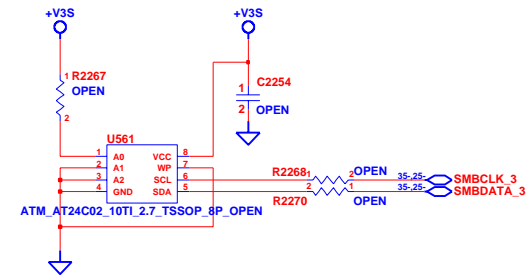
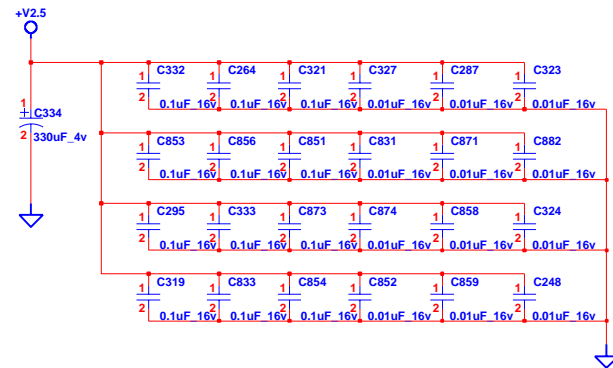
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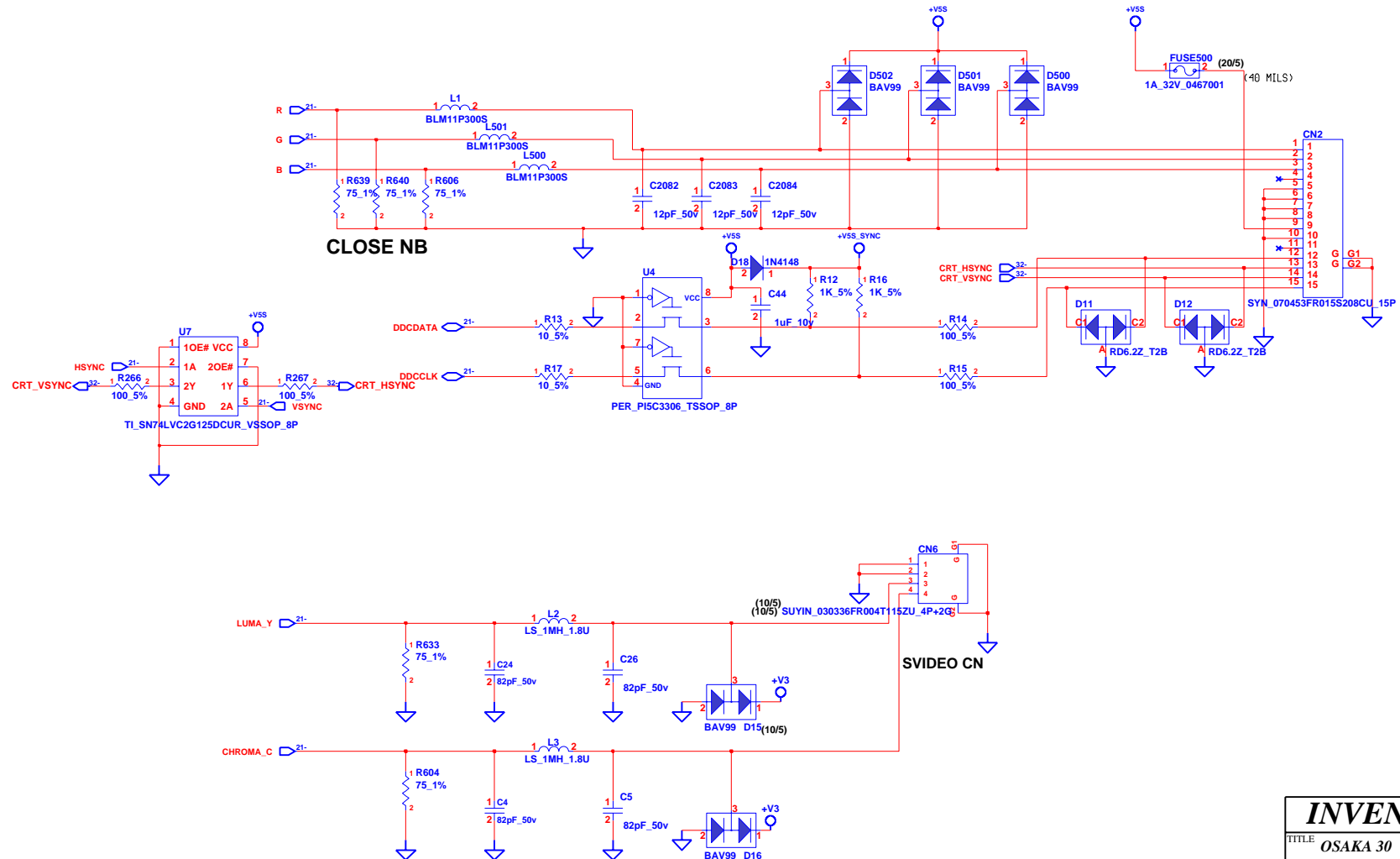
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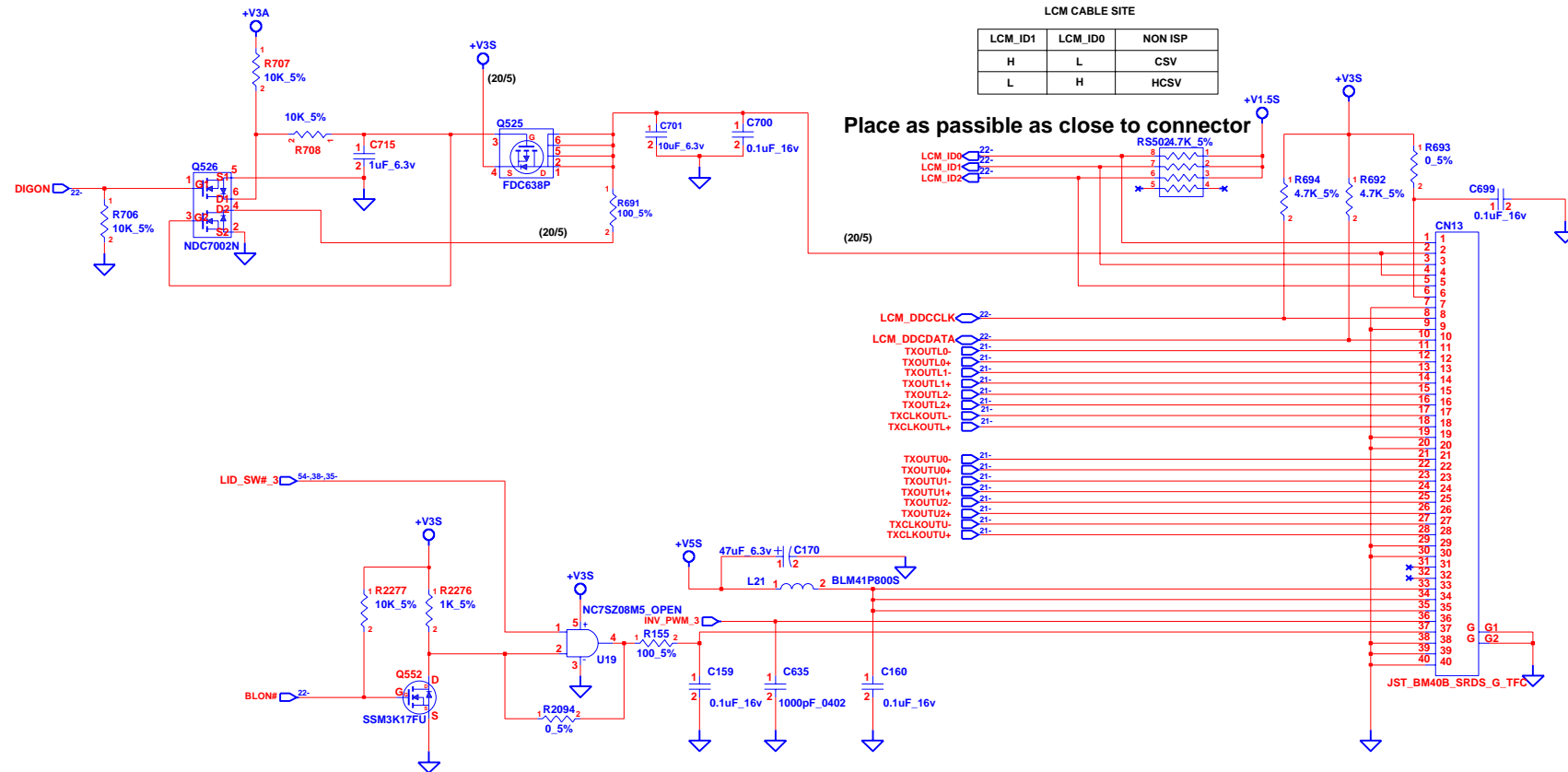
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Change IXP150 from version A44(6019A0219401)
to A45(6019A0219402) from CS1 BOM
to A46(P/N is the same as A45) from VP BOM

U522-1

A-LINK PULL UPS RESERVAL

A-LINK INTERFACE

PCI INTERFACE

PCI PULL UPS

ATI_IXP150_BGA_457P

INVENTEC

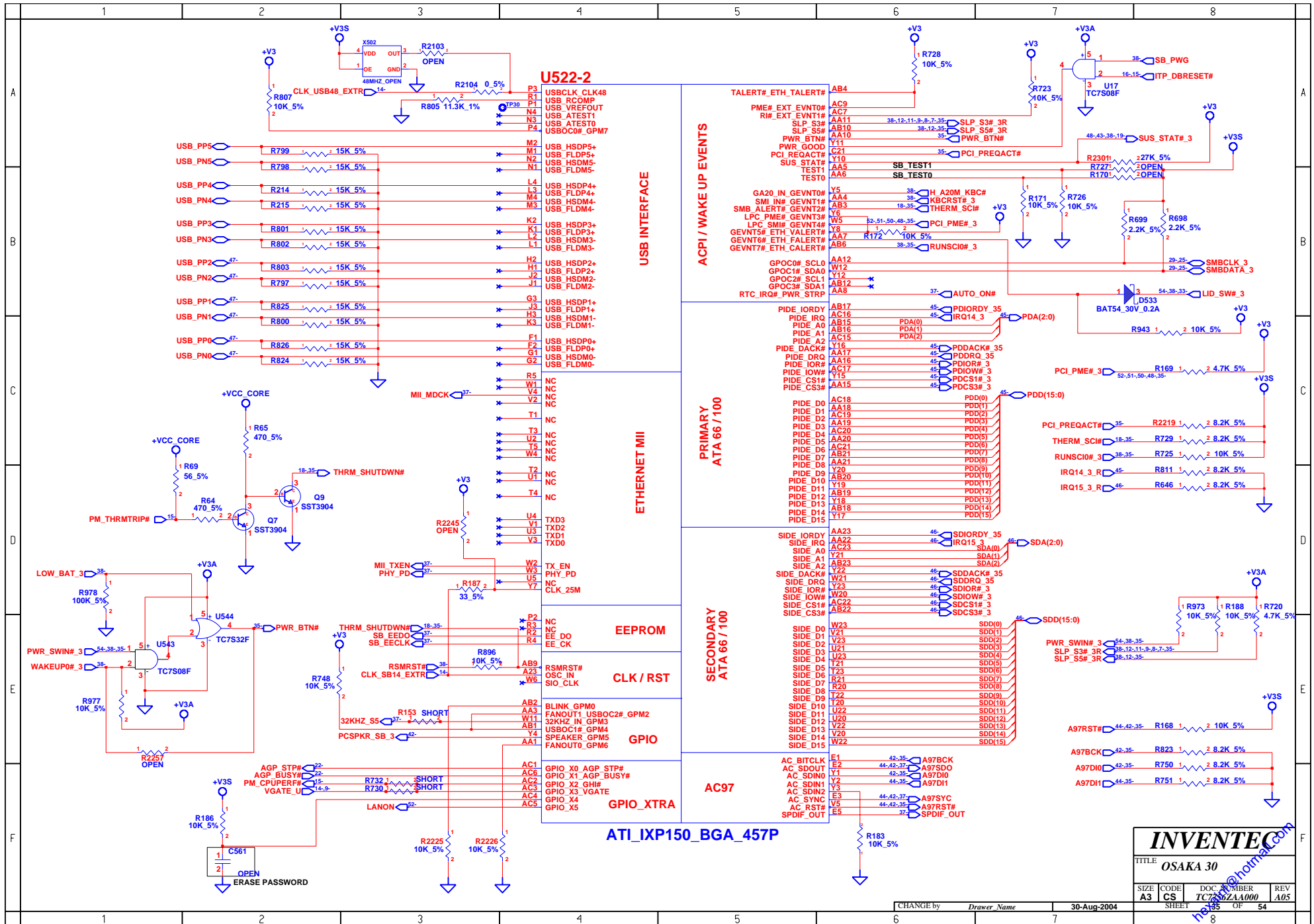
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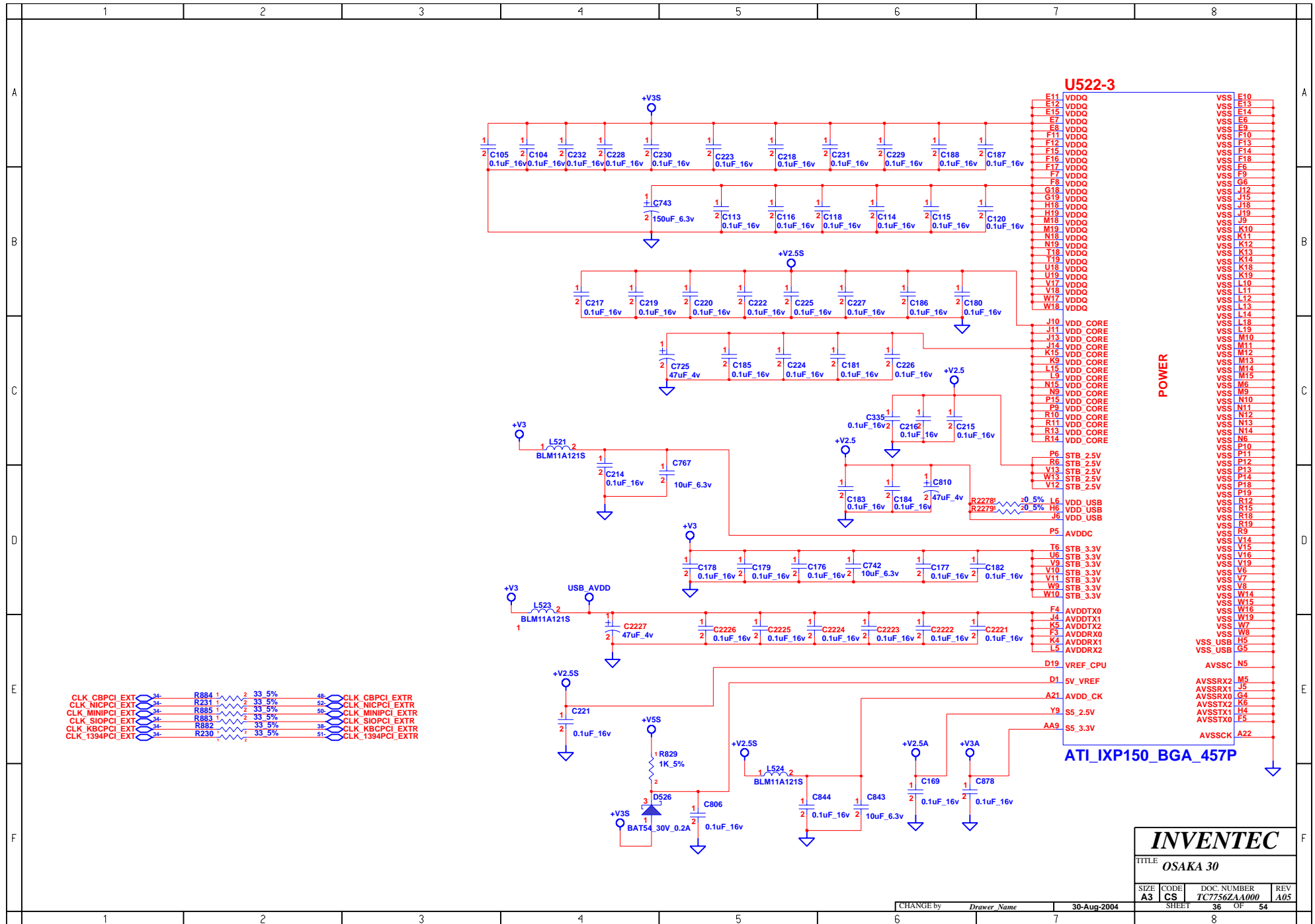
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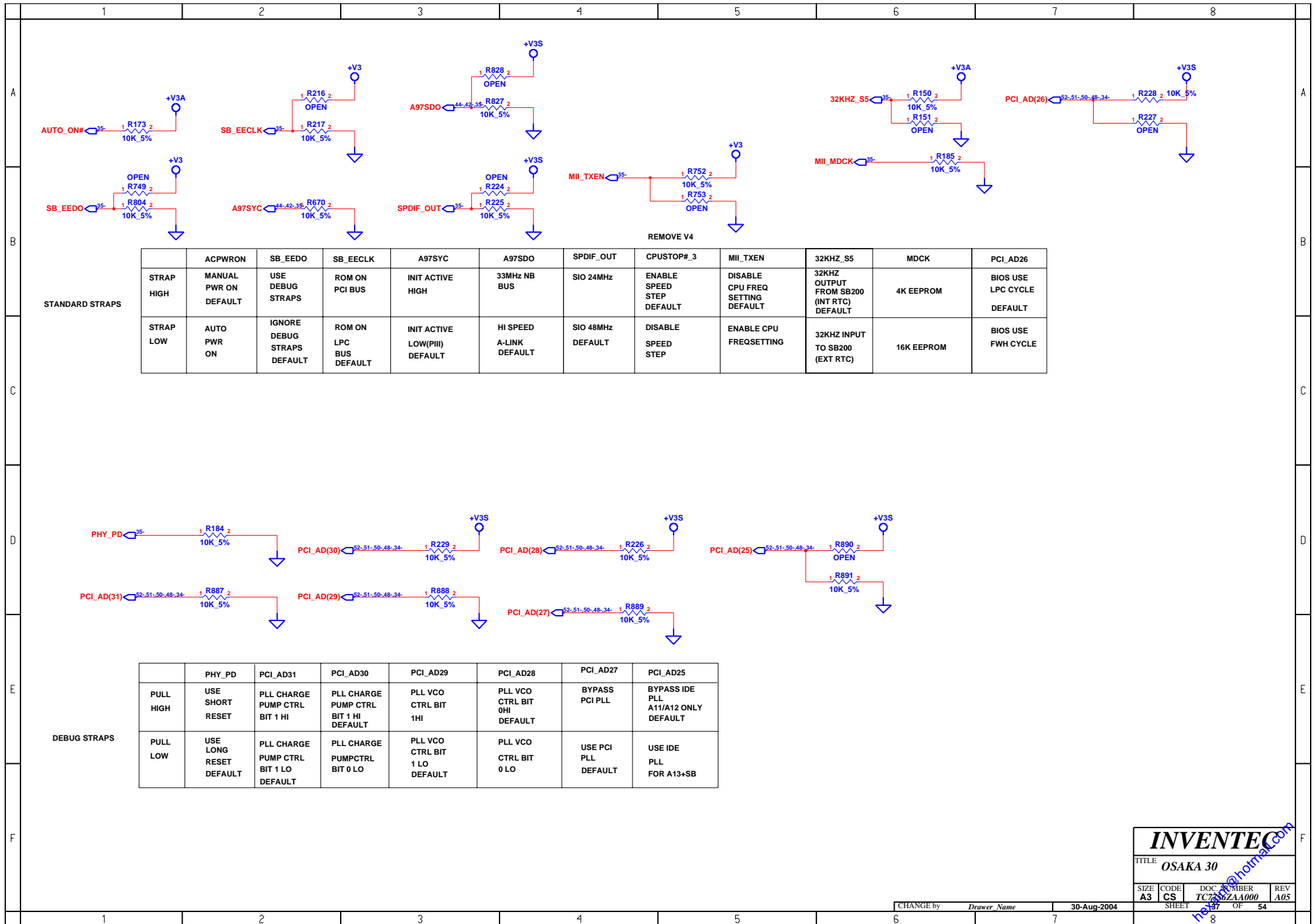
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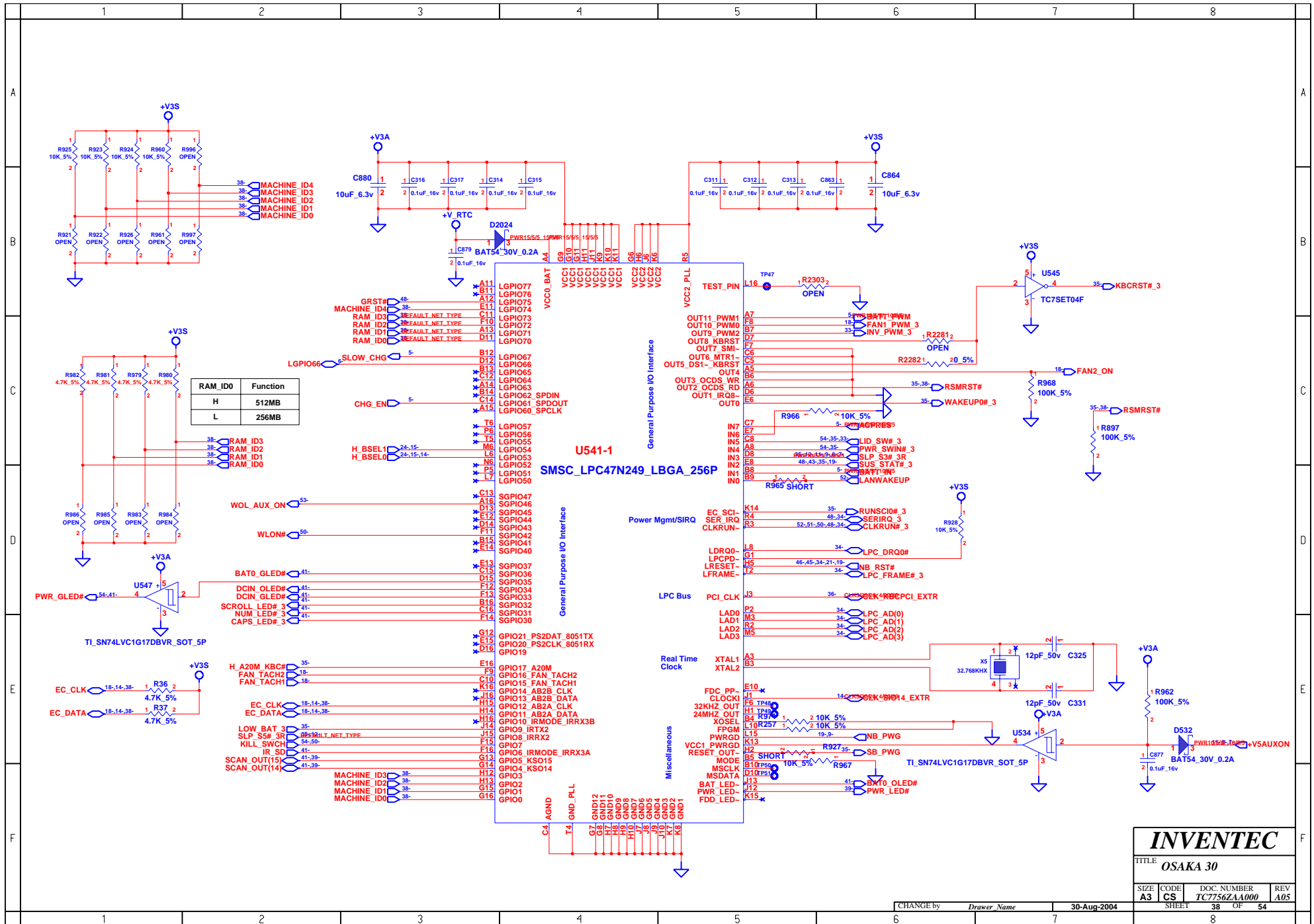
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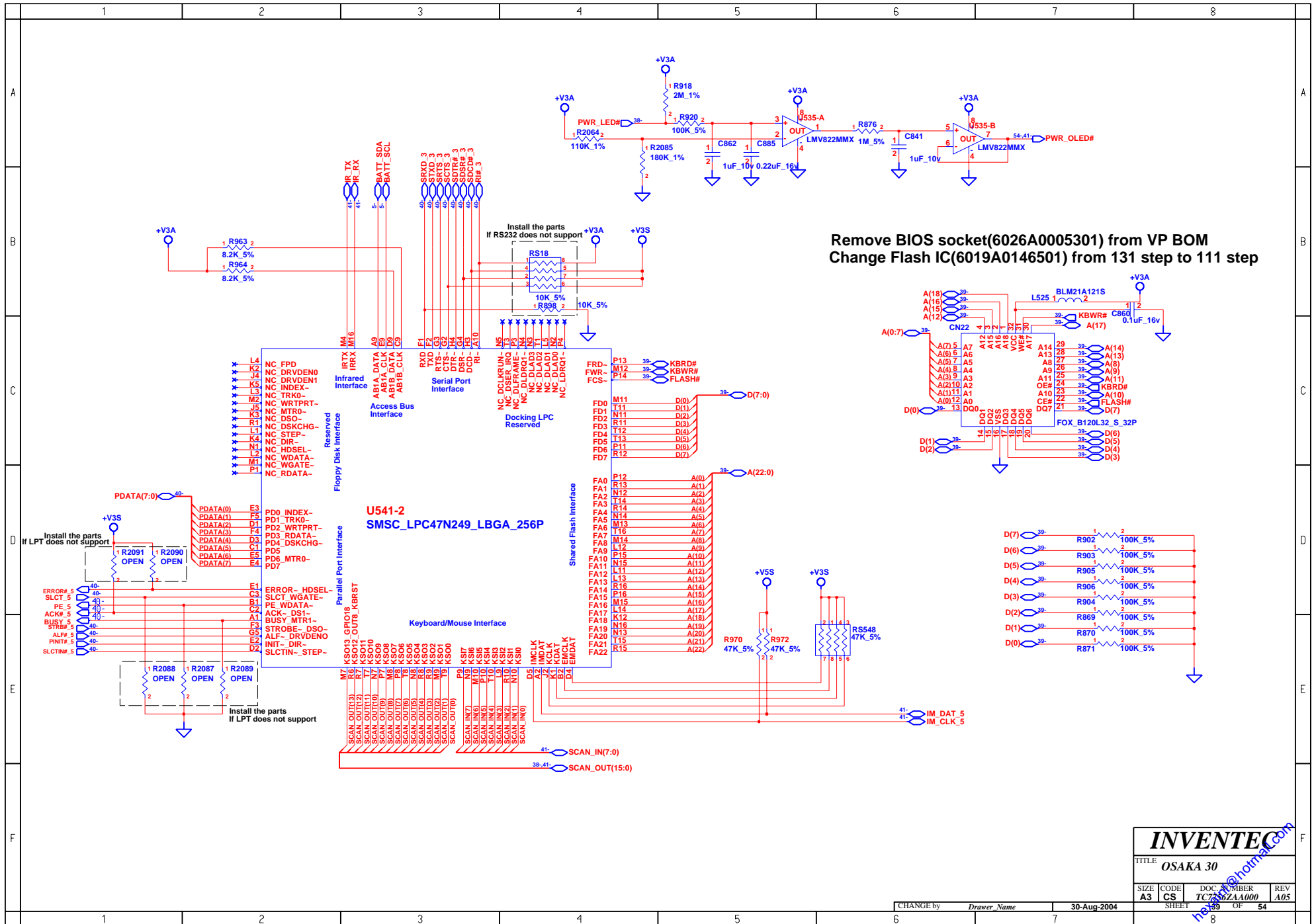
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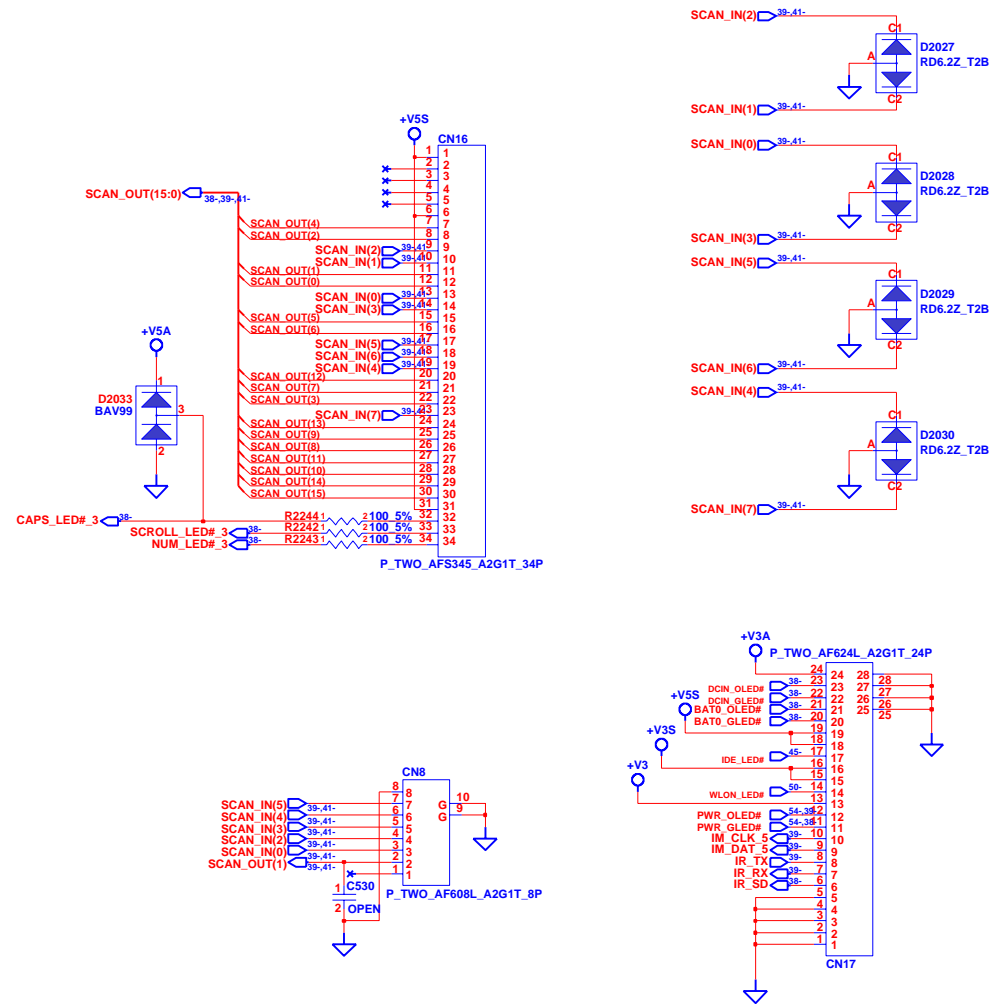
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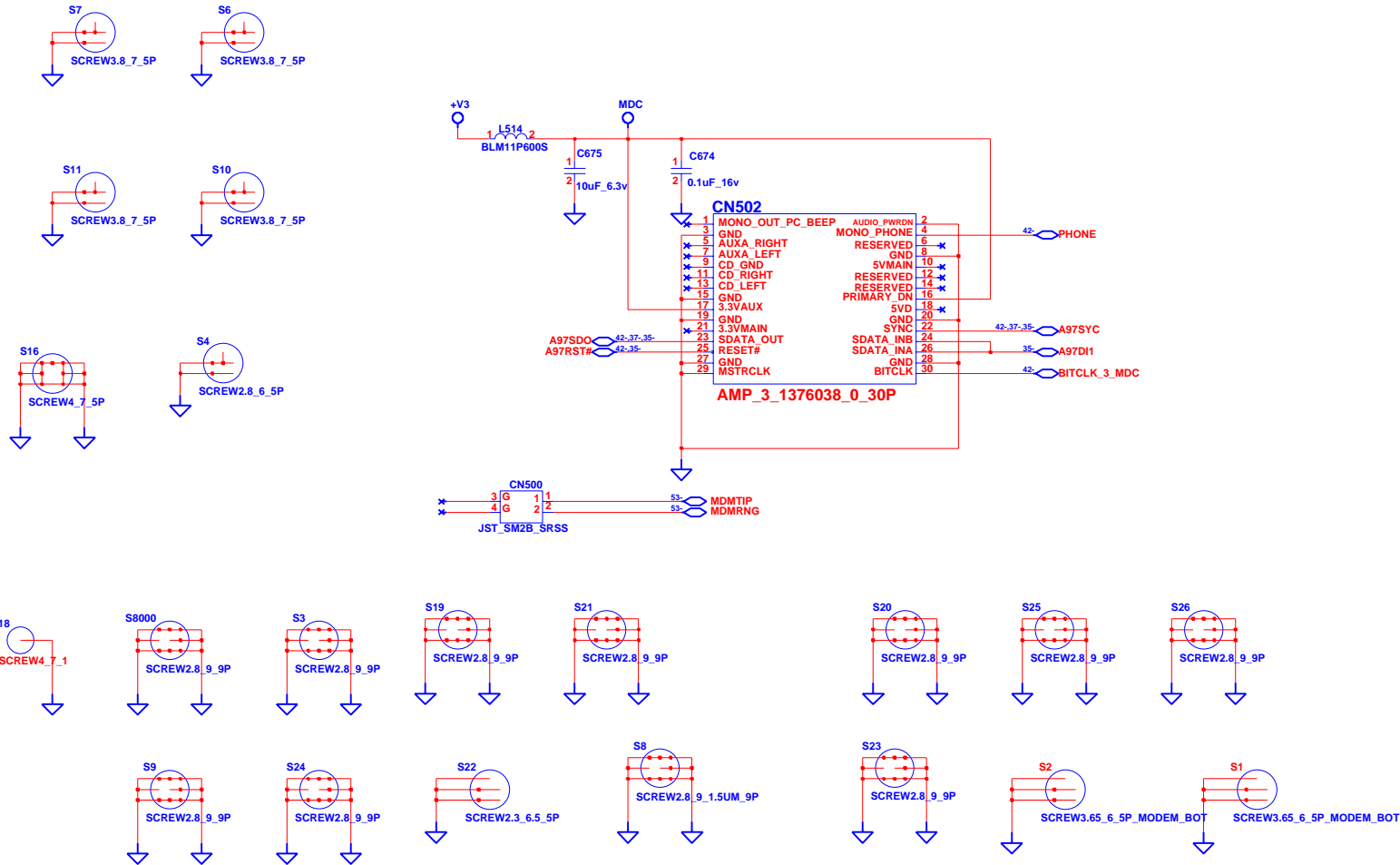




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REA_ALC250_LQFP_48P

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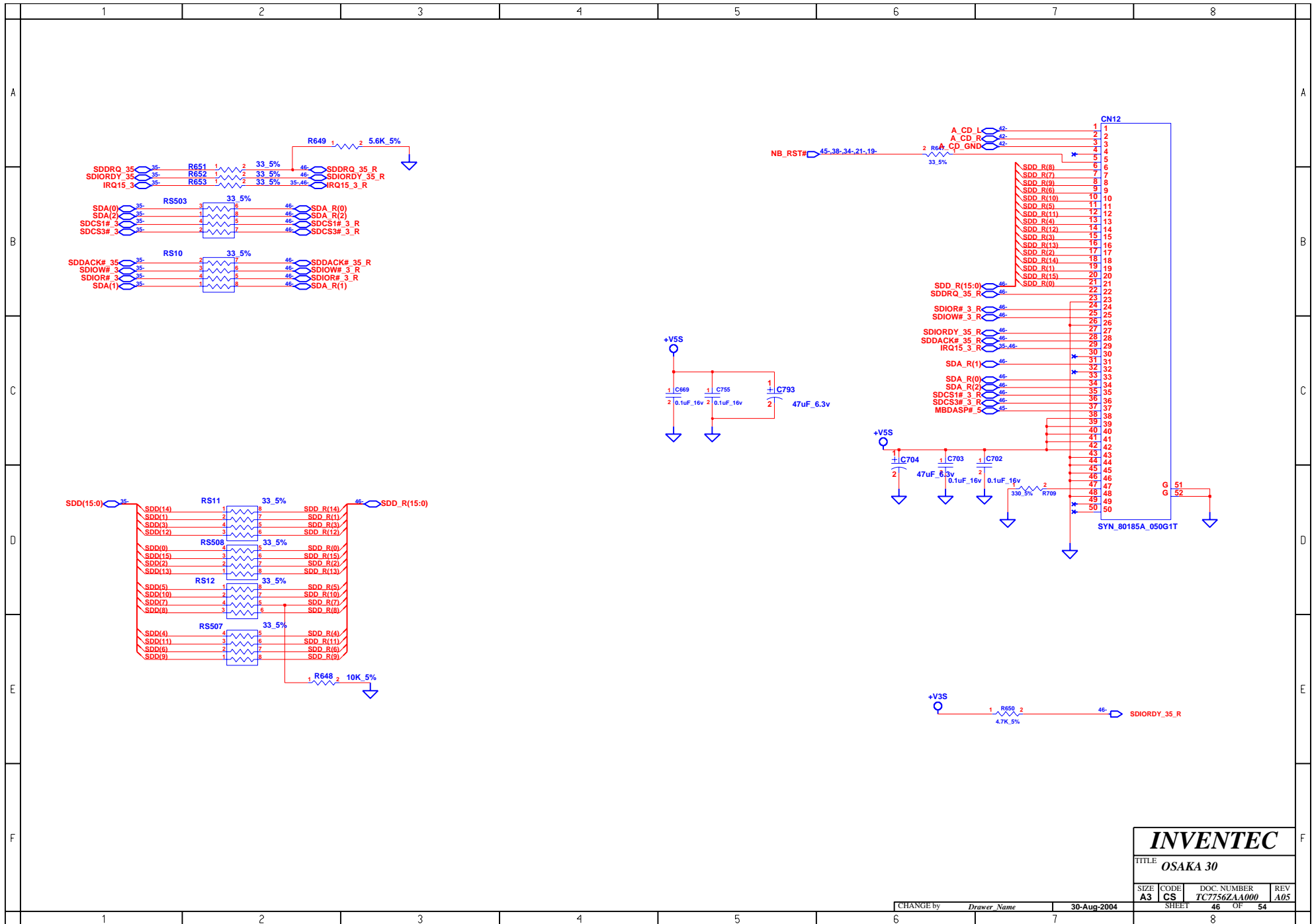
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TITLE OSAKA 30

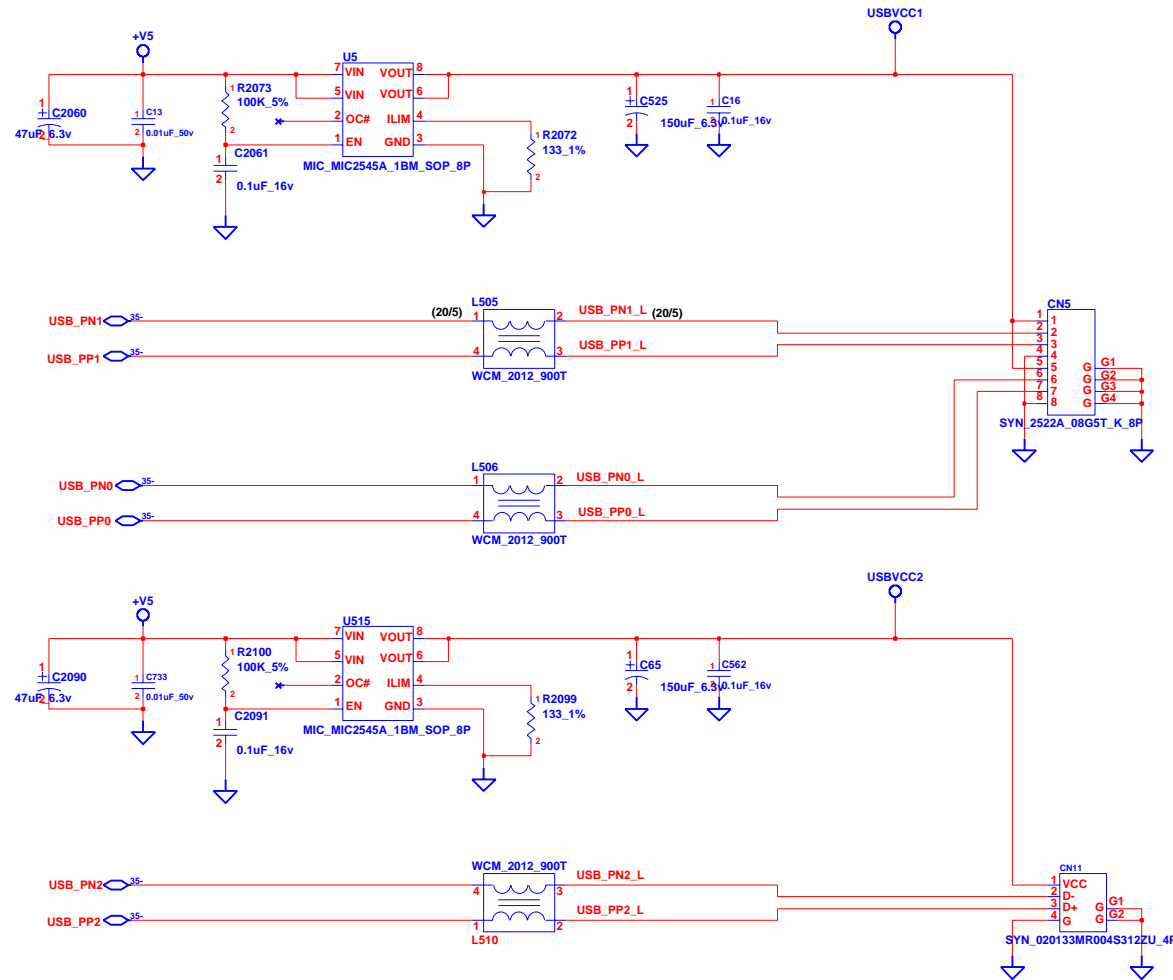
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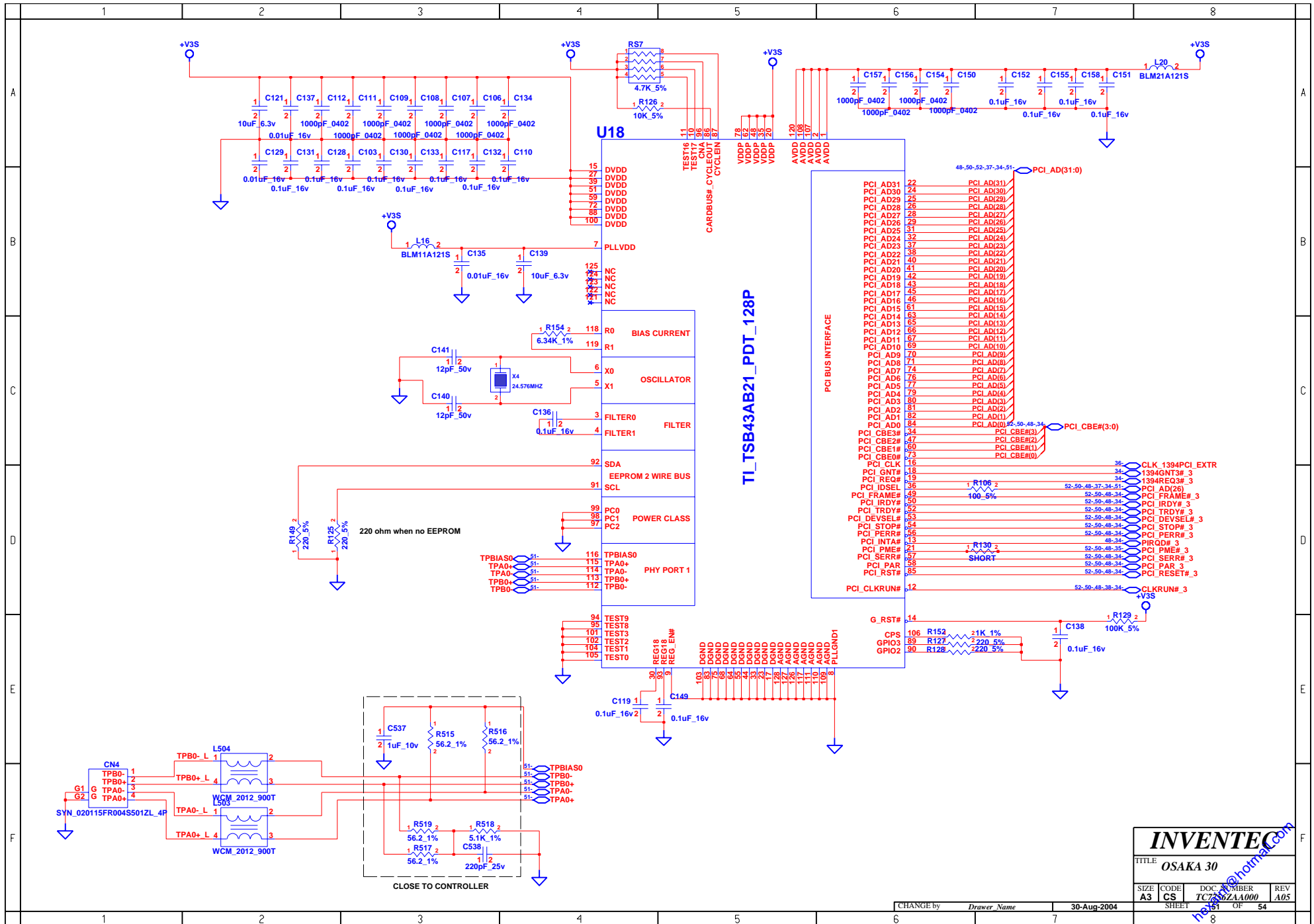
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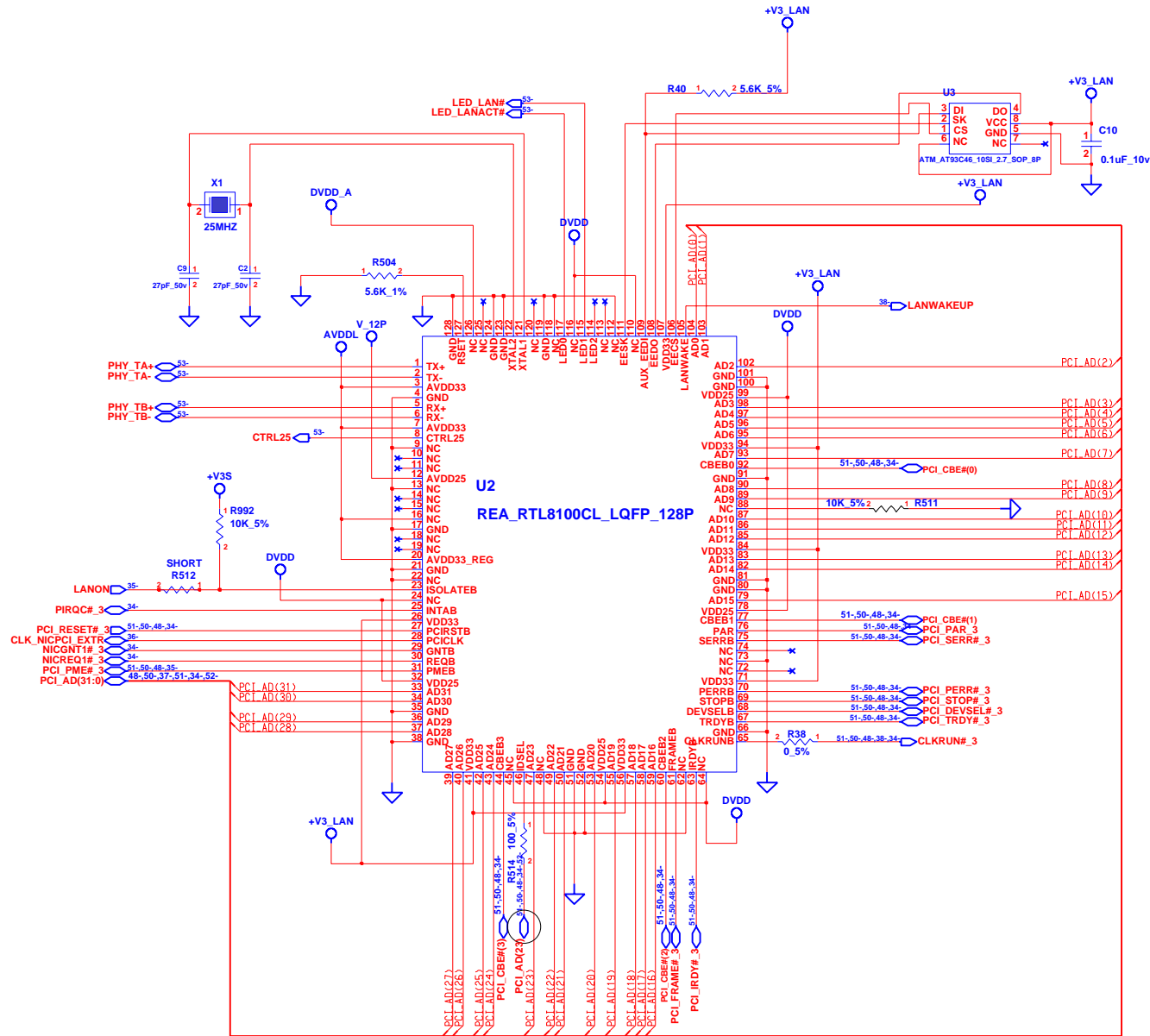
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