

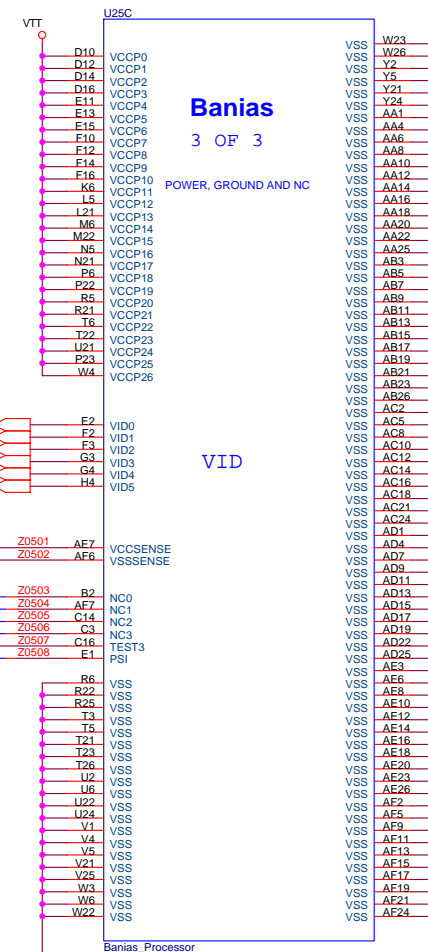
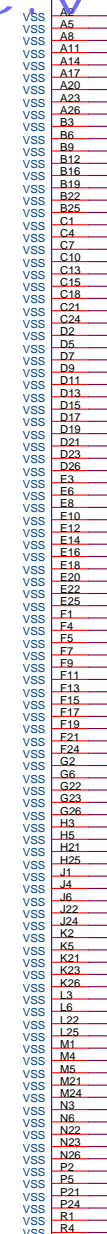
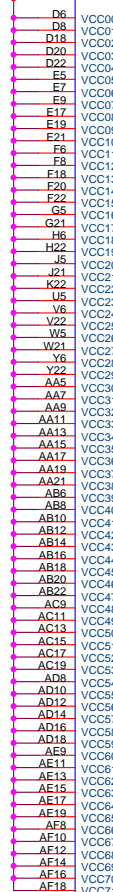
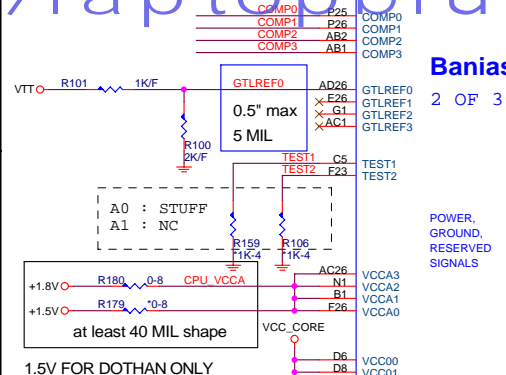
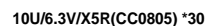
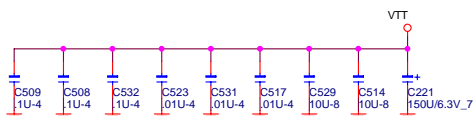
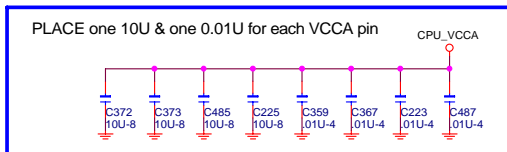
PCI ROUTING TABLE IDSEL INTERRUPT DEVICE

REQ0# / GNT0# AD18 INTD#
REQ1# / GNT1# AD20 INTB# , INTC#
REQ3# / GNT3# AD17 INTE#

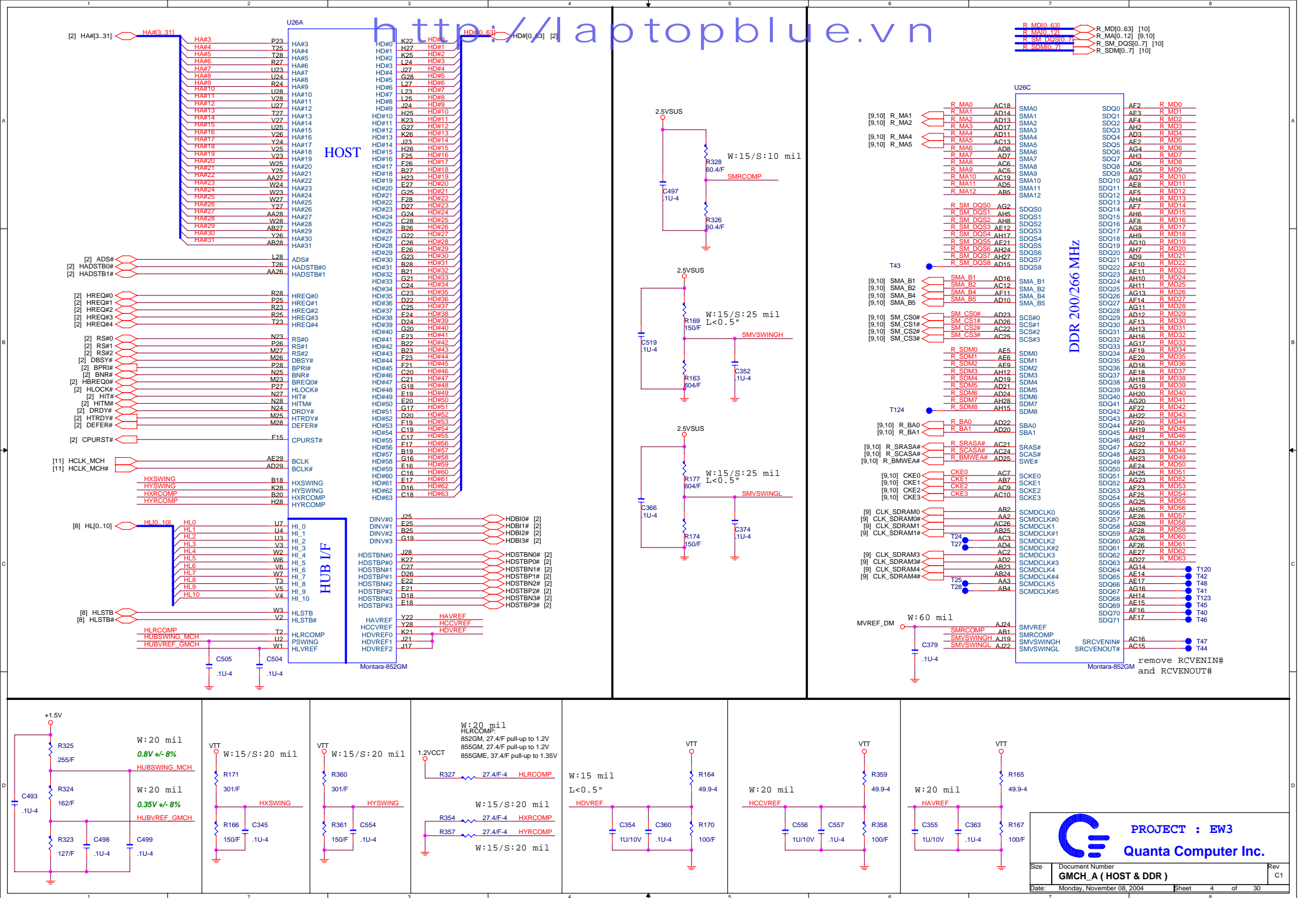
REALTEK LAN
MINI-PCI
TI 1410A



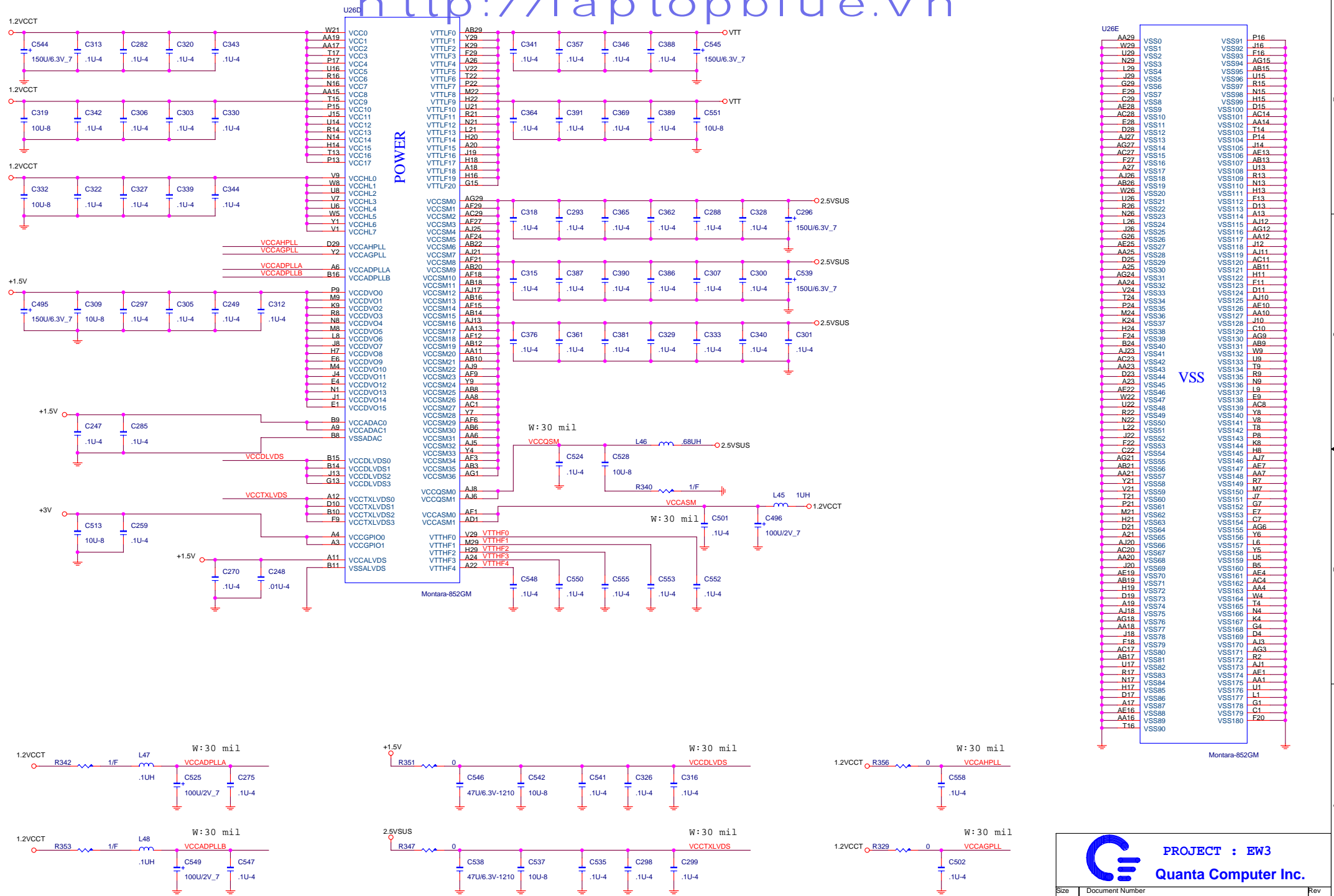
PROJECT : EW3
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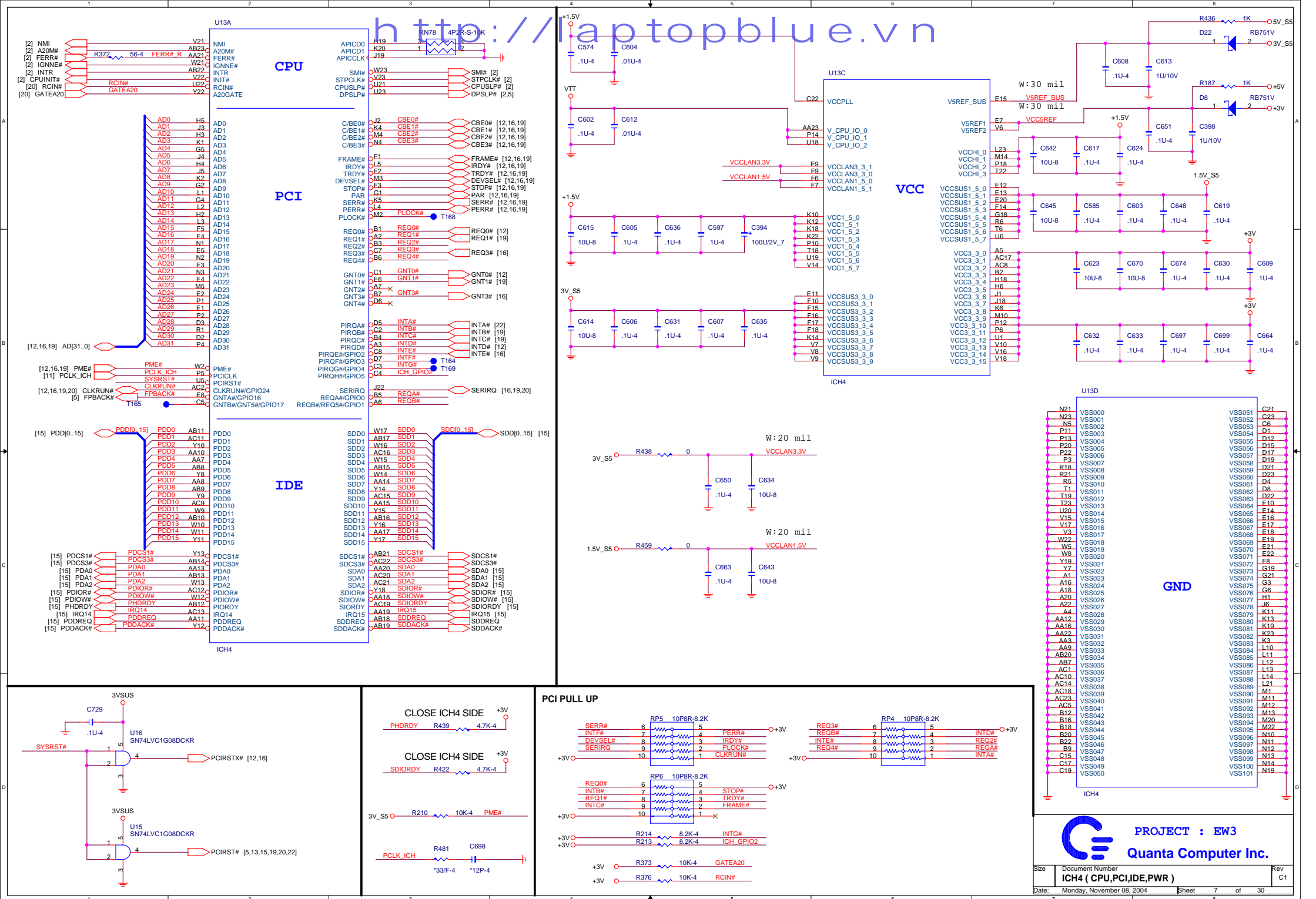


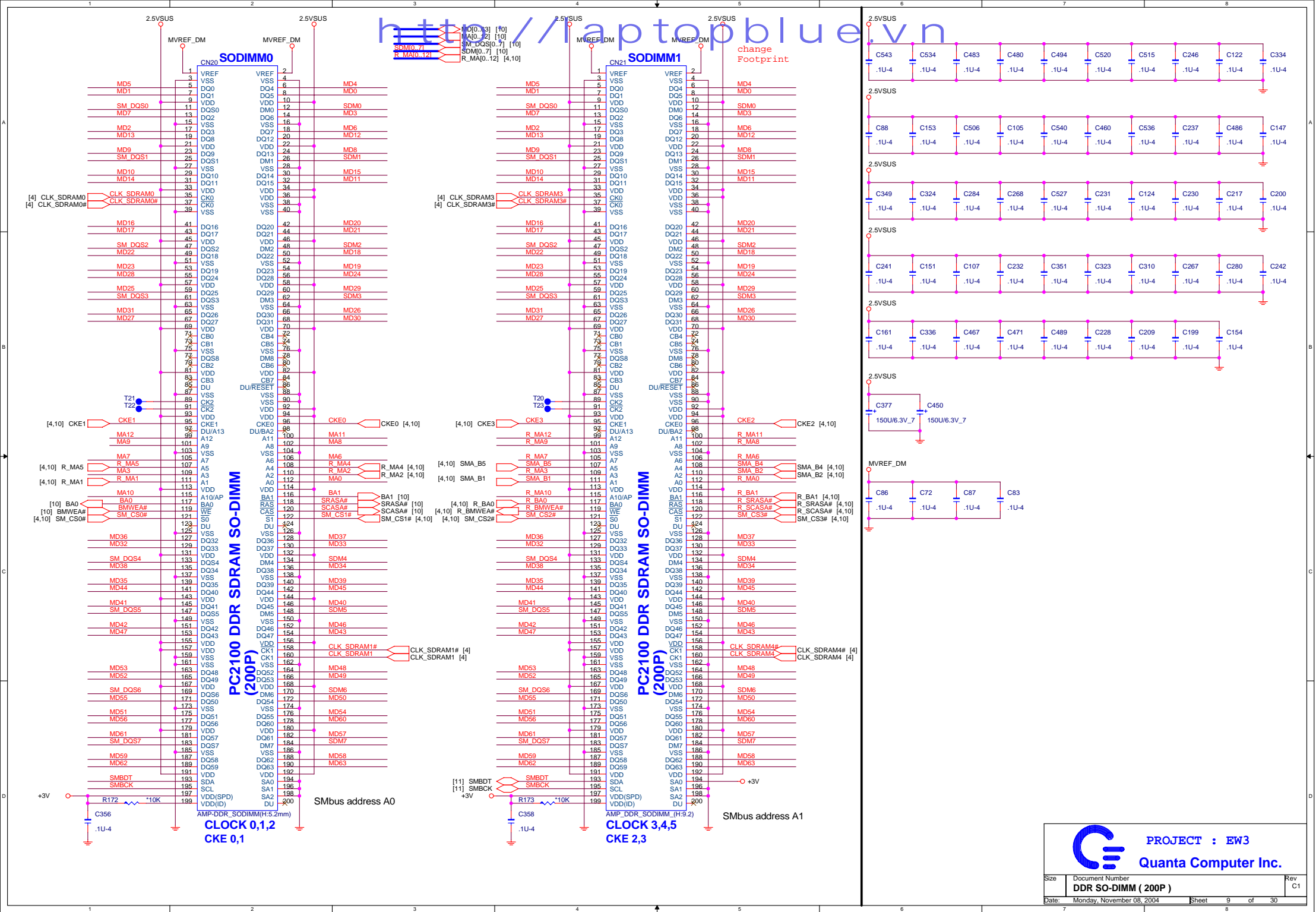
PROJECT : EW3
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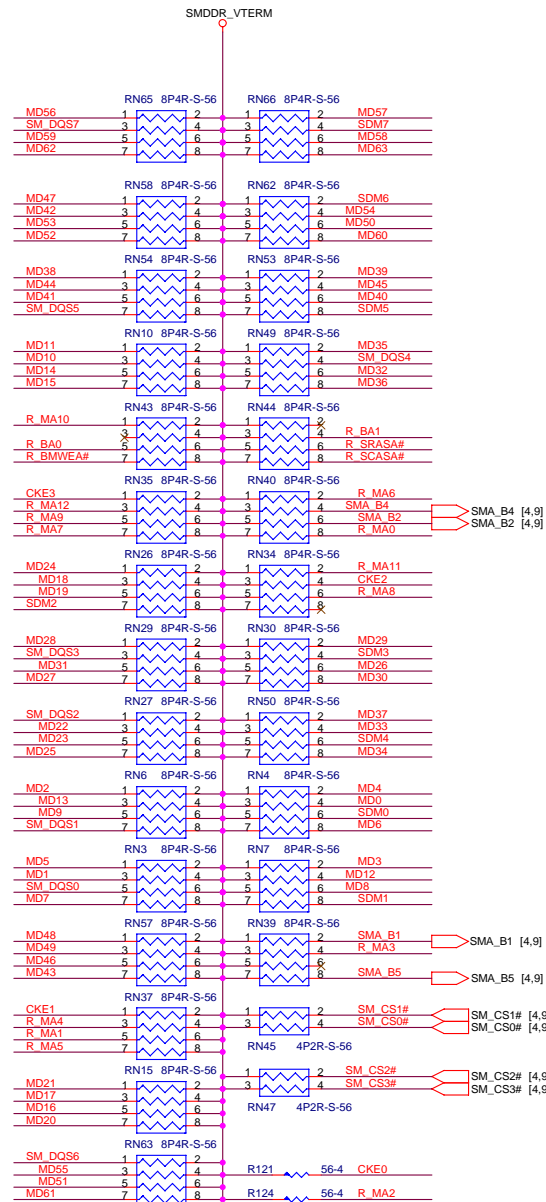


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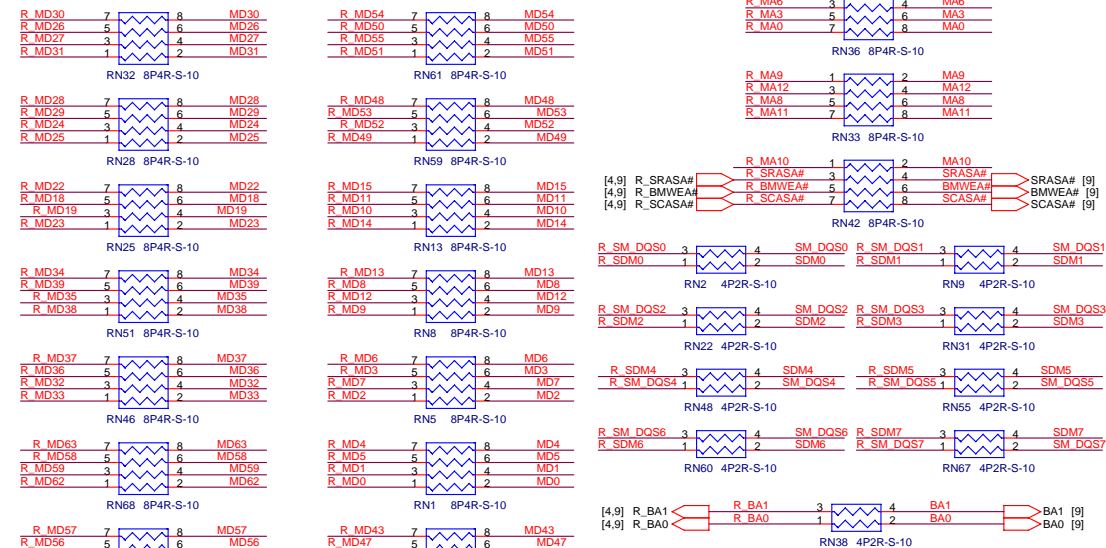
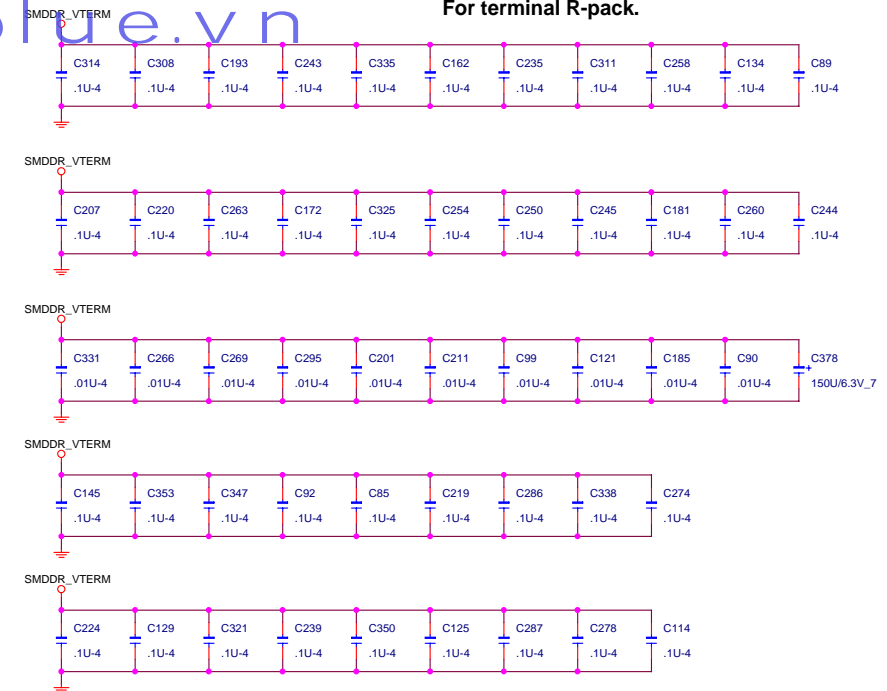
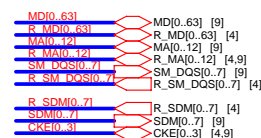






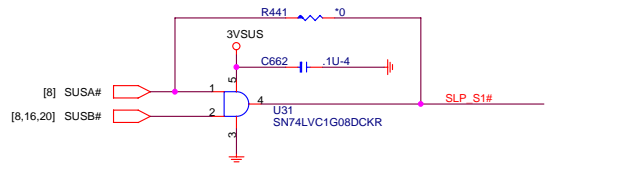
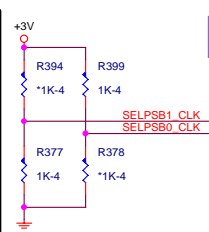


Damping put BOT, termination put TOP



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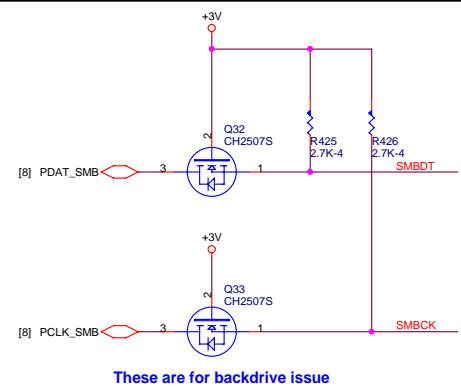
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66
0	0	1	100	66	66
0	1	0	200	66	66
0	1	1	133	66	66



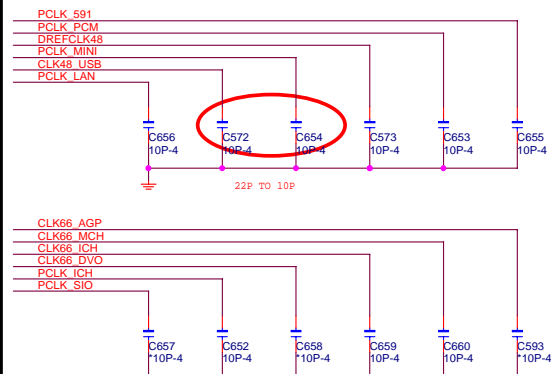
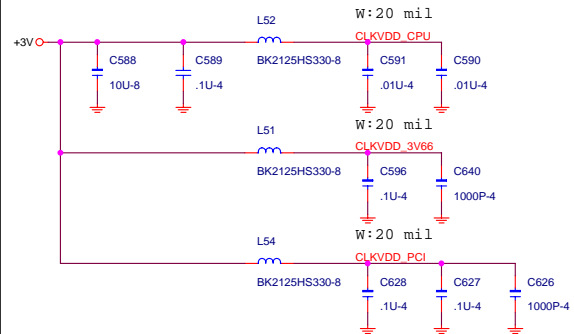
SMBus Byte 0 , Bit 5 = 0	66 MHz	W	S/S
SMBus Byte 0 , Bit 5 = 1	48 MHz	W/O	S/S

Byte 0 , Bit 7 = 0 Disable Spread Spectrum
Byte 0 , Bit 7 = 1 Enable Spread Spectrum

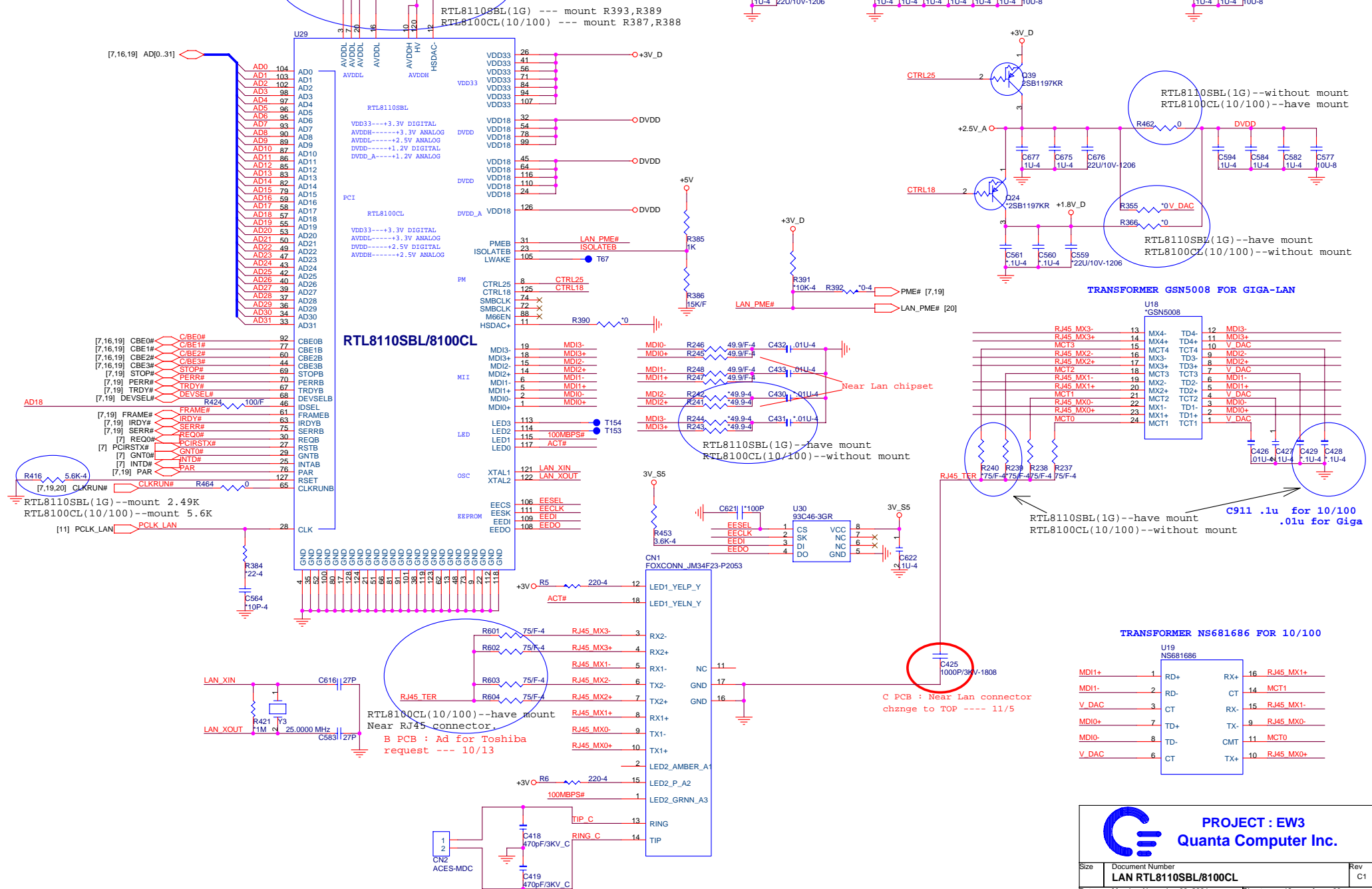
Byte 4 Bit 7	Byte 5 Bit 7	Byte 5 Bit 6	Spread Mode	Spread %
SS2	SS1	SS0		
0	0	0	DOWN	+0.00 , -0.25
0	0	1	DOWN	+0.00 , -0.50
0	1	0	DOWN	+0.00 , -0.75
0	1	1	DOWN	+0.00 , -1.00
1	0	0	CENTER	+0.13 , -0.13
1	0	1	CENTER	+0.25 , -0.25
1	1	0	CENTER	+0.37 , -0.37
1	1	1	CENTER	+0.50 , -1.50



These are for backdrive issue




ID Select : AD18
Interrupt Pin : INTD#
Request Indicate : REQ0#
Grant Indicate : GNT0#



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	LAN RTL8110SBL/8100CL	C1
Date:	Monday, November 08, 2004	Sheet 12 of 30

 <div style="display: inline-block; vertical-align: middle; margin-left: 20px;"> <p style="margin: 0;">PROJECT : EW3</p> <p style="margin: 0;">Quanta Computer Inc.</p> </div>			
Size	Document Number	Rev	
	DVO CH7011A & RJ45-11 CON		C1
Date:	Monday, November 08, 2004	Sheet	13 of 30

CRT PORT

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[5] INT_VGA_RED R66 BLM158B220SN1-4 VGA_RED
[5] INT_VGA_GRN R63 BLM158B220SN1-4 VGA_GRN
[5] INT_VGA_BLU R64 BLM158B220SN1-4 VGA_BLU

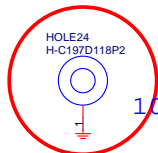
[5] INT_VSYNC RN12 3 4 4P2R-S-0 VSYNC
[5] INT_HSYNC 1 2

[5] INT_DDCCLK RN11 3 4 4P2R-S-0 DDCCLK
[5] INT_DDCDAT 1 2

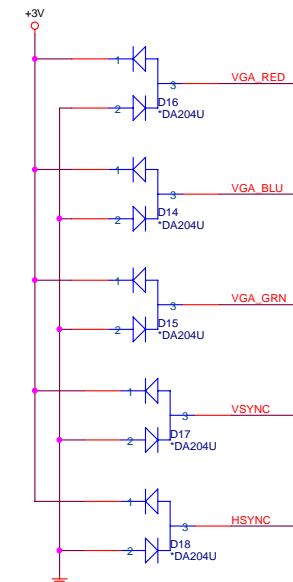
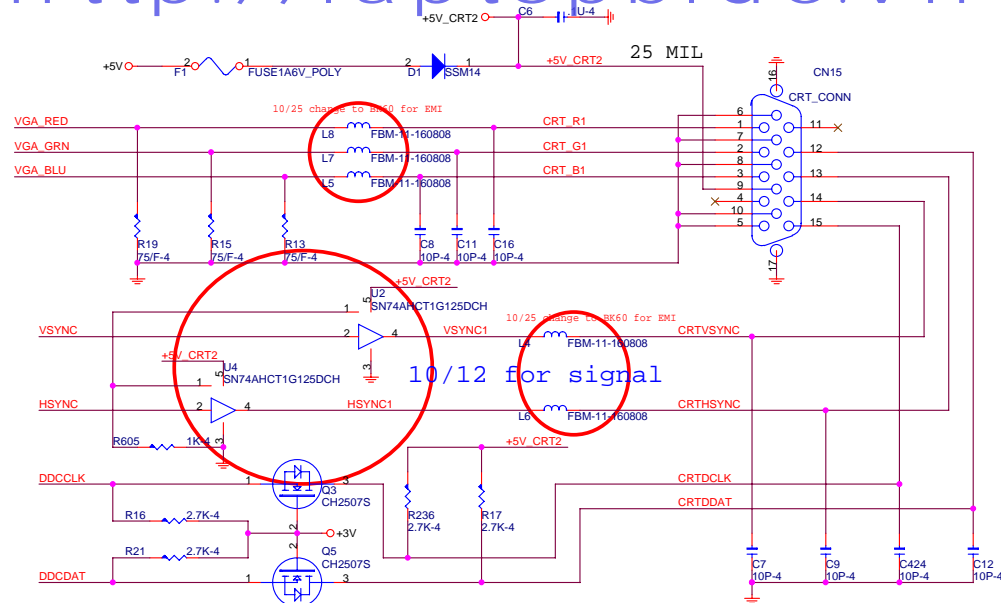
[22] VGA_RED
[22] VGA_GRN
[22] VGA_BLU

[22,24] VSYNC
[22,24] HSYNC

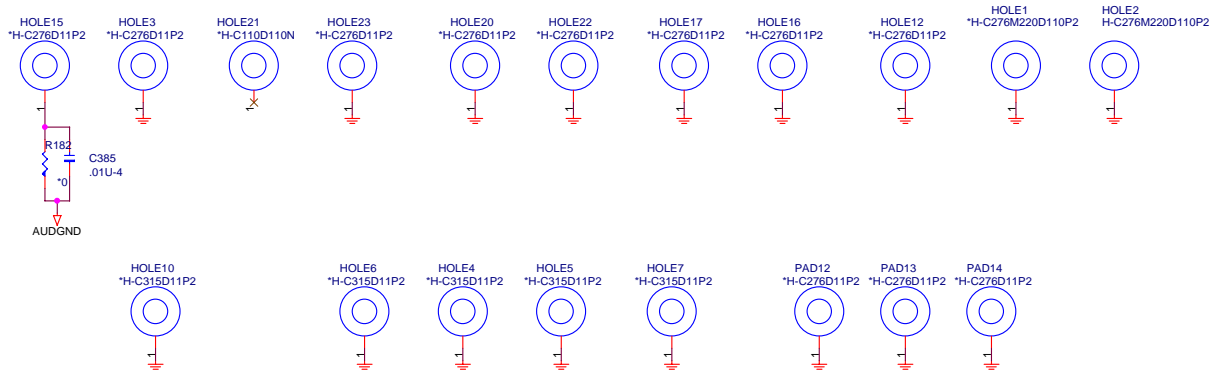
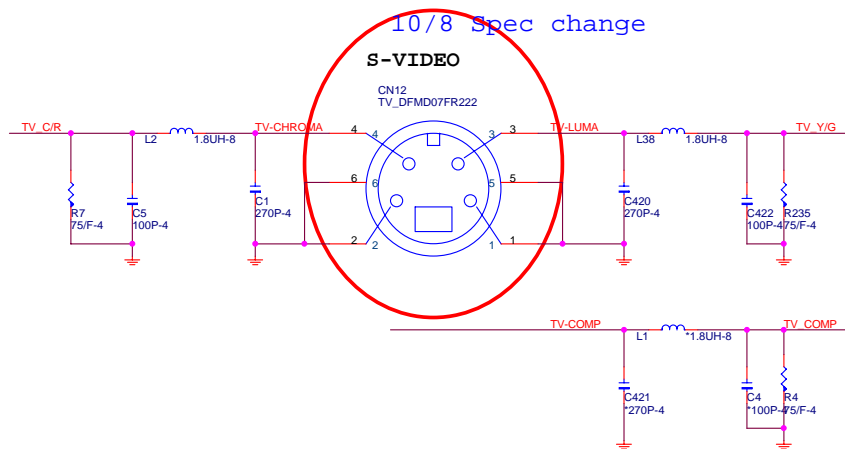
[22] DDCCLK
[22] DDCDAT



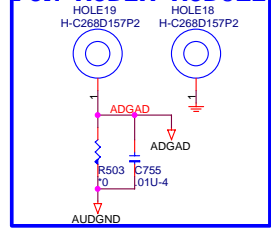
10/13 for ME



FOR M/B HOLE



FOR MODEM MODULE



FOR CPU HEATSINK

NEAR NORTHBRIDGE

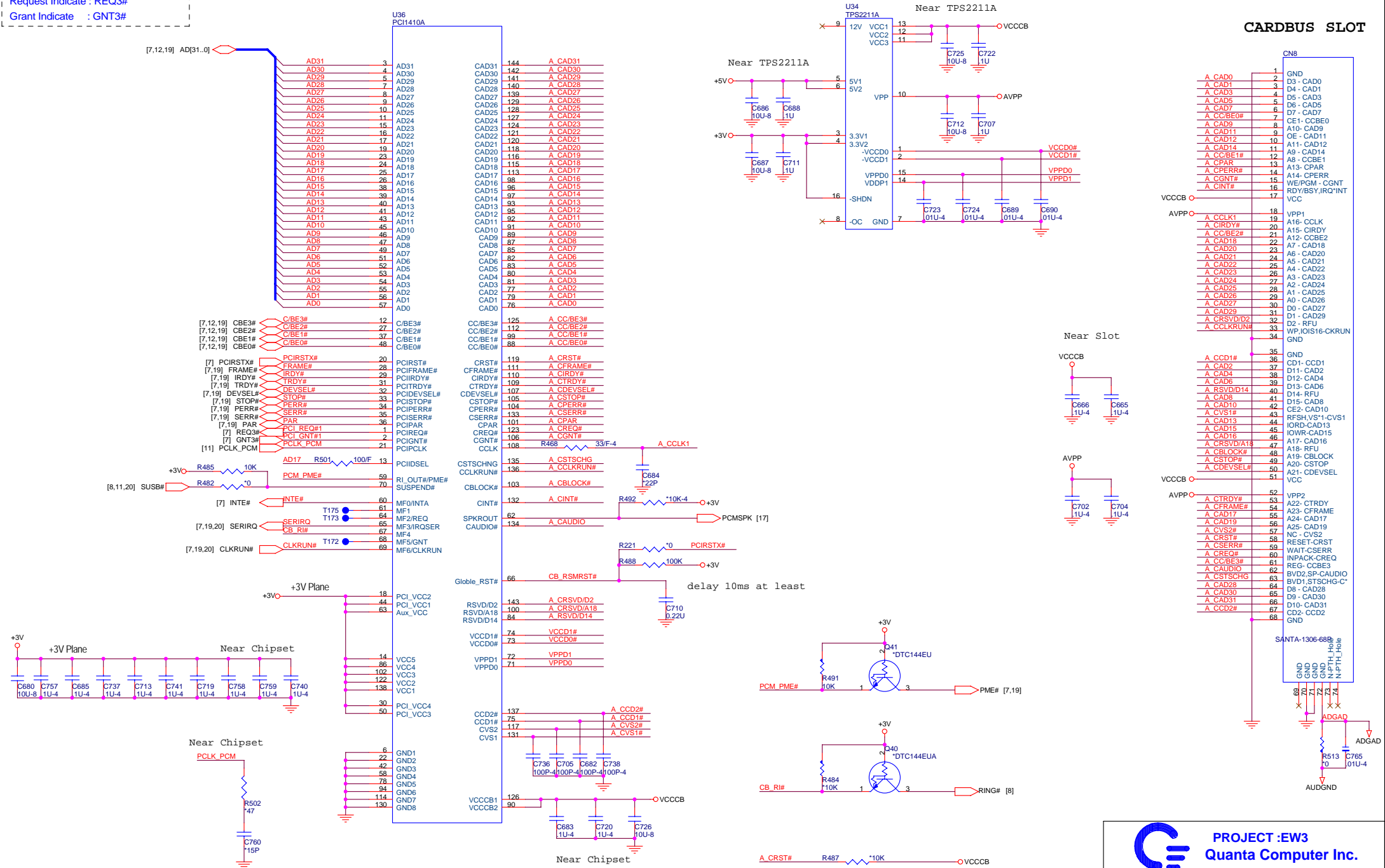
08/23

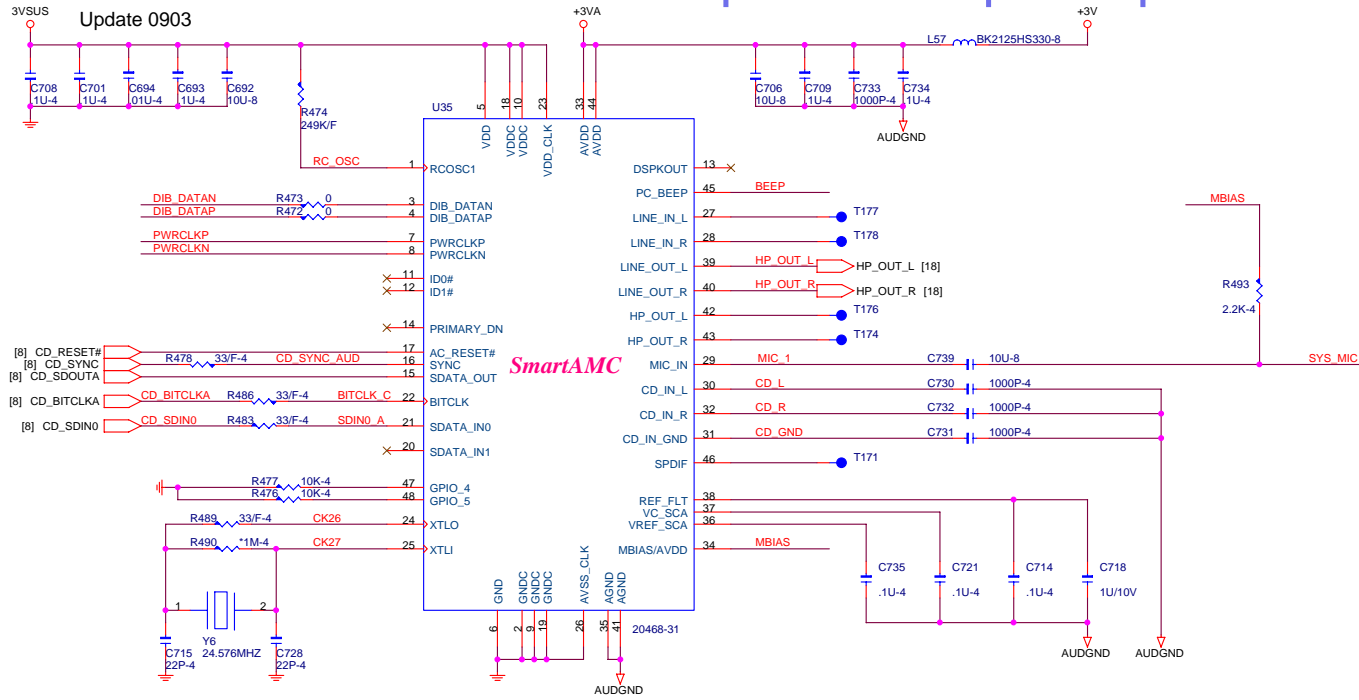


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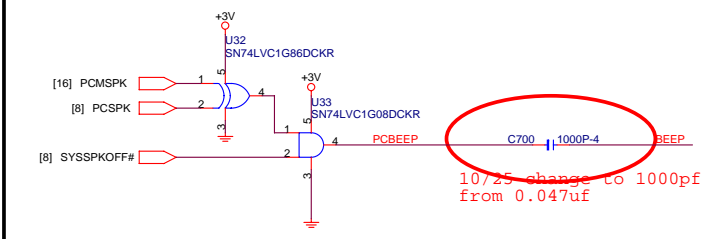
ID Select : AD17
Interrupt Pin : INTE#
Request Indicate : REQ3#
Grant Indicate : GNT3#

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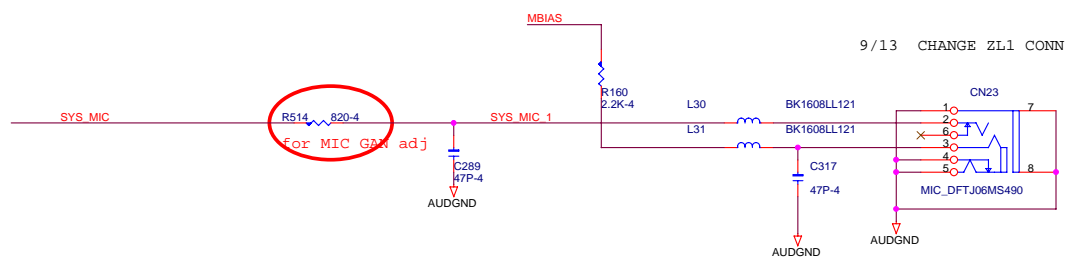




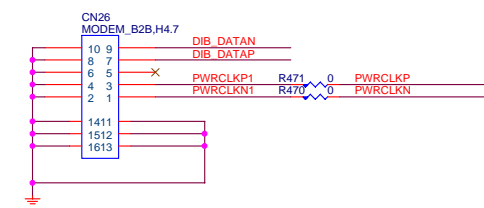
BEEP



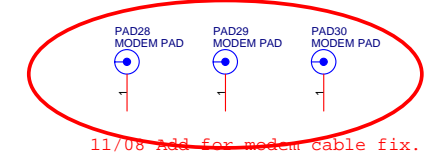
MIC



MODEM



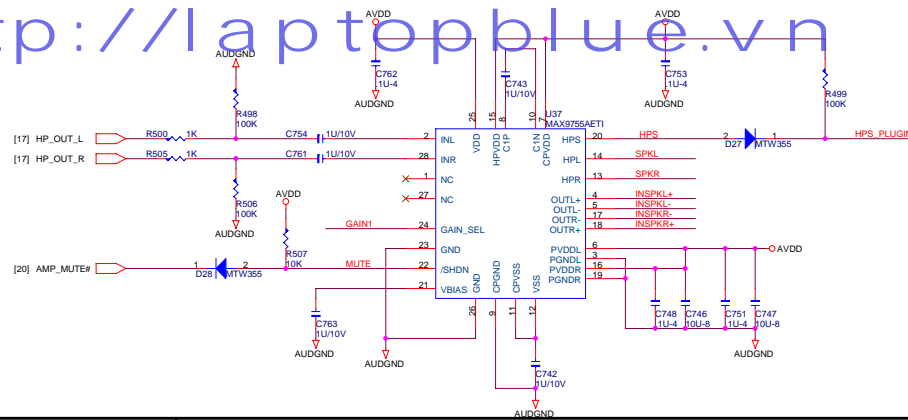
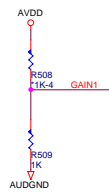
9/3 ADD MODEM Connect



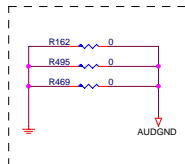
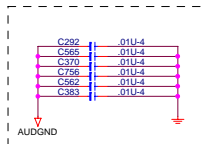
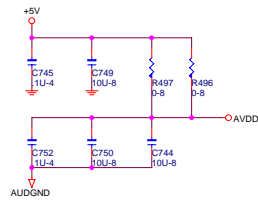
PROJECT : EW3
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Size	Document Number	Rev
	CODEC & LINE IN & MIC	C1
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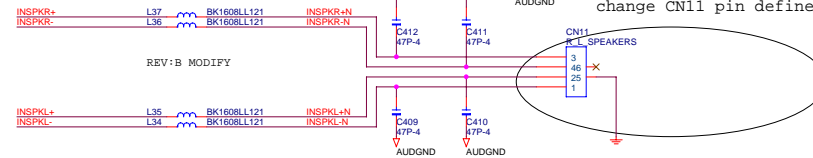
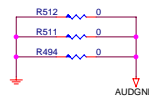
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



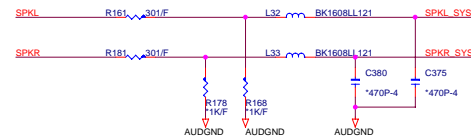
AMP POWER



SPEAKER CON.

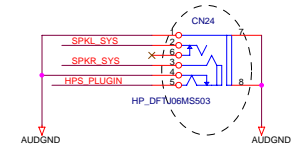


LINE-OUT

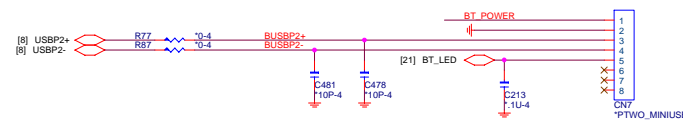
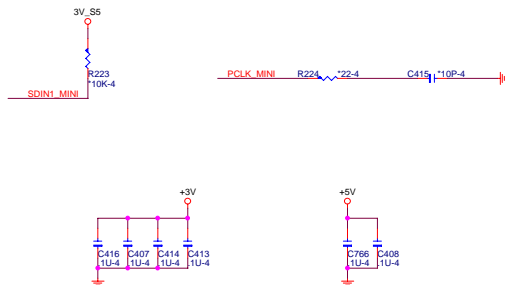


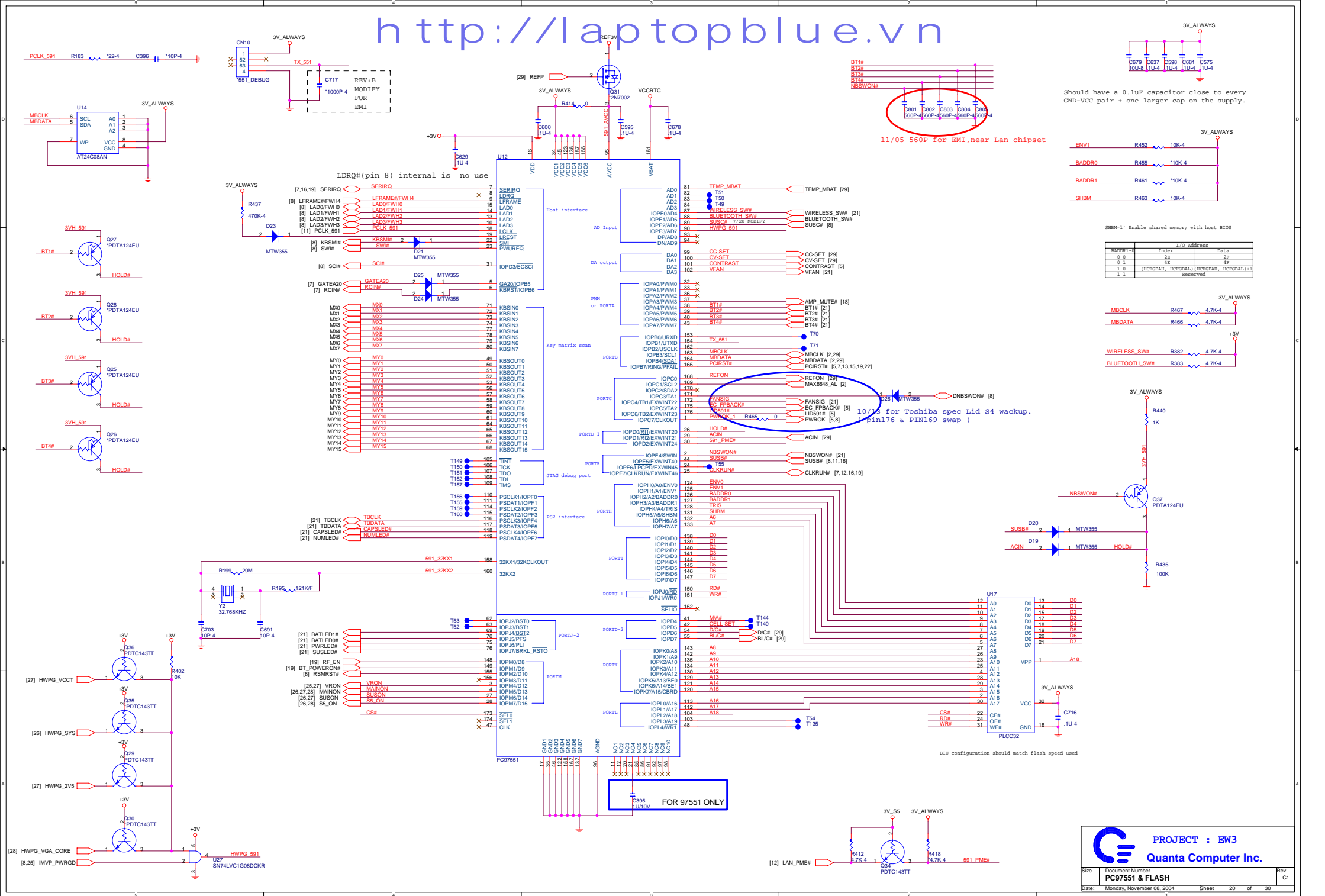
9/13 CHANGE ZL1 CONN

LINE OUT

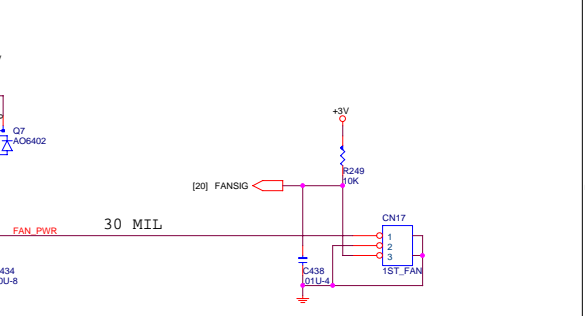
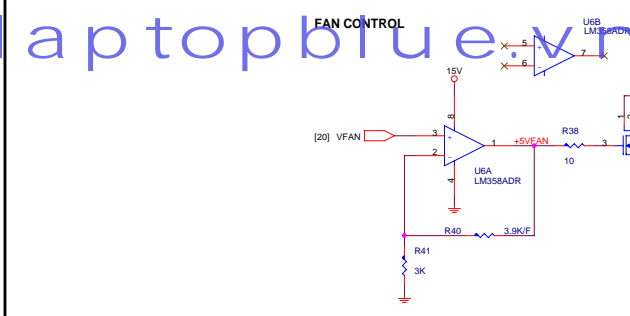
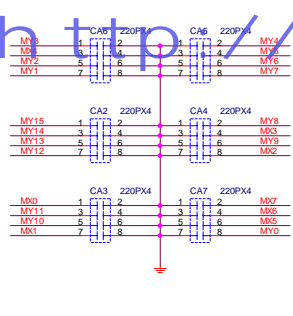
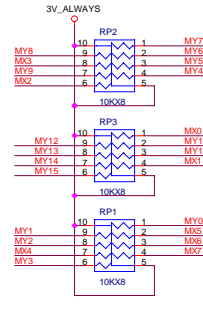
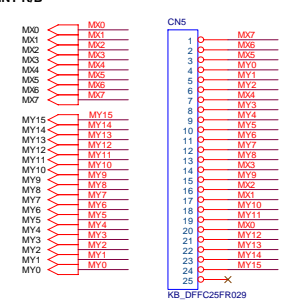


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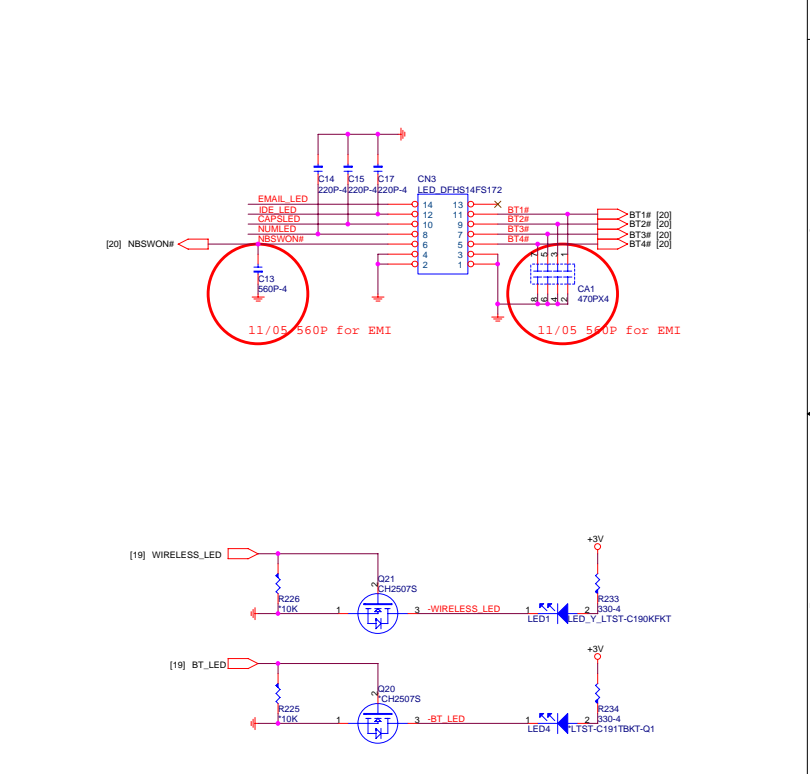
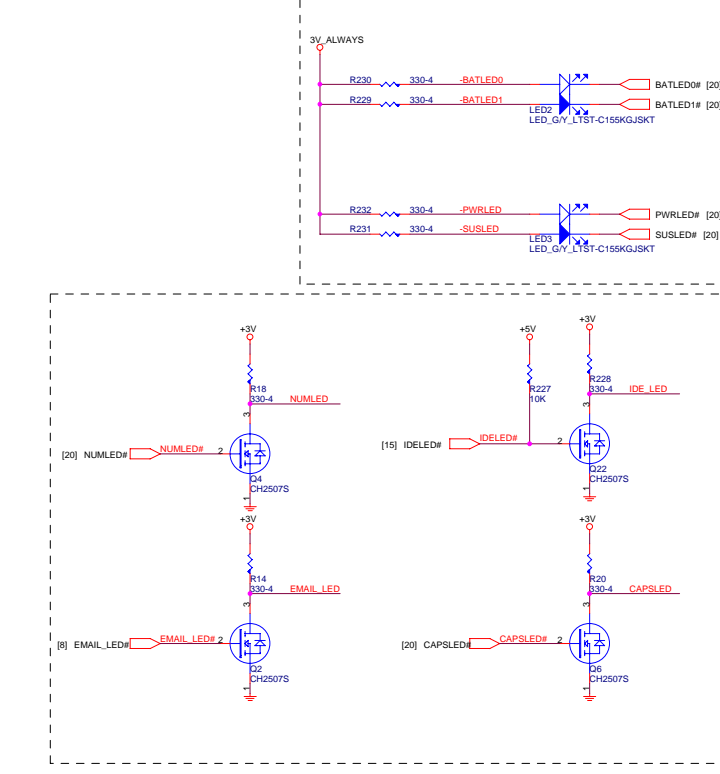
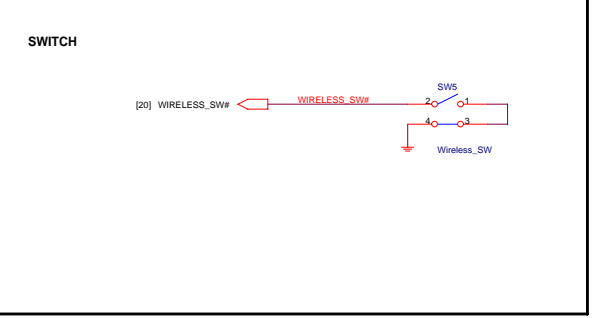
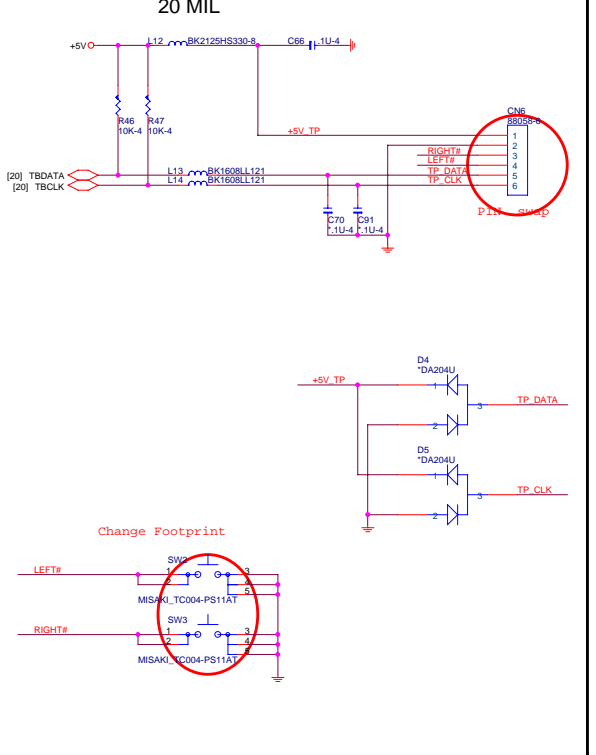


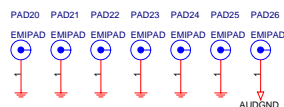
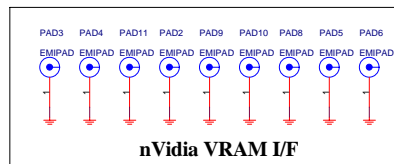
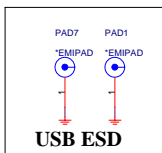
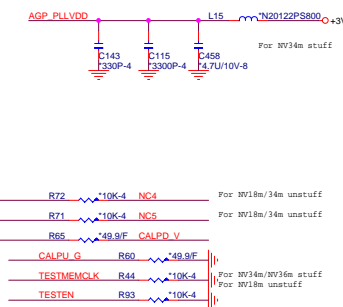
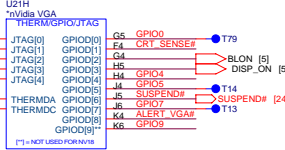
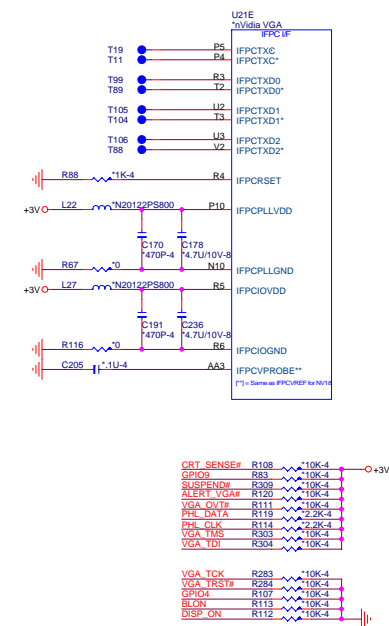
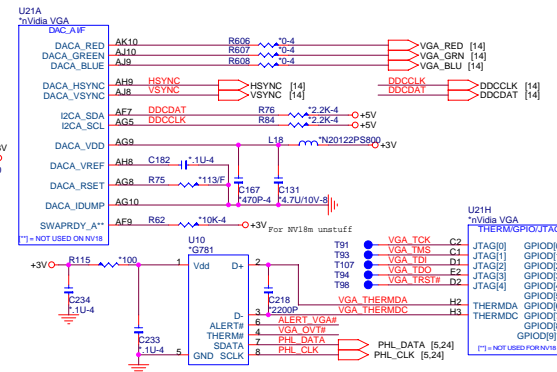
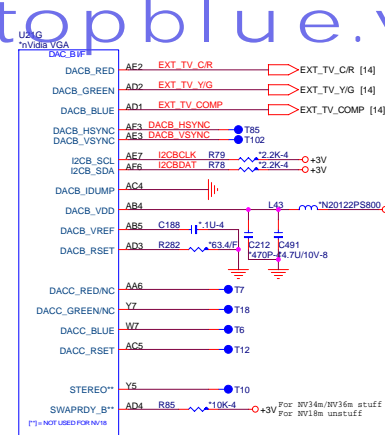
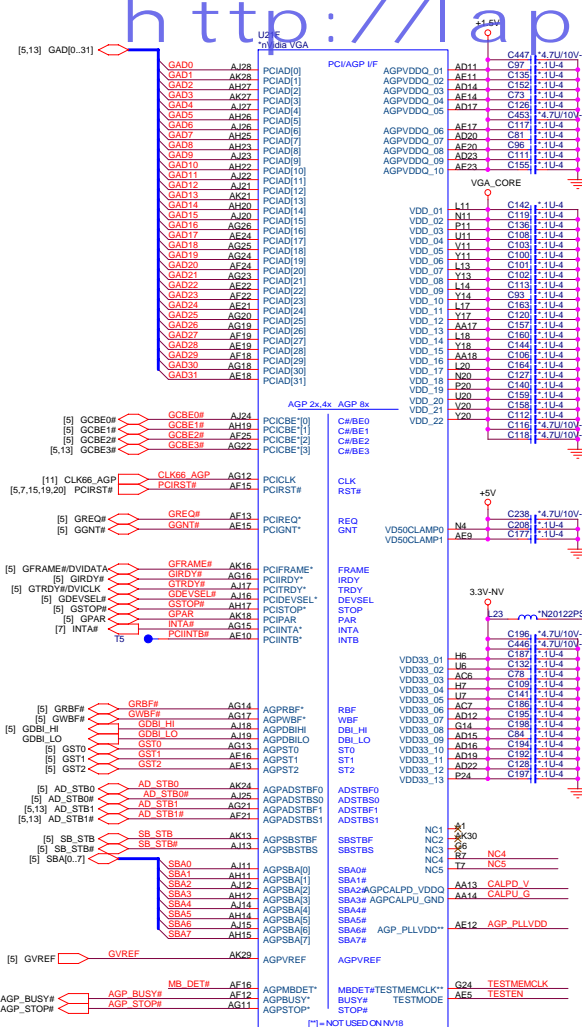


INT K/B



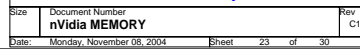
TOUCH PAD



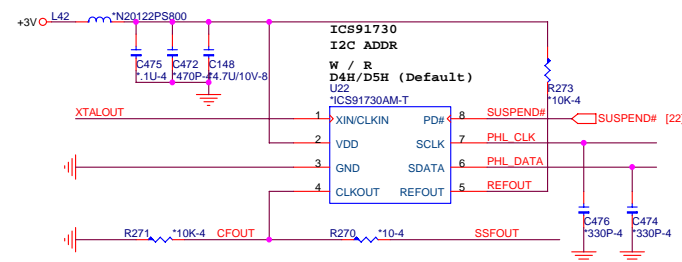
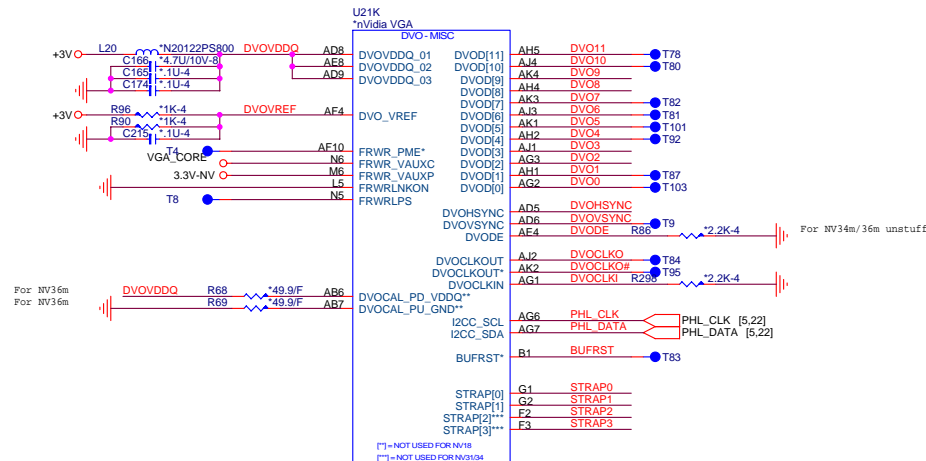
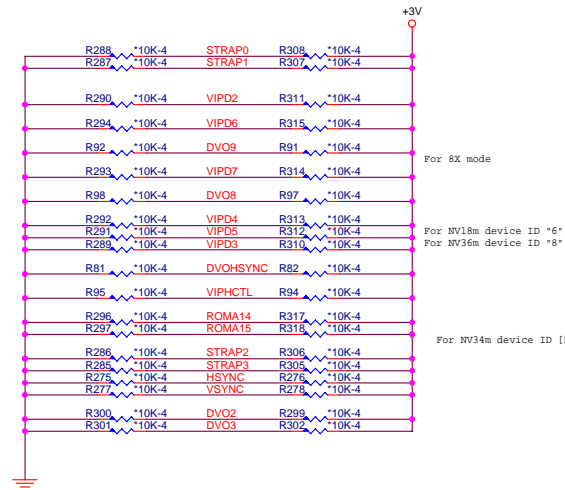
nVidia AGP I/F

GPIO	DEFAULT STATE	USAGE	I/O
0	PD	Spread spectrum control	O
1	PD	Hot plug/unplug	I/O
2	PD	Panel backlight enable	O
3	PD	Panel power enable	O
4	PD	TBD	I/O
5	PD	Spread spectrum control	O
6	PU	HW suspend	I
7	PU	Dynamic NVVDD voltage control	O
8	PU	Thermal monitor	I
9	PU	Fan Control	I/O

0=1.2V
1=1.35V



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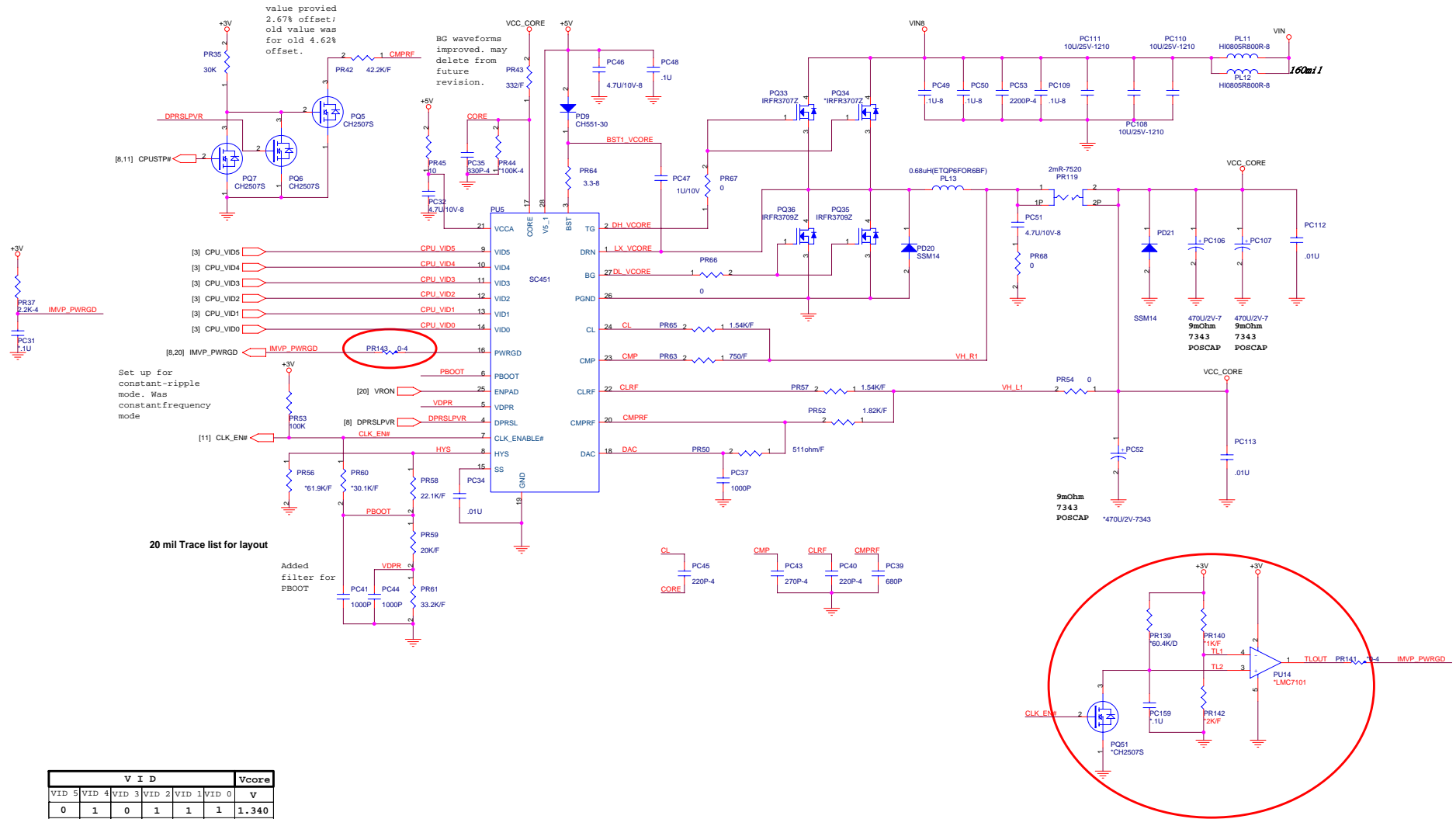
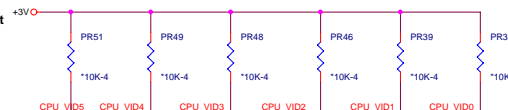


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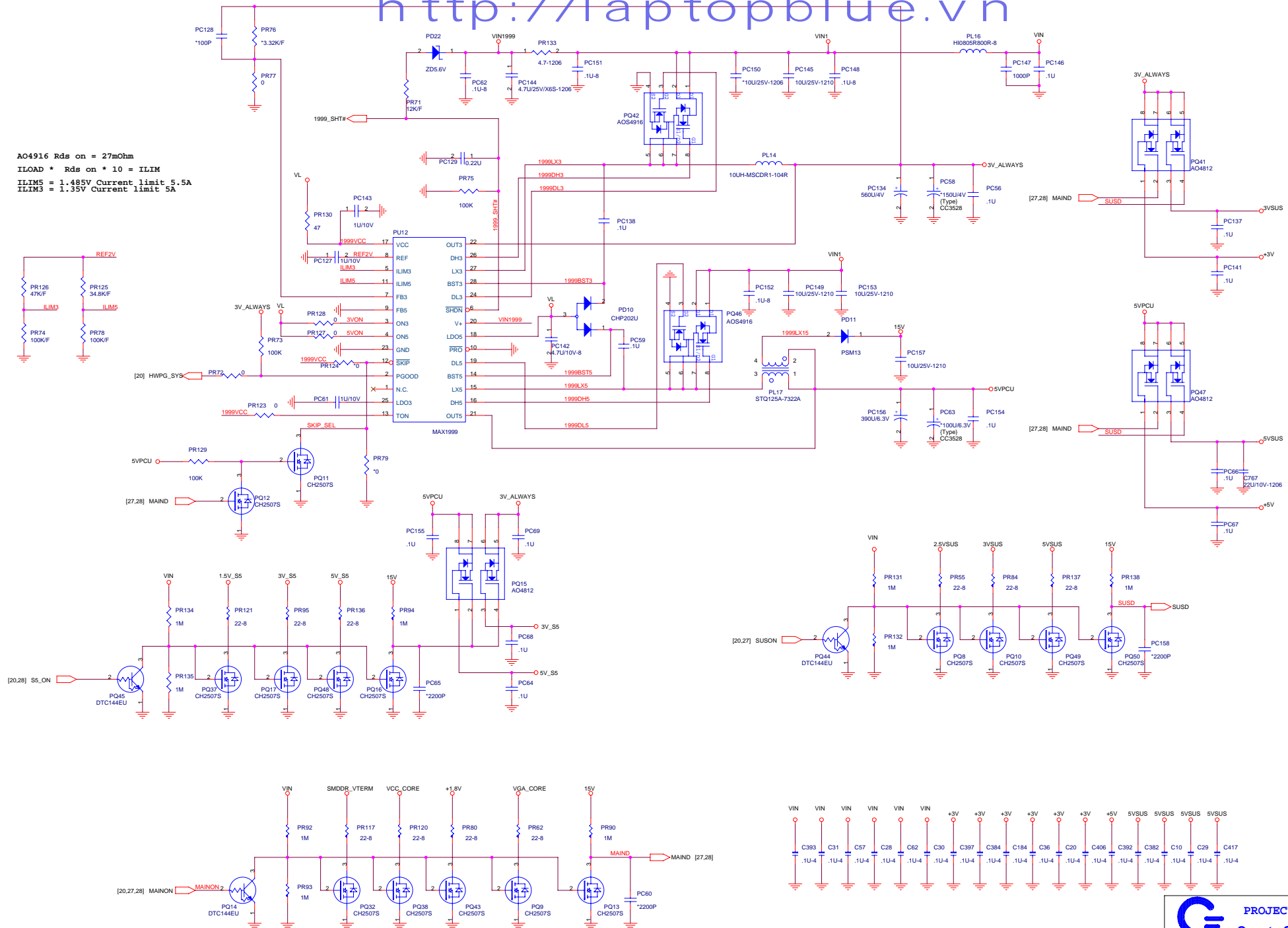
V I D							Vscore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0		V
0	1	0	1	1	1		1.340
0	1	1	0	0	0		1.324
0	1	1	0	1	0		1.292
0	1	1	1	0	0		1.260
0	1	1	1	0	1		1.244
0	1	1	1	1	1		1.212
1	0	0	0	0	1		1.180
1	0	0	0	1	1		1.148
1	0	0	1	1	0		1.100
1	0	1	0	0	1		1.052
1	0	1	0	1	1		1.020
1	0	1	1	1	0		0.972
1	1	0	0	0	0		0.940

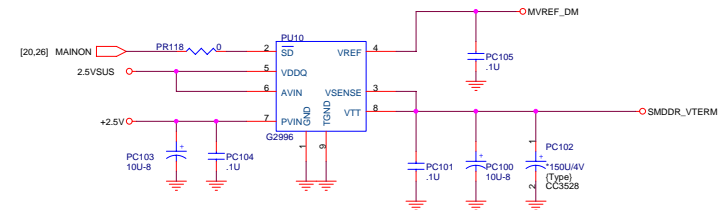
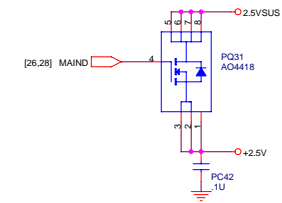
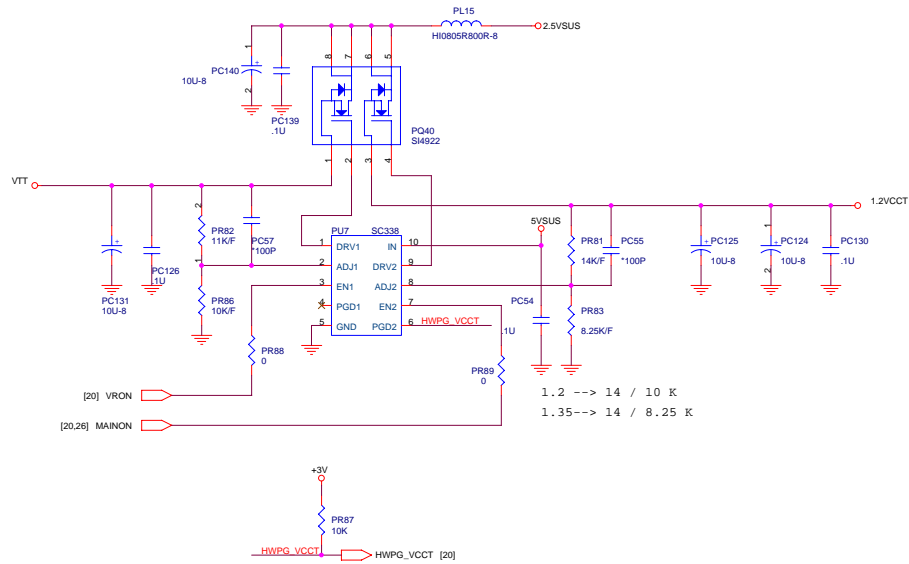
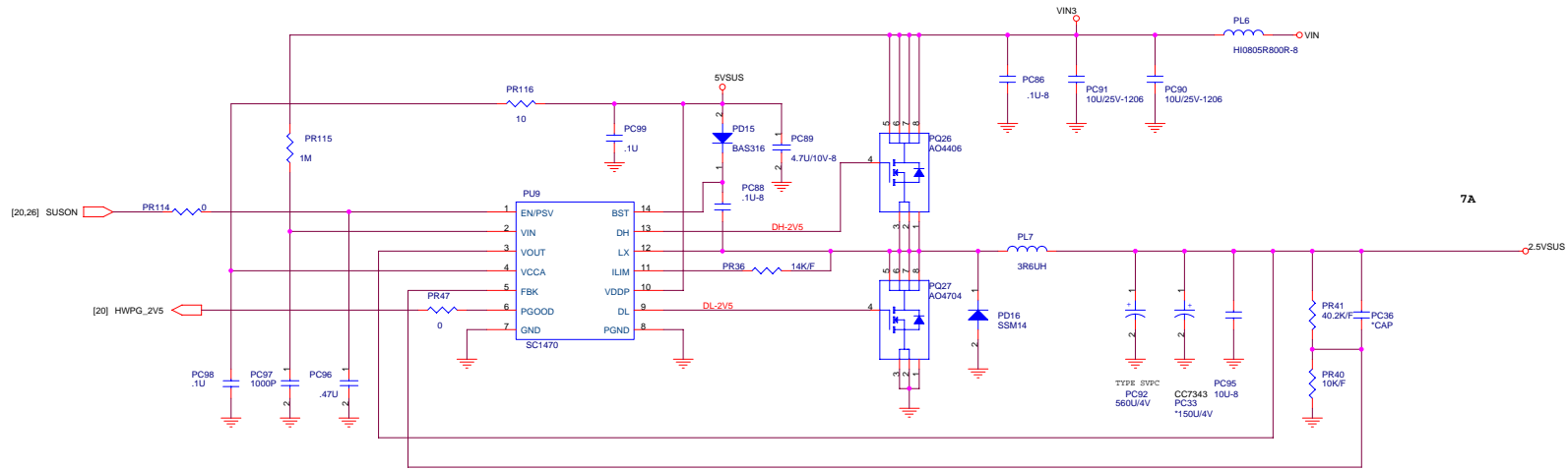
DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

SC1476
pin 4 pin
5 pin 7
pin 25
pin 30

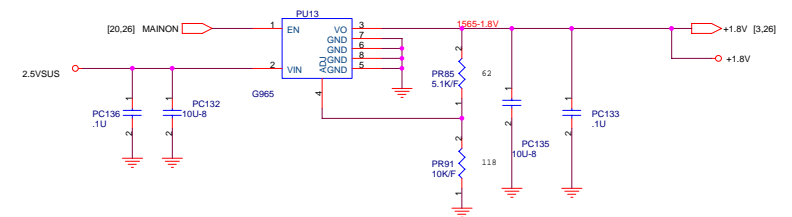


AO4916 Rds on = 27mOhm
 $I_{LOAD} * R_{ds\ on} * 10 = I_{LIM}$
 $I_{LIM5} = 1.485V$ Current limit 5.5A
 $I_{LIM3} = 1.35V$ Current limit 5A





PROJECT : EW3
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[illegible]

6/4 change ED1 connector

